```
trap 0x0000000e Page Fault
cr2 0xeebfdfc8
err 0x00000000 [kernel, read, not-present]
eip 0xf01007a4
cs 0x----0008
```

Error code

The Page Fault sets an error code:

	Length	Name	Description
P	1 bit	Present	When set, the page fault was caused by a page-protection violation. When not set, it was caused by a non-present page.
w	1 bit	Write	When set, the page fault was caused by a page write. When not set, it was caused by a page read.
U	1 bit	User	When set, the page fault was caused while CPL = 3. This does not necessarily mean that the page fault was a privilege violation.
R	1 bit	Reserved write	When set, the page fault was caused by reading a 1 in a reserved field.
1	1 bit	Instruction Fetch	When set, the page fault was caused by an instruction fetch.

In addition, it sets the value of the CR2 register to the virtual address which caused the Page Fault.

Entry.s _start:

```
ELFHDR->e_entry = 800020
00800020 <<u>start</u>>:
```

0x18 4 8 E entry This is the memory address of the entry point from where the process starts executing. This field is either 32 or 64 bits long depending on the format defined earlier.

thisenv= eec00000

ELFHDR->e_entry = 900030