

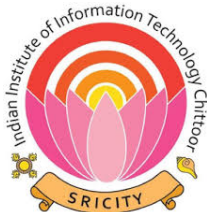
# CO: Computer Organization

## Tutorial-7

Indian Institute of Information Technology, Sri City

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Q1).Show a Pipeline execution for the following assembly code.

ADD R1, R2, R3
SUB R4, R5, R6
MUL R12, R4, R1
MUL R4, R1, R11
MUL R7, R1, R4
ADD R7, R8, R9
MUL R11, R1, R7

**Assume that each stage takes 1 clock cycle.**

Q2).Show a Pipeline execution for the following assembly code (Assume N=4).

LOAD R1, N
SUB R2, R2, R2
SUB R0, R0, R0
DO: INC R0
ADD R2, R0
CMP R0, R1
JLT DO
STORE X, R2

**Assume that each stage takes 1 clock cycle.**