CO: Computer Organization

Tutorial-2

Indian Institute of Information Technology, Sri City

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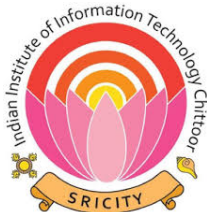
Tutorial -2

1. Consider two n-bit numbers in 2's complement form. Show that the logic expression $C_n \bigoplus C_{n-1}$ is a correct indicator for overflow detection. Where C_n is final carry.

2. Design an efficient 64-bit adder using 8-bit CLAs.