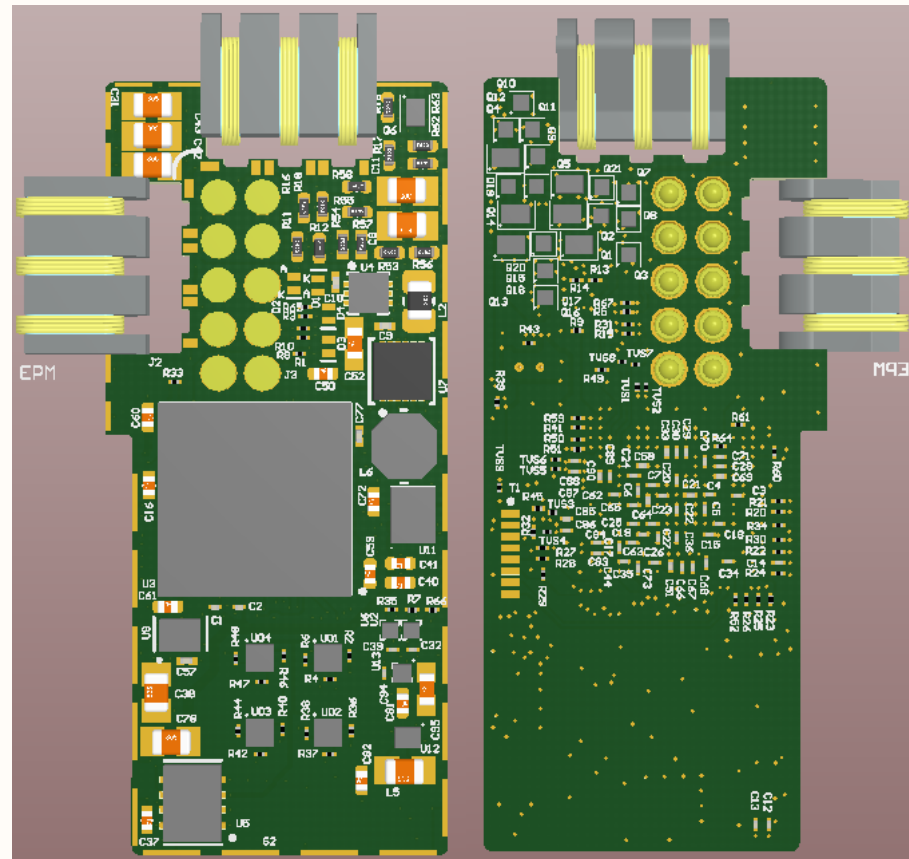
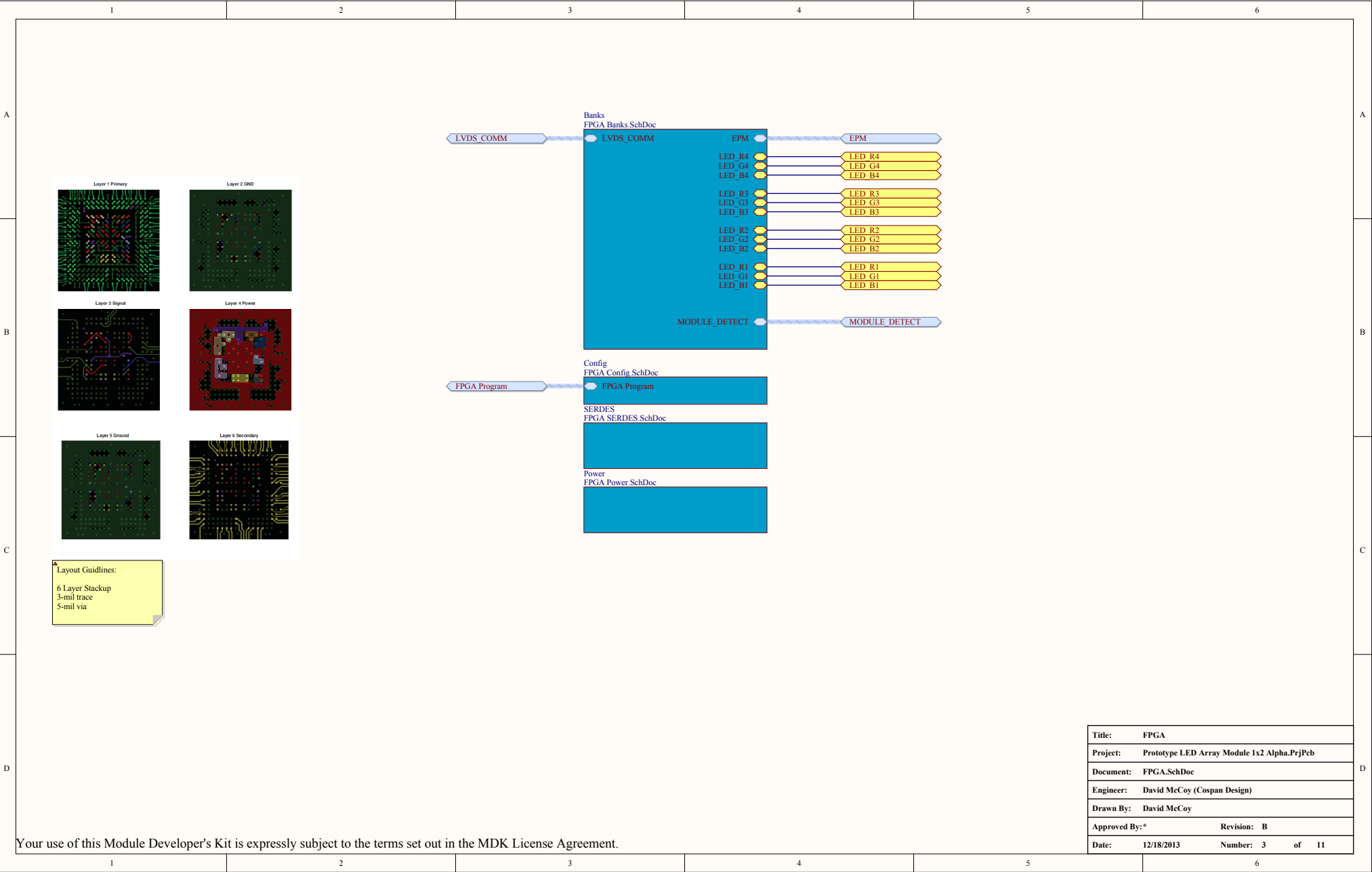


Alpha

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Layout Guidelines:

6 Layer Stackup

3-mil trace

5-mil via

Banks

FPGA Banks.SchDoc

LVDS_COMM

LVDS_COMM

Config

FPGA Config.SchDoc

FPGA Program

FPGA Program

SERDES

FPGA SERDES.SchDoc

Power

FPGA Power.SchDoc

EPM

LED_R4

LED_G4

LED_B4

LED_R3

LED_G3

LED_B3

LED_R2

LED_G2

LED_B2

LED_R1

LED_G1

LED_B1

MODULE_DETECT

EPM

LED_R4

LED_G4

LED_B4

LED_R3

LED_G3

LED_B3

LED_R2

LED_G2

LED_B2

LED_R1

LED_G1

LED_B1

MODULE_DETECT

Title: FPGA

Project: Prototype LED Array Module 1x2 Alpha.PrjPcb

Document: FPGA.SchDoc

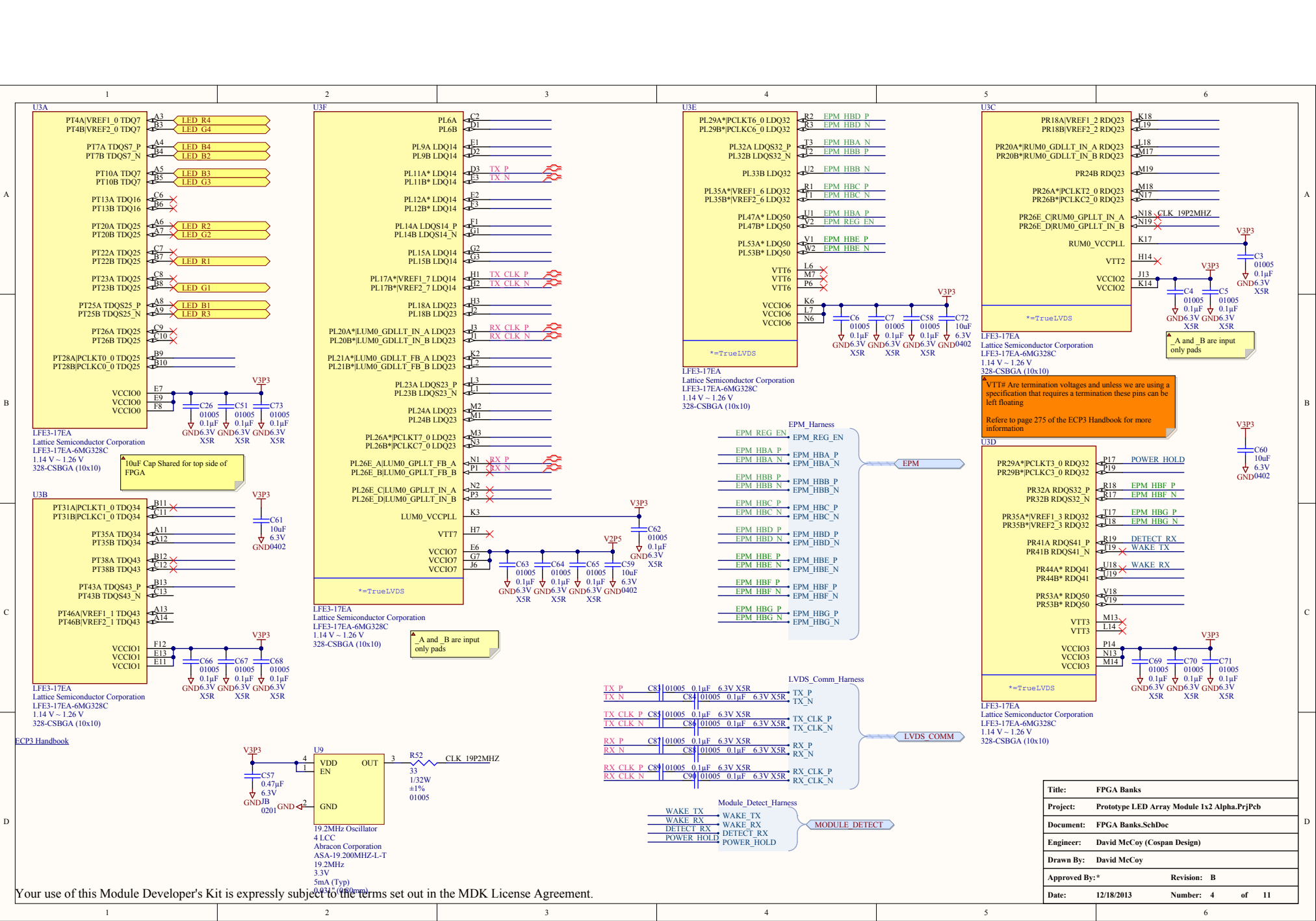
Engineer: David McCoy (Cospan Design)

Drawn By: David McCoy

Approved By: * Revision: B

Date: 12/18/2013 Number: 3 of 11

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A

B

C

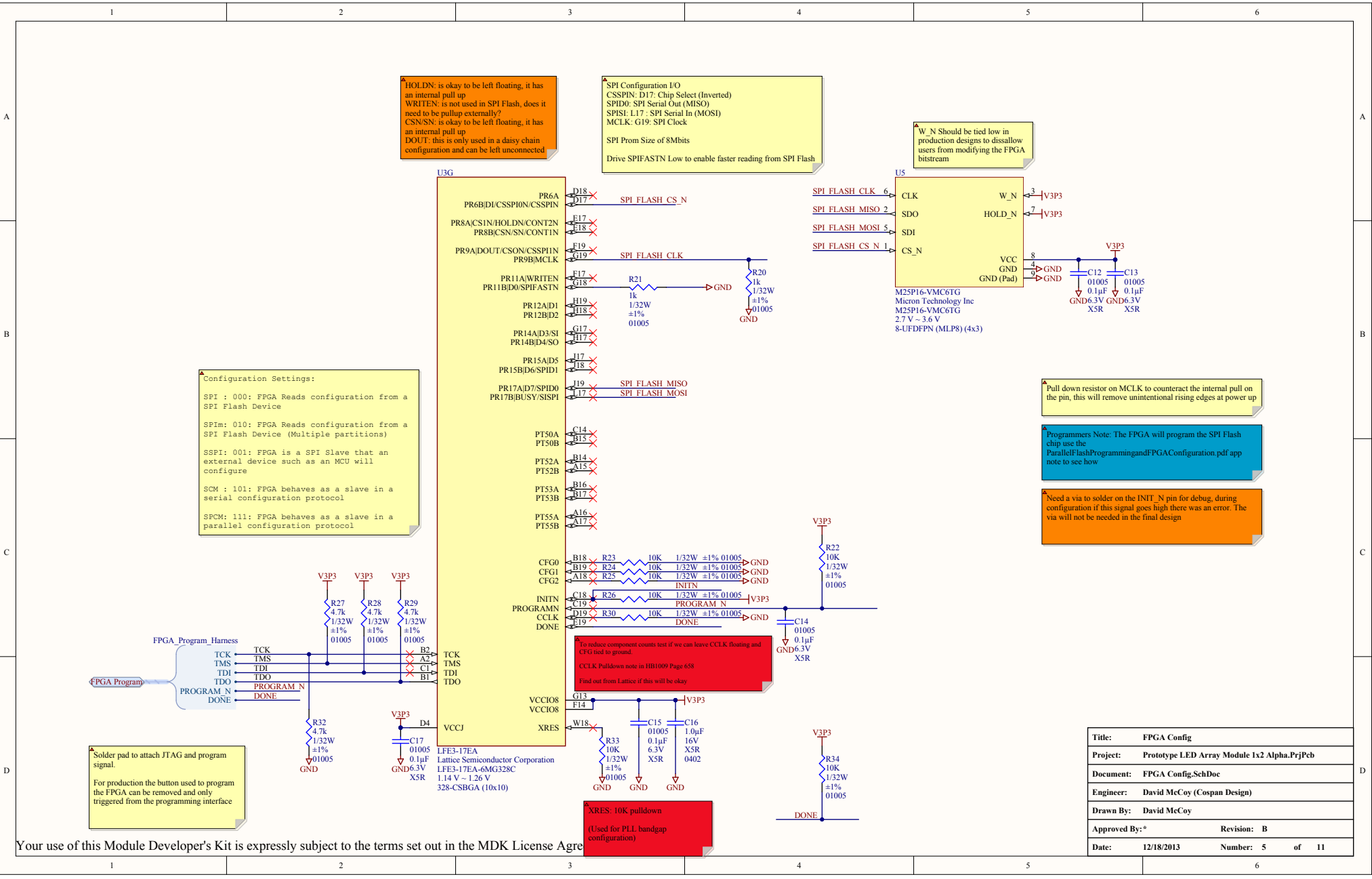
D

A

B

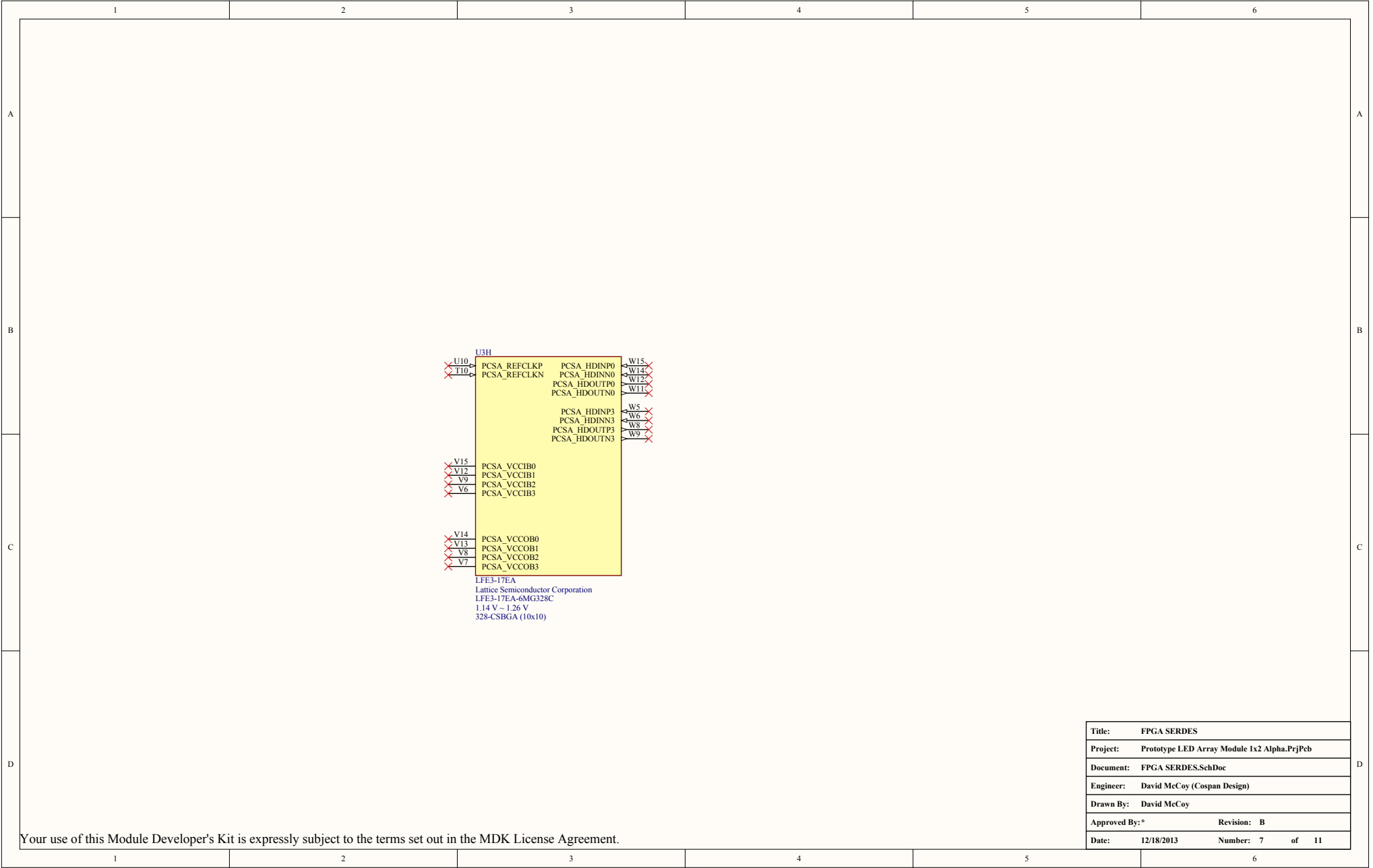
C

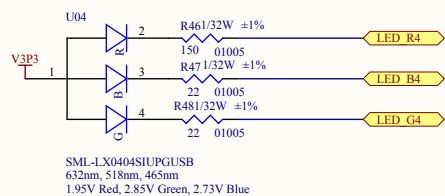
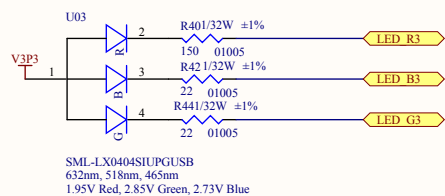
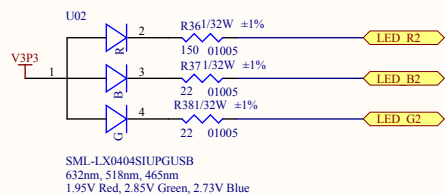
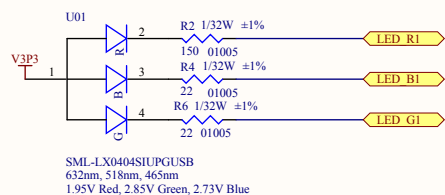
D



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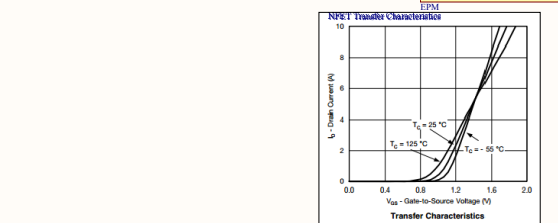
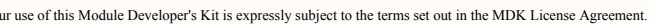
Title:	FPGA Config
Project:	Prototype LED Array Module 1x2 Alpha.PrjPcb
Document:	FPGA Config.SchDoc
Engineer:	David McCoy (Cospan Design)
Drawn By:	David McCoy
Approved By:*	Revision: B
Date:	12/18/2013
Number:	5 of 11





Title:	Tri-Colored LEDs		
Project:	Prototype LED Array Module 1x2 Alpha.PrjPcb		
Document:	LED Matrix.SchDoc		
Engineer:	Nan-Wei Gong		
Drawn By:	Nan-Wei Gong		
Approved By: *		Revision:	B
Date:	12/17/2013	Number:	8 of 11

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Title:	EPM		
Project:	Prototype LED Array Module 1x2 Alpha.PrjPcb		
Document:	EPM.SchDoc		
Engineer:	David McCoy (Cospan Design)		
Drawn By:	David McCoy		
Approved By: *	Revision:		B
Date:	12/18/2013	Number:	9 of 11

