enter deadline: 275

enter population size: 20

enter no of iteration: 150

CPU1 AND ASIC1

PATTERN = 0 1 1 0 1 1 0 1

COST = 120 TIME = 273

node 1 start = 0 end = 60

node 2 start = 60 end = 90

node 3 start = 90 end = 117

node 4 start = 90 end = 150

node 5 start = 117 end = 147

node 6 start = 150 end = 177

node 7 start = 150 end = 240

node 8 start = 240 end = 273

########

CPU1 AND ASIC2

PATTERN = 0 0 1 1 1 1 0 1

COST = 125 TIME = 275

node 1 start = 0 end = 60

node 2 start = 60 end = 150

node 3 start = 150 end = 165

node 4 start = 165 end = 175

node 5 start = 175 end = 190

node 6 start = 190 end = 210

node 7 start = 165 end = 255

node 8 start = 255 end = 275

#######

CPU2 AND ASIC1

PATTERN = 0 0 0 1 1 0 0 0

COST = 80 TIME = 270

node 1 start = 0 end = 30

node 2 start = 30 end = 80

node 3 start = 80 end = 134

node 4 start = 80 end = 100

node 5 start = 100 end = 130

node 6 start = 134 end = 164

node 7 start = 164 end = 214

node 8 start = 214 end = 270

#######

CPU2 AND ASIC2

PATTERN = 0 0 0 1 1 0 0 0

COST = 85 TIME = 270

node 1 start = 0 end = 30

node 2 start = 30 end = 80

node 3 start = 80 end = 134

node 4 start = 80 end = 90

node 5 start = 90 end = 105

node 6 start = 134 end = 164

node 7 start = 164 end = 214

node 8 start = 214 end = 270

Deadline : 275

|  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
|  | **CPU1 & ASIC1** | | | | **CPU1 & ASIC2** | | | | **CPU2 & ASIC1** | | | | **CPU2 & ASIC2** | | | |
|  | Opt. Cost | | Time | | Opt. Cost | | Time | | Opt. Cost | | Time | | Opt. Cost | | Time | |
|  | 120 | | 273 | | 125 | | 275 | | 80 | | 270 | | 85 | | 270 | |
| **Node** | **Map** | **Start** | | **End** | **Map** | **Start** | | **End** | **Map** | **Start** | | **End** | **Map** | **Start** | | **End** |
| 1 | CPU1 | 0 | | 60 | CPU1 | 0 | | 60 | CPU2 | 0 | | 30 | CPU2 | 0 | | 30 |
| 2 | ASIC1 | 60 | | 90 | CPU1 | 60 | | 150 | CPU2 | 30 | | 80 | CPU2 | 30 | | 80 |
| 3 | ASIC1 | 90 | | 117 | ASIC2 | 150 | | 165 | CPU2 | 80 | | 134 | CPU2 | 80 | | 134 |
| 4 | CPU1 | 90 | | 150 | ASIC2 | 165 | | 175 | ASIC1 | 80 | | 100 | ASIC2 | 80 | | 90 |
| 5 | ASIC1 | 117 | | 147 | ASIC2 | 175 | | 190 | ASIC1 | 100 | | 130 | ASIC2 | 90 | | 105 |
| 6 | ASIC1 | 150 | | 177 | ASIC2 | 190 | | 210 | CPU2 | 134 | | 164 | CPU2 | 134 | | 164 |
| 7 | CPU1 | 150 | | 240 | CPU1 | 165 | | 255 | CPU2 | 164 | | 214 | CPU2 | 164 | | 214 |
| 8 | ASIC1 | 240 | | 273 | ASIC2 | 255 | | 275 | CPU2 | 214 | | 270 | CPU2 | 214 | | 270 |