

Modeling code behaviour

Relies and uses
Slides from B.
Srivathsan

Chennai Mathematical Institute

Outline

- ▶ **Module 1:** Modeling simple code
- ▶ **Module 2:** Modeling hardware circuits
- ▶ **Module 3:** Modeling data dependent programs
- ▶ **Module 4:** Modeling concurrent systems

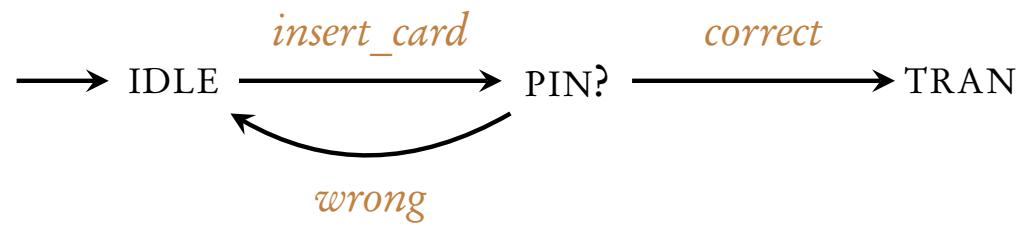
Module 1: Modeling code behaviour

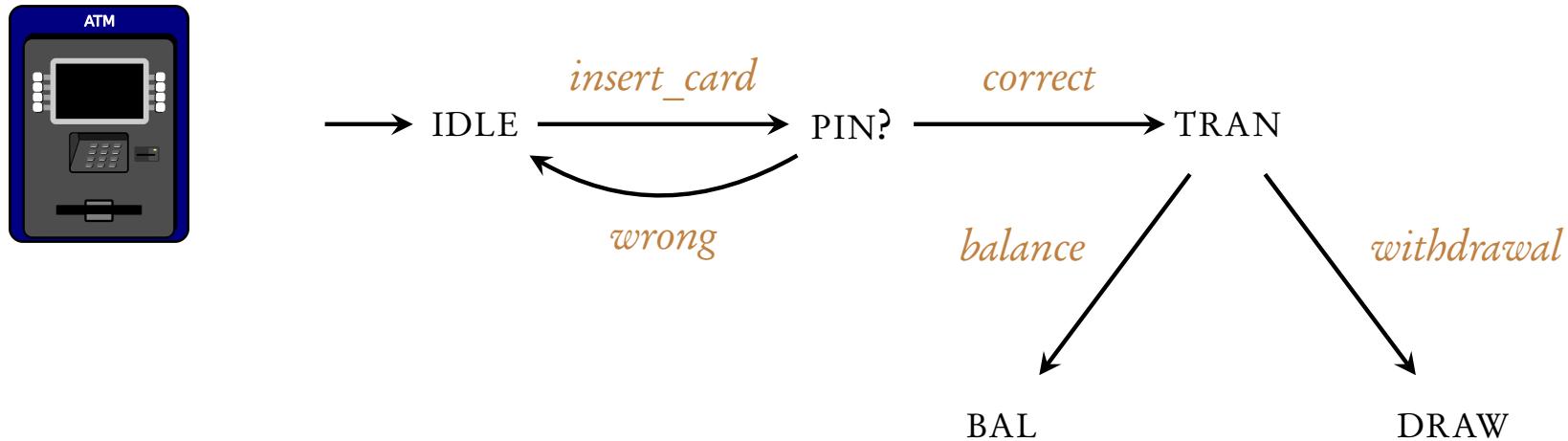


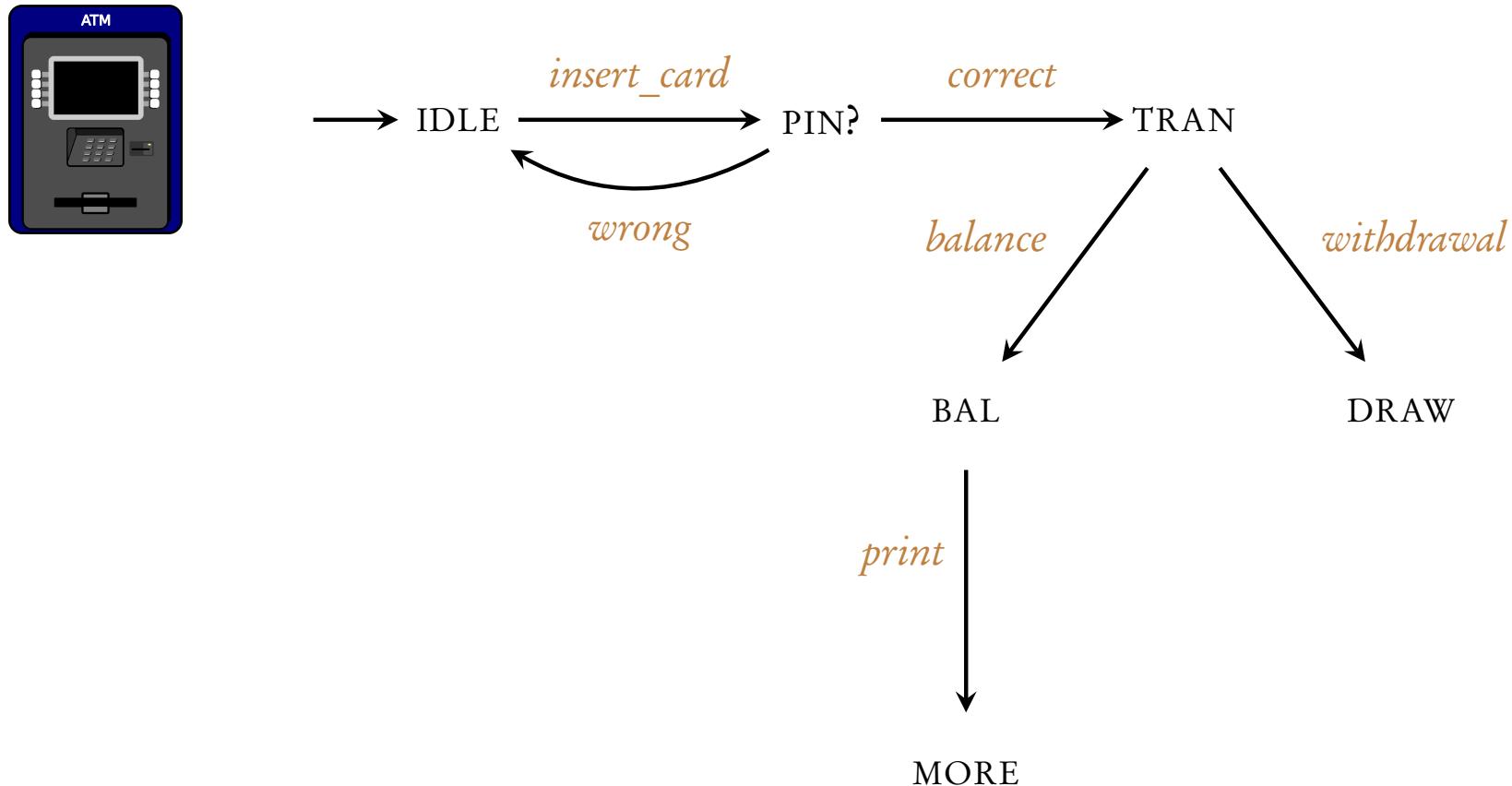


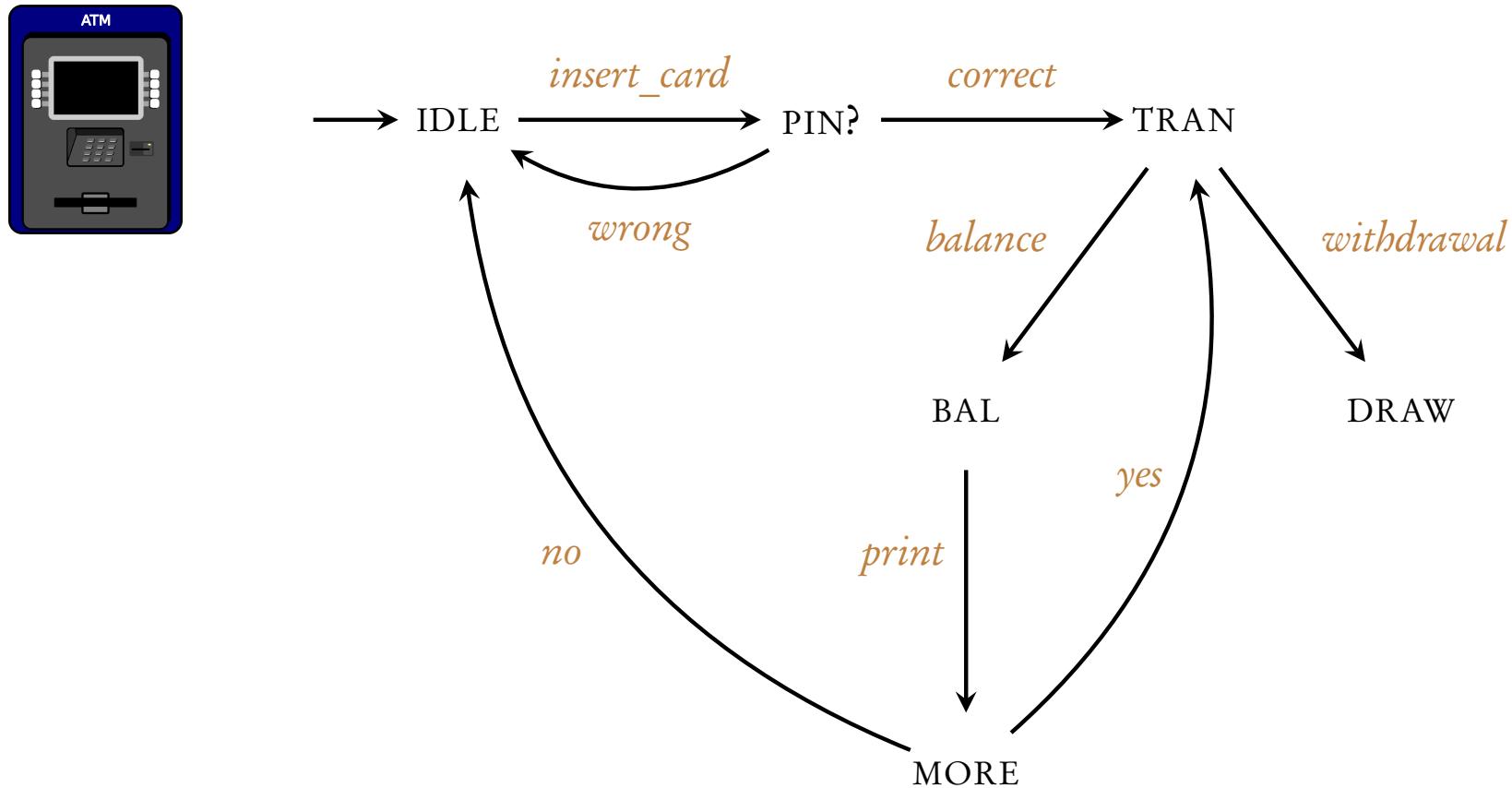
→ IDLE

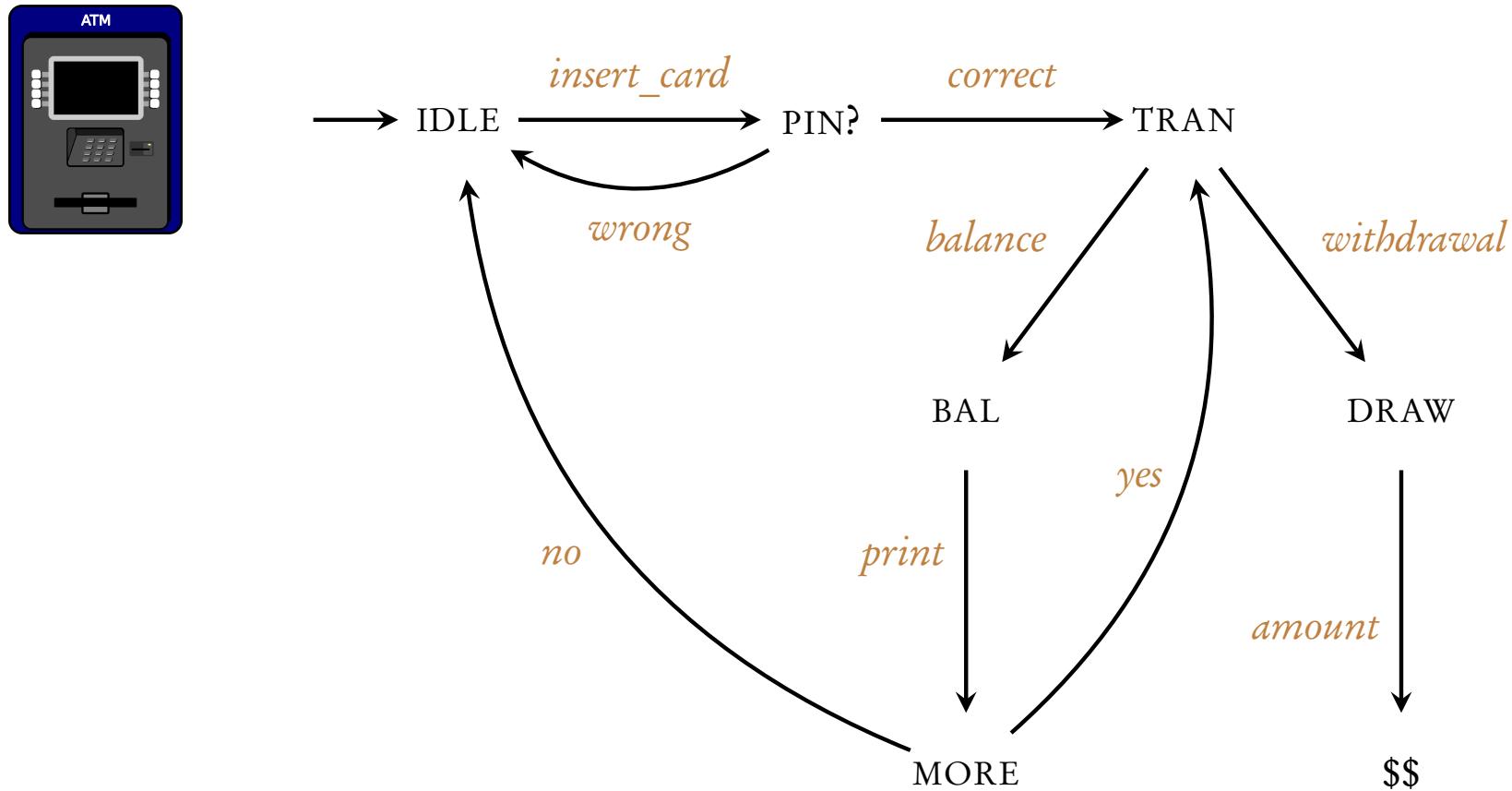


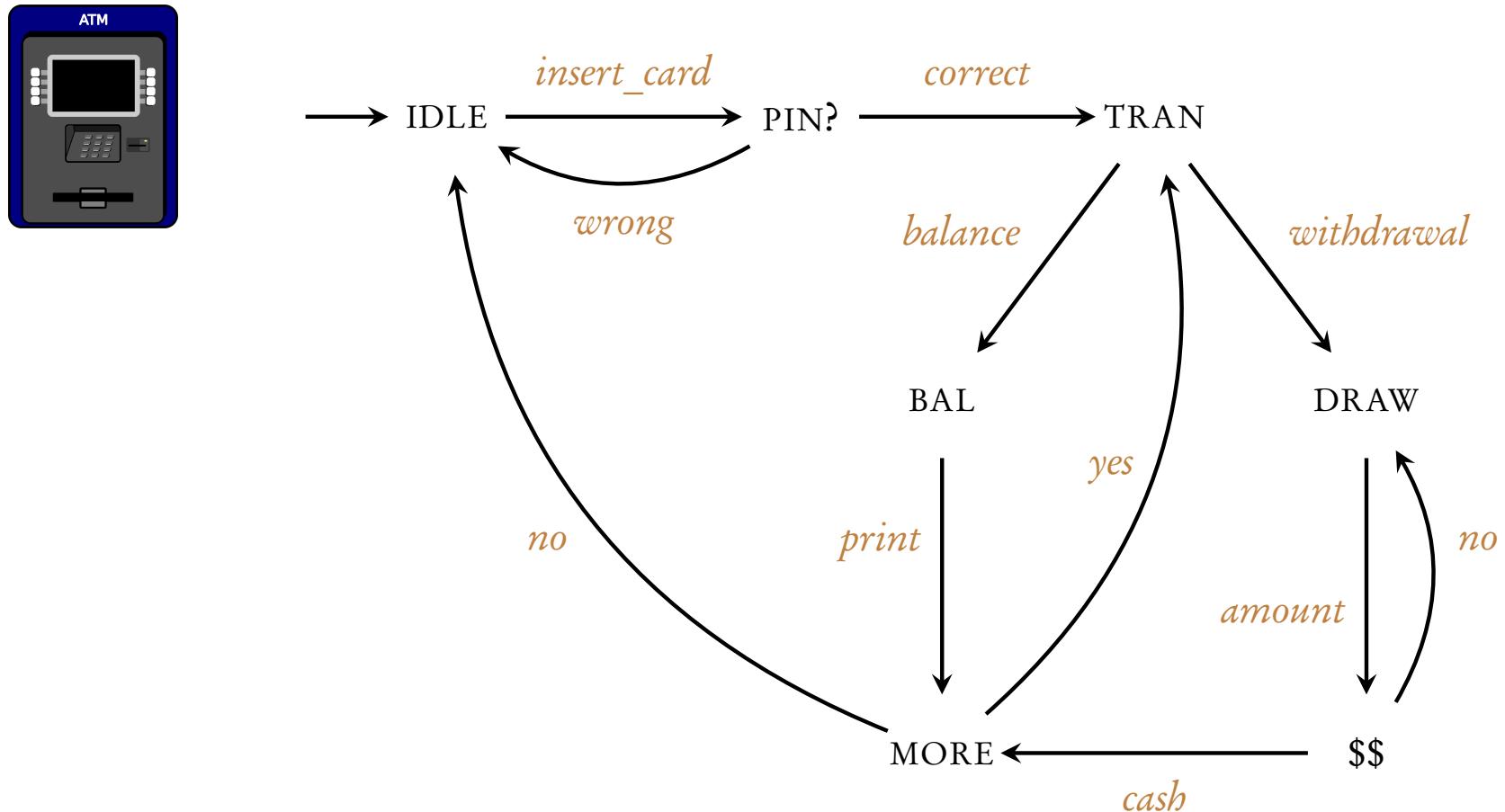


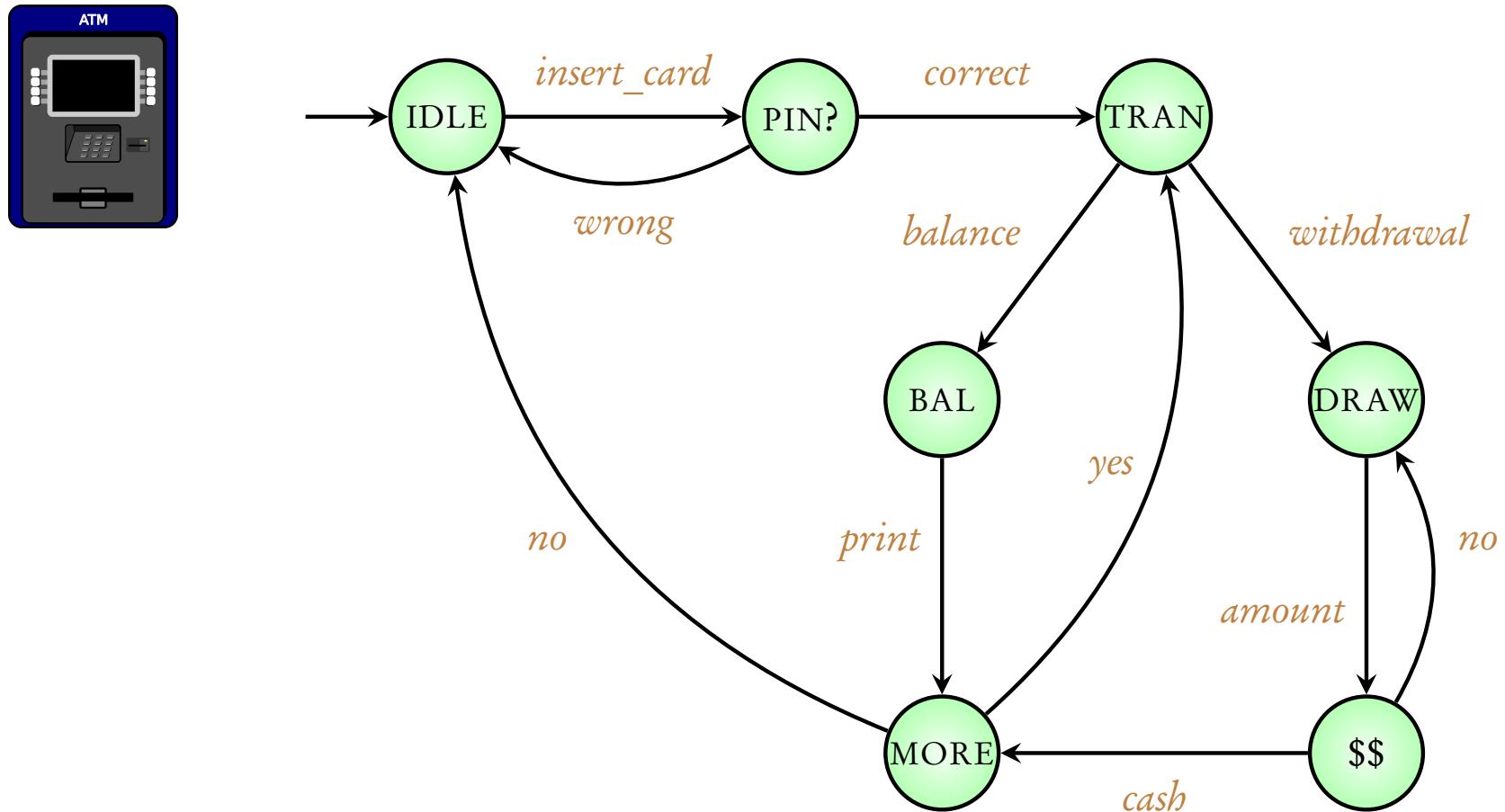


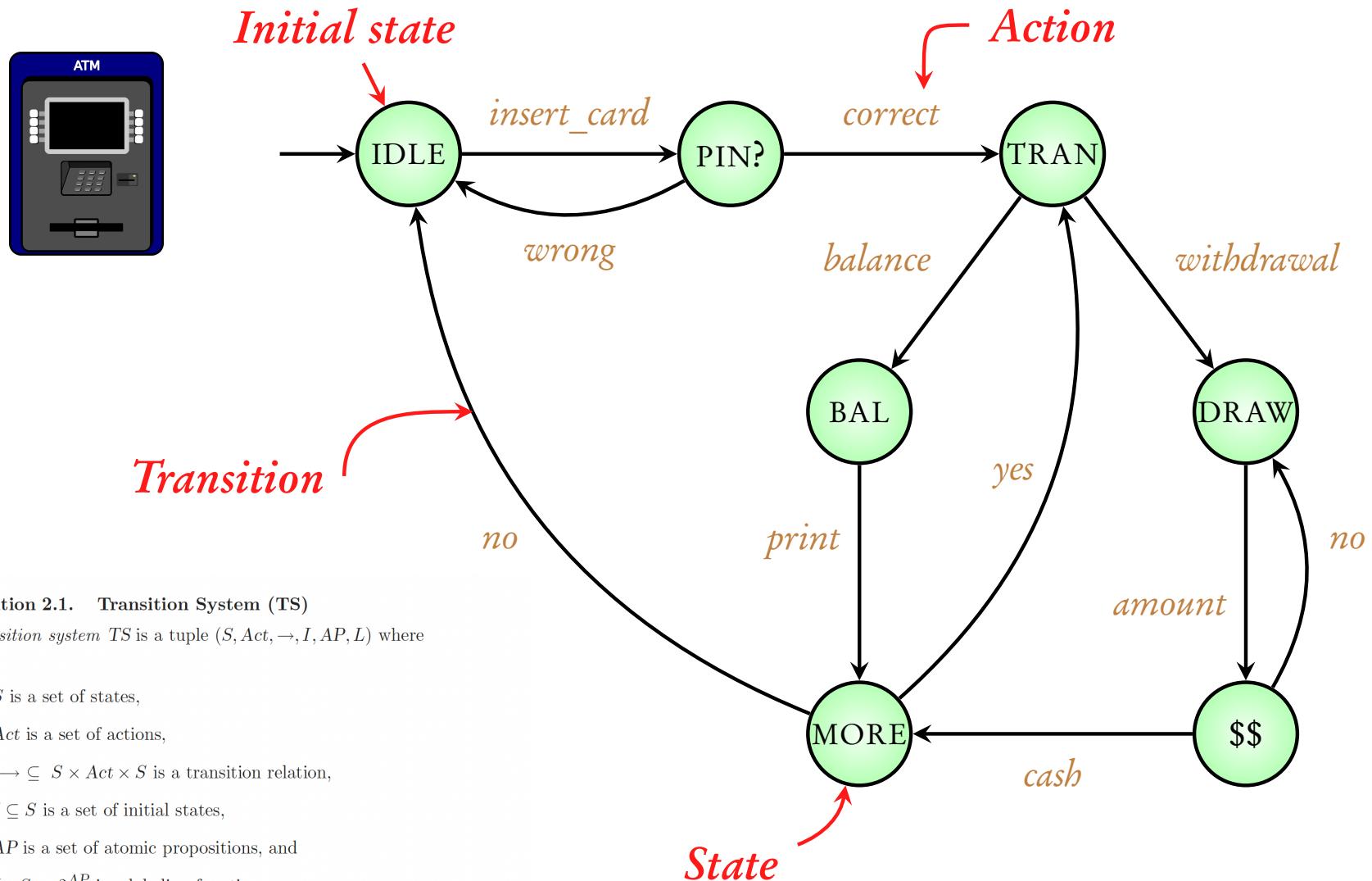












Definition 2.1. Transition System (TS)

A *transition system* TS is a tuple $(S, \text{Act}, \rightarrow, I, \text{AP}, L)$ where

- S is a set of states,
- Act is a set of actions,
- $\rightarrow \subseteq S \times \text{Act} \times S$ is a transition relation,
- $I \subseteq S$ is a set of initial states,
- AP is a set of atomic propositions, and
- $L : S \rightarrow 2^{\text{AP}}$ is a labeling function.

TS is called *finite* if S , Act , and AP are finite.

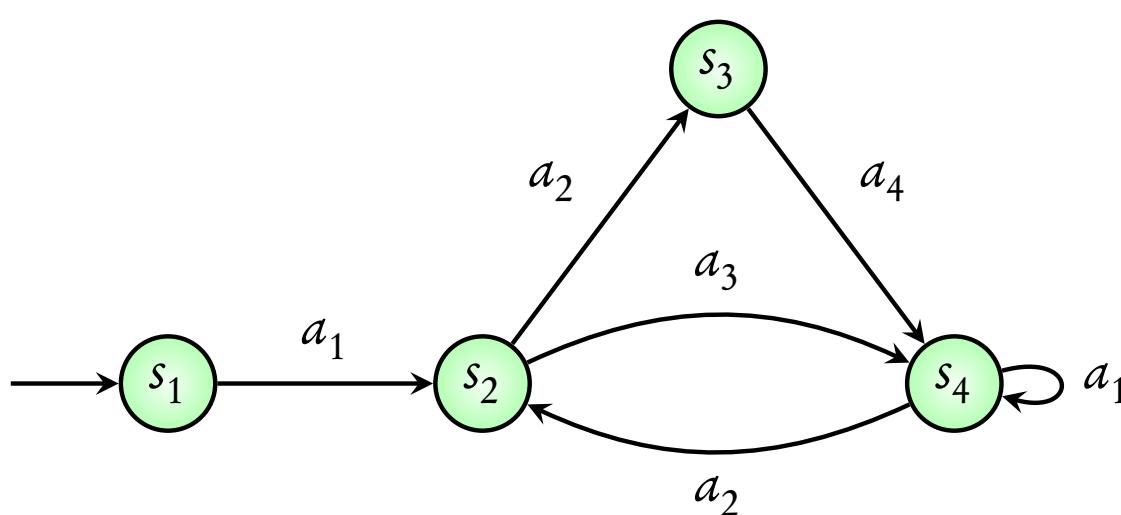
The labeling function L relates a set $L(s) \in 2^{\text{AP}}$ of atomic propositions to any state s .

$L(s)$ intuitively stands for exactly those atomic propositions $a \in \text{AP}$ which are satisfied by state s .

Given that Φ is a propositional logic formula,
then s satisfies the formula Φ if
the evaluation induced by $L(s)$ makes the formula Φ true; that is:

$$s \models \Phi \text{ iff } L(s) \models \Phi.$$

Transition system



States

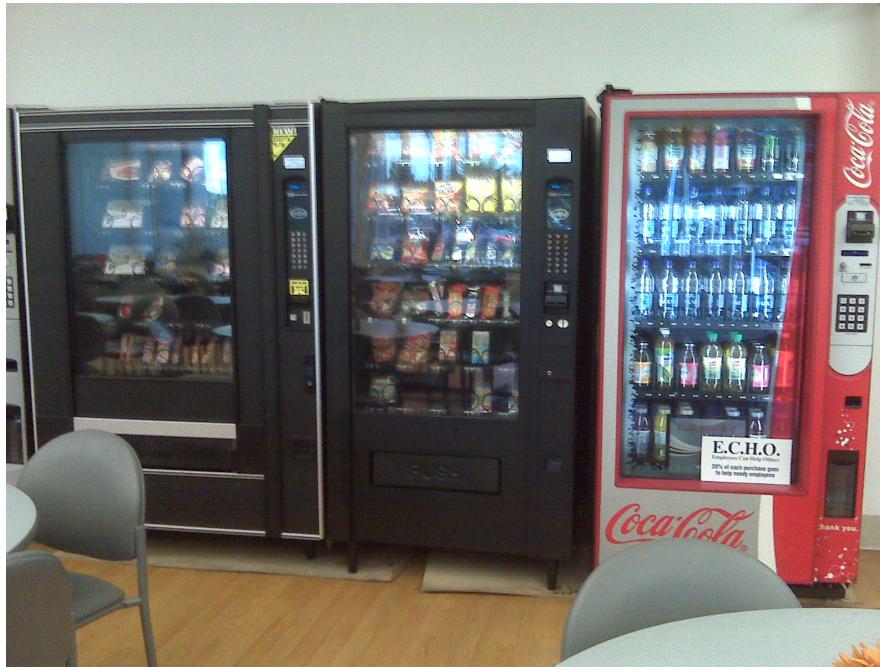
Actions

Transitions

Initial state

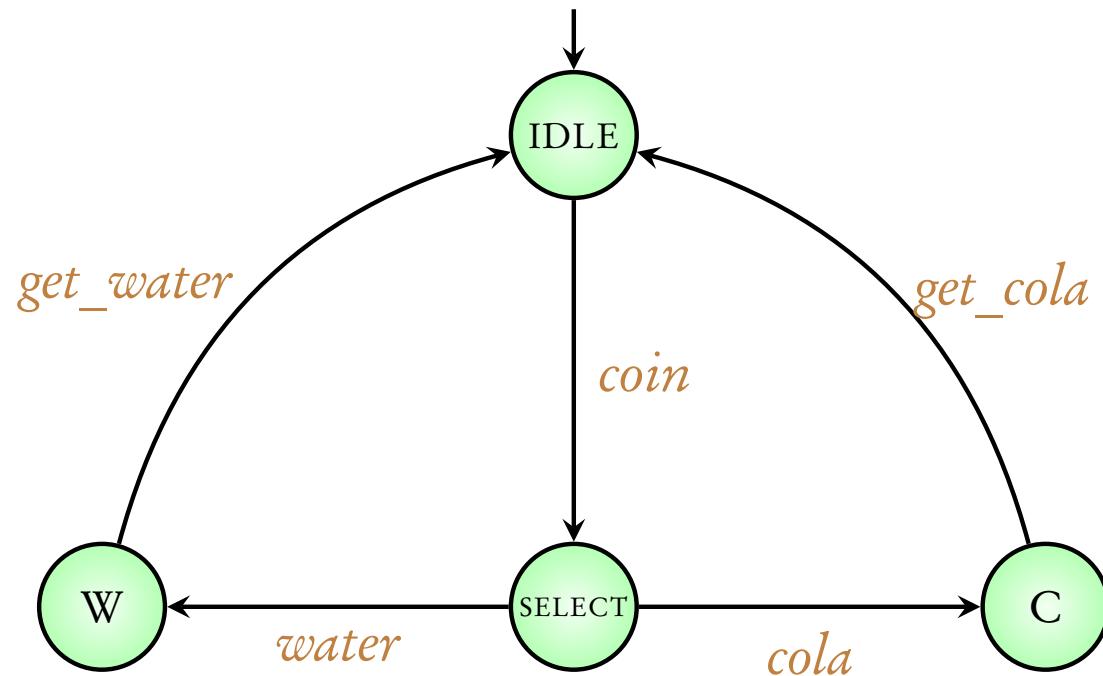
Other names: Finite-state machines, State-transition graphs

Modeling a vending machine

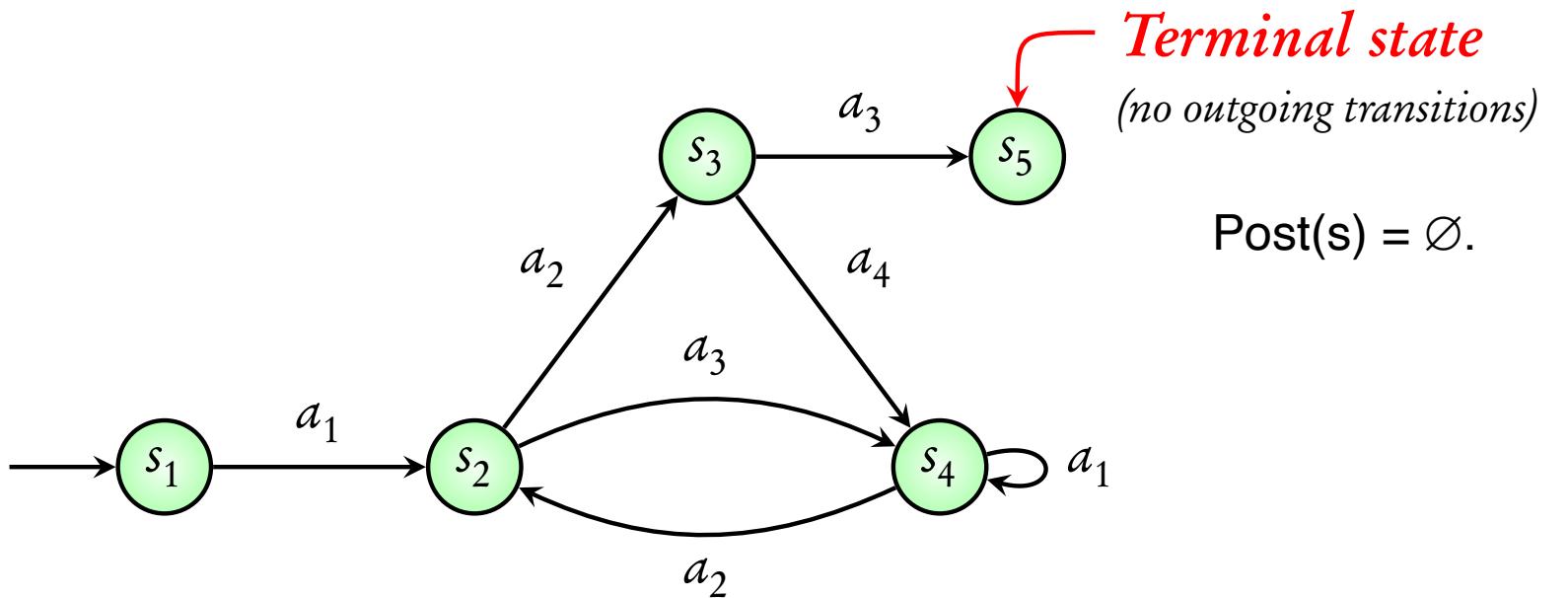


"Vending machines at hospital" by PCHS-NJROTC - Own work
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$L(\text{idle}) = \emptyset$, $L(W) = L(C) = \{ \text{paid} , \text{drink} \}$, $L(\text{select}) = \{ \text{paid} \}$.



Coming next: some **terminology**



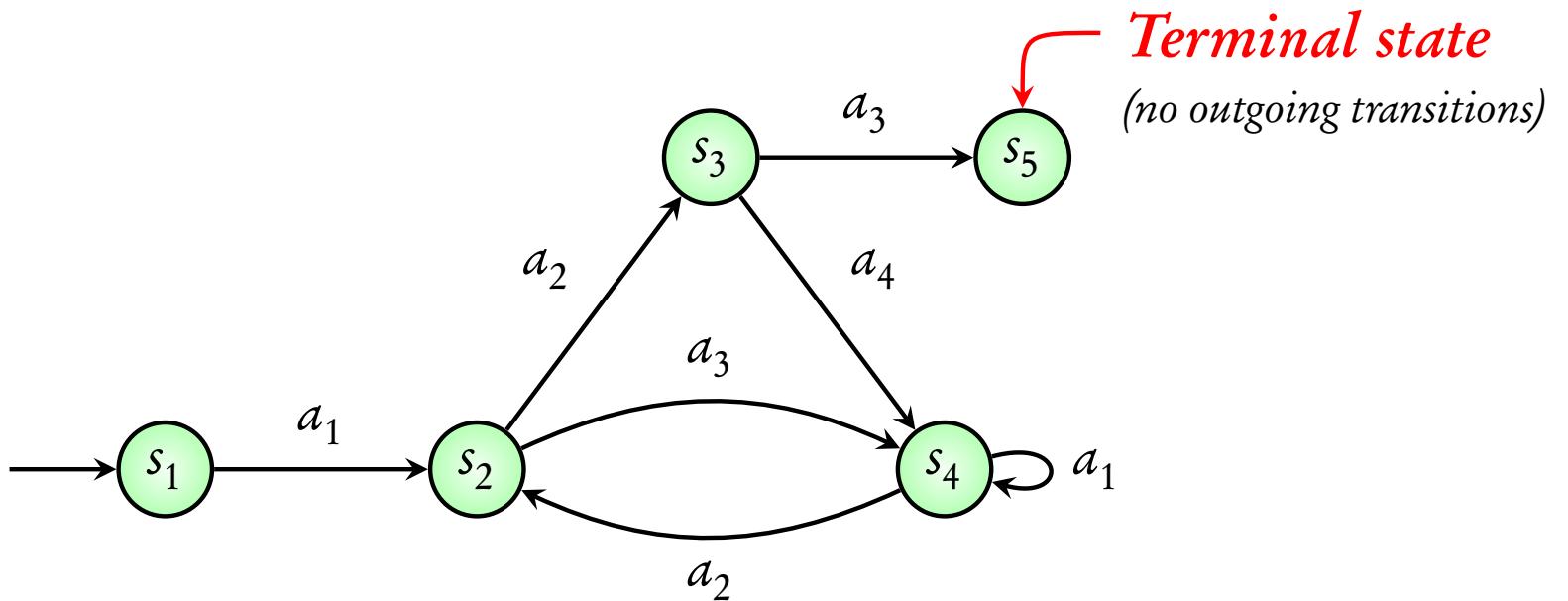
Definition 2.3. Direct Predecessors and Successors

Let \$TS = (S, Act, \rightarrow, I, AP, L)\$ be a transition system. For \$s \in S\$ and \$\alpha \in Act\$, the set of direct \$\alpha\$-successors of \$s\$ is defined as:

$$Post(s, \alpha) = \{ s' \in S \mid s \xrightarrow{\alpha} s' \}, \quad Post(s) = \bigcup_{\alpha \in Act} Post(s, \alpha).$$

The set of \$\alpha\$-predecessors of \$s\$ is defined by:

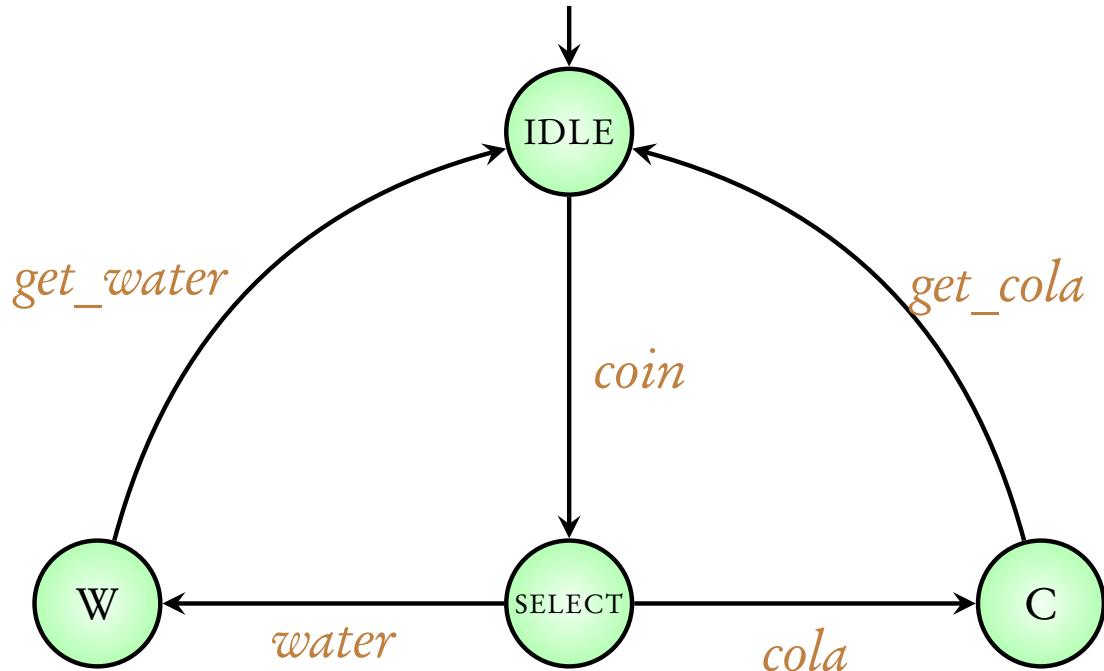
$$Pre(s, \alpha) = \{ s' \in S \mid s' \xrightarrow{\alpha} s \}, \quad Pre(s) = \bigcup_{\alpha \in Act} Pre(s, \alpha).$$



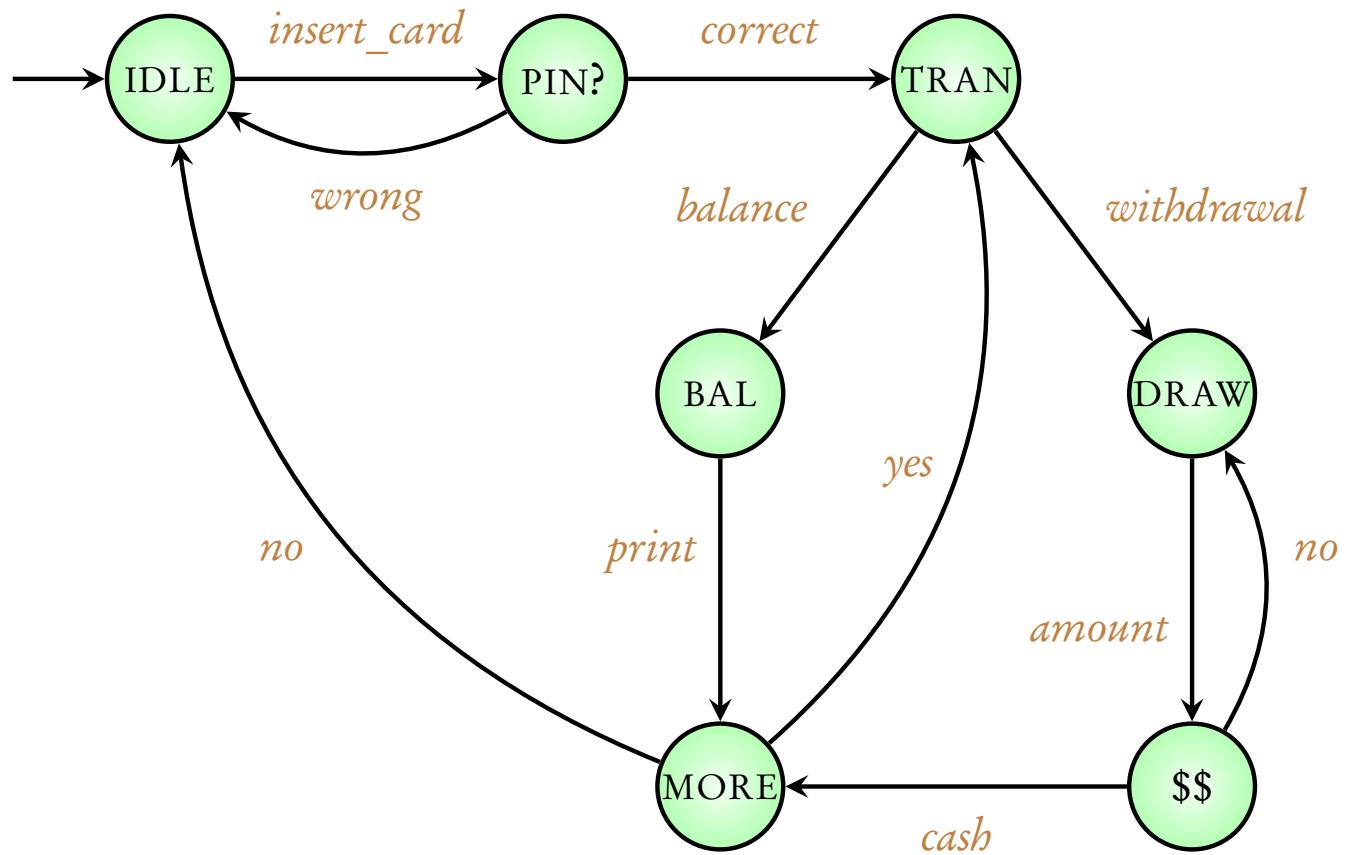
Execution: $s_1 \xrightarrow{a_1} s_2 \xrightarrow{a_3} s_4 \xrightarrow{a_2} s_2 \xrightarrow{a_3} s_4 \xrightarrow{a_2} s_2 \xrightarrow{a_3} s_4 \dots$

$s_1 \xrightarrow{a_1} s_2 \xrightarrow{a_2} s_3 \xrightarrow{a_4} s_4 \xrightarrow{a_1} s_4 \xrightarrow{a_1} s_4 \xrightarrow{a_1} s_4 \dots$

$s_1 \xrightarrow{a_1} s_2 \xrightarrow{a_2} s_3 \xrightarrow{a_4} s_4 \xrightarrow{a_2} s_2 \xrightarrow{a_2} s_3 \xrightarrow{a_3} s_5$



Execution: $\text{IDLE} \xrightarrow{\text{coin}} \text{SELECT} \xrightarrow{\text{water}} \text{W} \xrightarrow{\text{get_water}} \text{IDLE} \xrightarrow{\text{coin}} \text{SELECT} \xrightarrow{\text{cola}} \dots$



Execution: IDLE $\xrightarrow{\text{insert_card}}$ PIN $\xrightarrow{\text{wrong}}$ IDLE $\xrightarrow{\text{insert_card}}$ PIN ...

Executions formally

Definition 2.6. Execution Fragment

Let $TS = (S, Act, \rightarrow, I, AP, L)$ be a transition system. A *finite* execution fragment ϱ of TS is an alternating sequence of states and actions ending with a state

$$\varrho = s_0 \alpha_1 s_1 \alpha_2 \dots \alpha_n s_n \text{ such that } s_i \xrightarrow{\alpha_{i+1}} s_{i+1} \text{ for all } 0 \leq i < n,$$

where $n \geq 0$. We refer to n as the length of the execution fragment ϱ . An *infinite* execution fragment ρ of TS is an infinite, alternating sequence of states and actions:

$$\rho = s_0 \alpha_1 s_1 \alpha_2 s_2 \alpha_3 \dots \text{ such that } s_i \xrightarrow{\alpha_{i+1}} s_{i+1} \text{ for all } 0 \leq i.$$

Definition 2.7. Maximal and Initial Execution Fragment

A *maximal* execution fragment is either a finite execution fragment that ends in a terminal state, or an infinite execution fragment. An execution fragment is called *initial* if it starts in an initial state, i.e., if $s_0 \in I$. ■

Definition 2.9. Execution

An *execution* of transition system TS is an initial, maximal execution fragment.

Module 2: Modeling hardware circuits



x_1	x_2	y
0	0	0
0	1	1
1	0	1
1	1	1



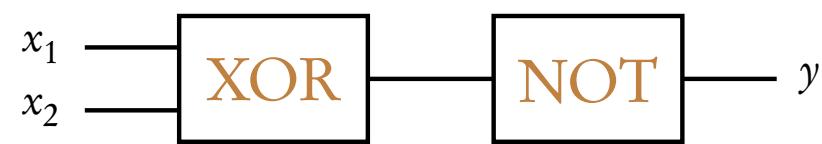
x_1	x_2	y
0	0	0
0	1	0
1	0	0
1	1	1



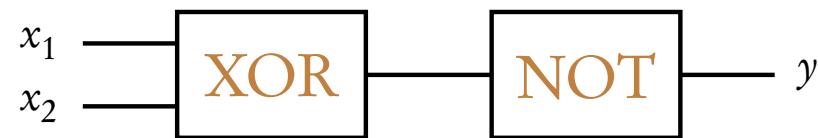
x	y
0	1
1	0



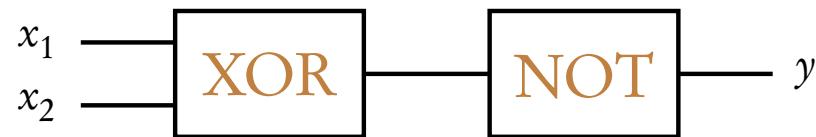
x_1	x_2	y
0	0	0
0	1	1
1	0	1
1	1	0



$$y = \text{NOT}(\text{XOR}(x_1, x_2))$$

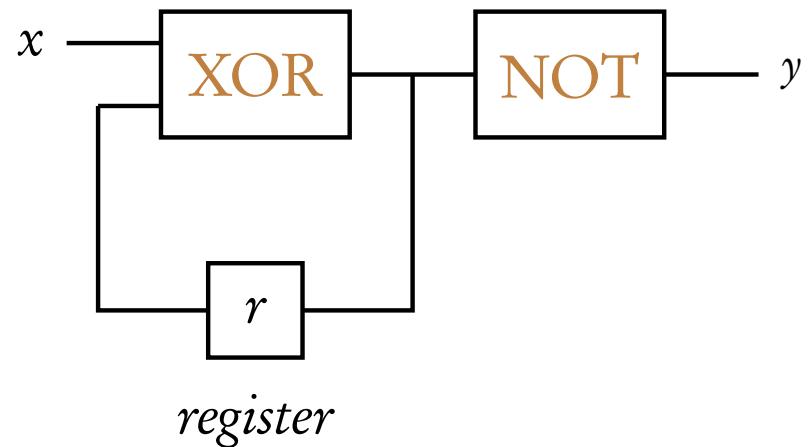


$$y = \text{NOT}(\text{XOR}(x_1, x_2))$$



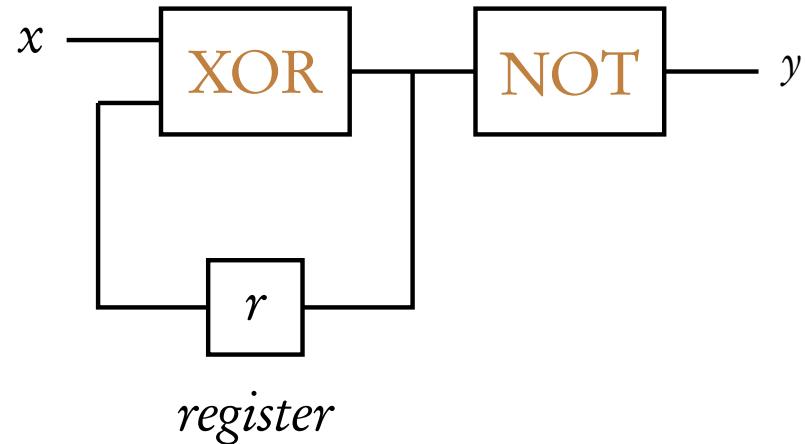
x_1	x_2	y
0	0	1
0	1	0
1	0	0
1	1	1

$$y = \text{NOT}(\text{XOR}(x, r))$$



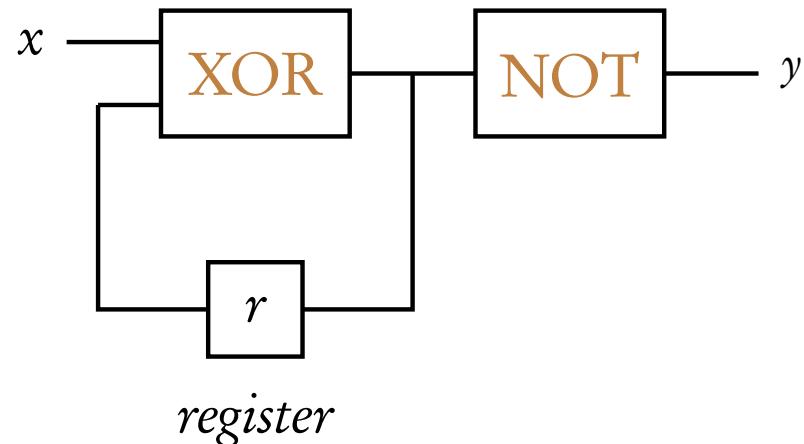
$$y = \text{NOT}(\text{XOR}(x, r))$$

$$r_{next} = \text{XOR}(x, r)$$



$$y = \text{NOT}(\text{XOR}(x, r))$$

$$r_{next} = \text{XOR}(x, r)$$



$$x \quad 1$$

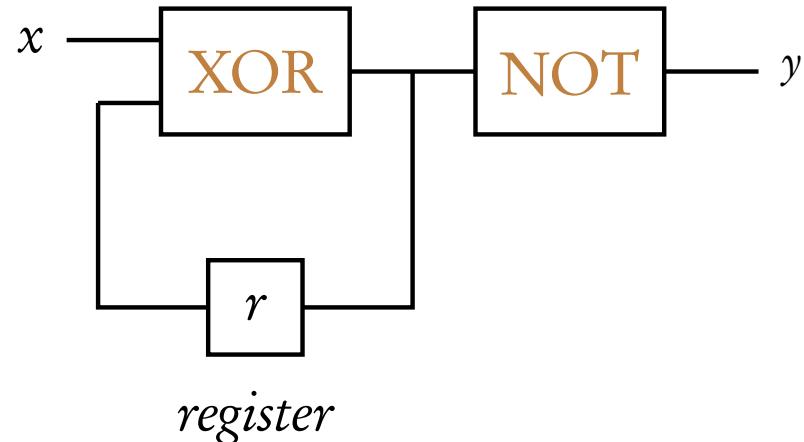
$$r \quad 0$$

$$y \quad 0$$



$$y = \text{NOT}(\text{XOR}(x, r))$$

$$r_{next} = \text{XOR}(x, r)$$



$$x \quad 1$$

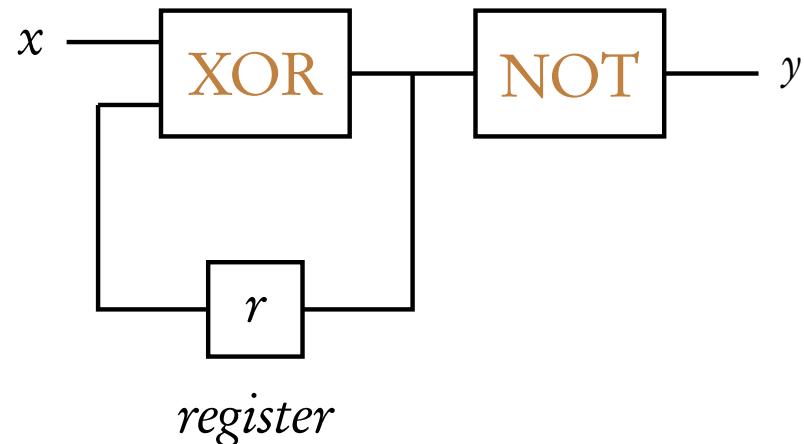
$$r \quad 0 \qquad 1$$

$$y \quad 0$$



$$y = \text{NOT}(\text{XOR}(x, r))$$

$$r_{next} = \text{XOR}(x, r)$$

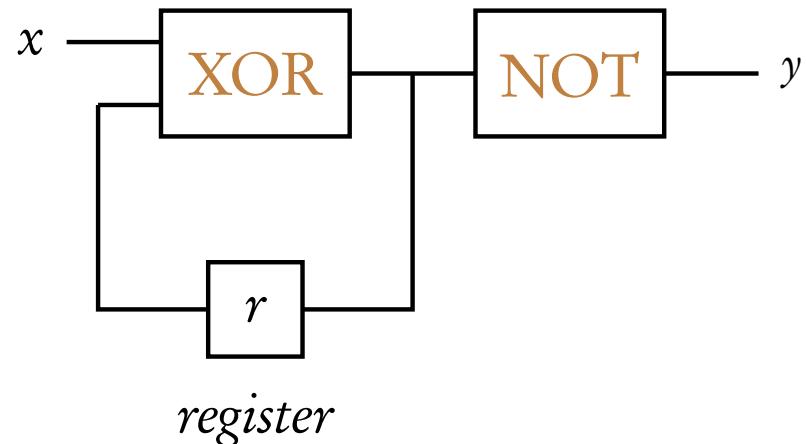


x	1	1
r	0	1
y	0	1



$$y = \text{NOT}(\text{XOR}(x, r))$$

$$r_{next} = \text{XOR}(x, r)$$

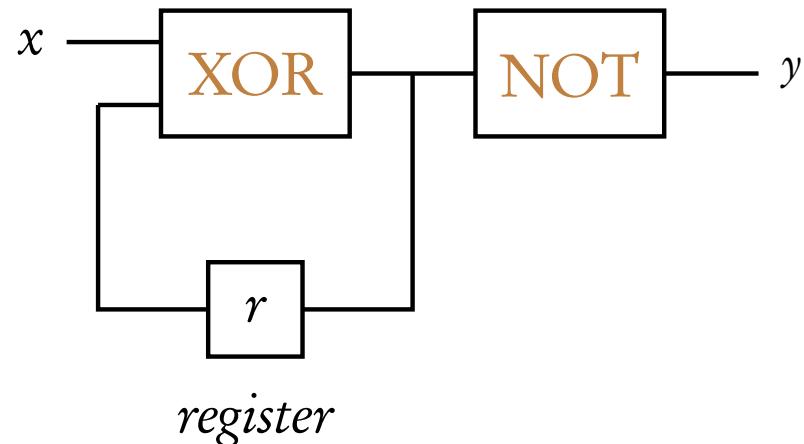


x	1	1
r	0	1
y	0	1



$$y = \text{NOT}(\text{XOR}(x, r))$$

$$r_{next} = \text{XOR}(x, r)$$

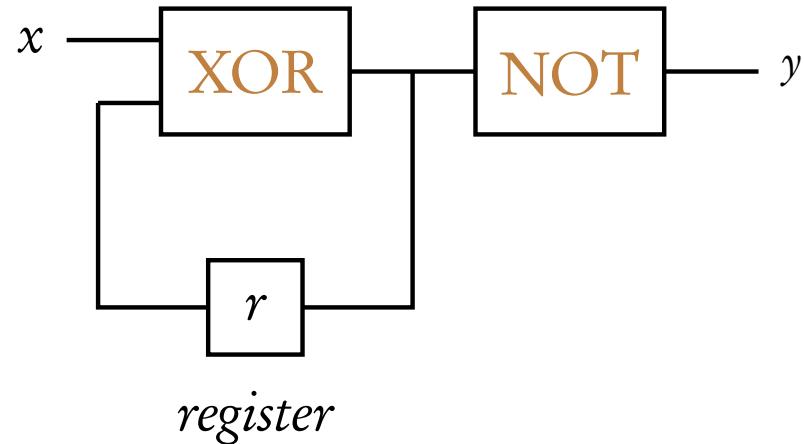


x	1	1	0
r	0	1	0
y	0	1	1



$$y = \text{NOT}(\text{XOR}(x, r))$$

$$r_{next} = \text{XOR}(x, r)$$

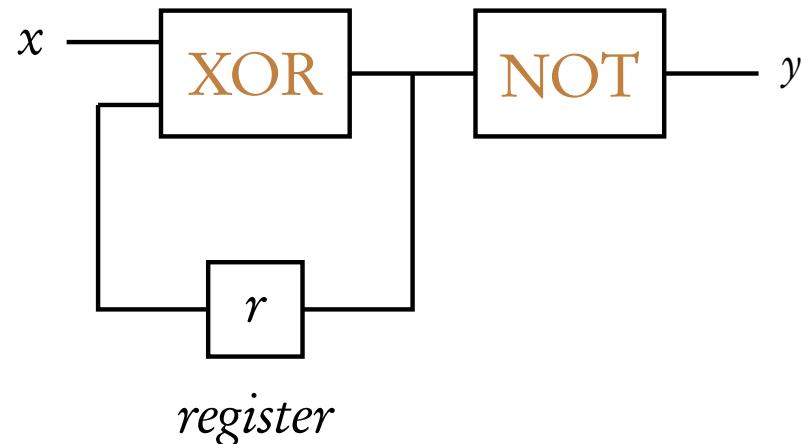


x	1	1	0
r	0	1	0
γ	0	1	1



$$y = \text{NOT}(\text{XOR}(x, r))$$

$$r_{next} = \text{XOR}(x, r)$$

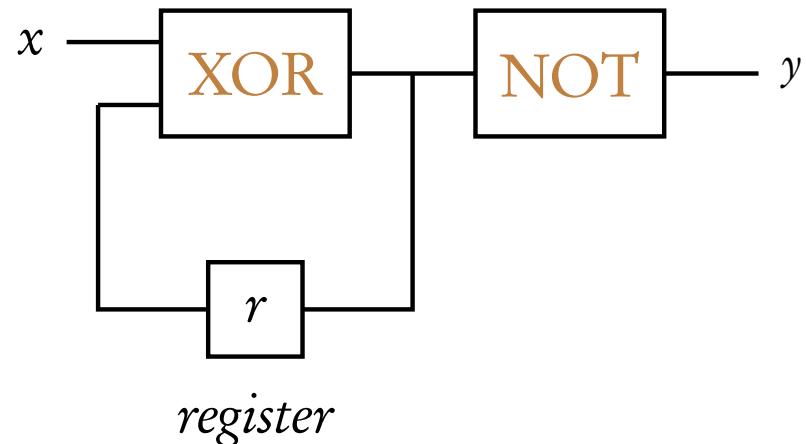


x	1	1	0	1
r	0	1	0	0
y	0	1	1	0



$$y = \text{NOT}(\text{XOR}(x, r))$$

$$r_{next} = \text{XOR}(x, r)$$

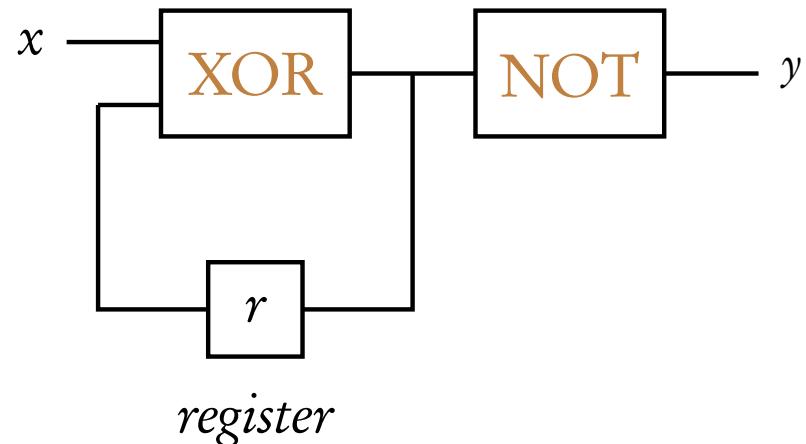


x	1	1	0	1
r	0	1	0	0
y	0	1	1	0



$$y = \text{NOT}(\text{XOR}(x, r))$$

$$r_{next} = \text{XOR}(x, r)$$

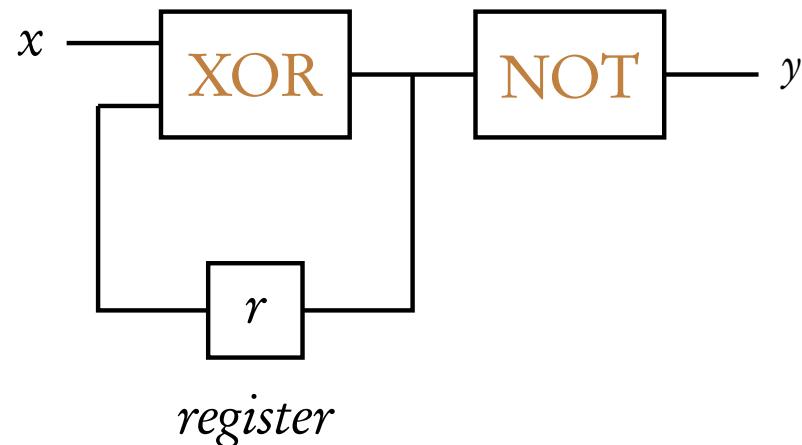


x	1	1	0	1	1
r	0	1	0	0	1
y	0	1	1	0	1



$$y = \text{NOT}(\text{XOR}(x, r))$$

$$r_{next} = \text{XOR}(x, r)$$

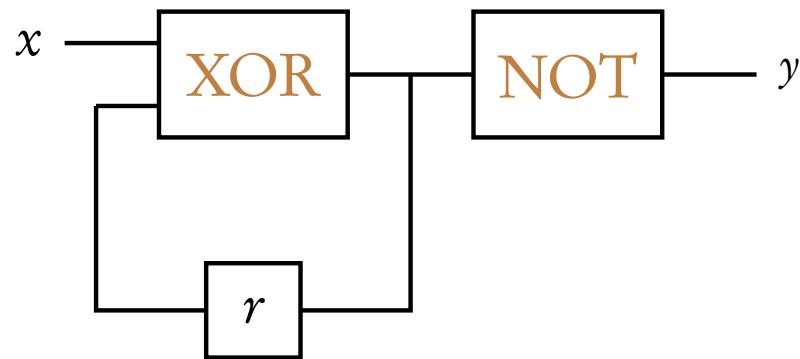


x	1	1	0	1	1	1	0	0	1	0	\dots
r	0	1	0	0	1	0	1	1	1	0	
y	0	1	1	0	1	0	0	0	1	1	

Below the table, there is a horizontal line with vertical tick marks at each column position. Red checkmarks are placed under the first, third, fifth, seventh, ninth, and eleventh columns, corresponding to the values of y in the table.

$$y = \text{NOT}(\text{XOR}(x, r))$$

$$r_{next} = \text{XOR}(x, r)$$



register

$$x = 0, r = 0, y = 1$$

$$x = 1, r = 0, y = 0$$

$$x = 0, r = 1, y = 0$$

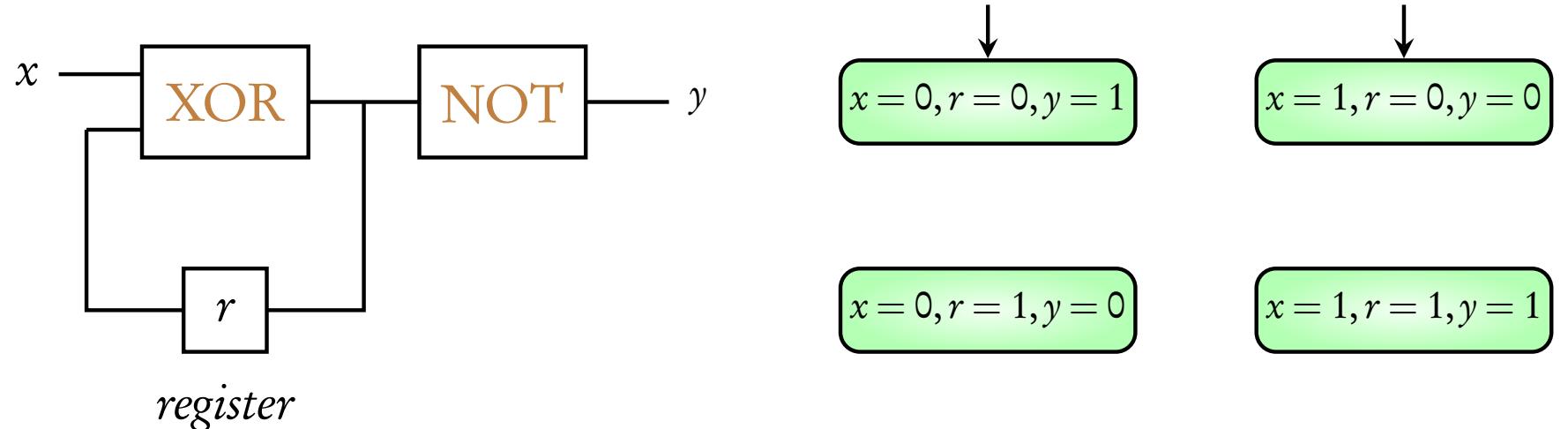
$$x = 1, r = 1, y = 1$$

x	1	1	0	1	1	1	0	0	1	0	\dots
r	0	1	0	0	1	0	1	1	1	0	
y	0	1	1	0	1	0	0	0	1	1	

Below the table, a horizontal bar is divided into 12 segments by vertical tick marks. Red checkmarks are placed under the 2nd, 4th, 6th, 8th, 10th, and 12th segments.

$$y = \text{NOT}(\text{XOR}(x, r))$$

$$r_{next} = \text{XOR}(x, r)$$

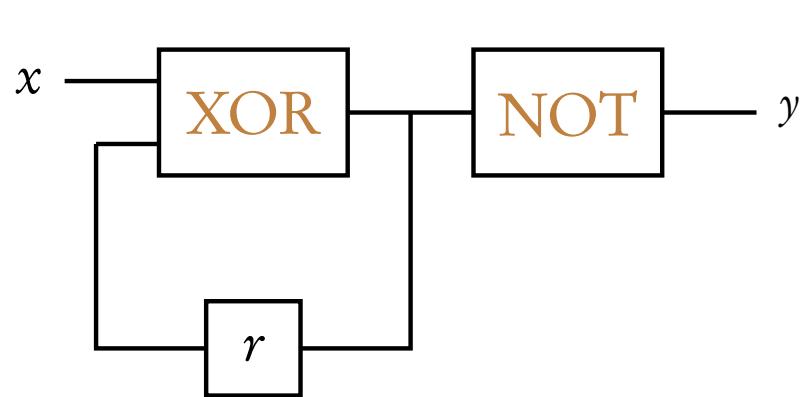


x	1	1	0	1	1	1	0	0	1	0	\dots
r	0	1	0	0	1	0	1	1	1	0	
y	0	1	1	0	1	0	0	0	1	1	

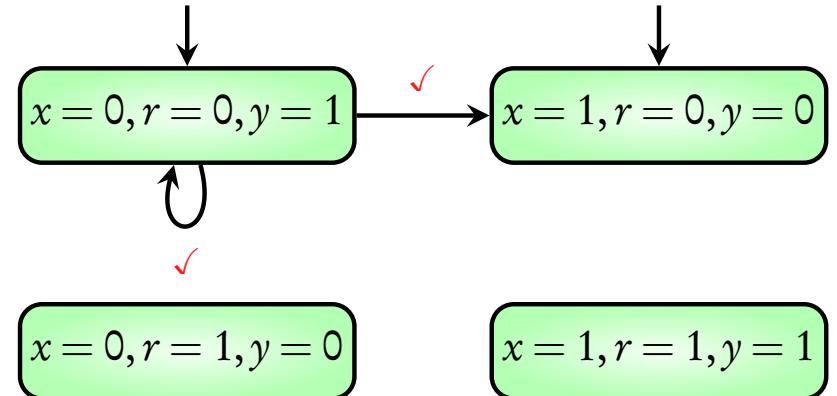
A horizontal bar with vertical tick marks spans the width of the table. Below the bar, red checkmarks are placed under the second, fourth, sixth, eighth, tenth, and eleventh columns, corresponding to the states where $y = 1$.

$$y = \text{NOT}(\text{XOR}(x, r))$$

$$r_{next} = \text{XOR}(x, r)$$



register

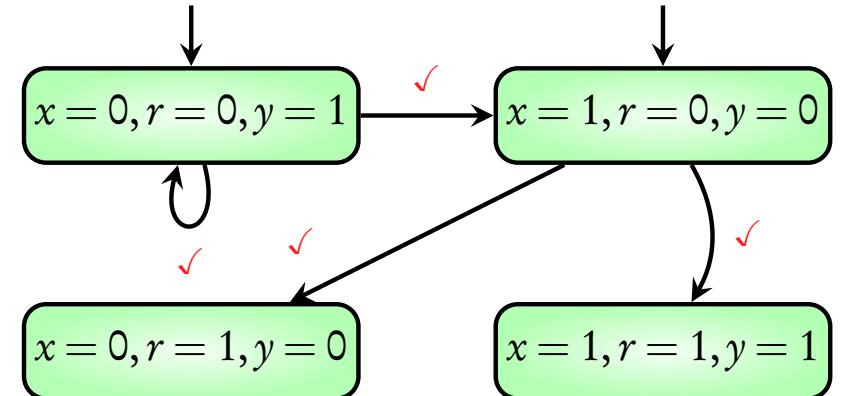
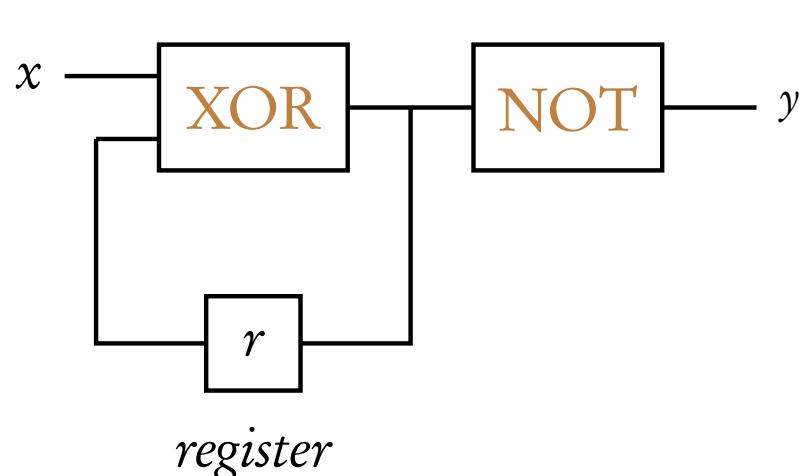


x	1	1	0	1	1	1	0	0	1	0	\dots
r	0	1	0	0	1	0	1	1	1	0	
y	0	1	1	0	1	0	0	0	1	1	

A horizontal bar with vertical tick marks spans the width of the table. Below each tick mark, there is a red checkmark (\checkmark) under the y column values 1, 0, 1, 0, 1, 0, 1, 0, 1, 0, 1.

$$y = \text{NOT}(\text{XOR}(x, r))$$

$$r_{next} = \text{XOR}(x, r)$$

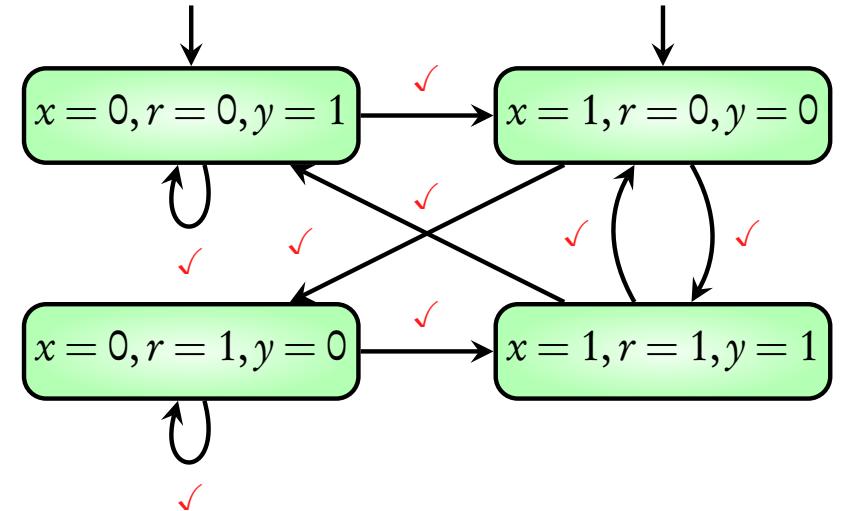
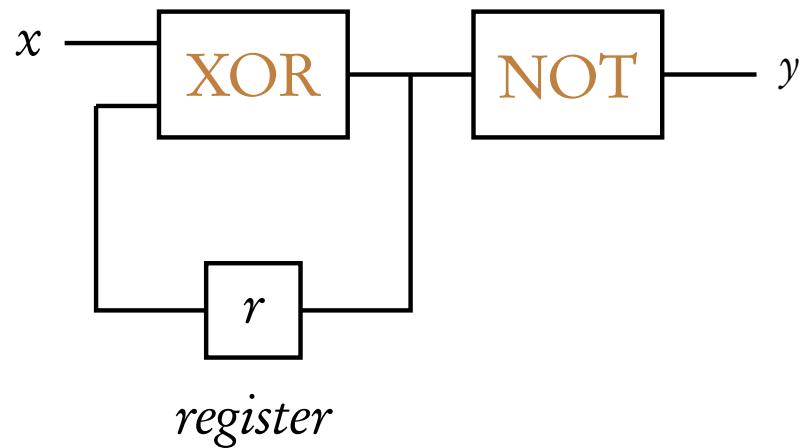


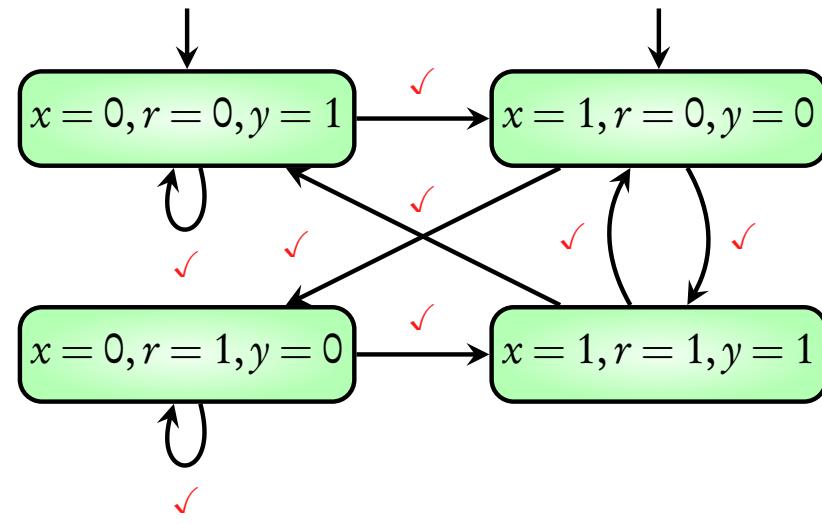
x	1	1	0	1	1	1	0	0	1	0	\dots
r	0	1	0	0	1	0	1	1	1	0	
y	0	1	1	0	1	0	0	0	1	1	

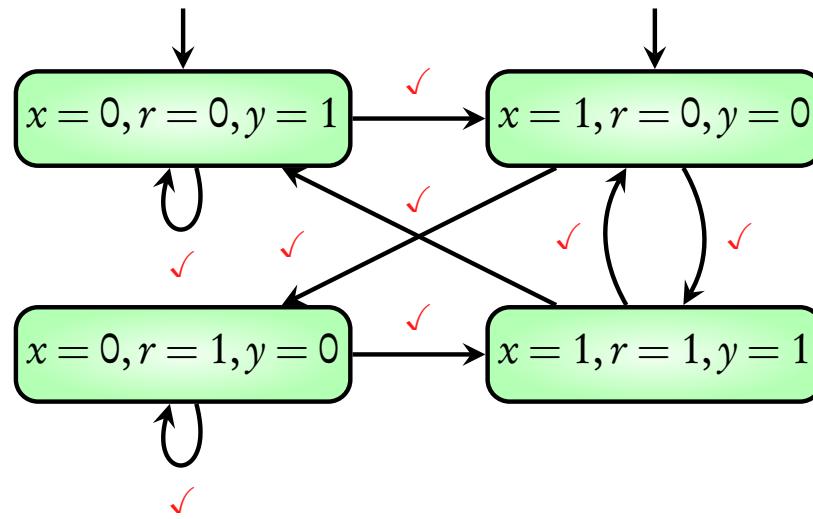
Below the table, a horizontal bar has vertical tick marks at each bit position. Red checkmarks are placed under the tick marks for the bits where y is 1: the second, fourth, fifth, eighth, ninth, and tenth bits.

$$y = \text{NOT}(\text{XOR}(x, r))$$

$$r_{next} = \text{XOR}(x, r)$$



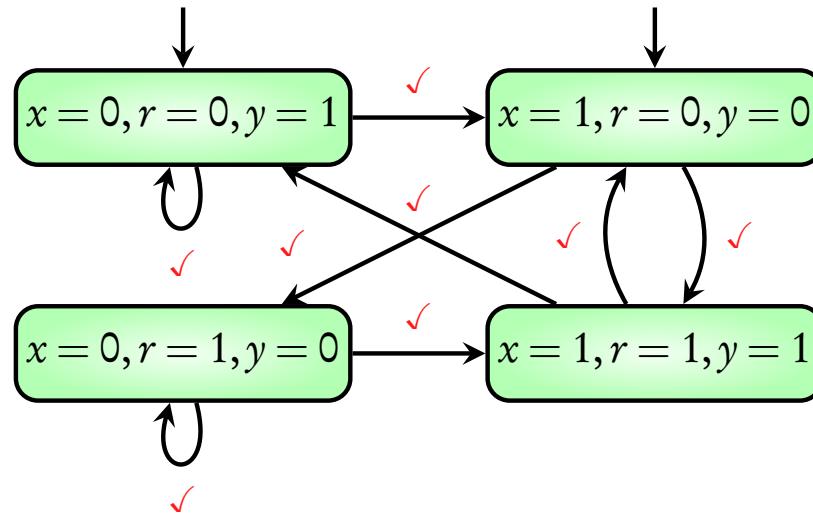




More than one initial state

States with **more than one transition** on an action

Non-deterministic transition system



More than one initial state

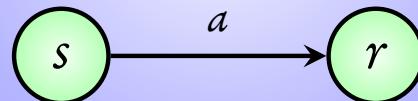
States with **more than one transition** on an action

Transition Systems

Deterministic

Single initial state

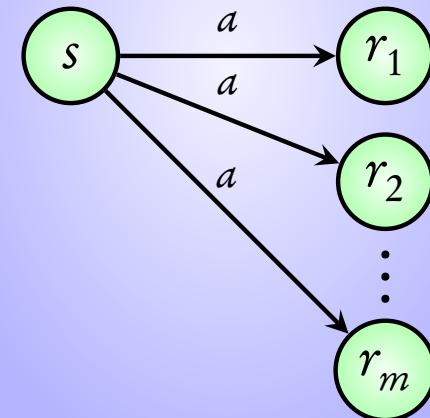
and



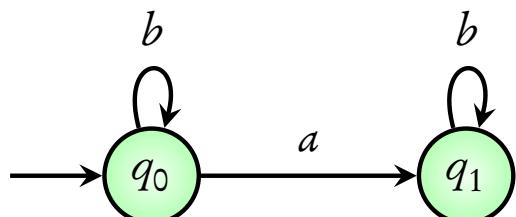
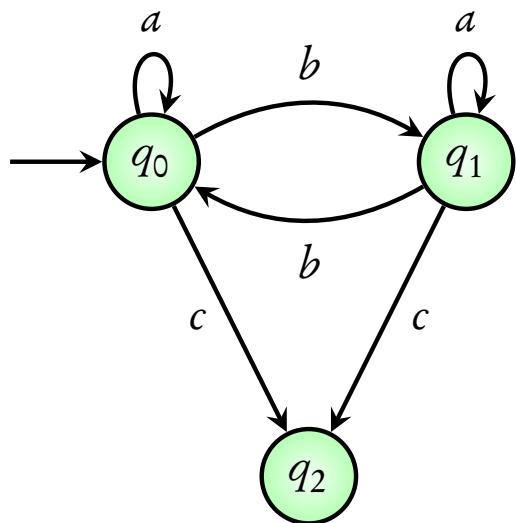
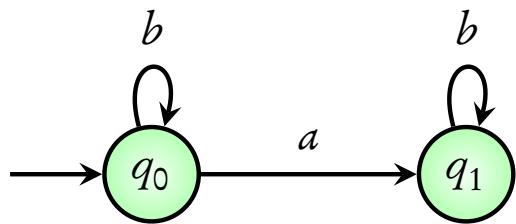
Non-deterministic

Multiple initial states

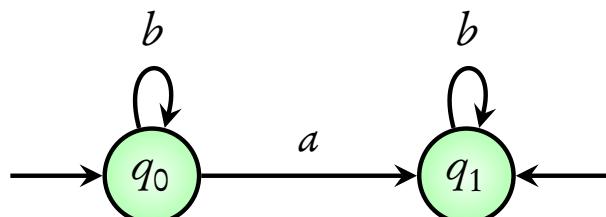
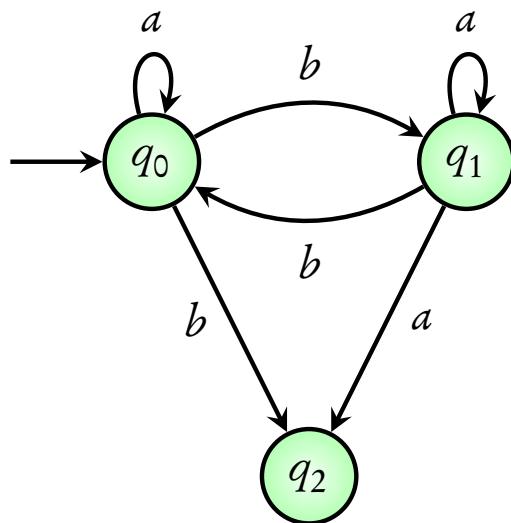
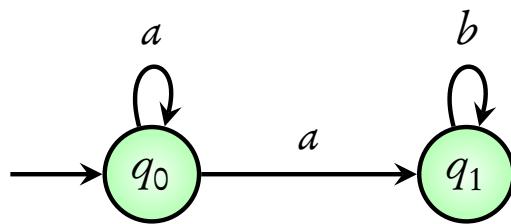
or



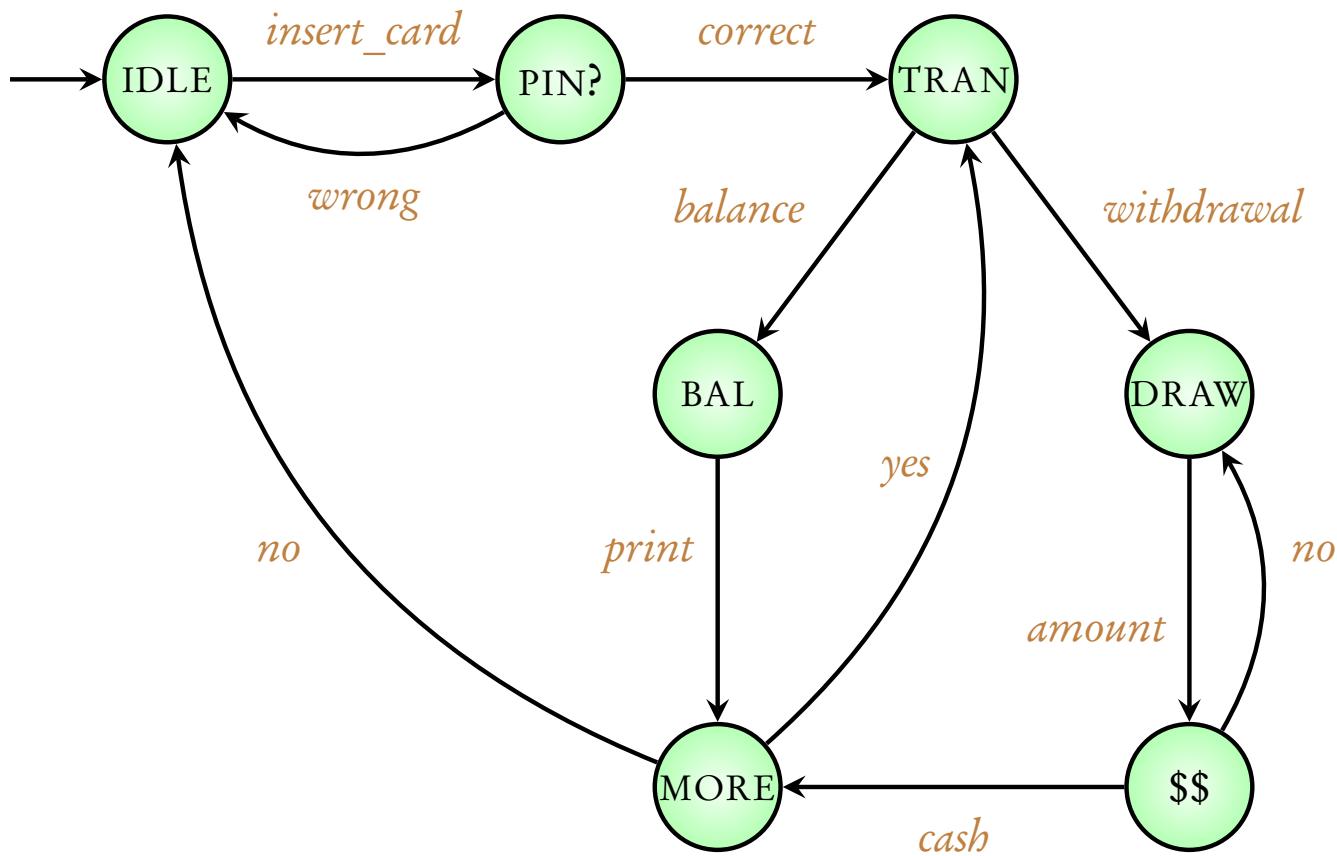
Deterministic



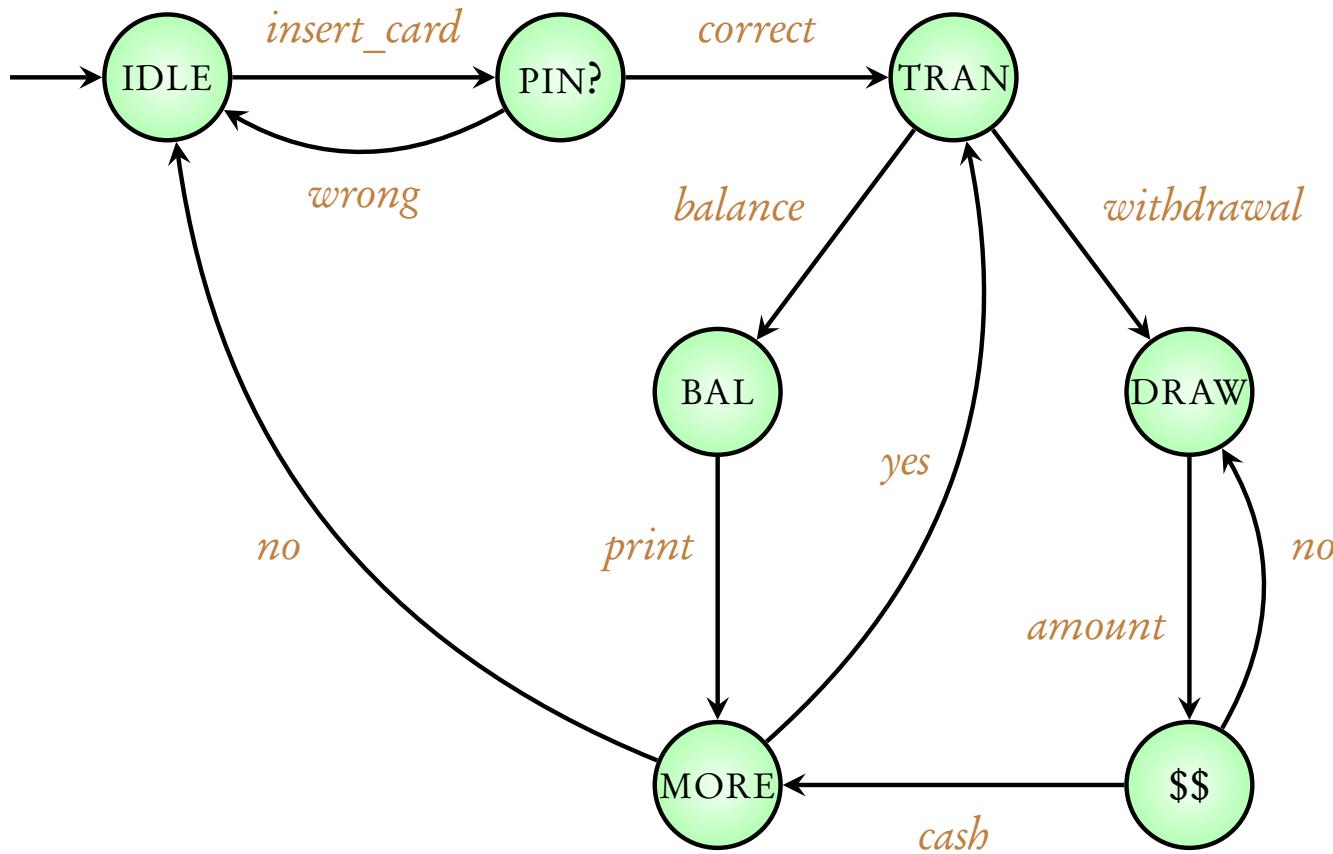
Non-deterministic



Model of ATM

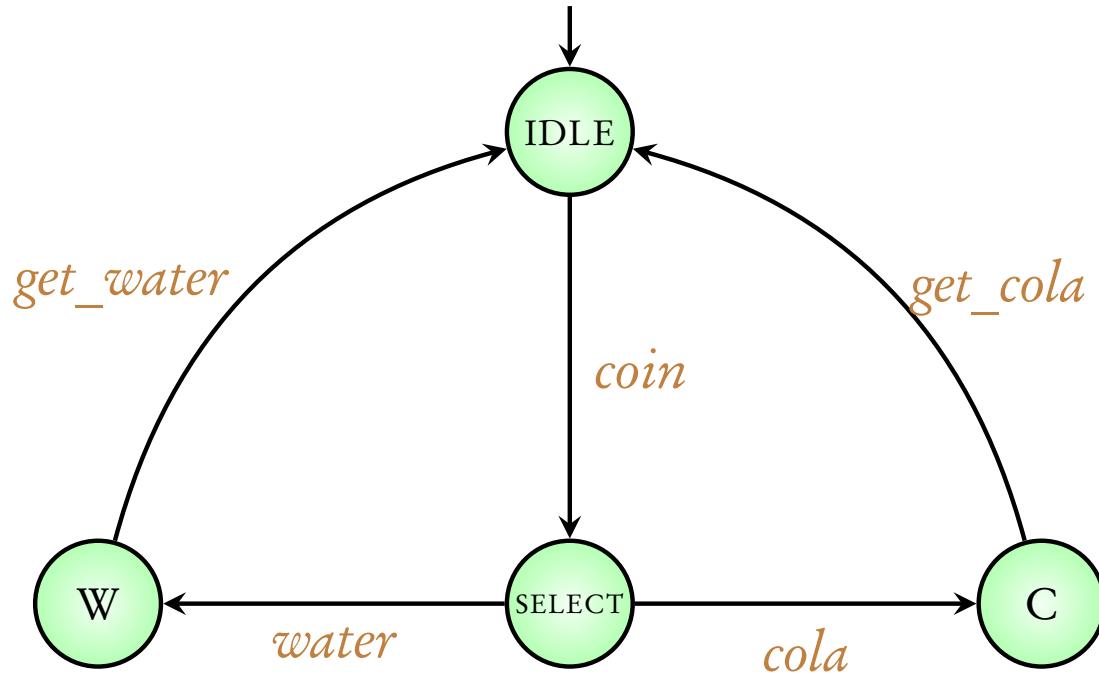


Model of ATM

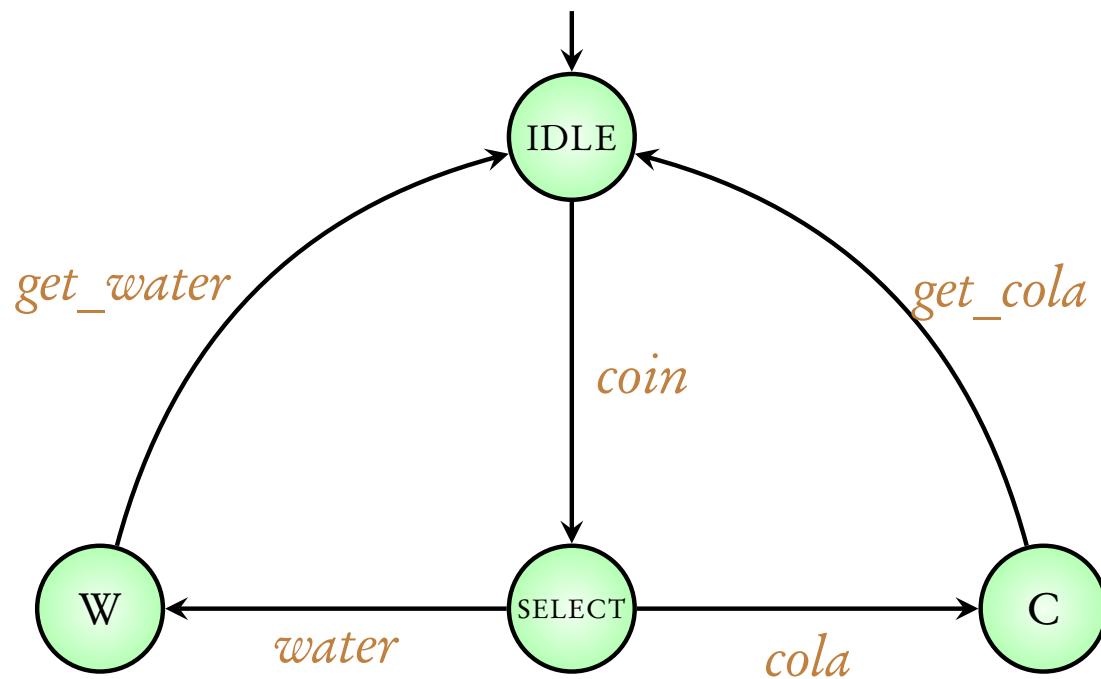


Deterministic transition system

Model of vending machine

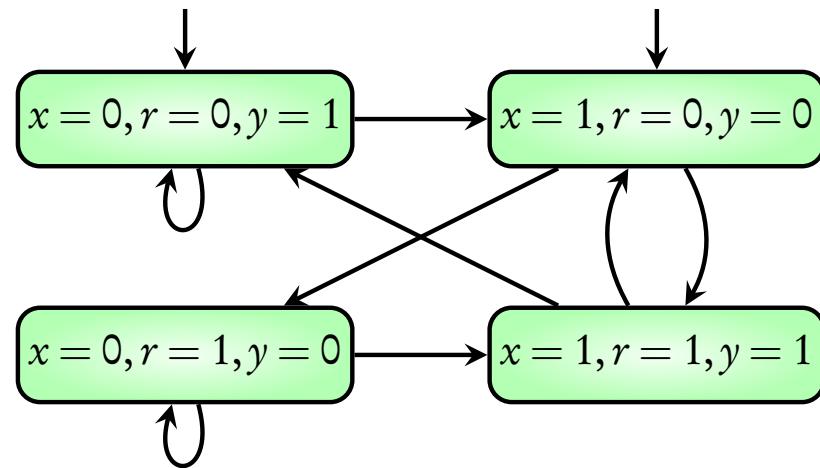


Model of vending machine

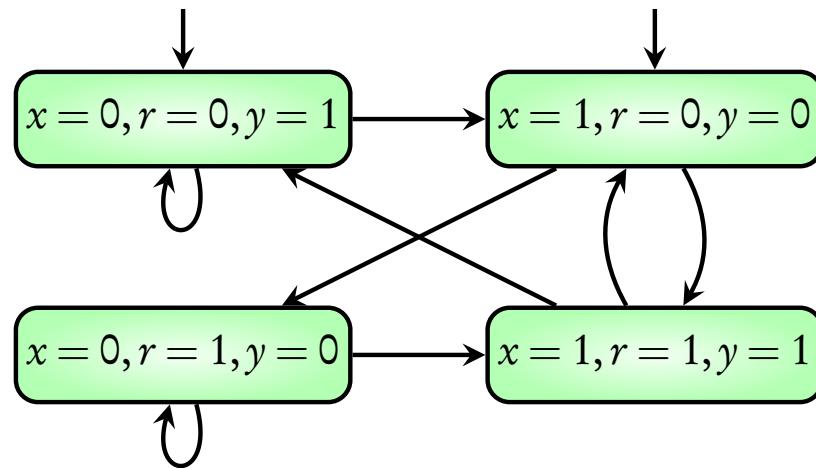


Deterministic transition system

Model of hardware circuit

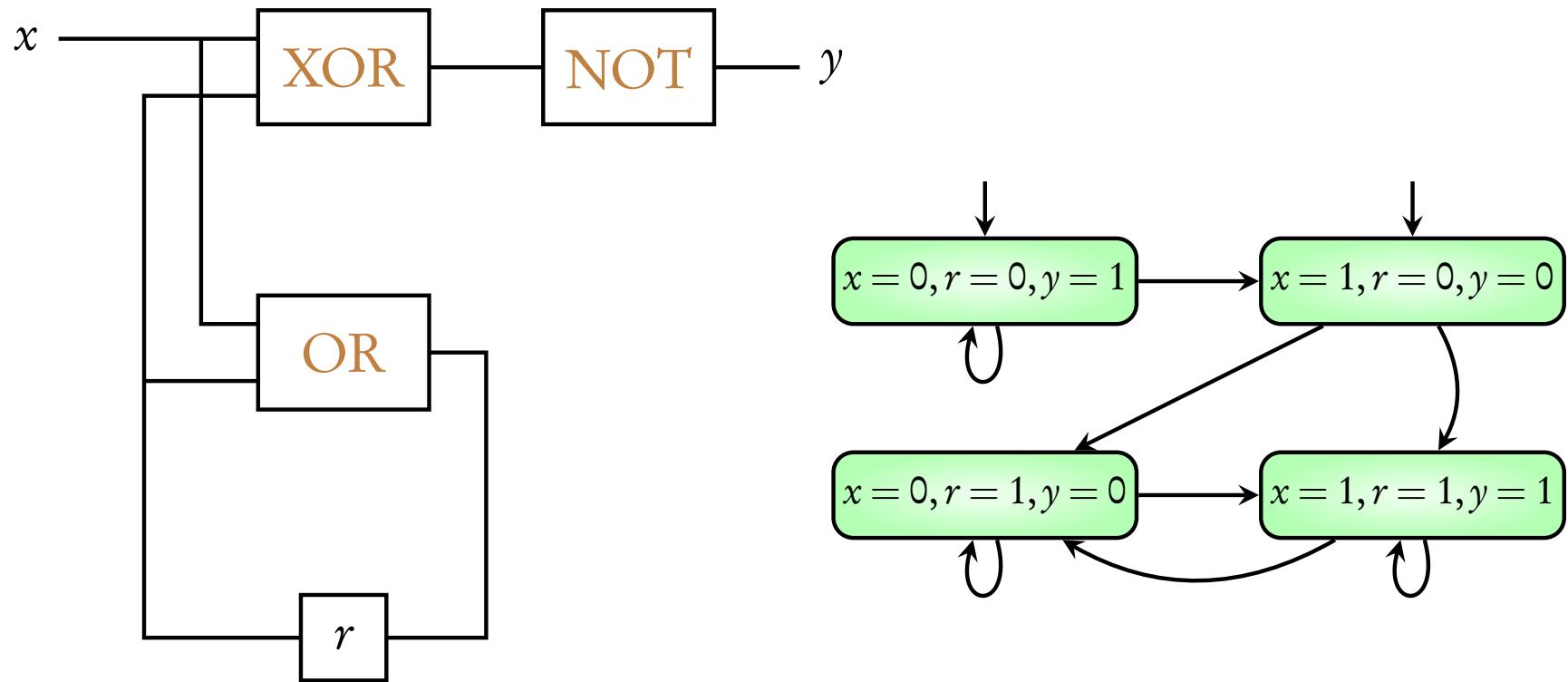


Model of hardware circuit



Non-deterministic transition system: to model incomplete information

Another Example



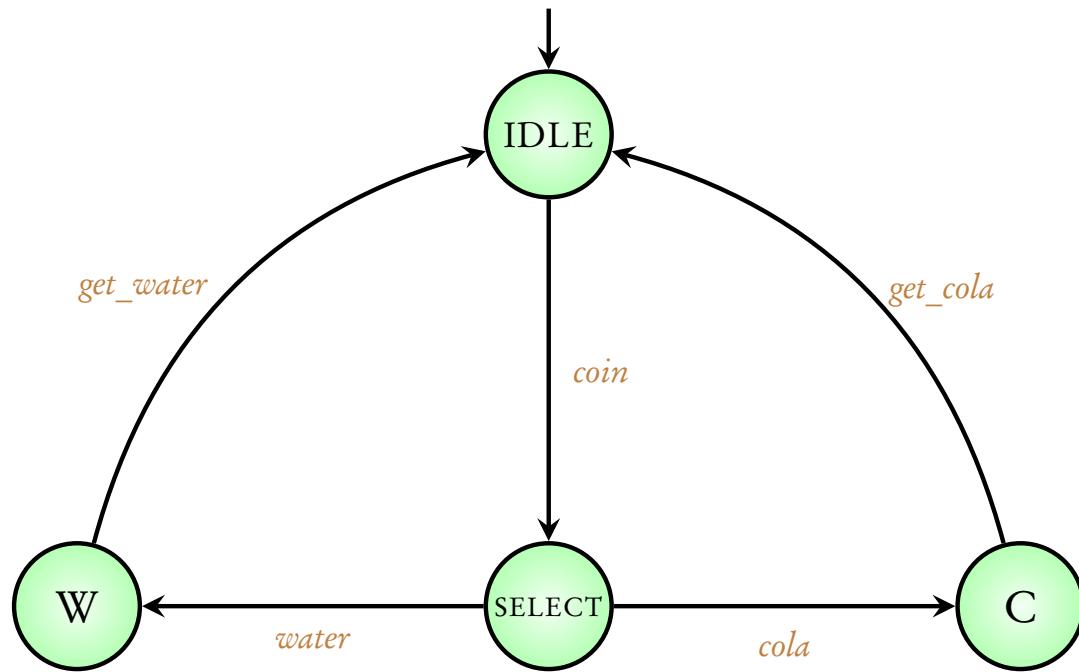
Module 3: Modeling data-dependent programs

Data-dependent programs:

Variables + Conditional branching + Assignments

if $x \% 2 = 1$ **then** $x := x + 1$ **else** $x := 2 \cdot x$ **fi.**

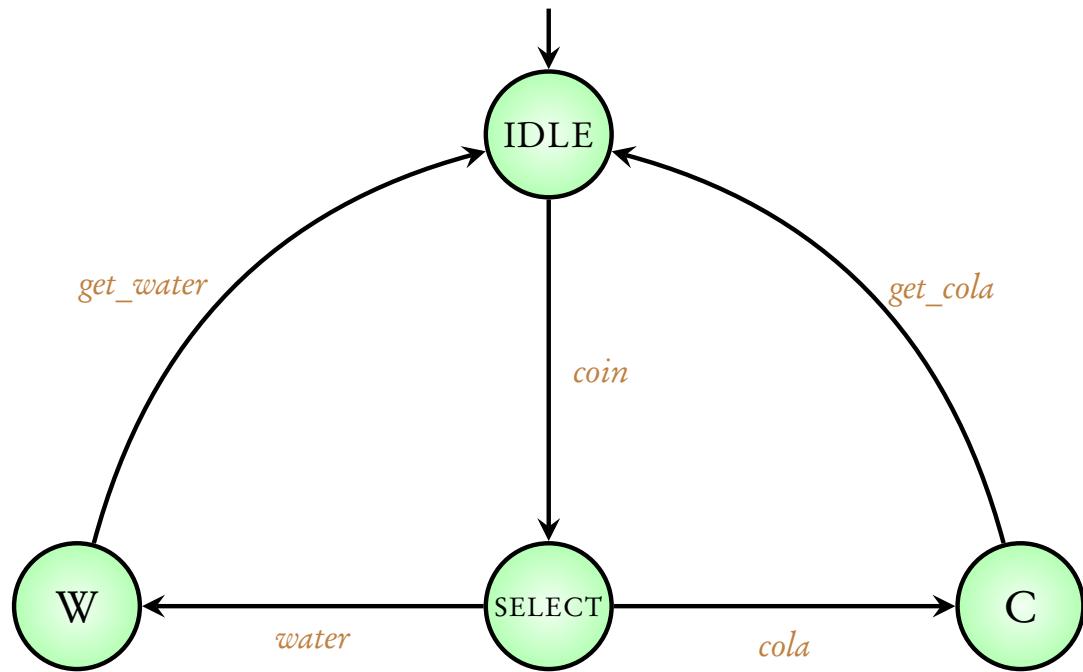
Coming next: vending machine revisited



Count the number of bottles and return coins if the machine is empty...

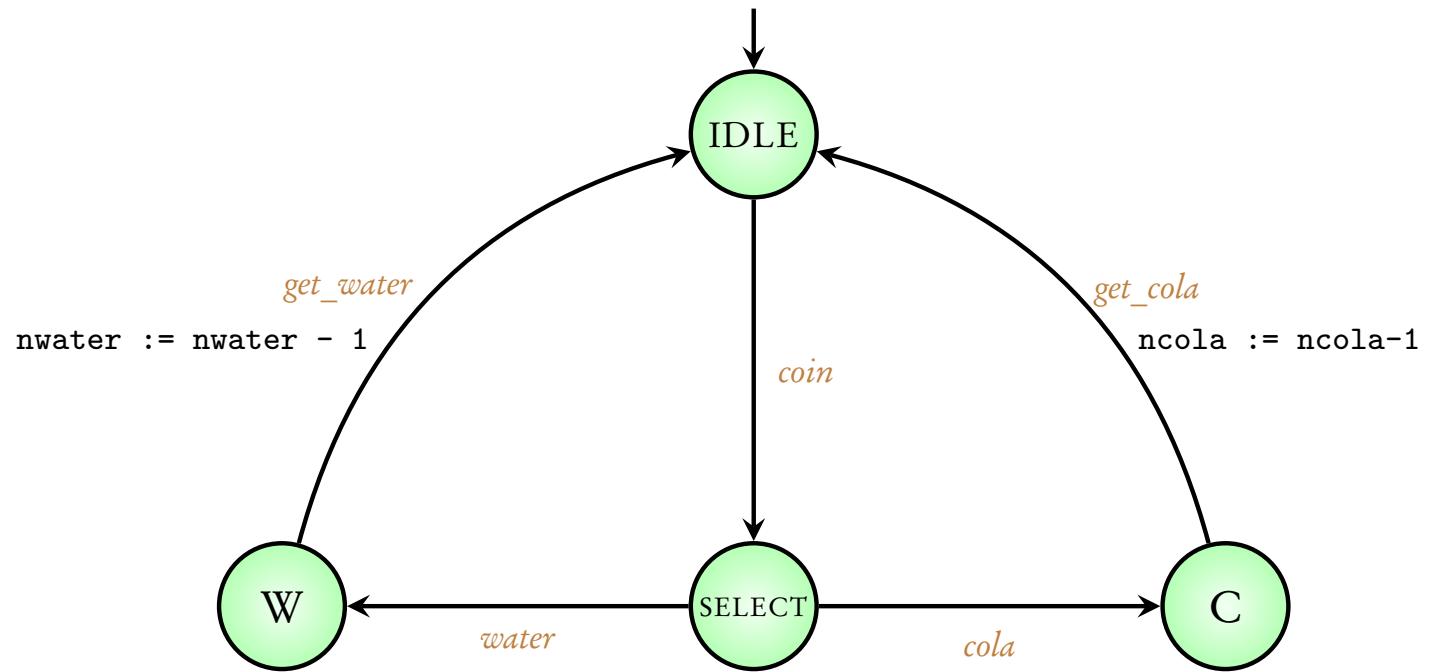
Variables:

nwater, ncola, max



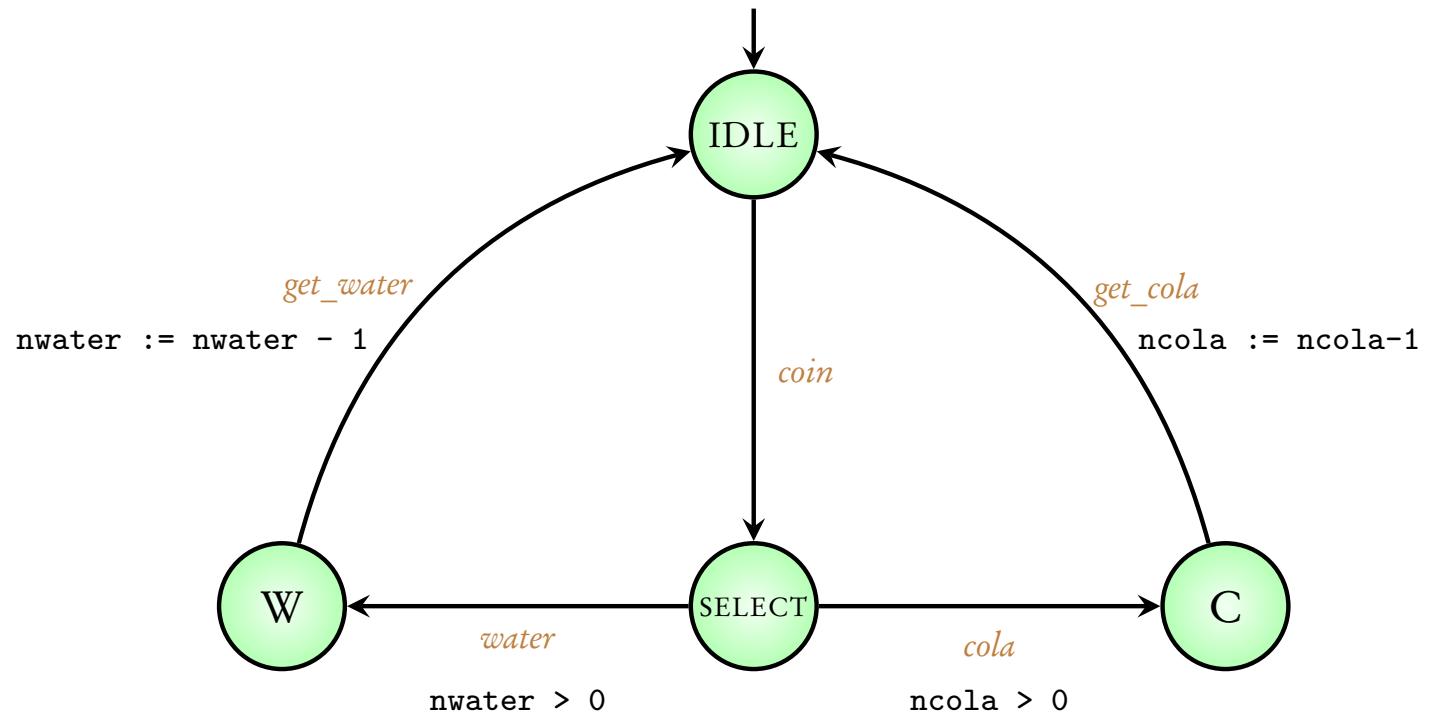
Variables:

nwater, ncola, max



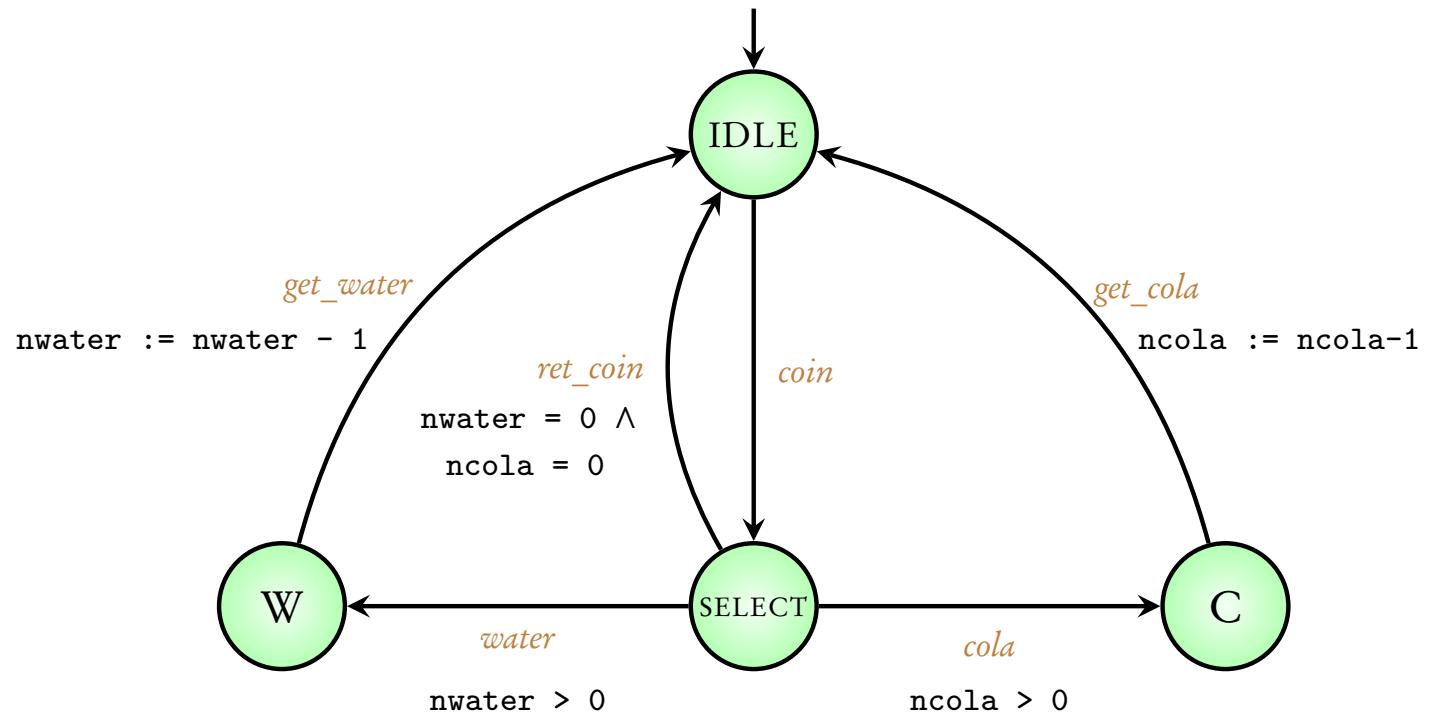
Variables:

nwater, ncola, max



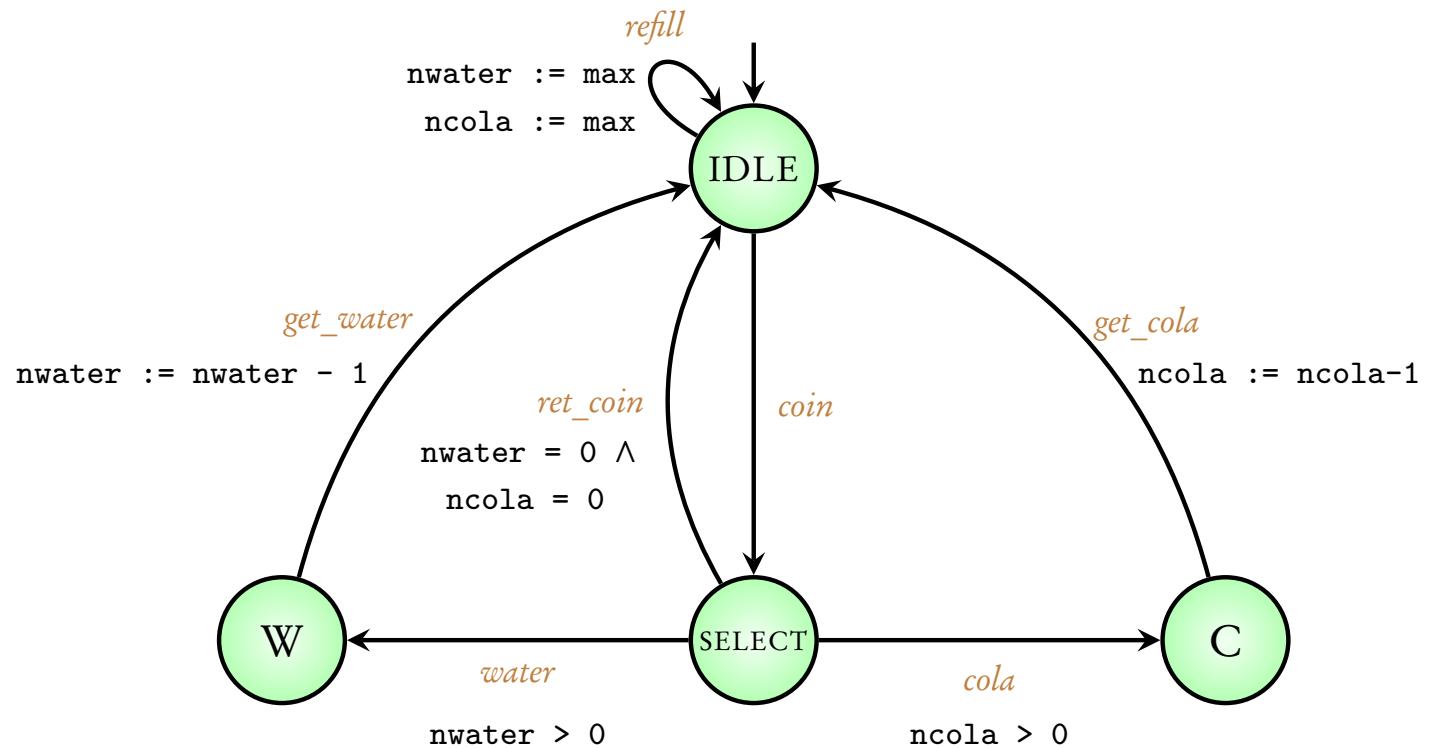
Variables:

nwater, ncola, max



Variables:

nwater, ncola, max

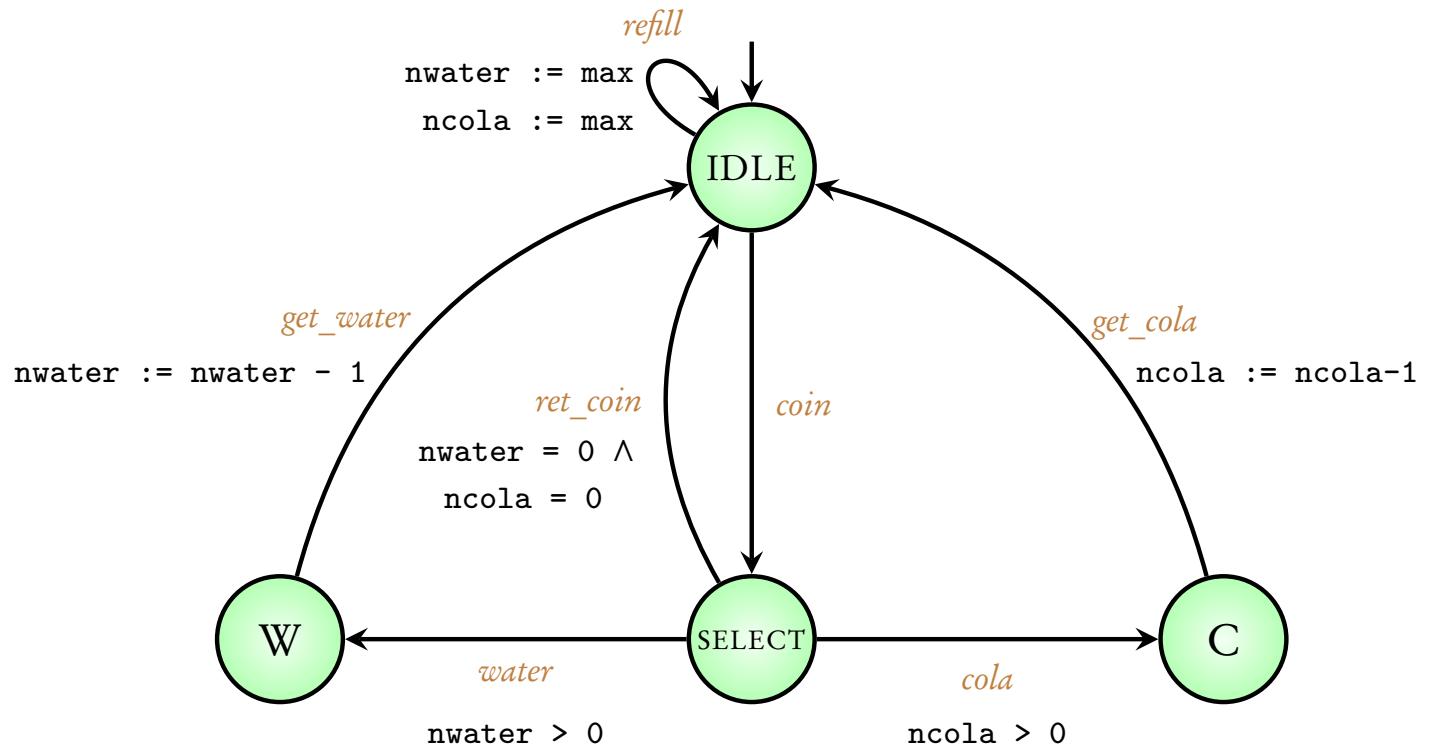


Initial condition:

nwater = max, ncola = max

Variables:

nwater, ncola, max

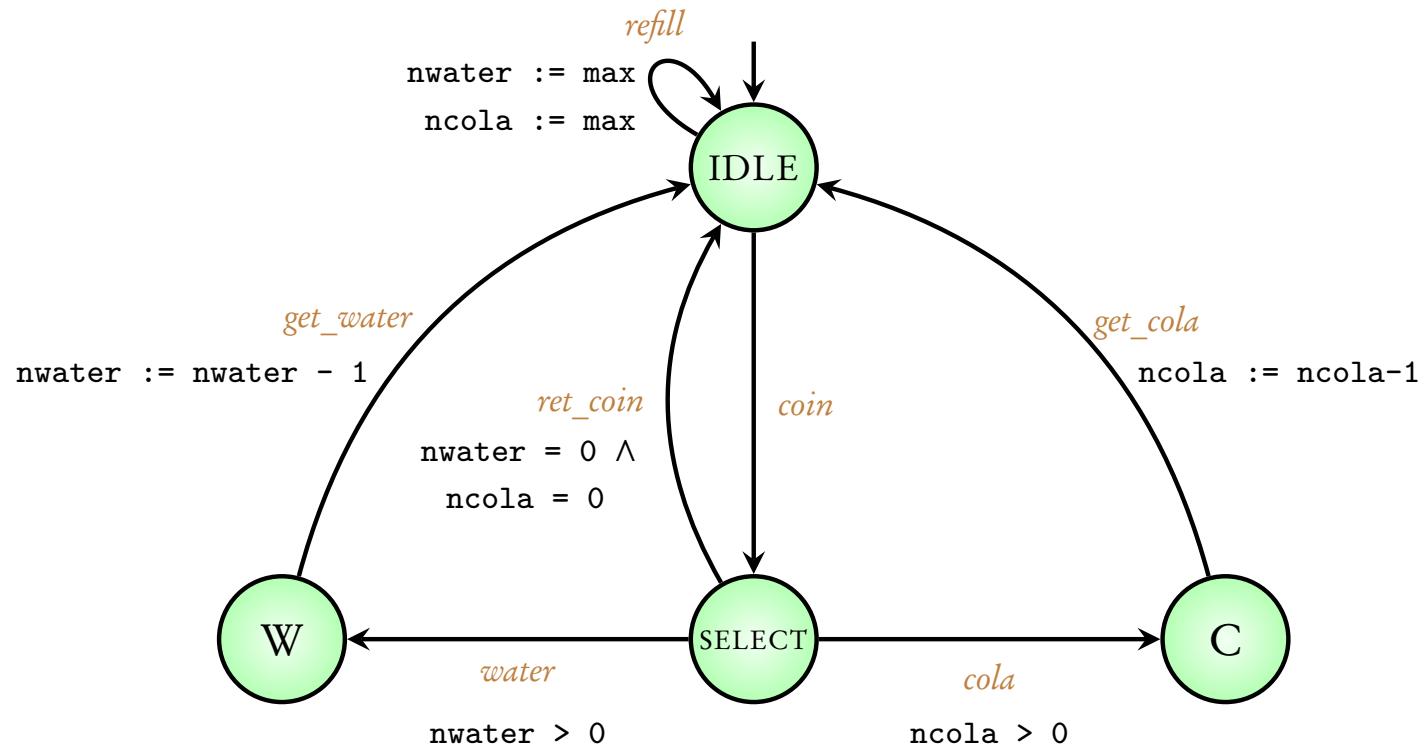


Initial condition:

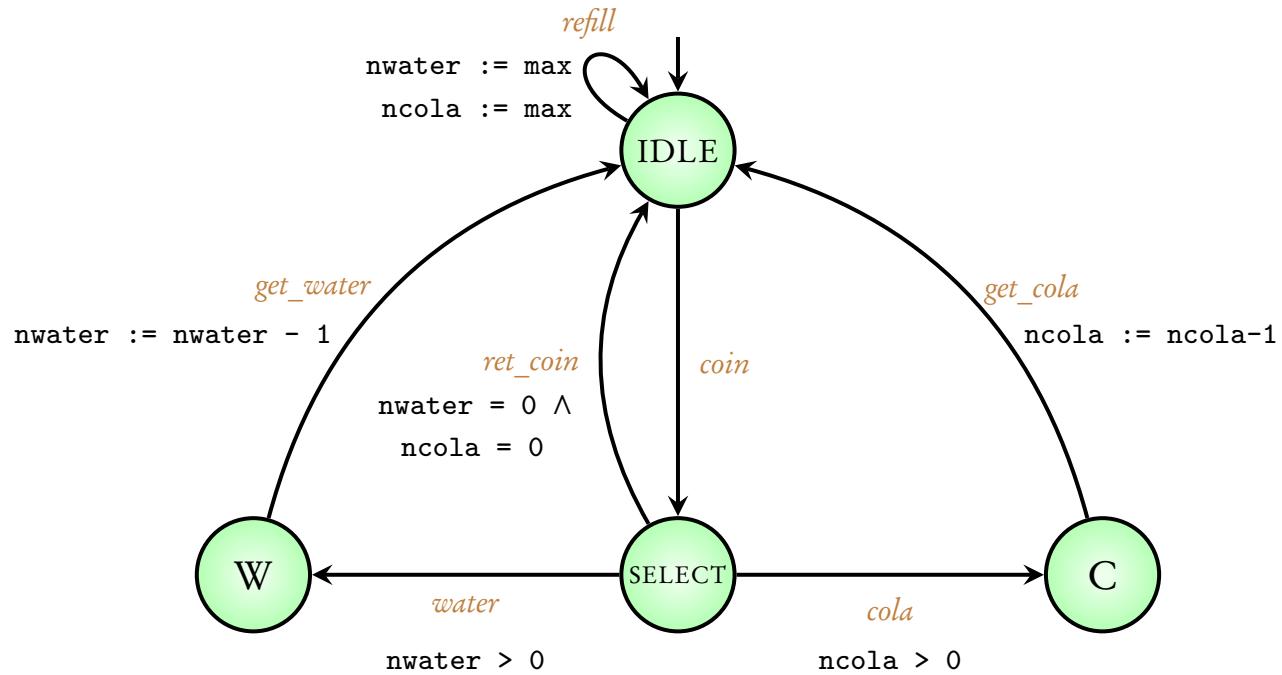
nwater = max, ncola = max

Variables:

nwater, ncola, max

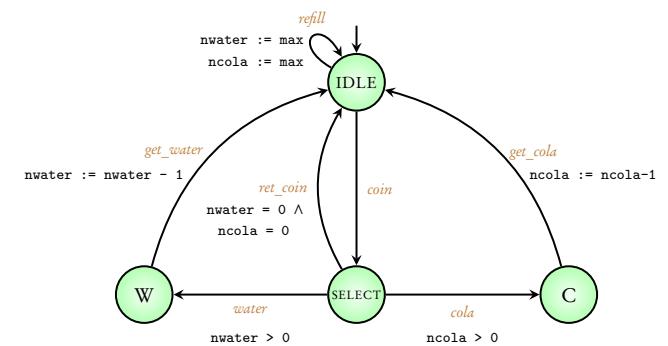


Program graph

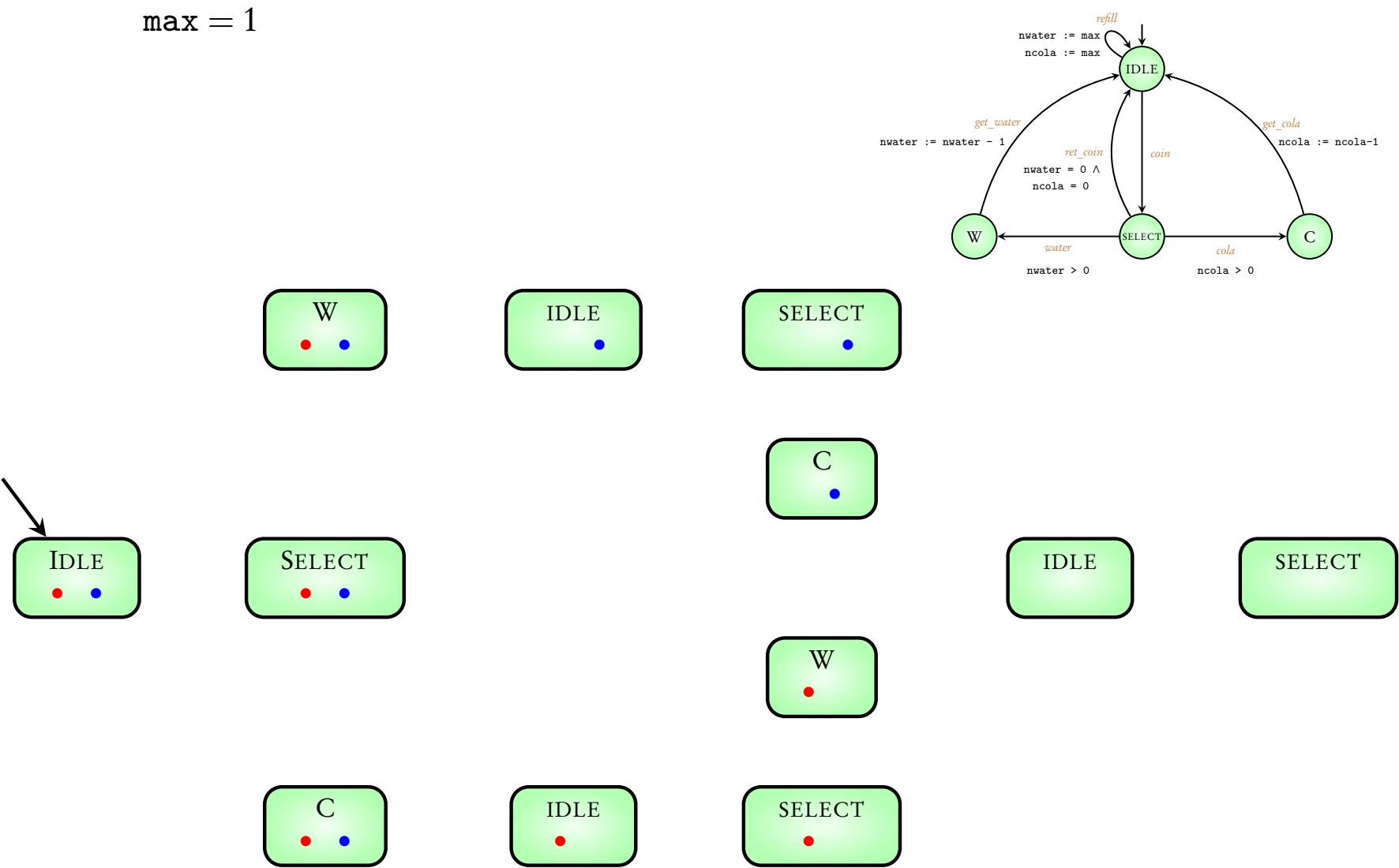


Coming next: Transition system corresponding to $\text{max} = 1$

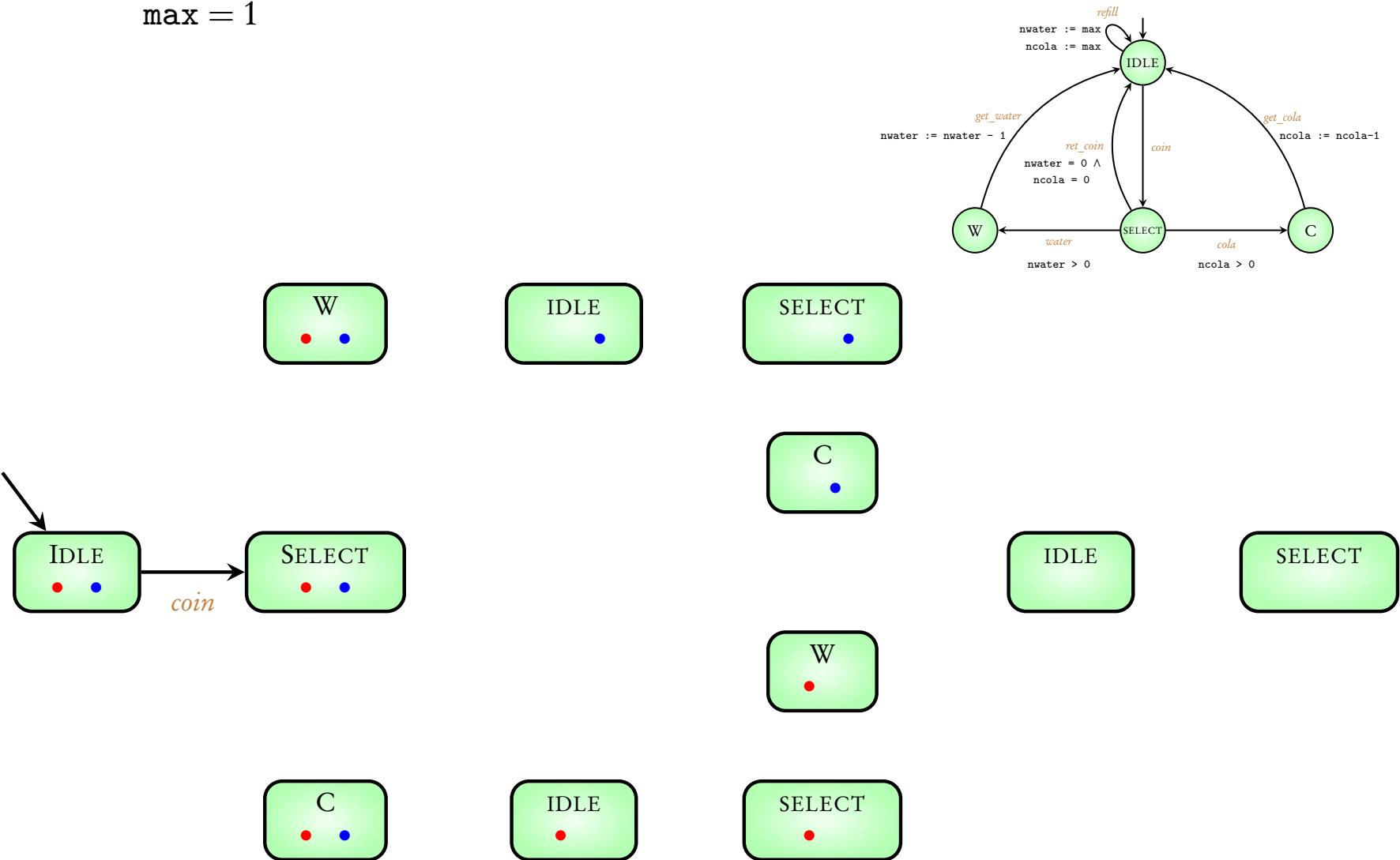
$\max = 1$



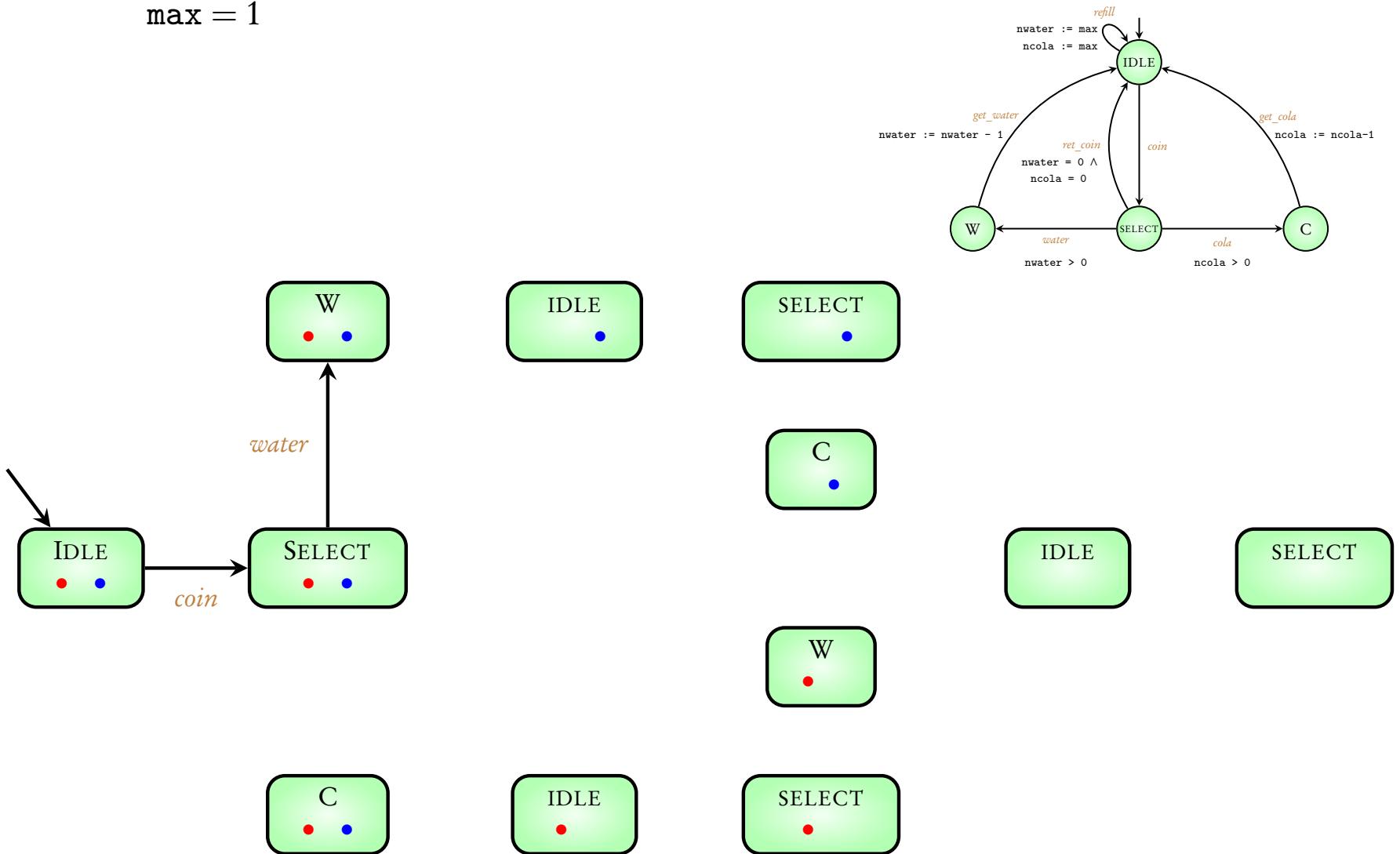
$\max = 1$



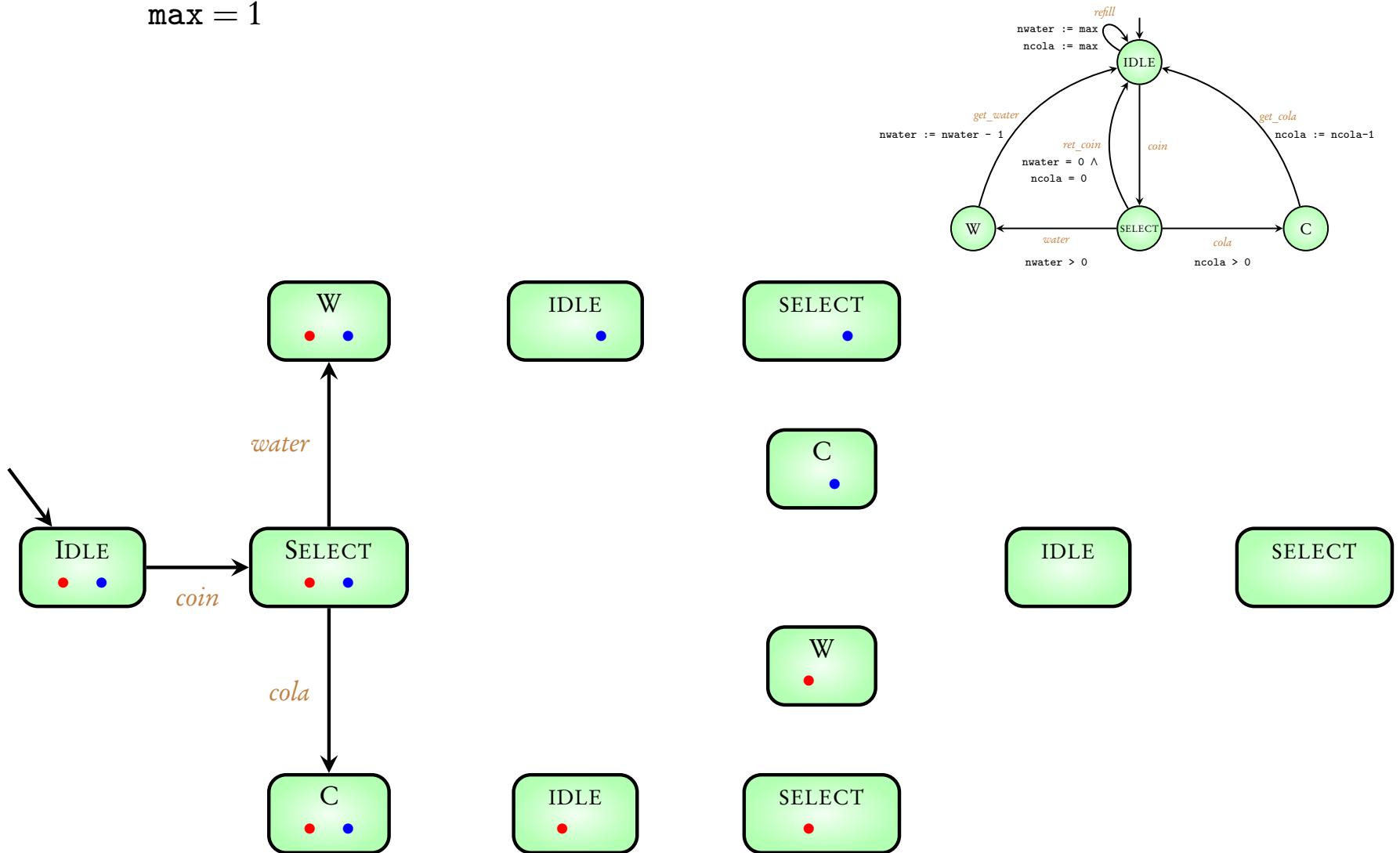
$\max = 1$



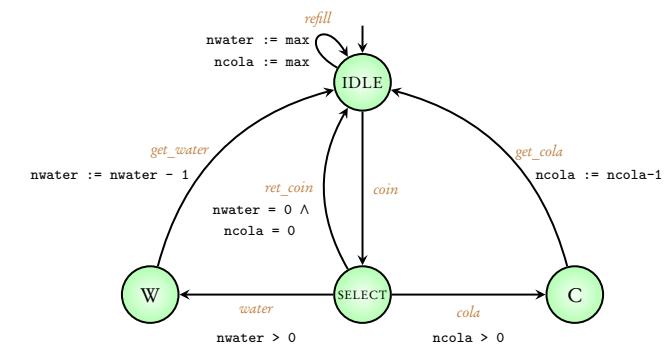
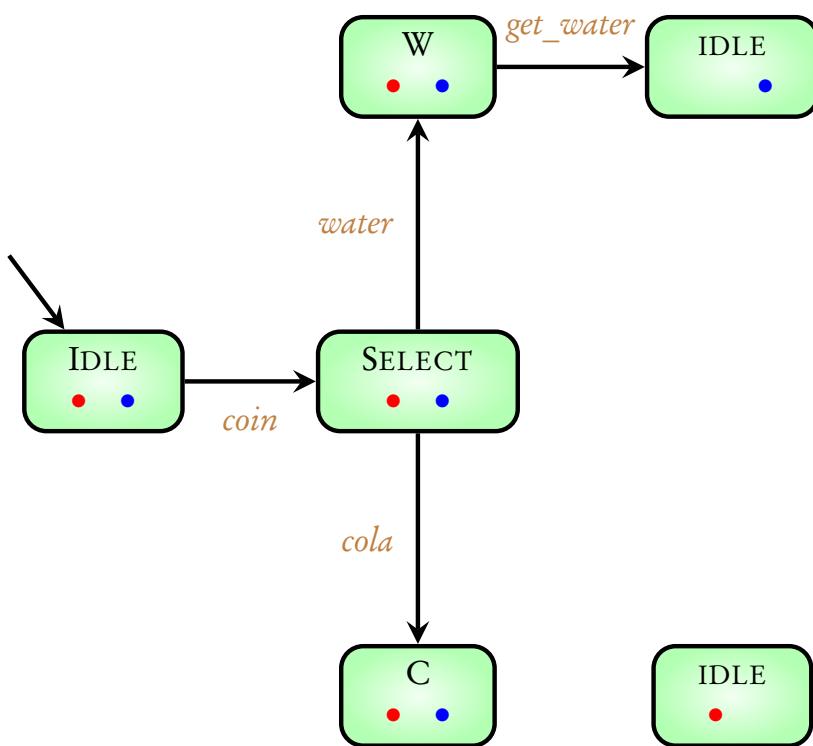
$\max = 1$



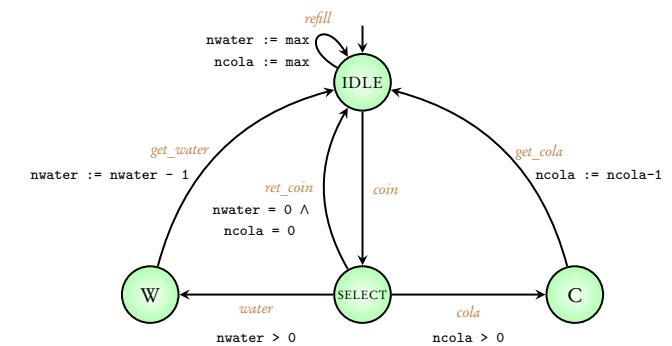
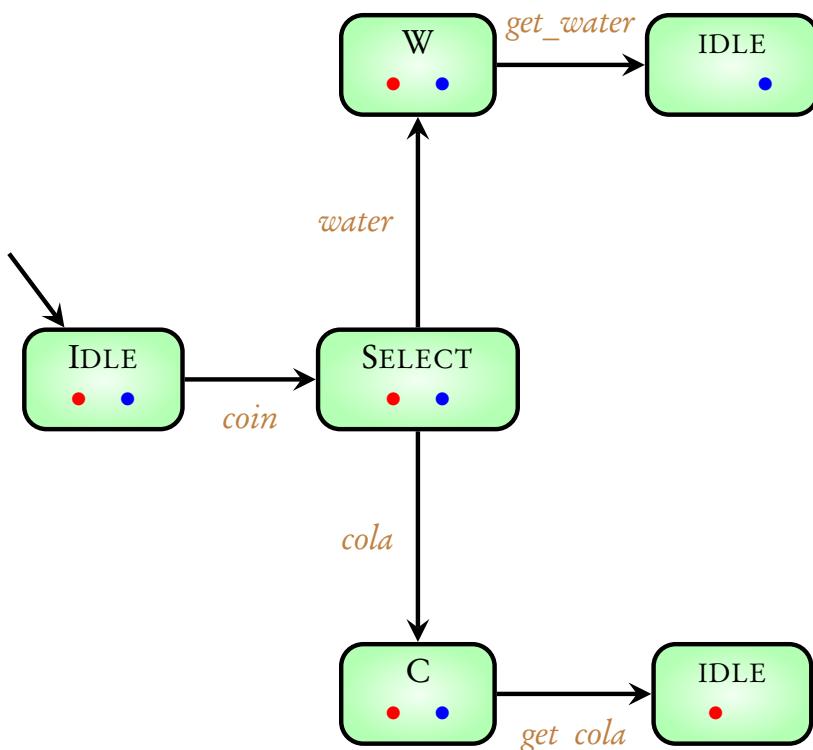
$\max = 1$



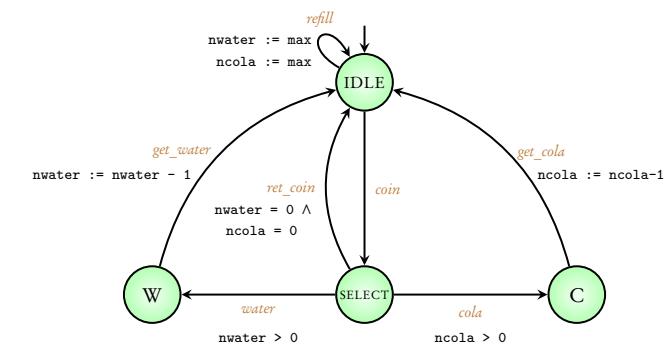
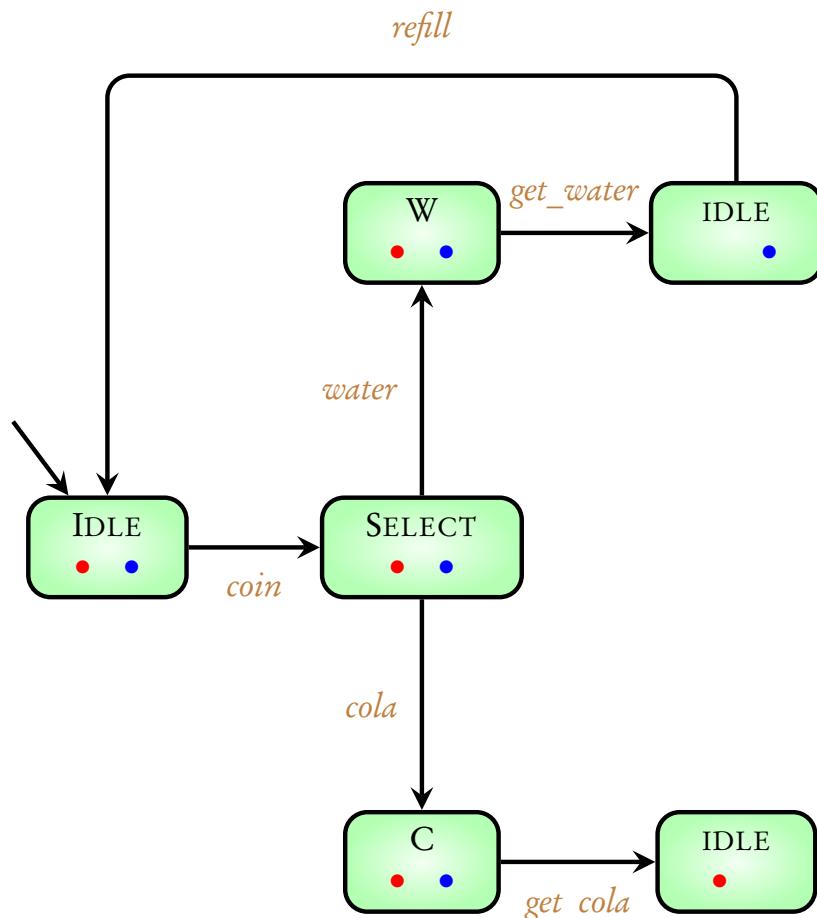
$\max = 1$



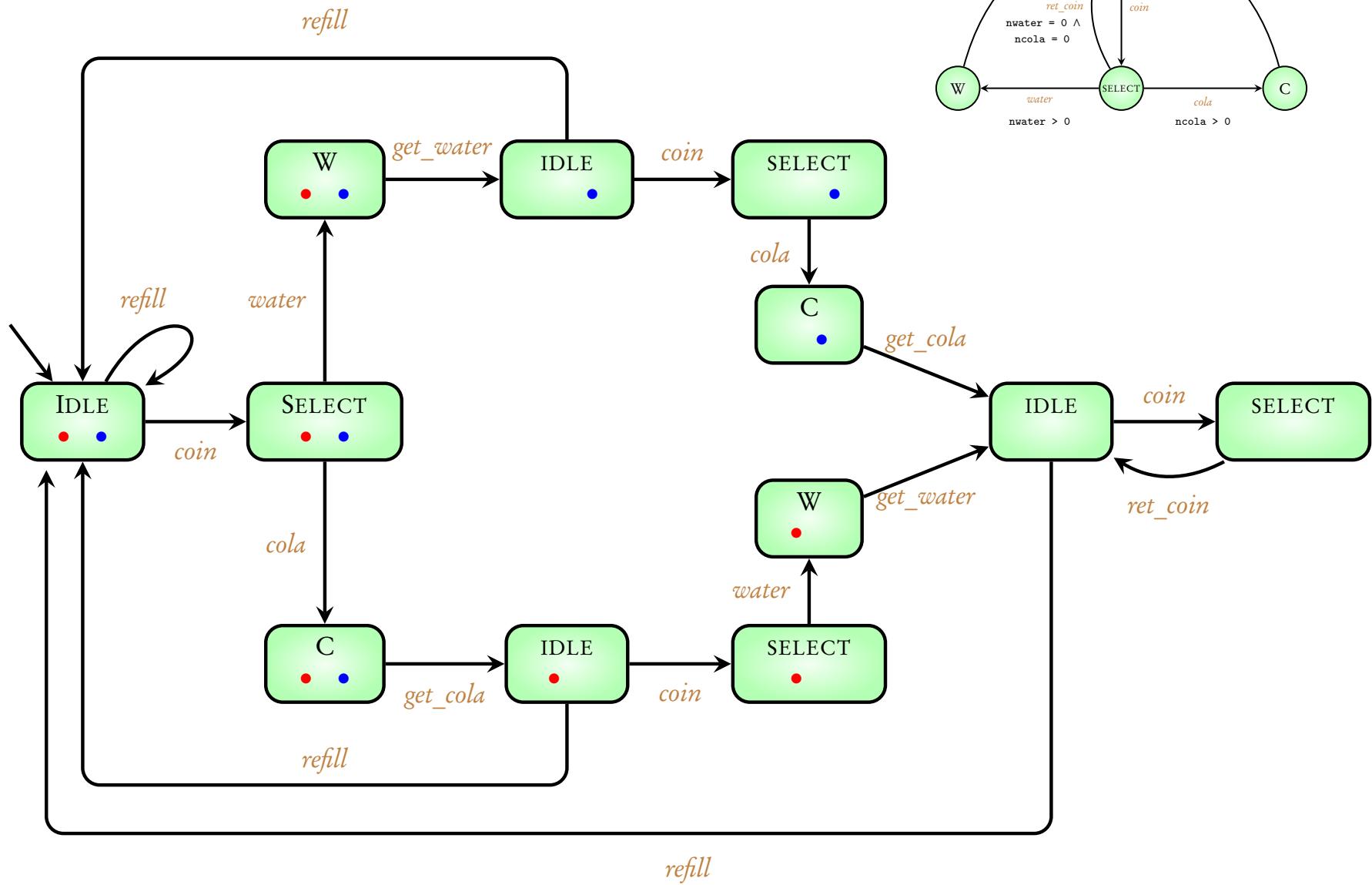
$\max = 1$



$\max = 1$



max = 1



• • •

while (x > 0)

if (x mod 2 = 0)

x := x - 2

else x := x - 1

• • •

• • •

$l_1:$ **while** (x > 0)

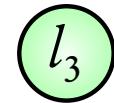
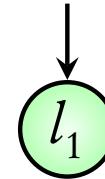
if (x mod 2 = 0)

$l_2:$ x := x - 2

else x := x - 1

• • •

$l_3:$



• • •

$l_1:$ **while** ($x > 0$)

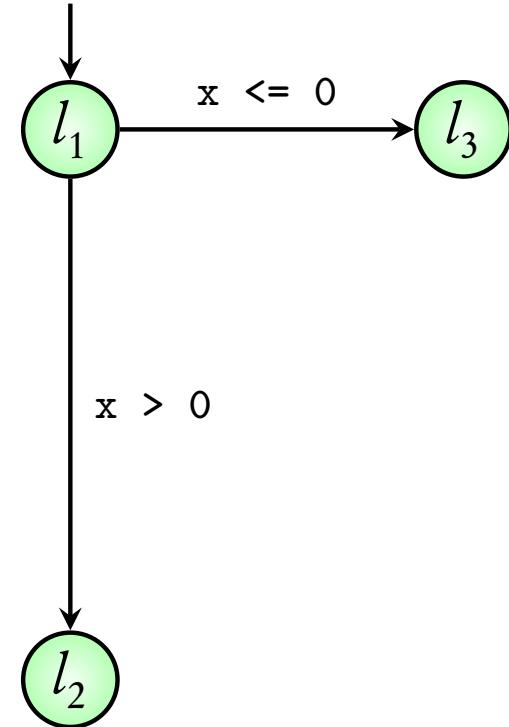
if ($x \bmod 2 = 0$)

$l_2:$ $x := x - 2$

else $x := x - 1$

• • •

$l_3:$



• • •

$l_1:$ **while** ($x > 0$)

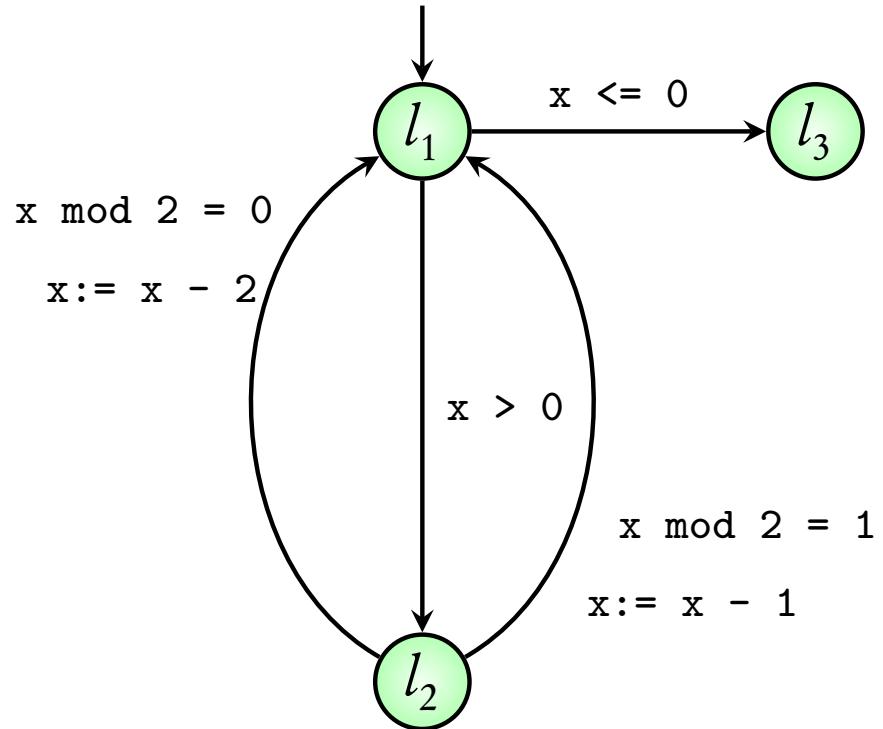
if ($x \bmod 2 = 0$)

$l_2:$ $x := x - 2$

else $x := x - 1$

• • •

$l_3:$



\dots

$l_1:$ **while** ($x > 0$)

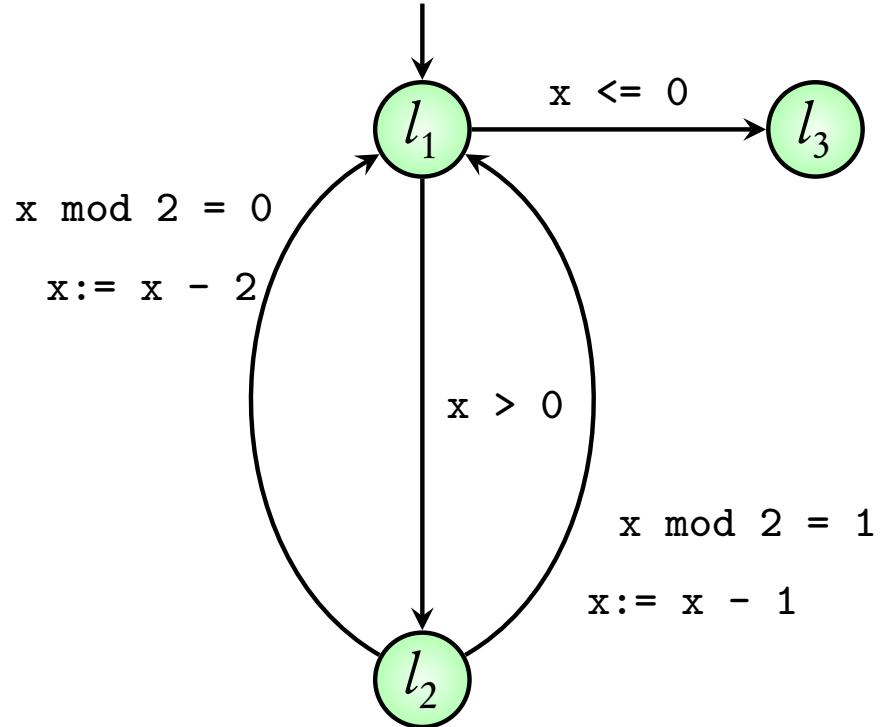
if ($x \bmod 2 = 0$)

$l_2:$ $x := x - 2$

else $x := x - 1$

\dots

$l_3:$



Program graph

Definition 2.13. Program Graph (PG)

A program graph PG over set Var of typed variables is a tuple $(Loc, Act, Effect, \rightarrow, Loc_0, g_0)$ where

- Loc is a set of locations and Act is a set of actions,
- $Effect : Act \times Eval(Var) \rightarrow Eval(Var)$ is the effect function,
- $\rightarrow \subseteq Loc \times Cond(Var) \times Act \times Loc$ is the conditional transition relation,
- $Loc_0 \subseteq Loc$ is a set of initial locations,
- $g_0 \in Cond(Var)$ is the initial condition.

Definition 2.15. Transition System Semantics of a Program Graph

The transition system $TS(PG)$ of program graph

$$PG = (Loc, Act, Effect, \hookrightarrow, Loc_0, g_0)$$

over set Var of variables is the tuple $(S, Act, \longrightarrow, I, AP, L)$ where

- $S = Loc \times \text{Eval}(\text{Var})$
- $\longrightarrow \subseteq S \times Act \times S$ is defined by the following rule (see remark below):

$$\frac{\ell \xrightarrow{g:\alpha} \ell' \quad \wedge \quad \eta \models g}{\langle \ell, \eta \rangle \xrightarrow{\alpha} \langle \ell', \text{Effect}(\alpha, \eta) \rangle}$$

- $I = \{ \langle \ell, \eta \rangle \mid \ell \in Loc_0, \eta \models g_0 \}$
- $AP = Loc \cup \text{Cond}(\text{Var})$
- $L(\langle \ell, \eta \rangle) = \{ \ell \} \cup \{ g \in \text{Cond}(\text{Var}) \mid \eta \models g \}.$

\dots

$l_1:$ **while** ($x > 0$)

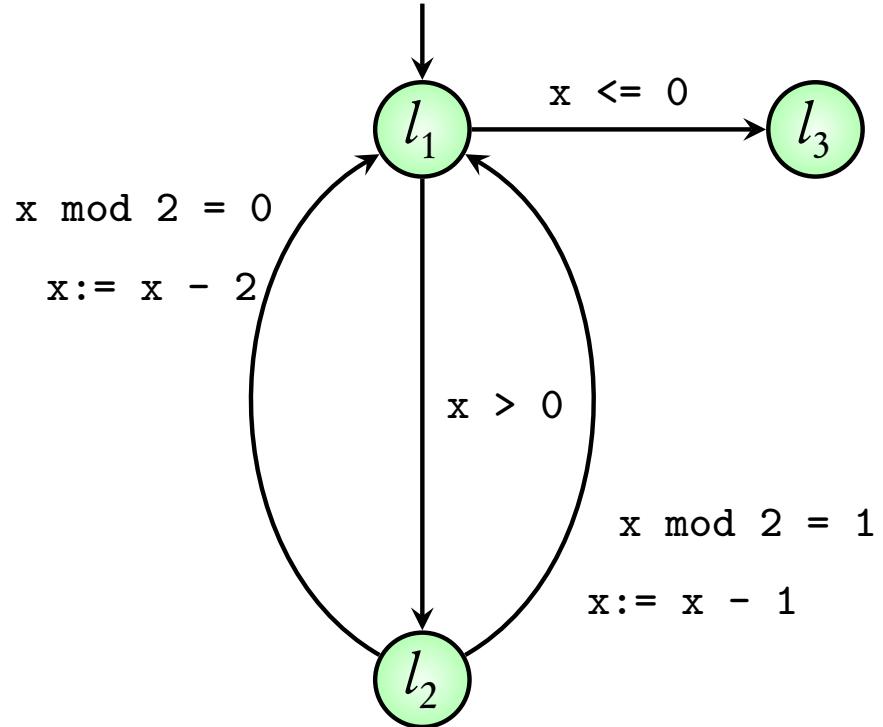
if ($x \bmod 2 = 0$)

$l_2:$ $x := x - 2$

else $x := x - 1$

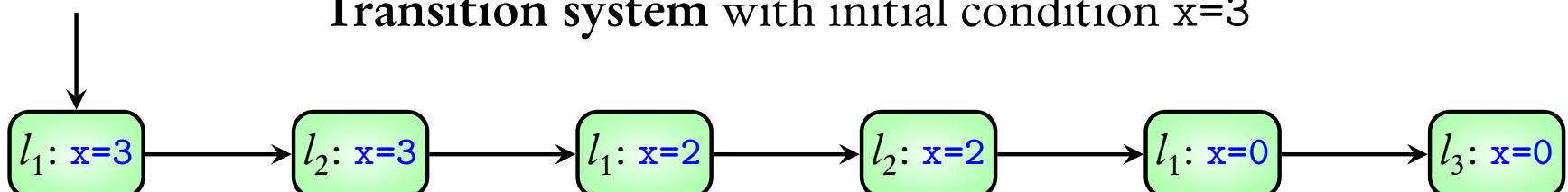
\dots

$l_3:$



Program graph

Transition system with initial condition $x=3$



Module 4: Modeling concurrent systems

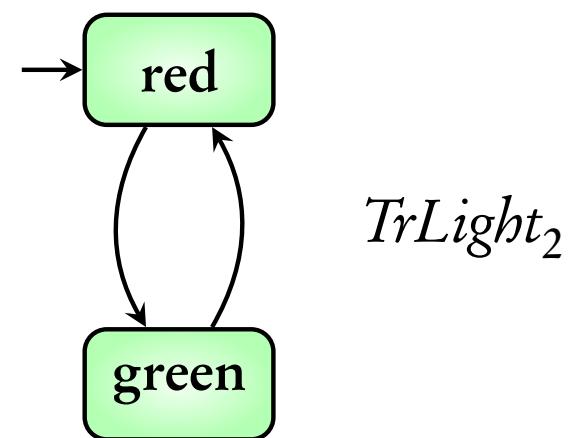
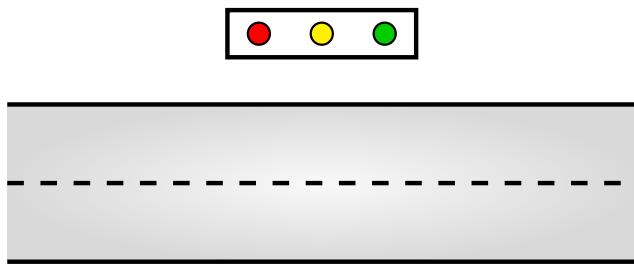
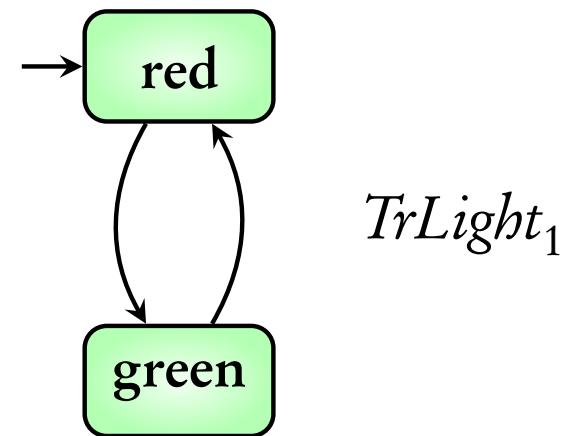
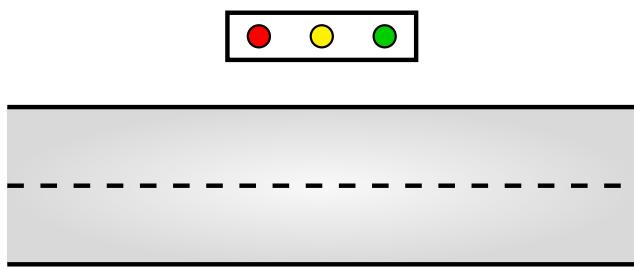
$$TS = TS_1 \parallel TS_2 \parallel \dots \parallel TS_n$$

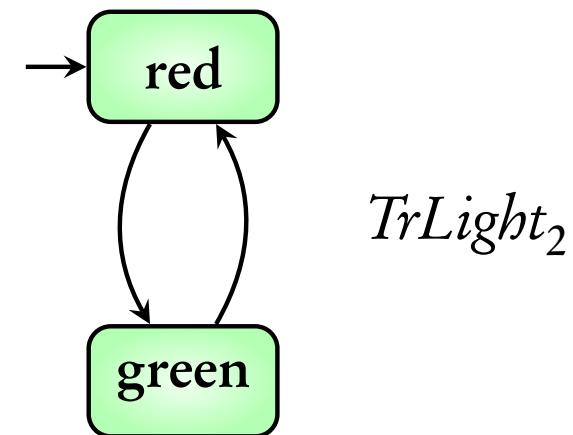
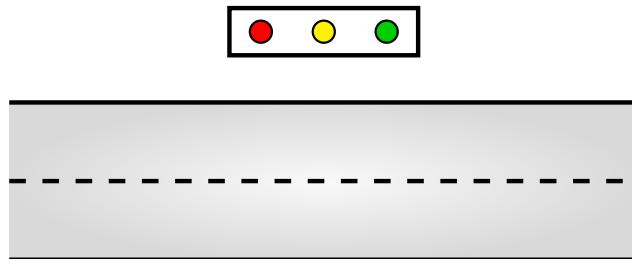
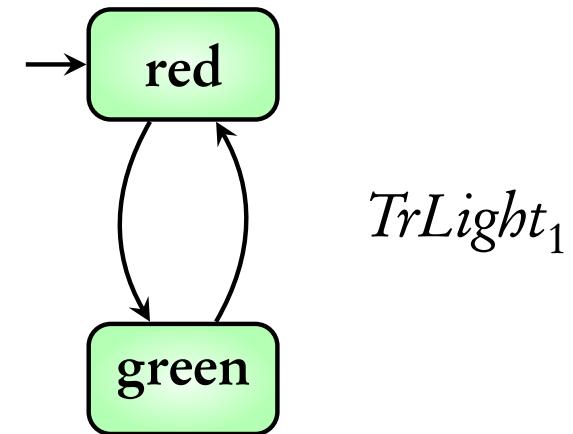
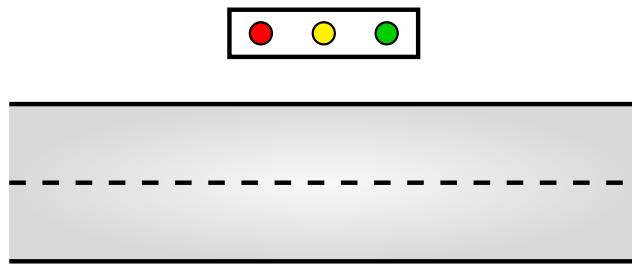
Concurrent systems

Independent

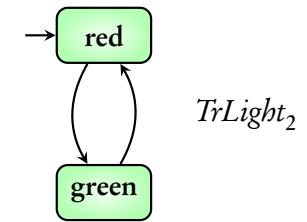
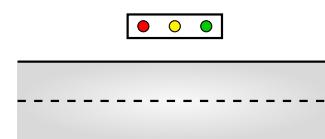
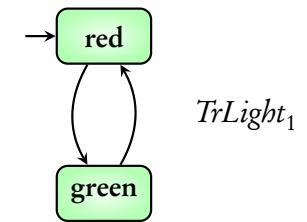
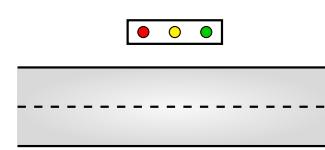
Shared variables

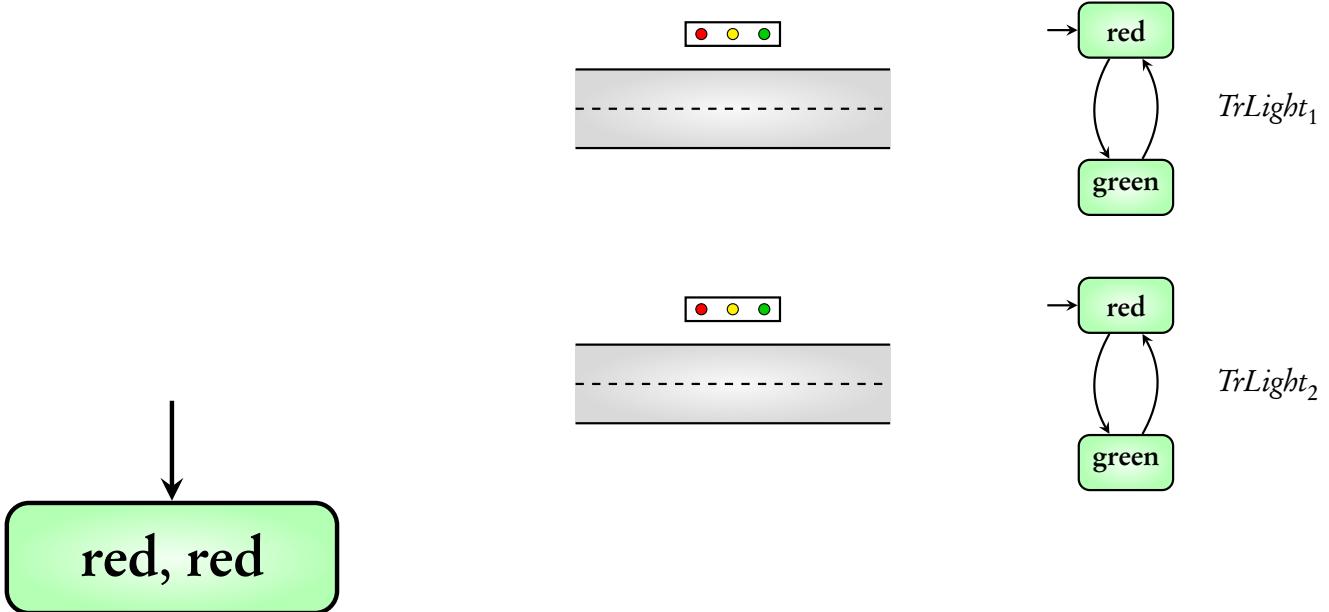
Shared actions

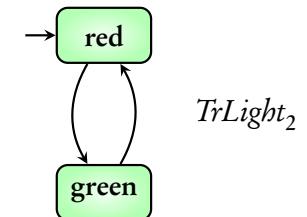
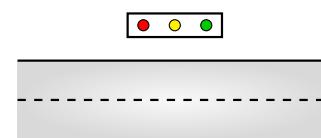
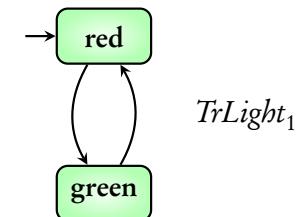
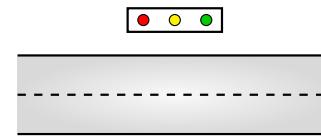
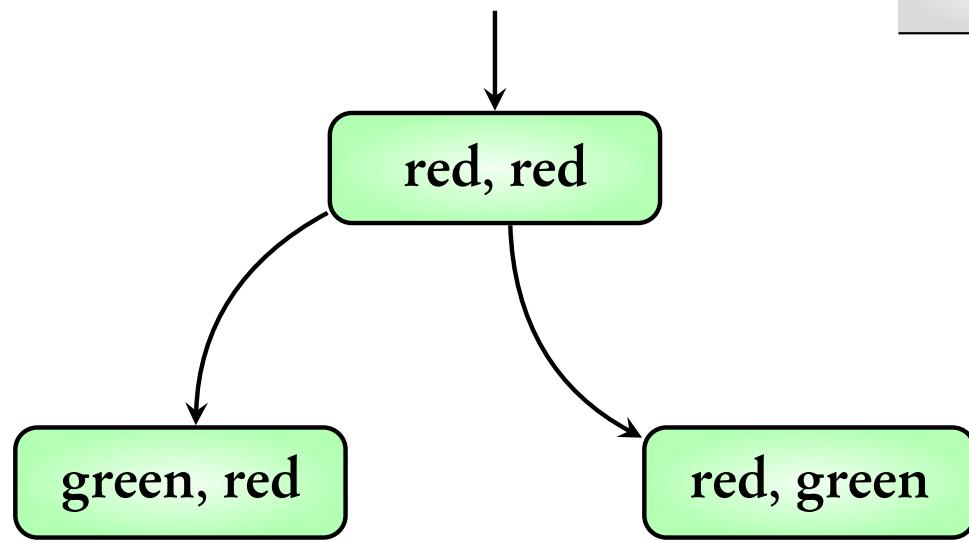


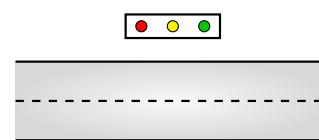
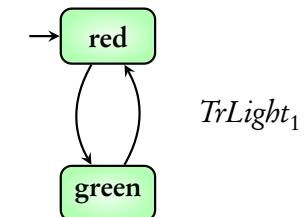
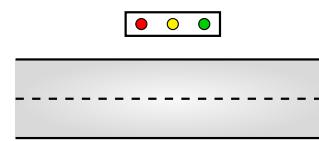
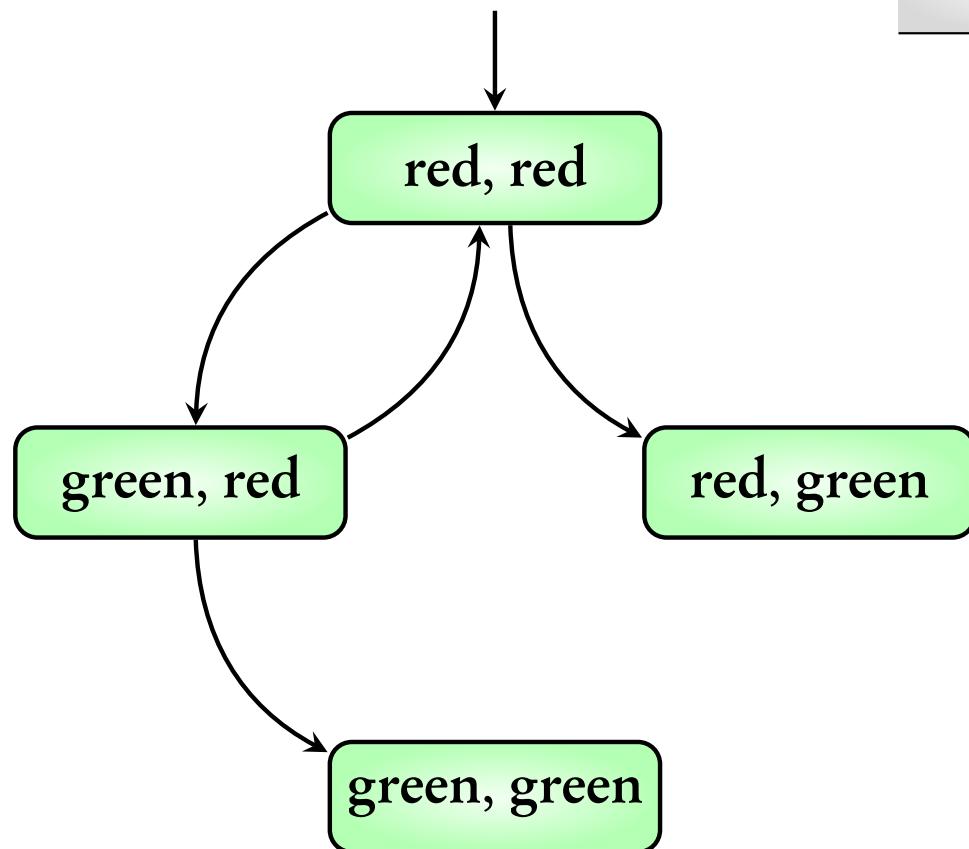


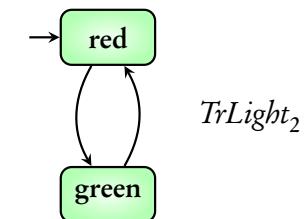
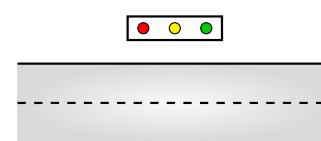
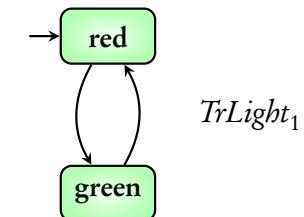
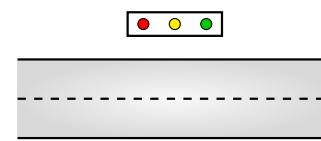
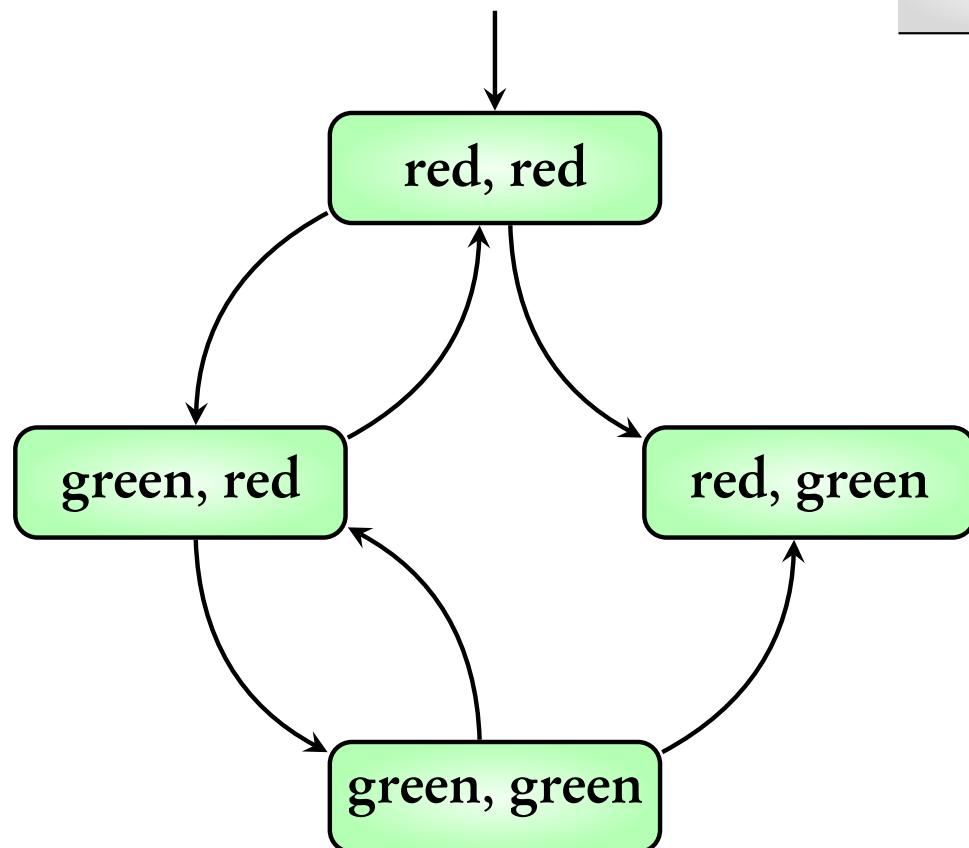
What is the transition system for the **joint behaviour**?

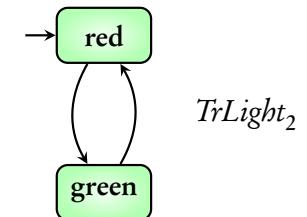
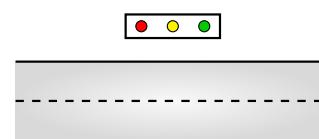
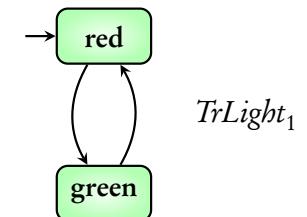
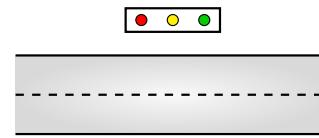
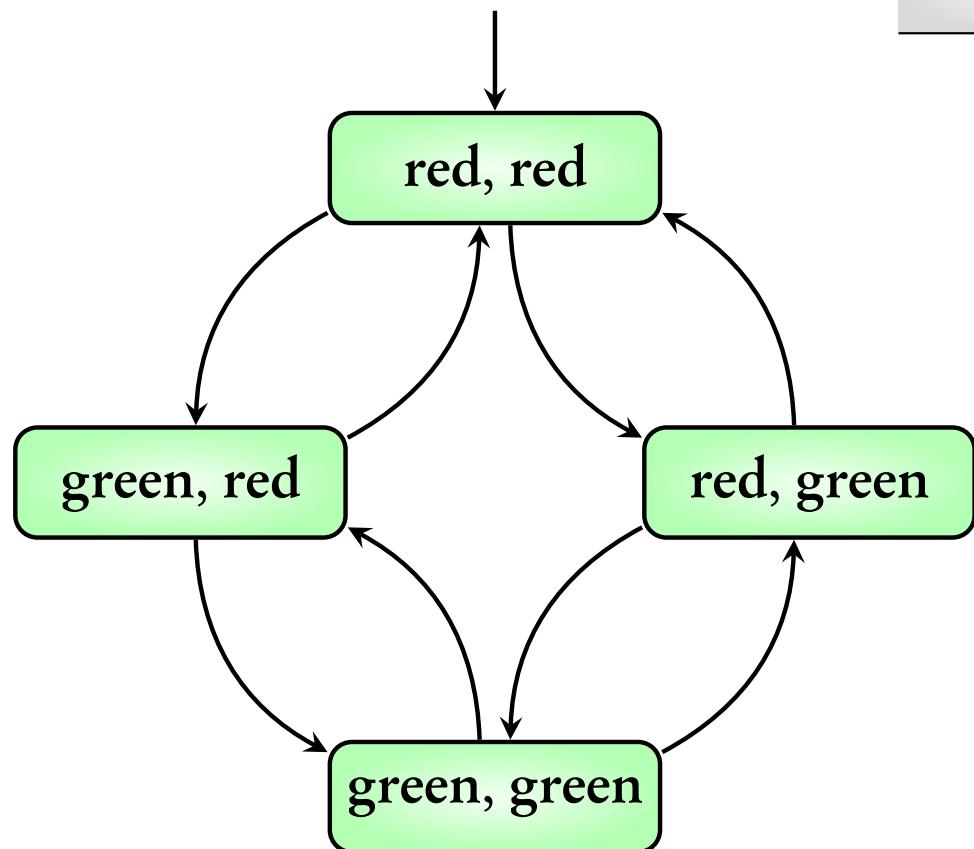


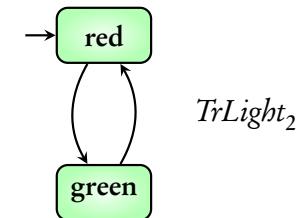
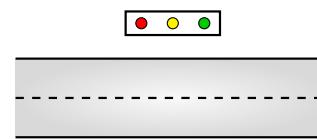
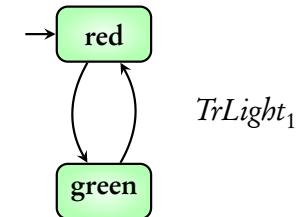
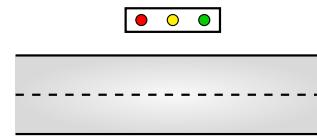
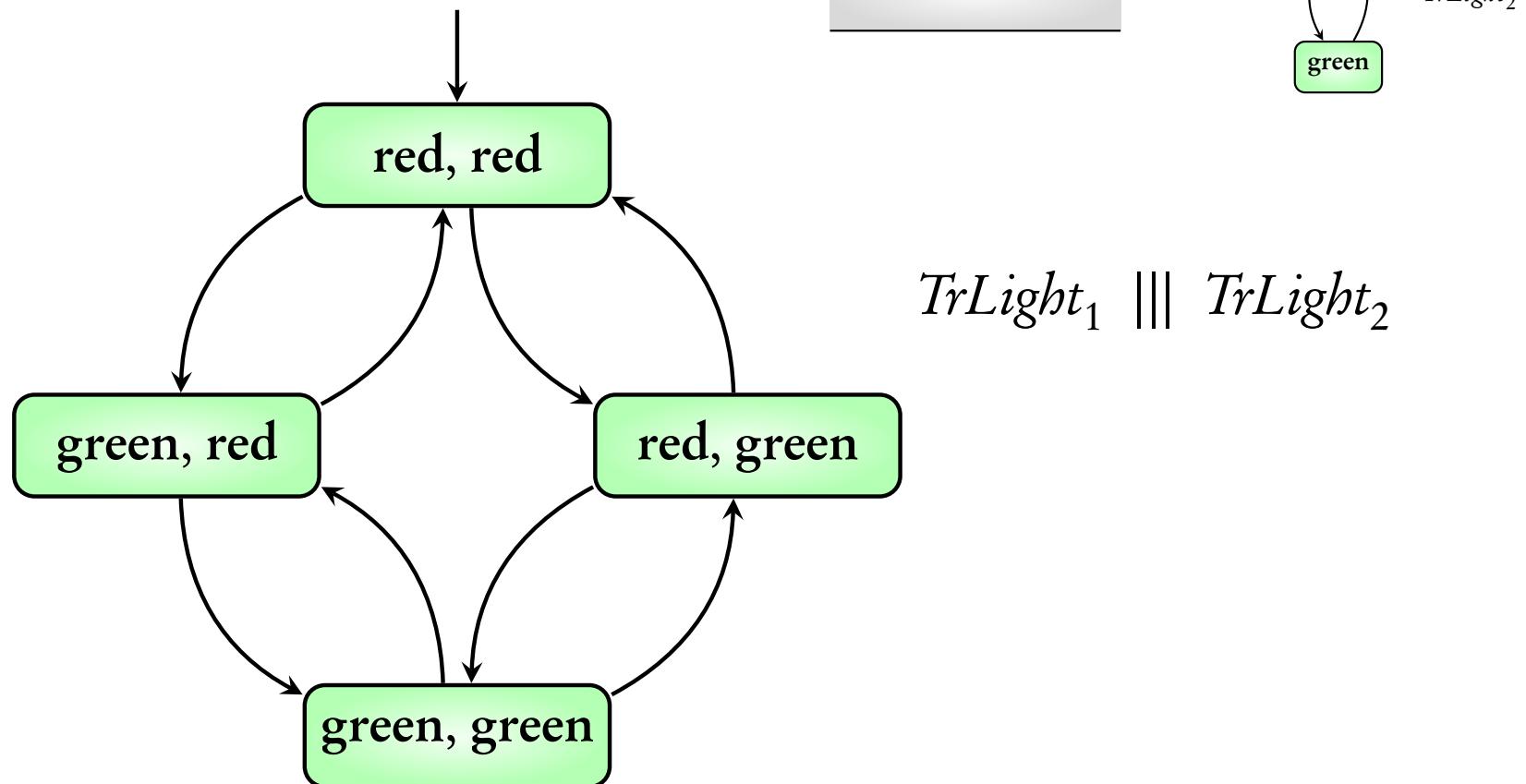




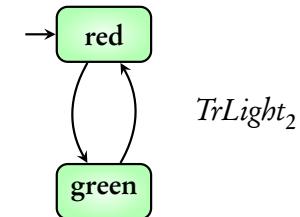
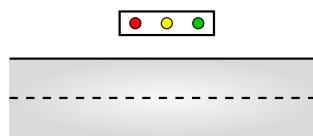
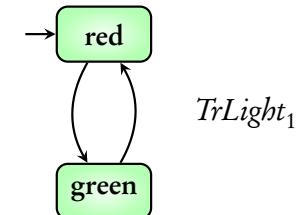
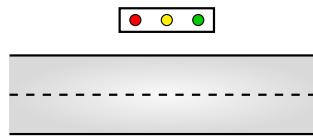
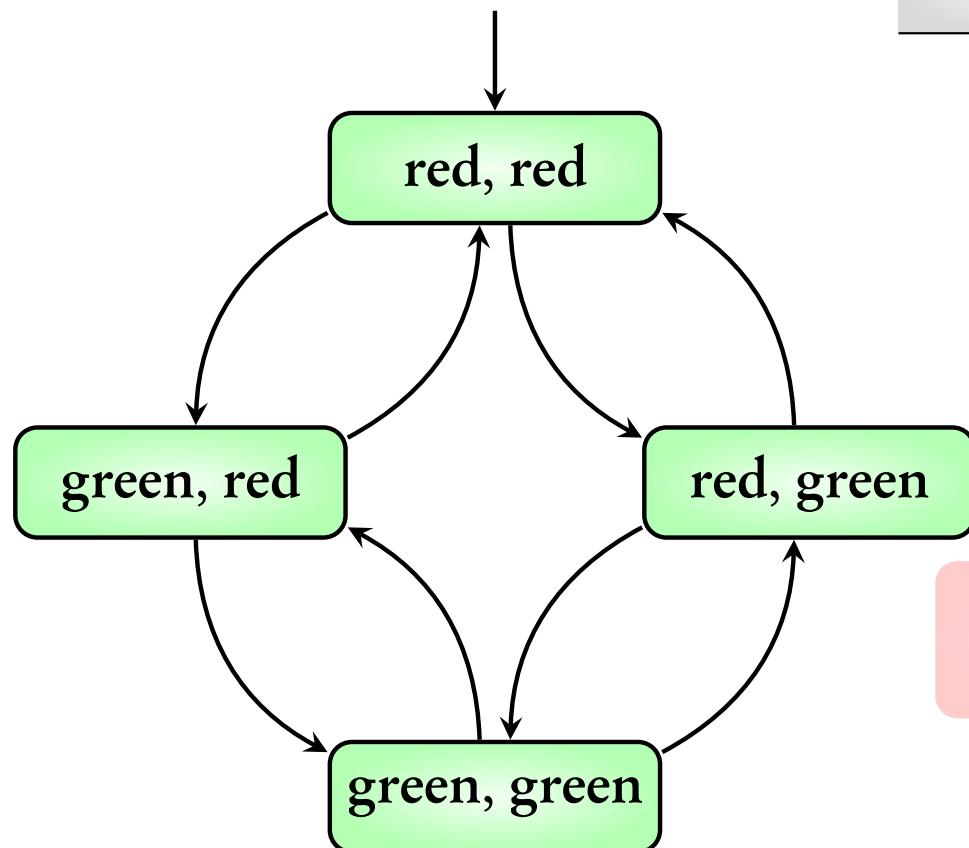






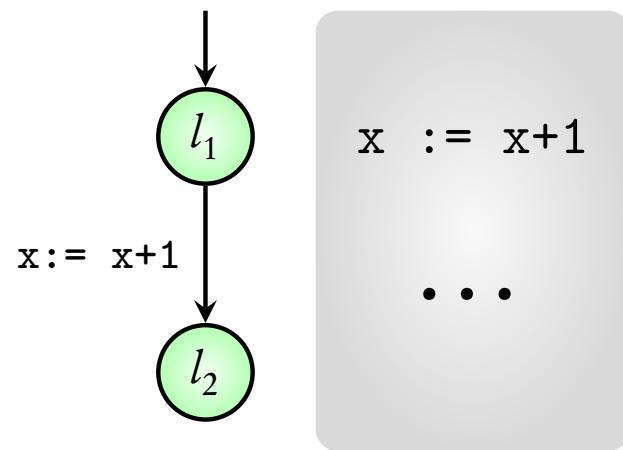


$TrLight_1 \parallel TrLight_2$

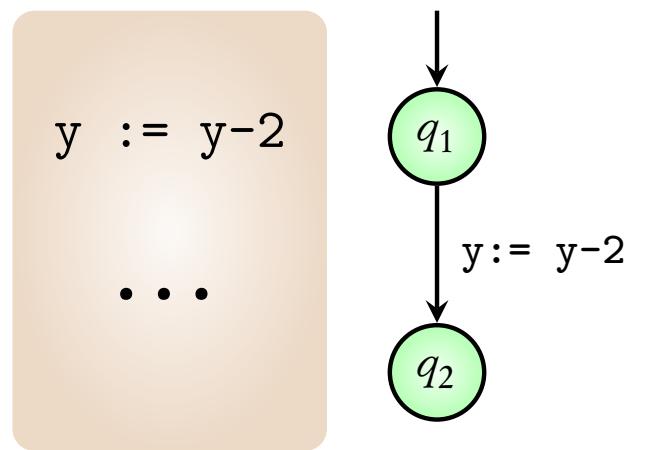


$TrLight_1 \parallel TrLight_2$

\parallel : Interleaving operator

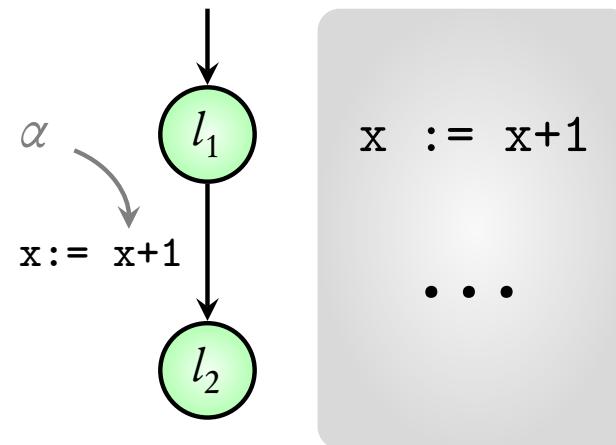


PG_1

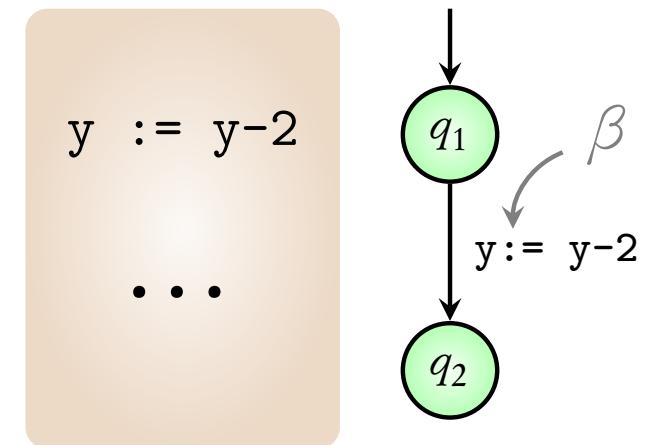


PG_2

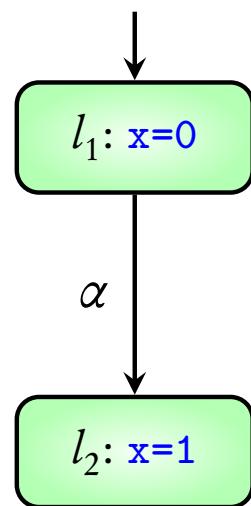
independent actions



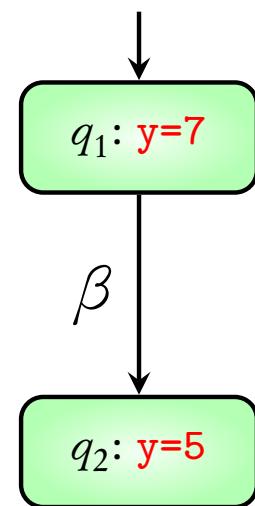
PG_1



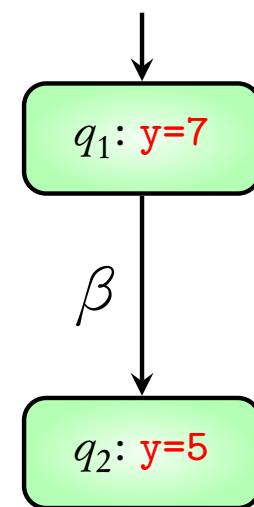
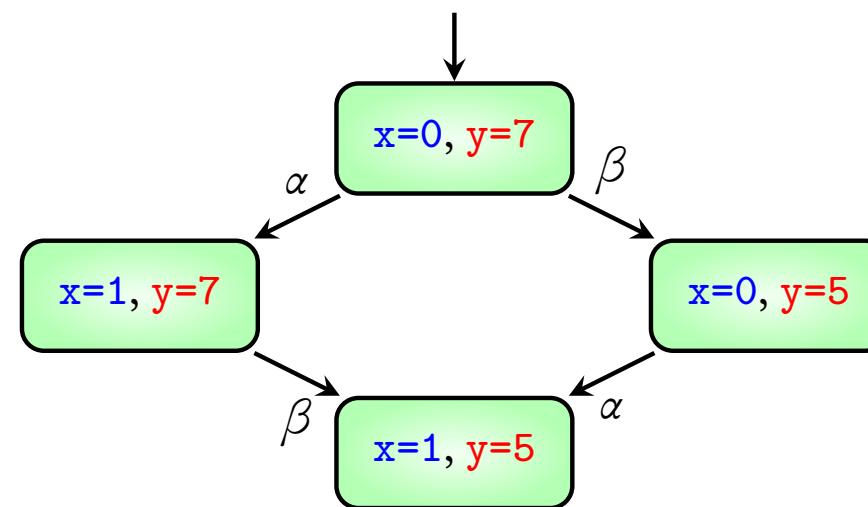
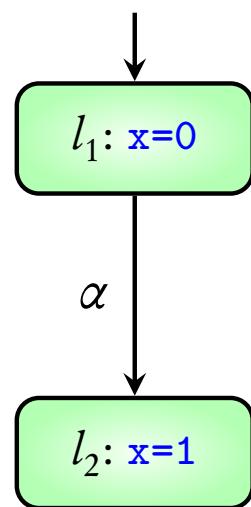
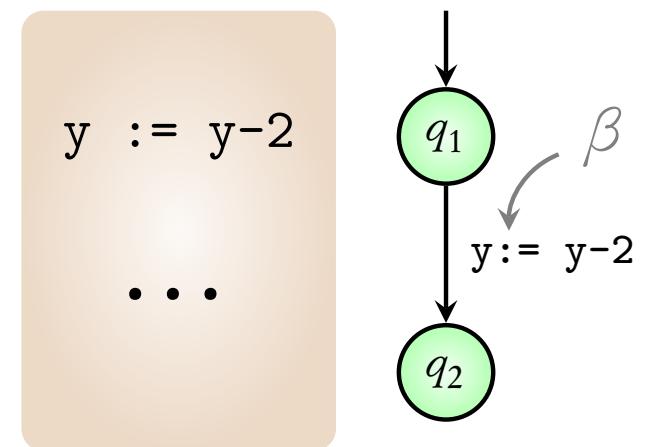
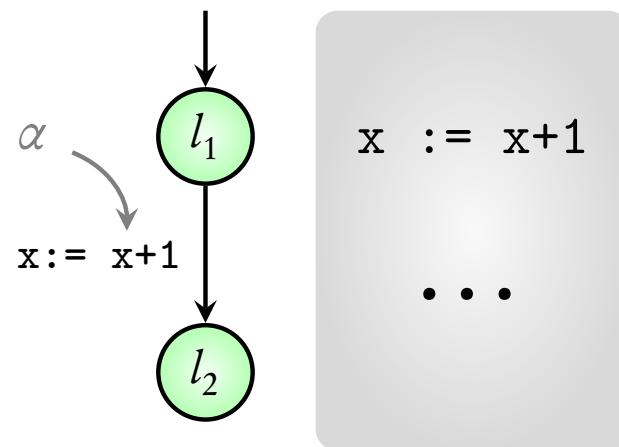
PG_2



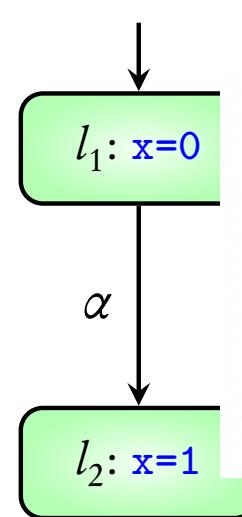
TS_1
(initially $x=0$)



TS_2
(initially $y=7$)



independent actions



TS_1
(initially $x=0$)

Definition 2.18. Interleaving of Transition Systems

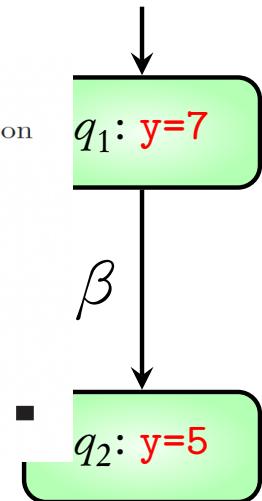
Let $TS_i = (S_i, Act_i, \rightarrow_i, I_i, AP_i, L_i)$ $i=1, 2$, be two transition systems. The transition system $TS_1 \parallel| TS_2$ is defined by:

$$TS_1 \parallel| TS_2 = (S_1 \times S_2, Act_1 \cup Act_2, \rightarrow, I_1 \times I_2, AP_1 \cup AP_2, L)$$

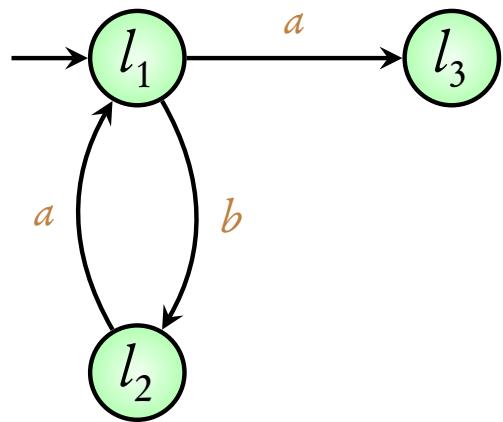
where the transition relation \rightarrow is defined by the following rules:

$$\frac{s_1 \xrightarrow{\alpha} s'_1}{\langle s_1, s_2 \rangle \xrightarrow{\alpha} \langle s'_1, s_2 \rangle} \quad \text{and} \quad \frac{s_2 \xrightarrow{\alpha} s'_2}{\langle s_1, s_2 \rangle \xrightarrow{\alpha} \langle s_1, s'_2 \rangle}$$

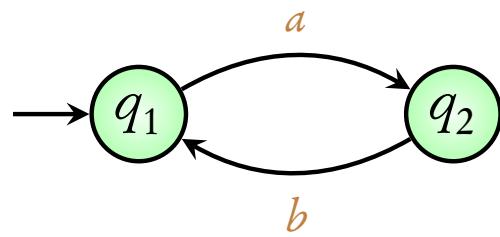
and the labeling function is defined by $L(\langle s_1, s_2 \rangle) = L(s_1) \cup L(s_2)$.

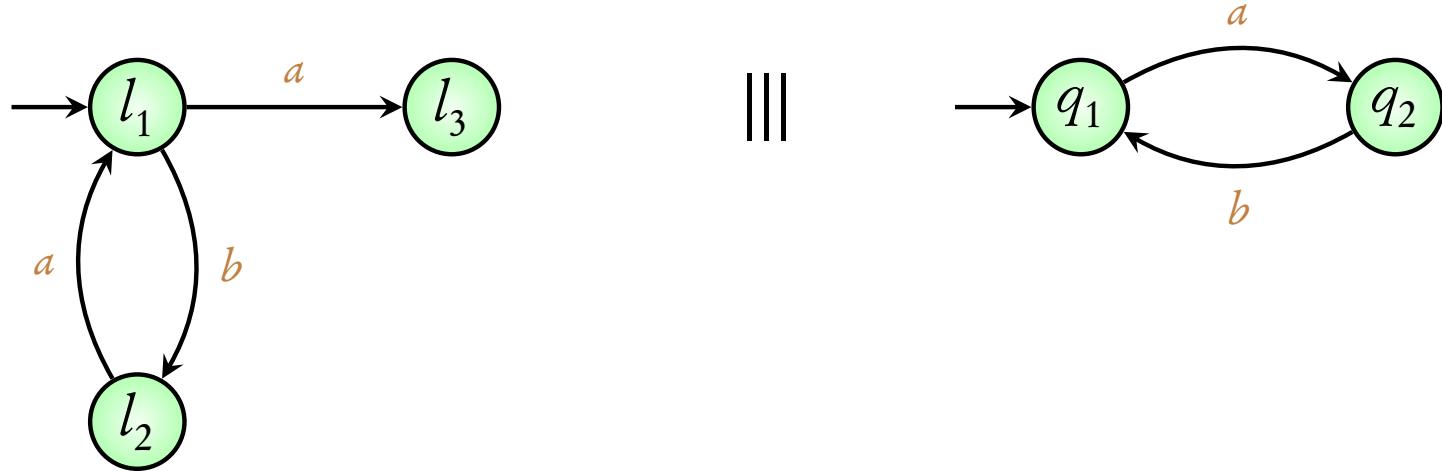


TS_2
(initially $y=7$)



|||





$\longrightarrow l_1, q_1$

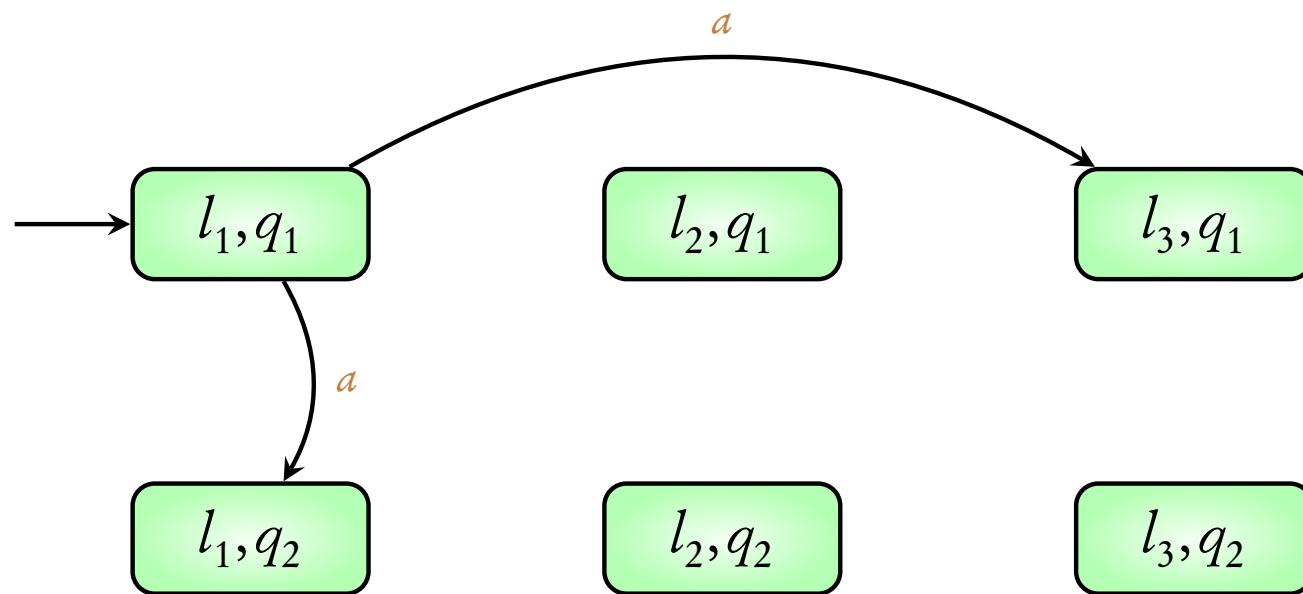
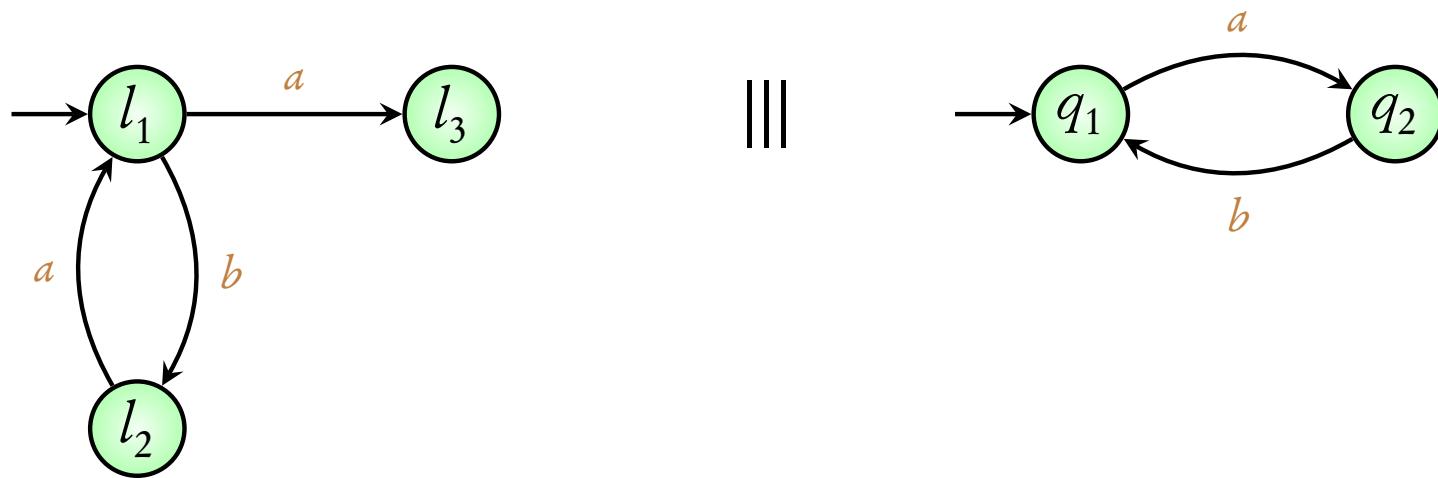
l_2, q_1

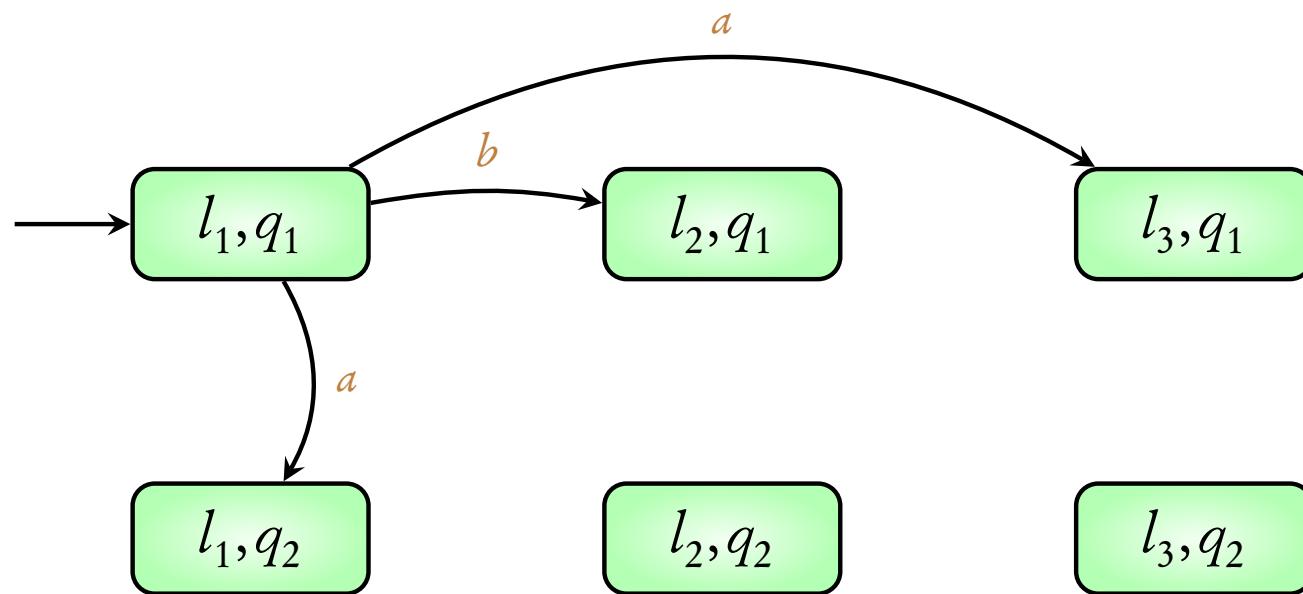
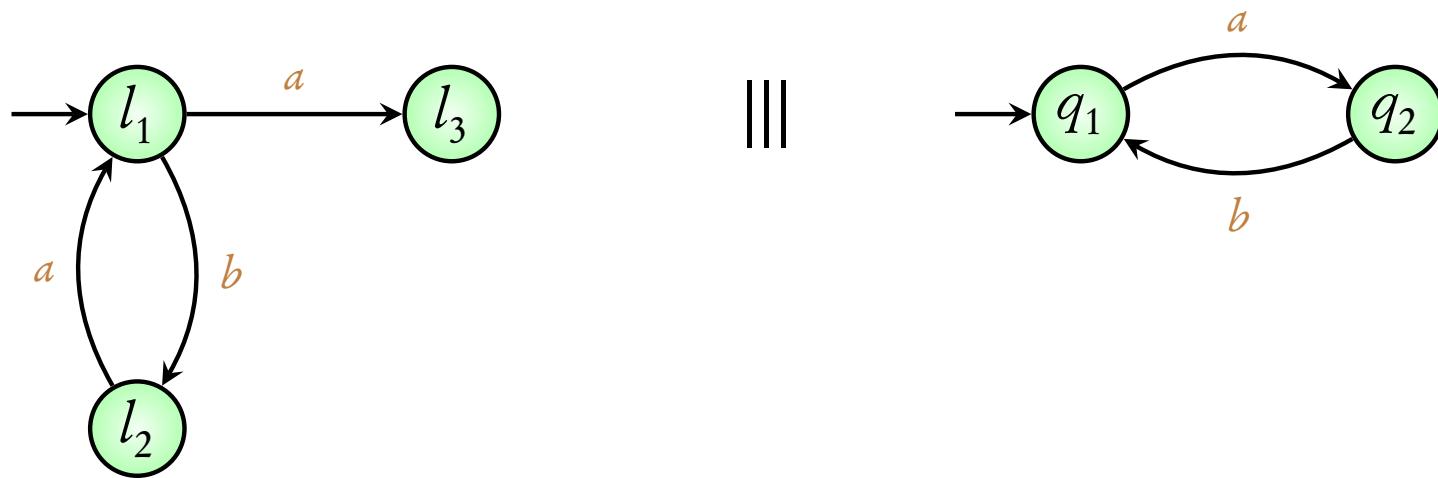
l_3, q_1

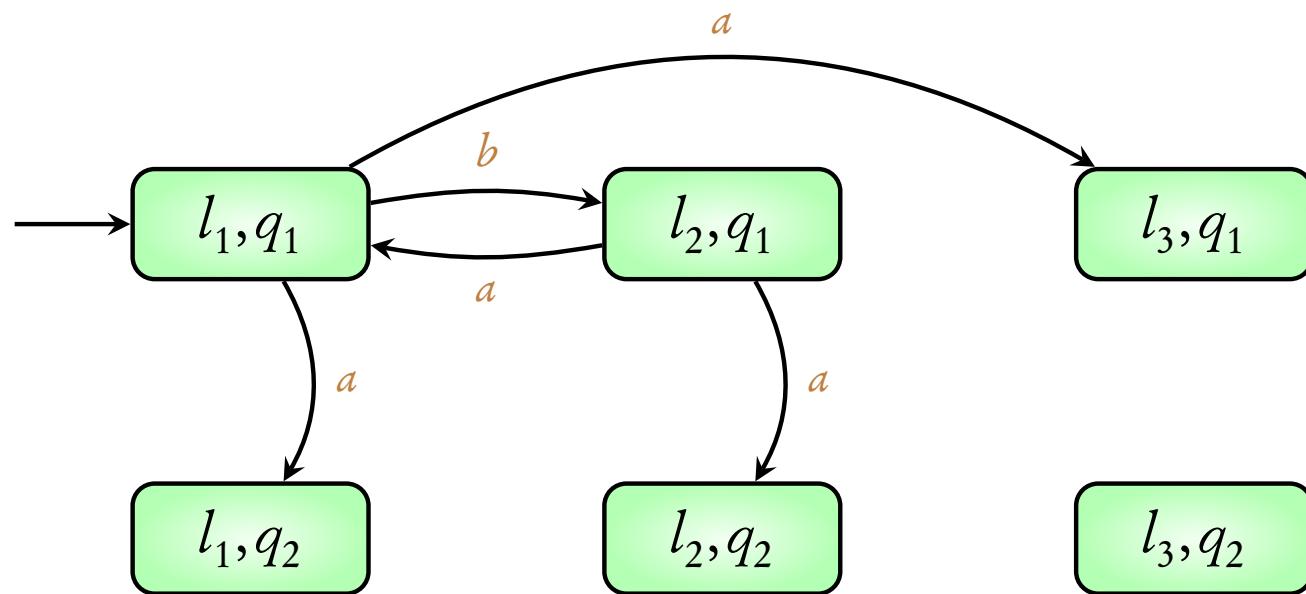
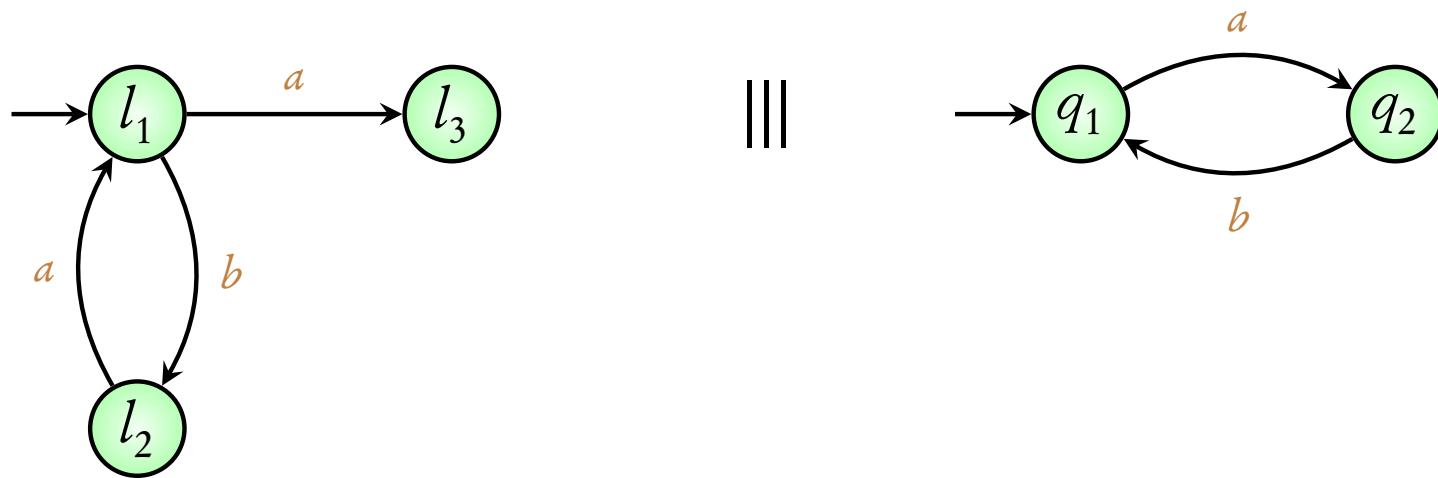
l_1, q_2

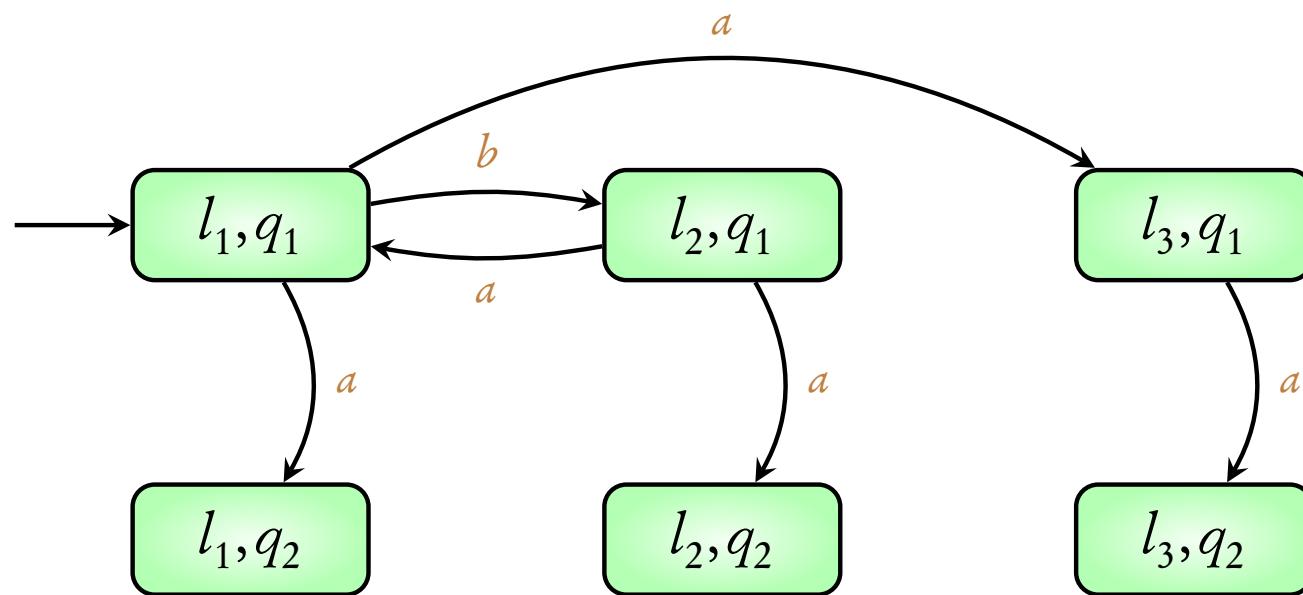
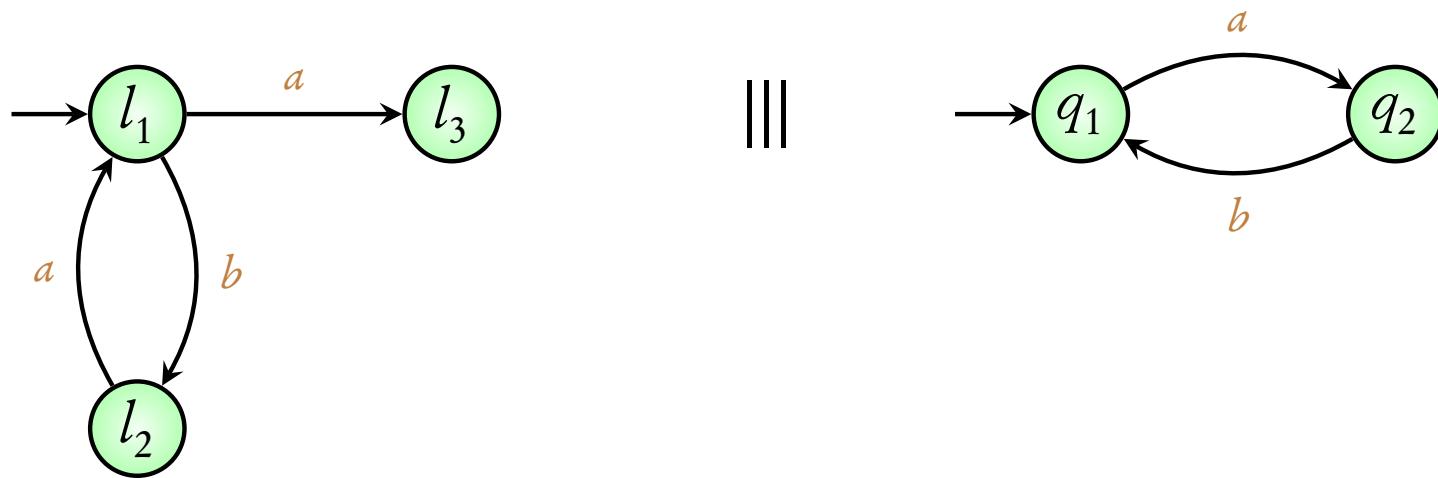
l_2, q_2

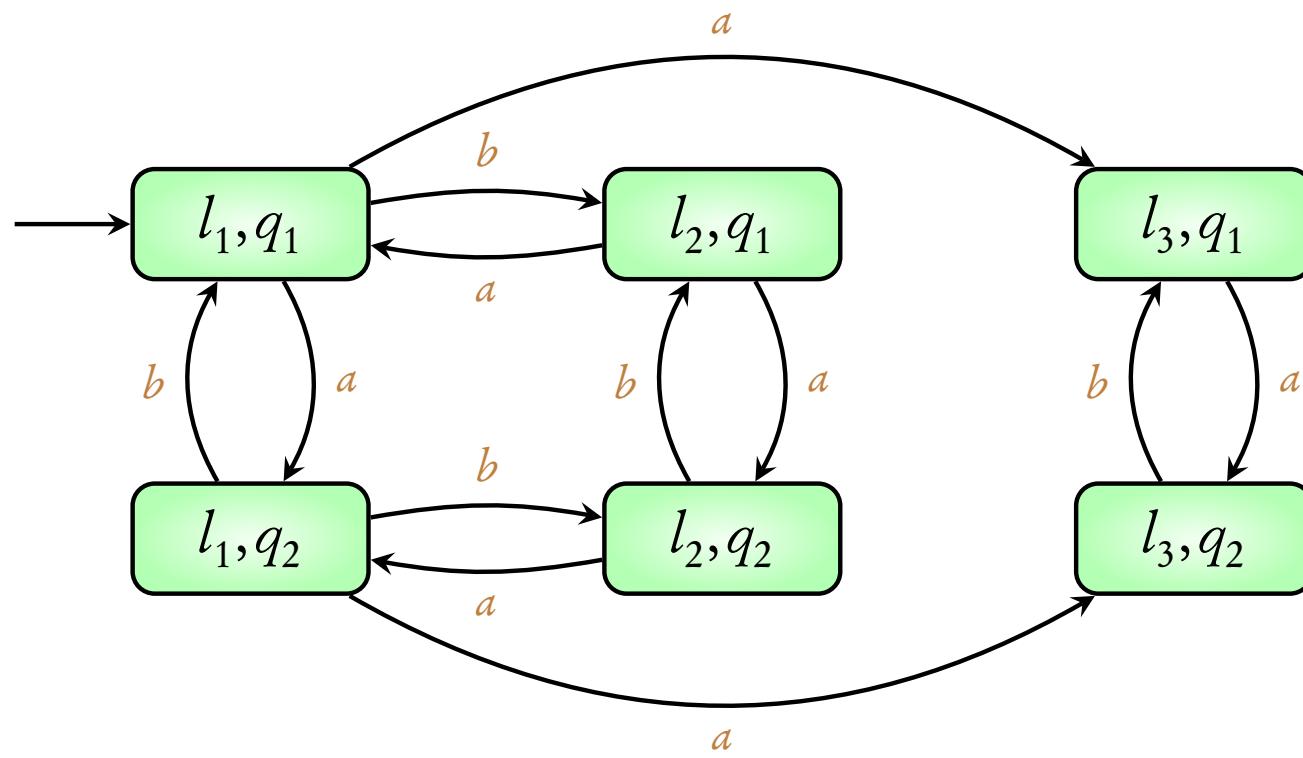
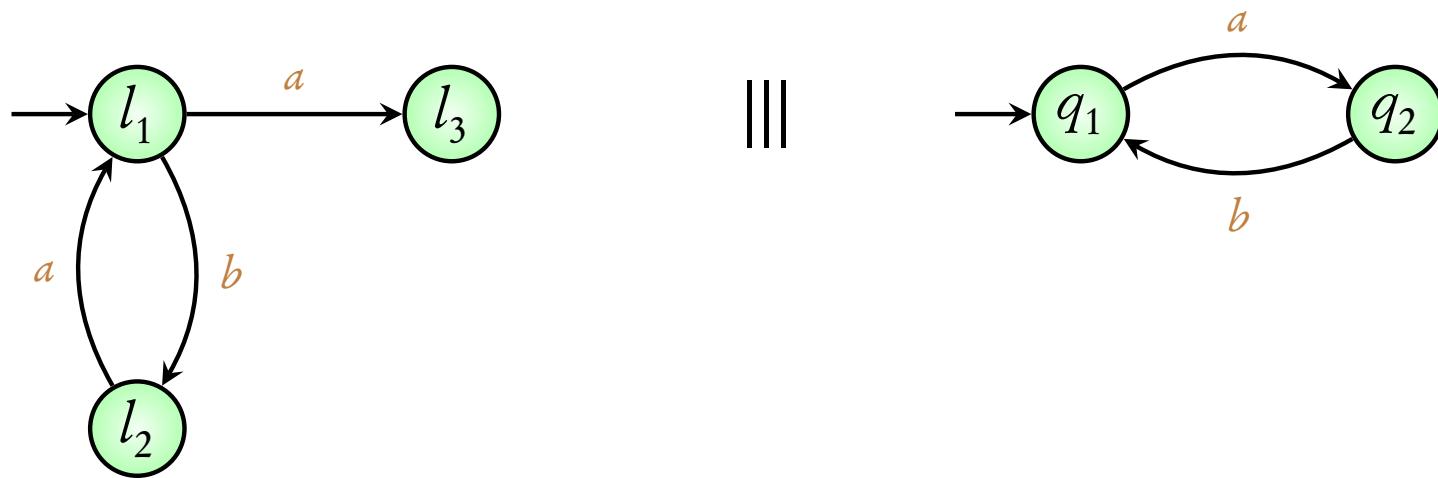
l_3, q_2











Multiple systems

TS₁ ||| TS₂ ||| ... ||| TS_n

Multiple systems

$\text{TS}_1 \text{ } ||| \text{ } \text{TS}_2 \text{ } ||| \dots \text{ } ||| \text{ } \text{TS}_n$

Exercise: Try out an example of interleaving **three** systems

Concurrent systems

Independent

Interleaving

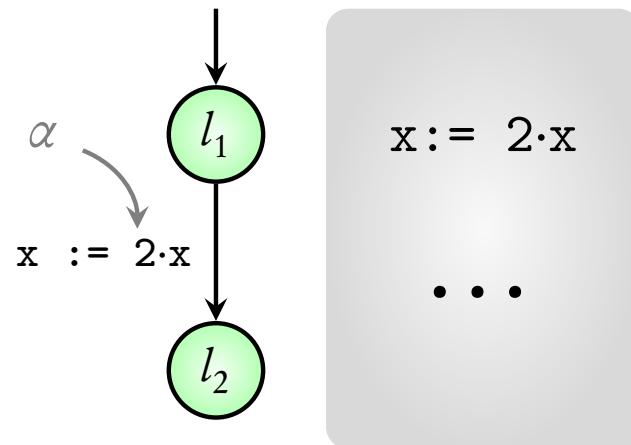
$\text{TS}_1 \parallel \text{TS}_2 \parallel \dots \parallel \text{TS}_n$

Modeling Asynchronous
Concurrency,
No communication

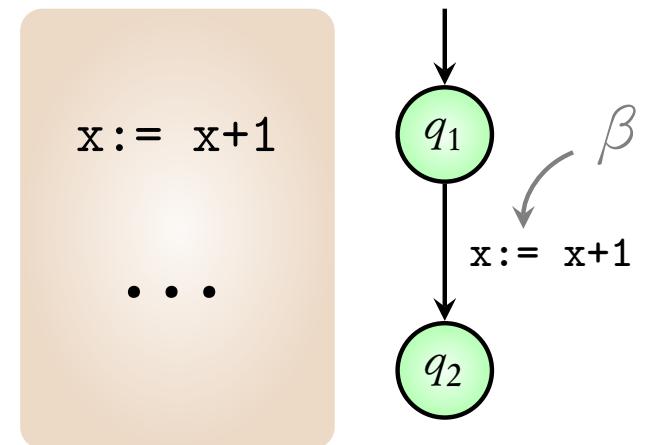
Shared variables

Shared actions

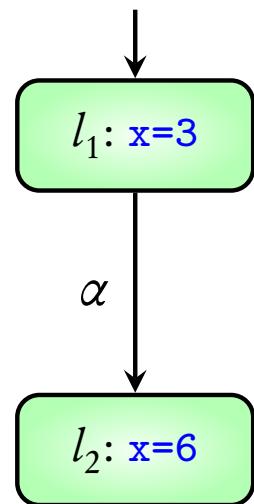




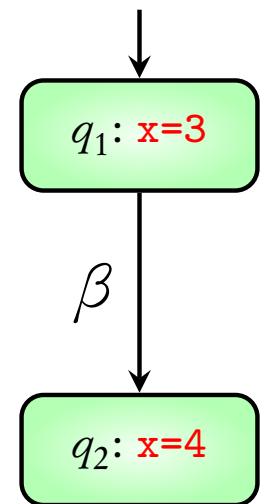
PG_1



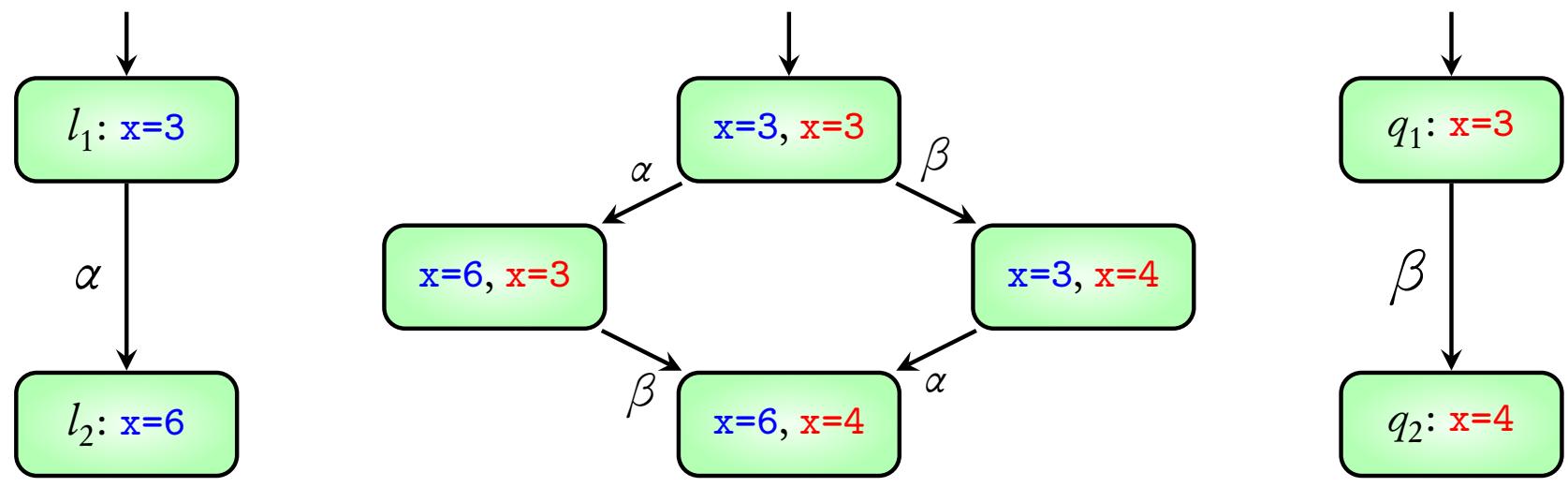
PG_2

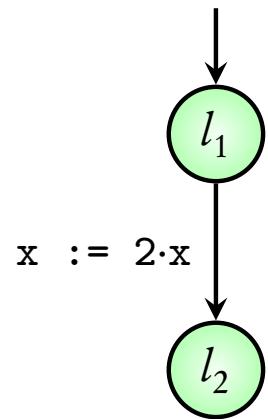


TS_1
(initially $x=3$)

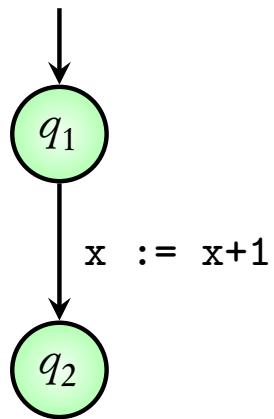


TS_2
(initially $x=3$)

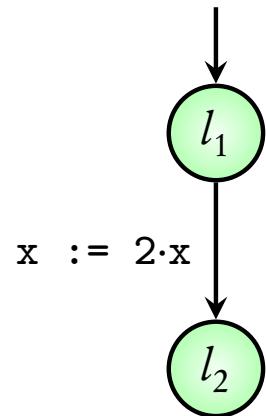




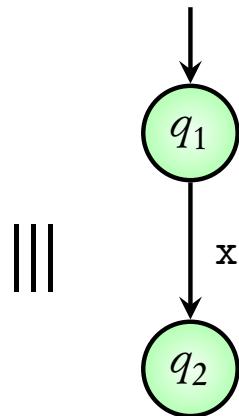
PG₁



PG₂



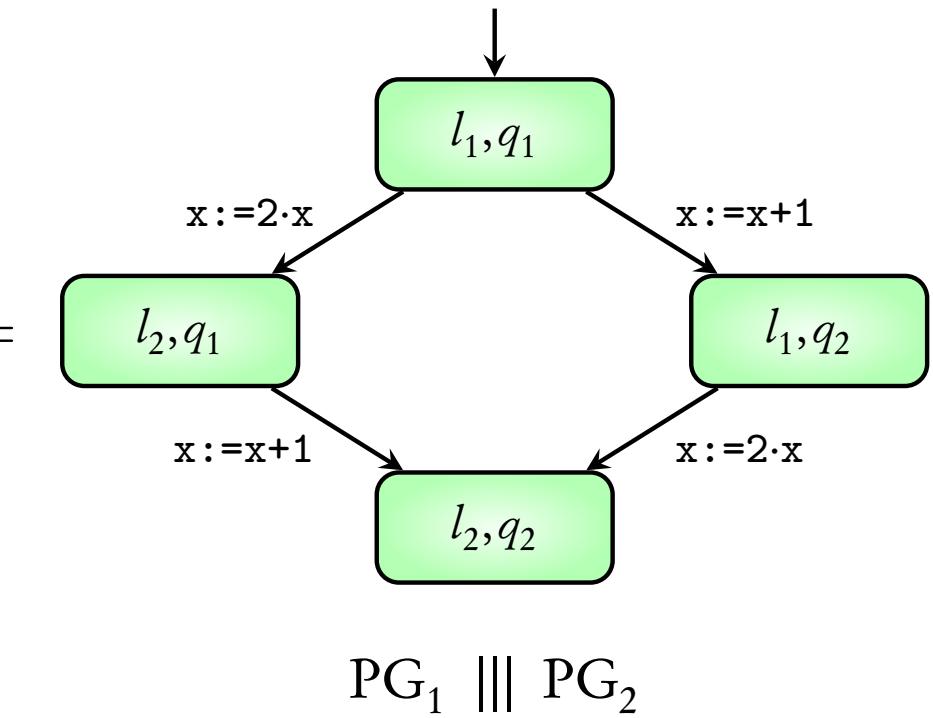
$x := 2 \cdot x$

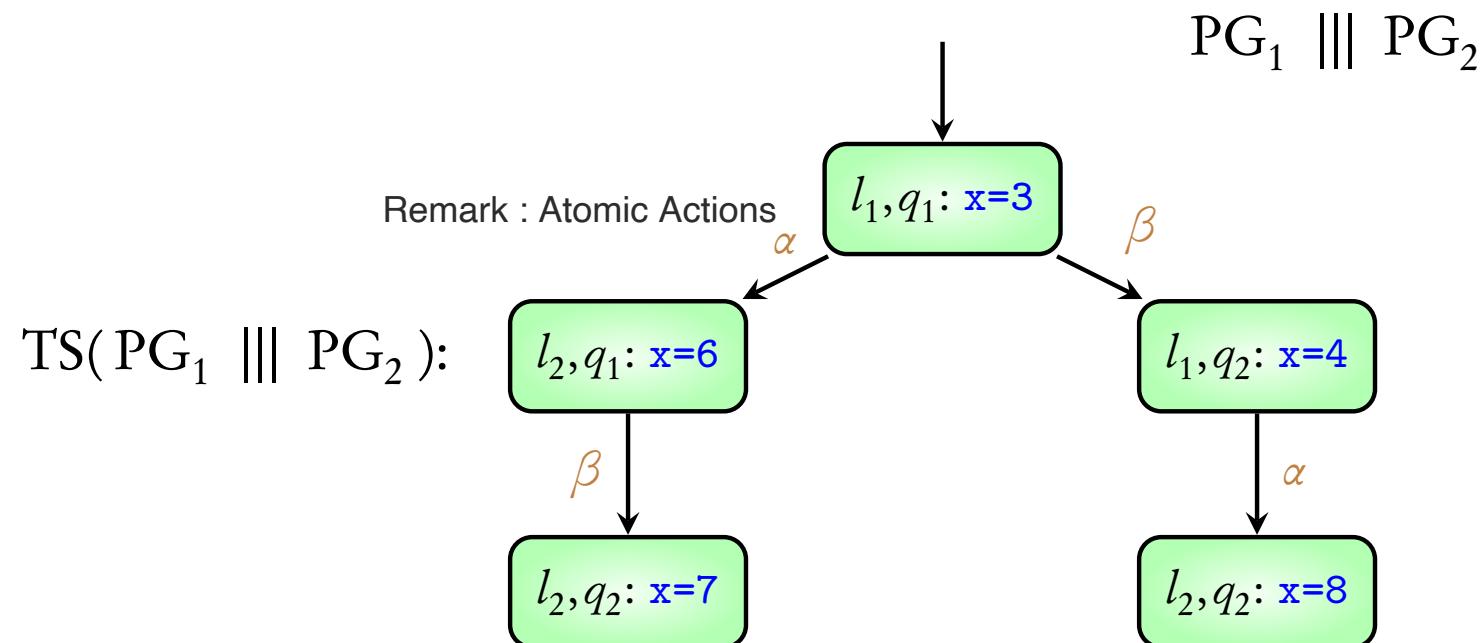
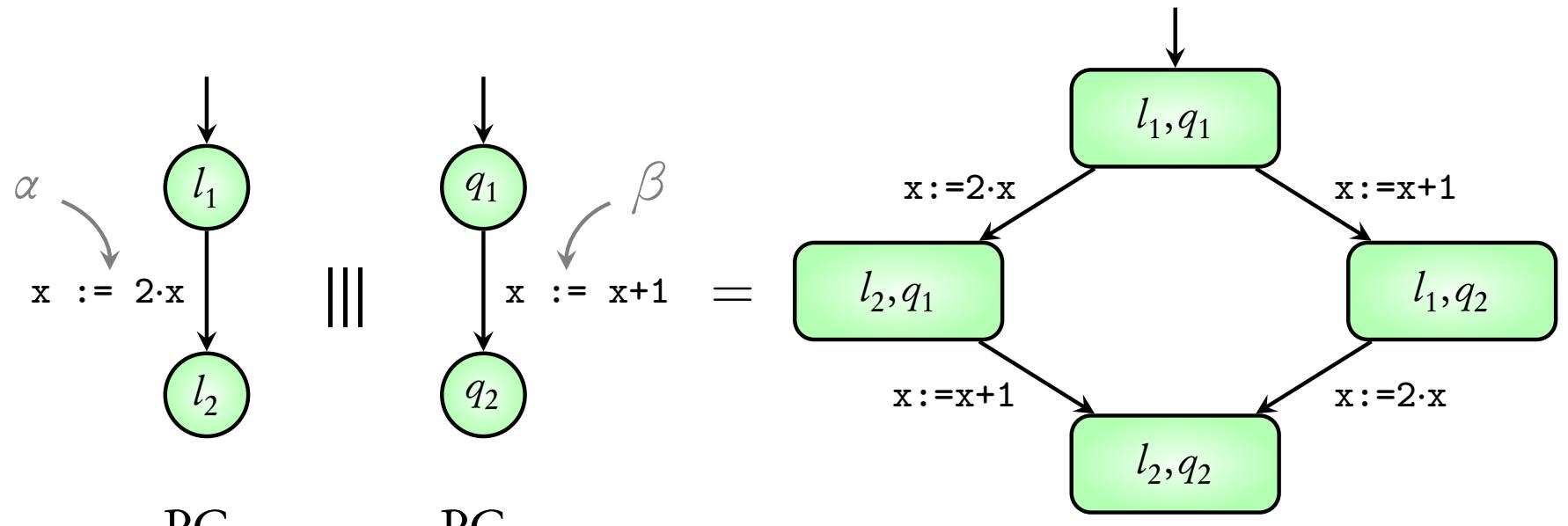


$x := x+1$

PG₂

|||





Concurrent systems

Independent

Interleaving

$\text{TS}_1 \parallel \text{TS}_2 \parallel \dots \parallel \text{TS}_n$

Shared variables

$\text{TS}(\text{PG}_1 \parallel \text{PG}_2 \parallel \dots \parallel \text{PG}_n)$

Shared actions

while $x < 200$

$x := x+1$

while $x > 0$

$x := x-1$

while $x = 200$

$x := 0$

```
while x < 200
```

```
x := x+1
```

```
while x>0
```

```
x := x-1
```

```
while x=200
```

```
x := 0
```

Is the value of x always between 0 and 200?

while $x < 200$

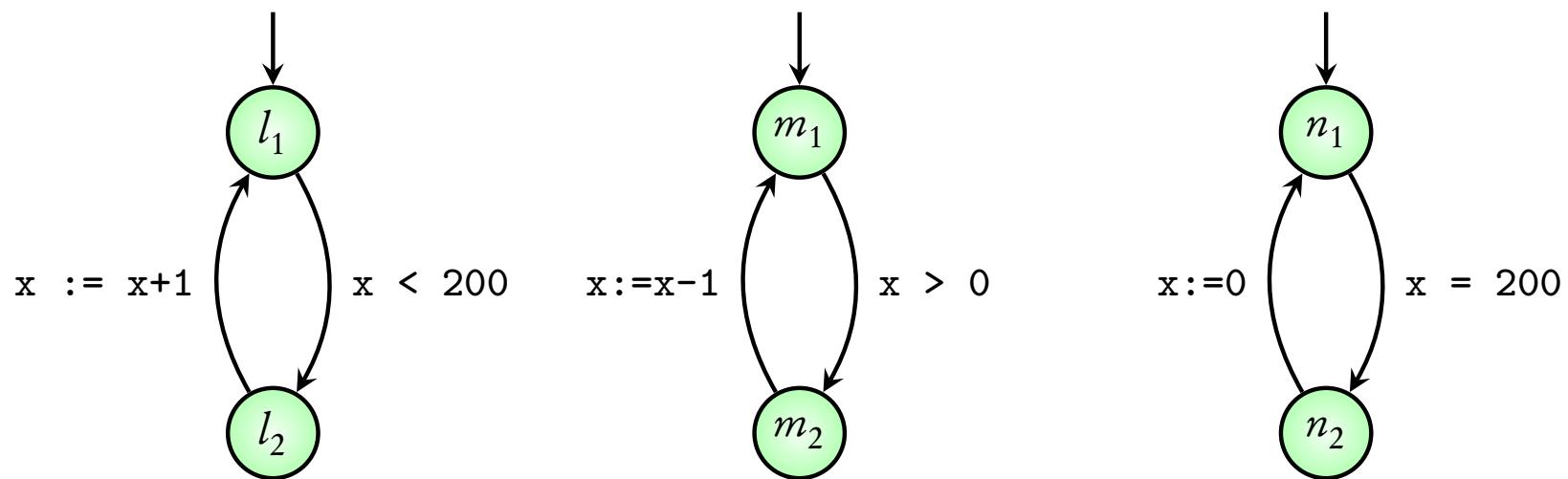
$x := x+1$

while $x > 0$

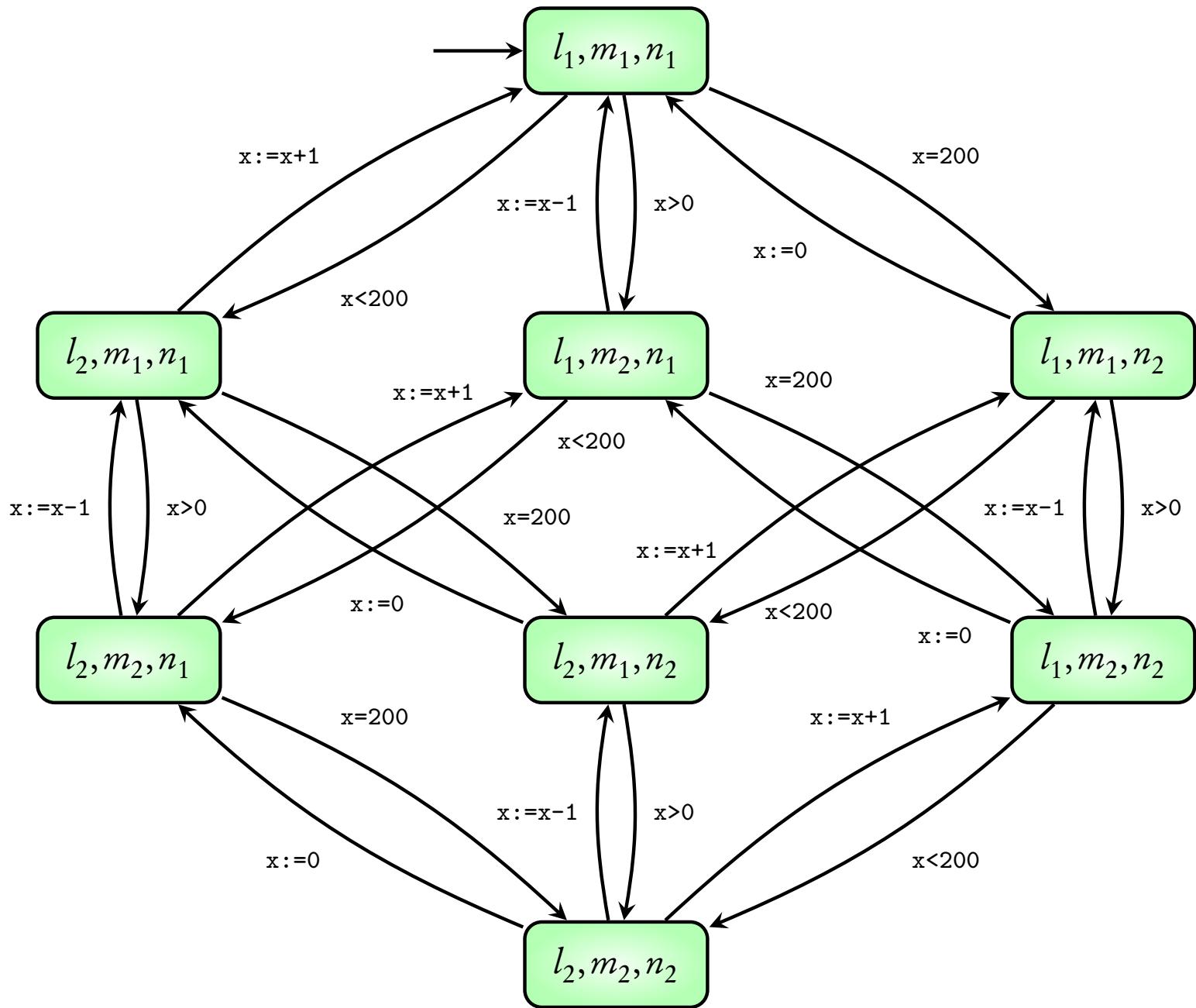
$x := x-1$

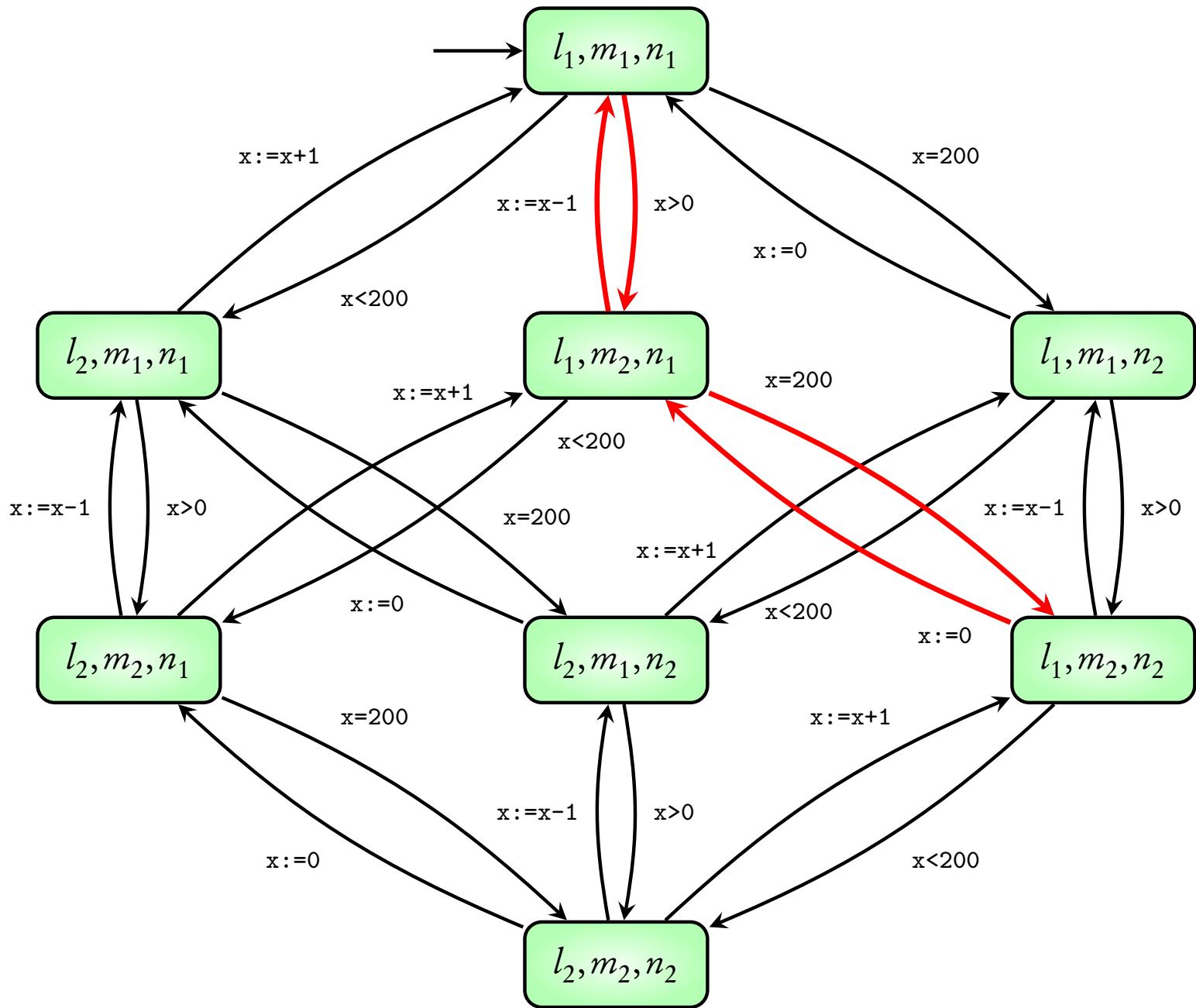
while $x = 200$

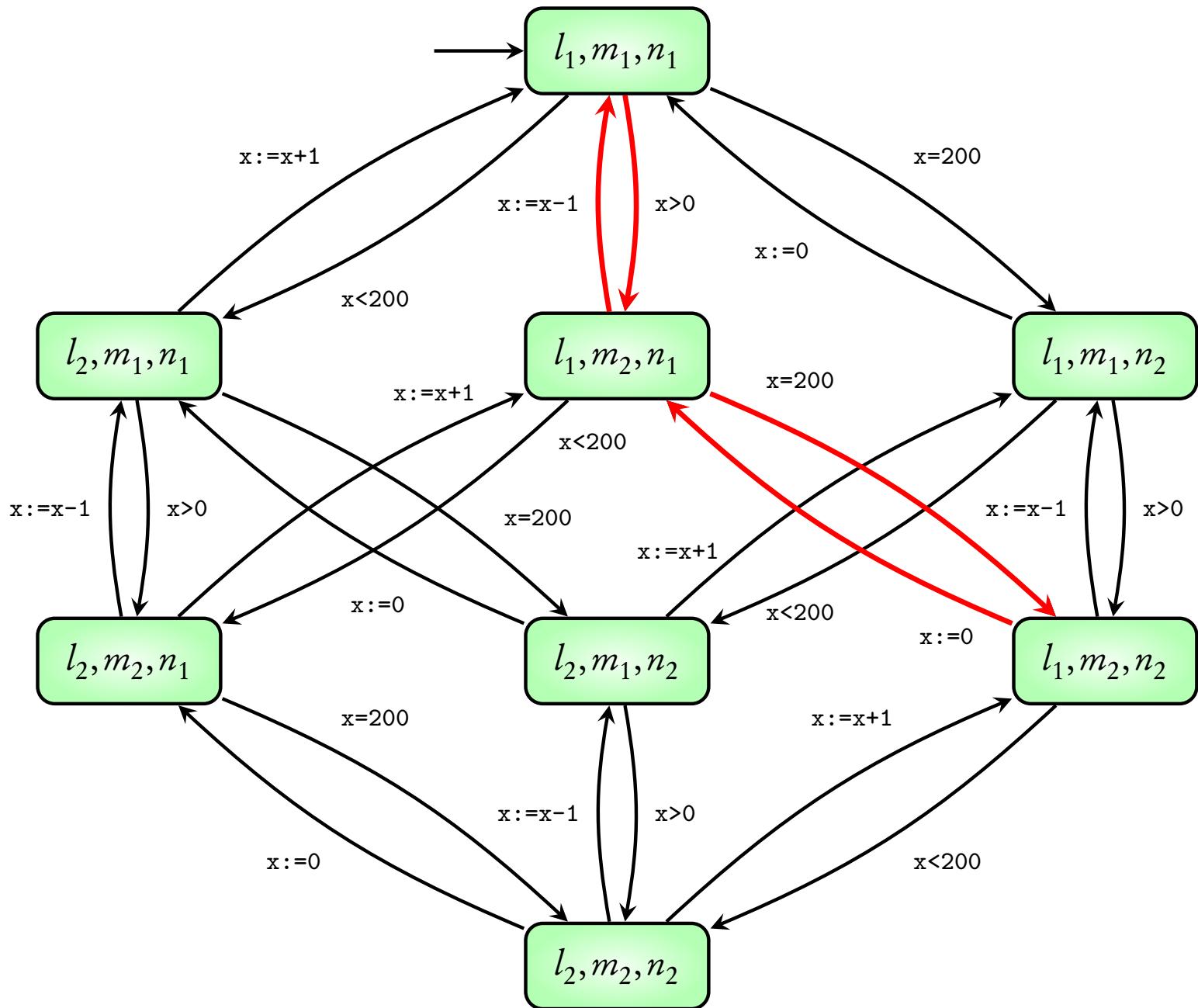
$x := 0$



Is the value of x always **between** 0 and 200?

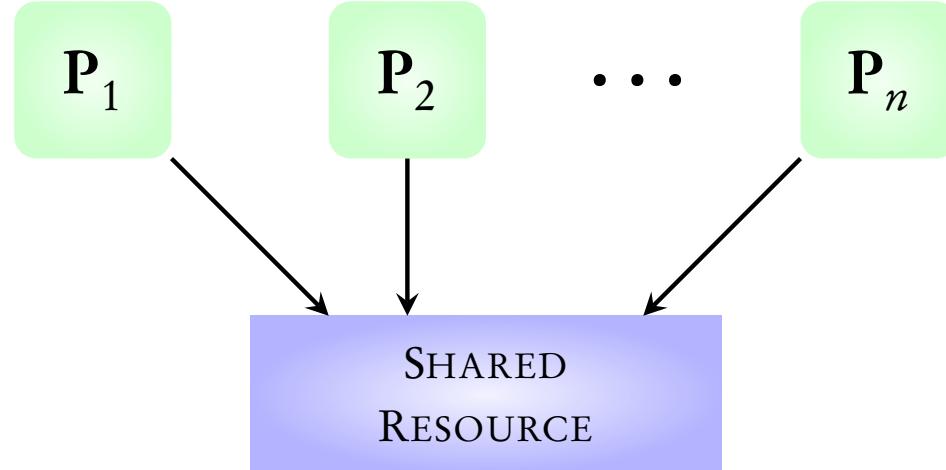






Is the value of x always between 0 and 200? **No**

Coming next: Mutual exclusion



(variable, printer, ...)

Mutual Exclusion: No two processes can access the resource simultaneously

Goal: Modeling the **protocols** used for mutual exclusion

P_1

loop forever

: *non-critical actions*

:

request

critical section

release

: *non-critical actions*

:

end loop

P_2

loop forever

: *non-critical actions*

:

request

critical section

release

: *non-critical actions*

:

end loop

P_1

loop forever

: *non-critical actions*

:

request

critical section

release

:

non-critical actions

end loop

P_2

loop forever

: *non-critical actions*

:

request

critical section

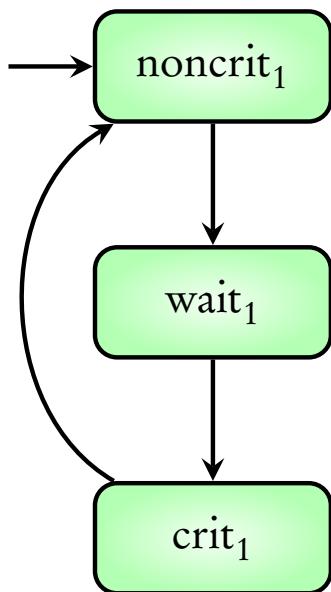
release

:

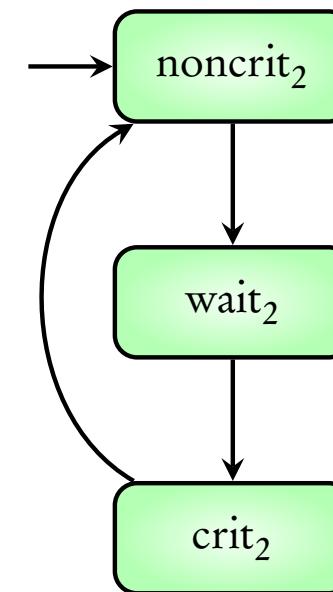
non-critical actions

end loop

PG_1



PG_2



P_1

loop forever

: *non-critical actions*

:

{ if $y > 0$: $y := y - 1$ } *request*

critical section

$y := y + 1$ *release*

: *non-critical actions*

end loop

P_2

loop forever

: *non-critical actions*

:

{ if $y > 0$: $y := y - 1$ } *request*

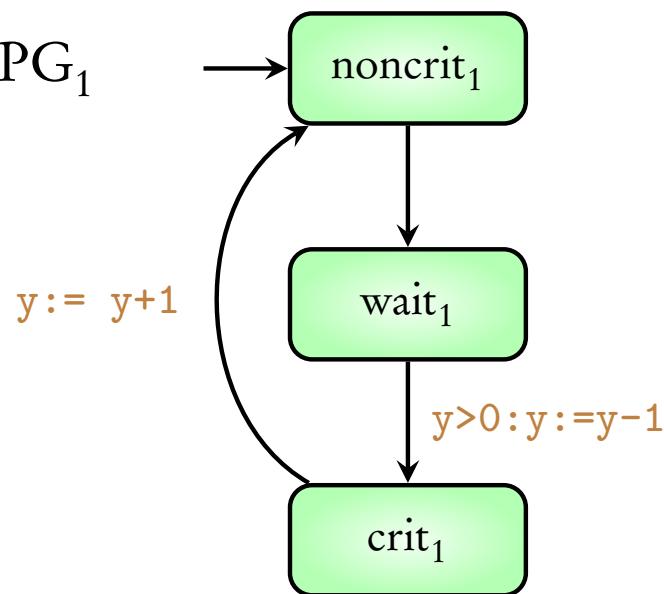
critical section

$y := y + 1$ *release*

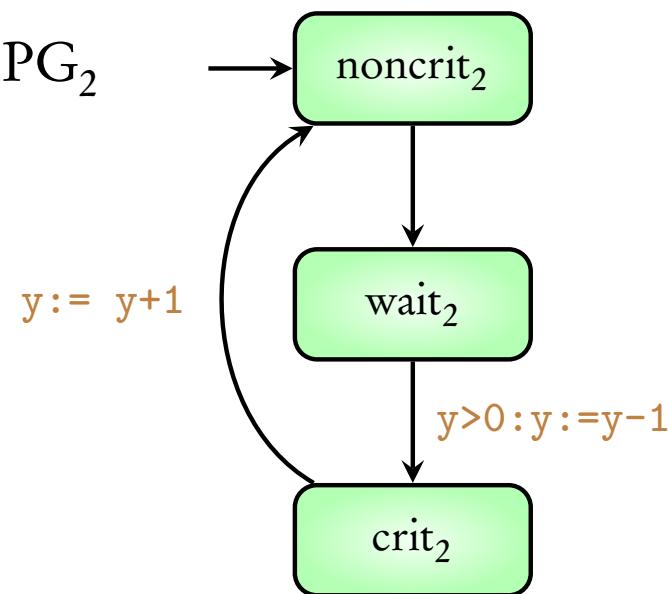
: *non-critical actions*

end loop

PG_1



PG_2



P_1

loop forever

: *non-critical actions*

:

$\langle \text{if } y > 0: \quad y := y - 1 \rangle \quad \text{*request*}$

critical section

$y := y + 1 \quad \text{*release*}$

: *non-critical actions*

end loop

P_2

loop forever

: *non-critical actions*

:

$\langle \text{if } y > 0: \quad y := y - 1 \rangle \quad \text{*request*}$

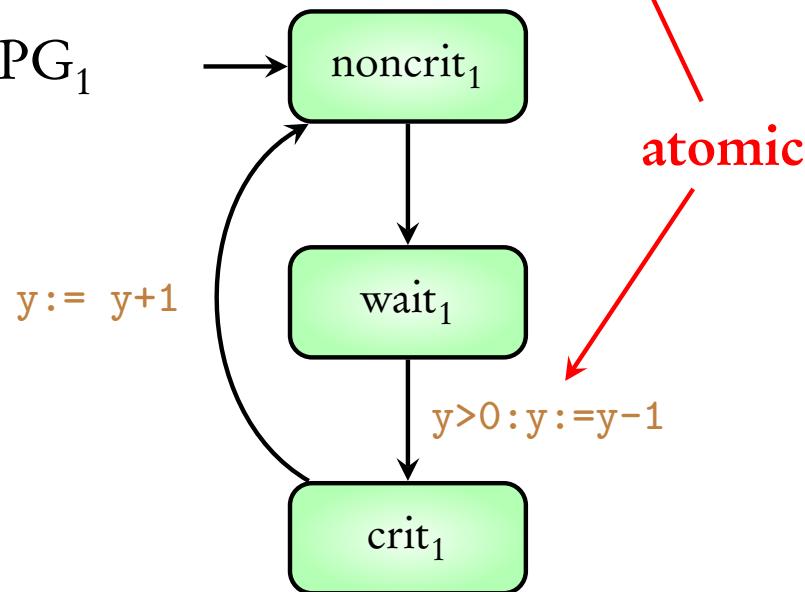
critical section

$y := y + 1 \quad \text{*release*}$

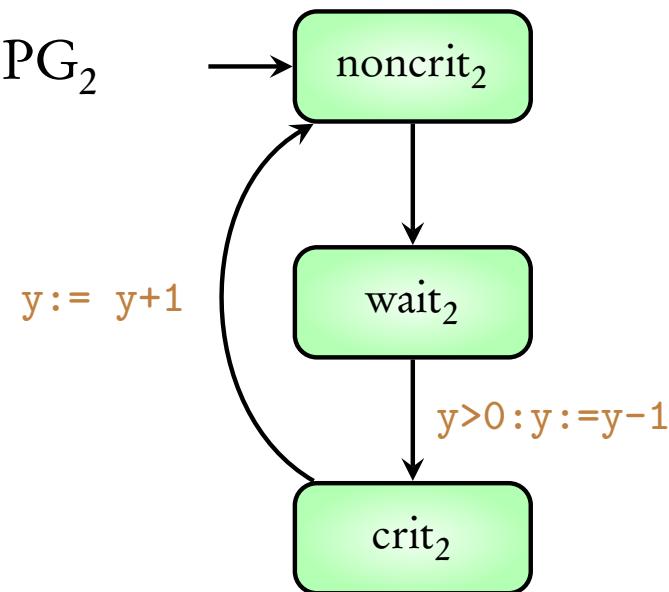
: *non-critical actions*

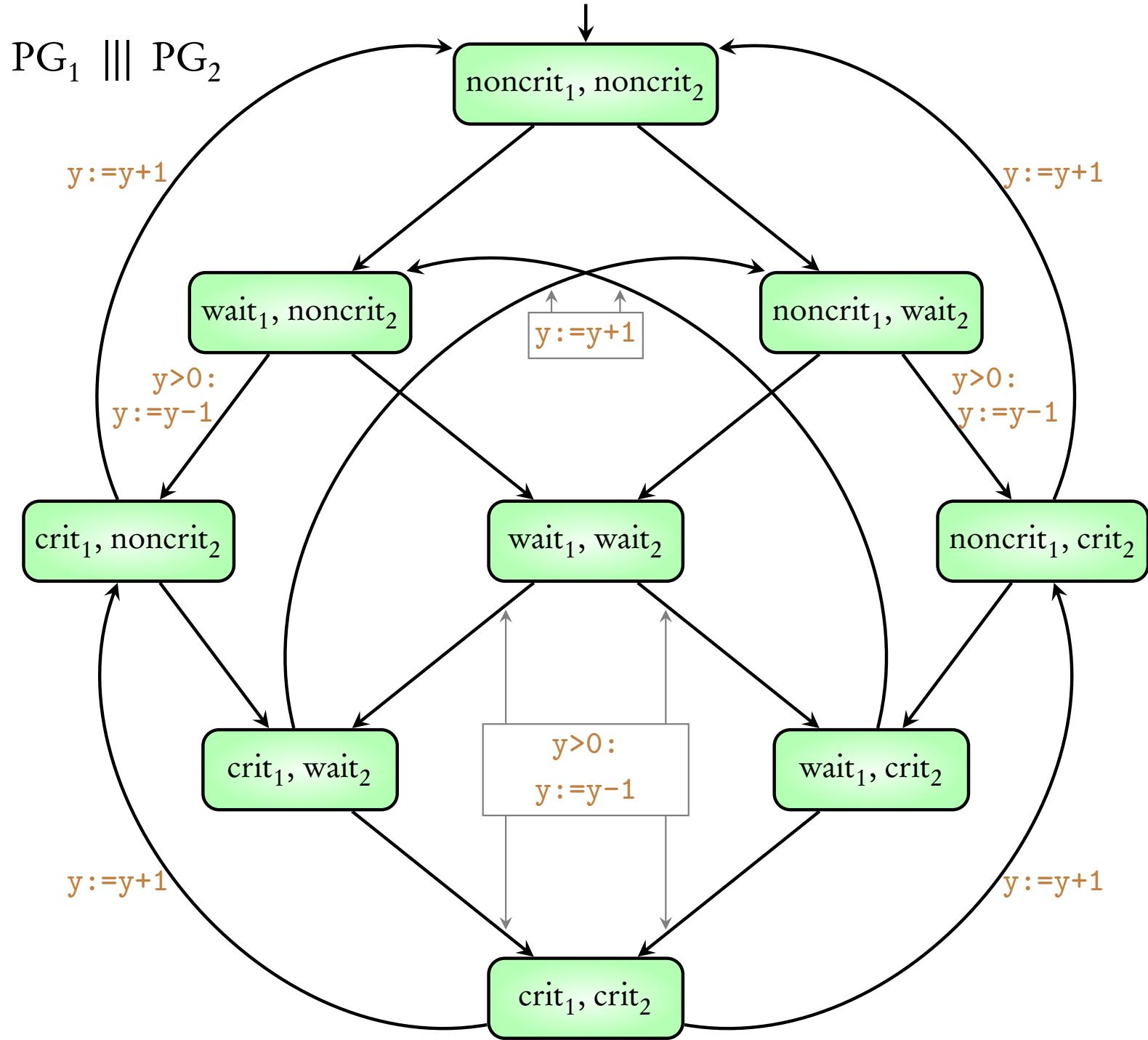
end loop

PG_1

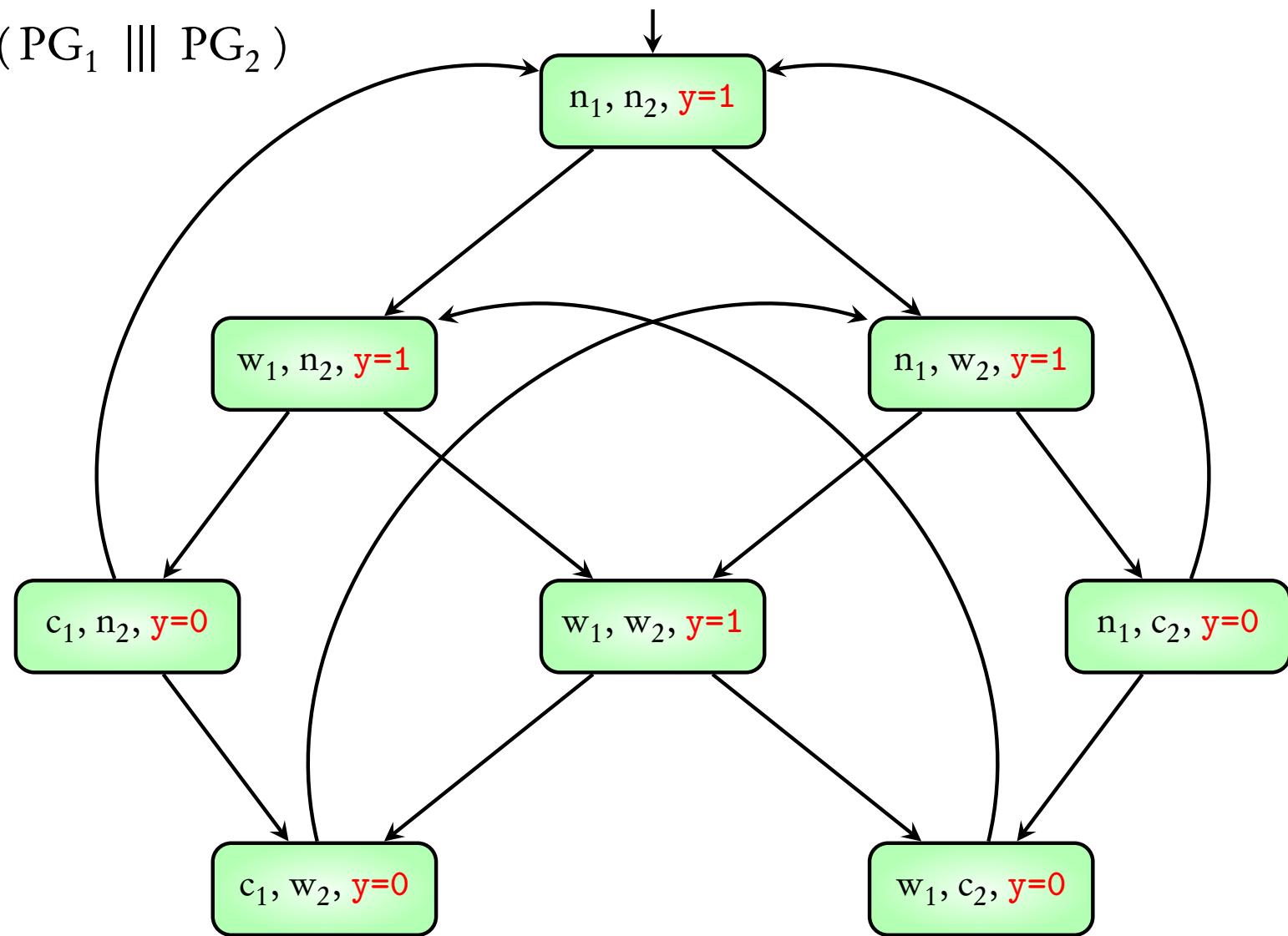


PG_2

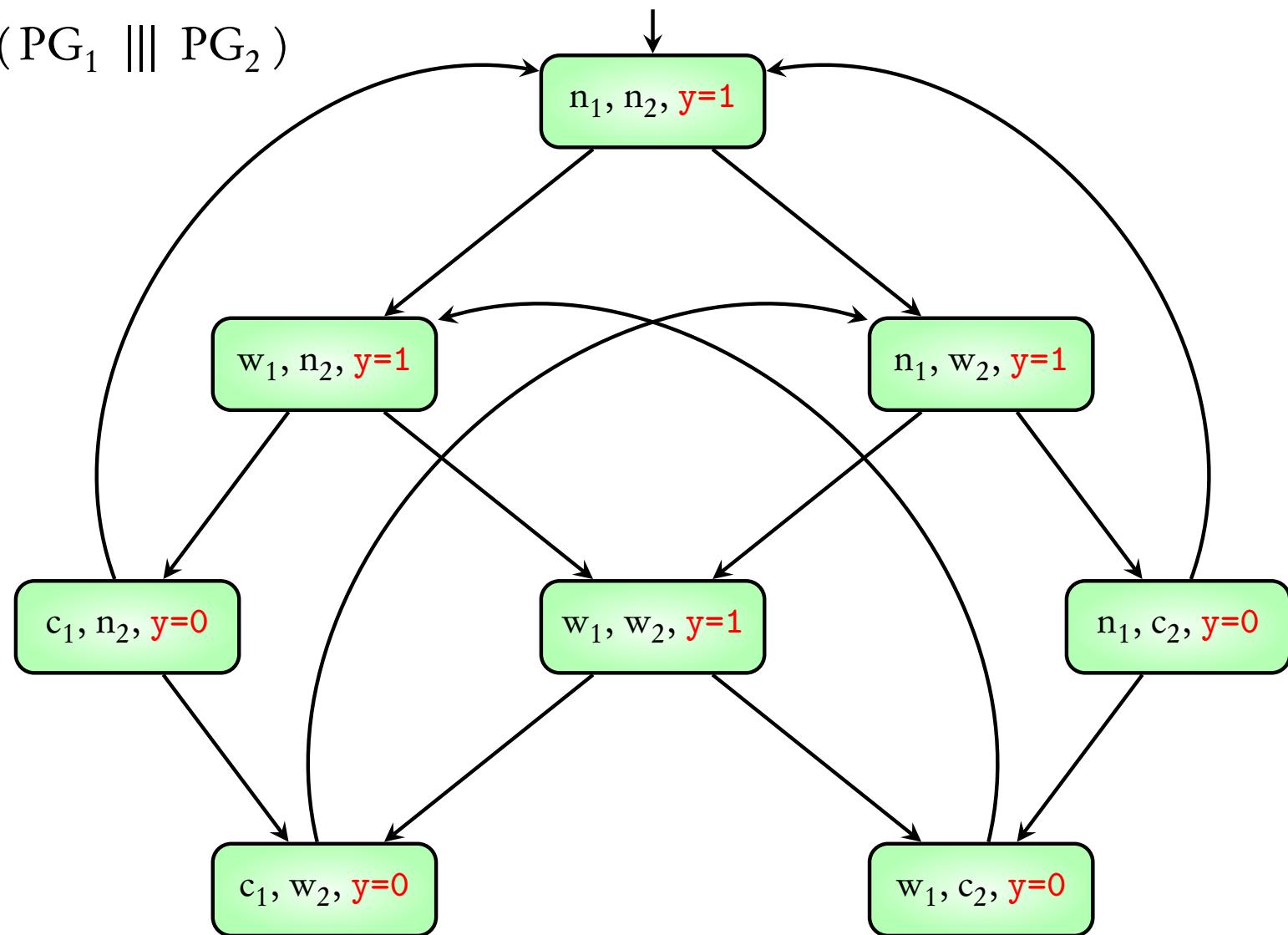




$\text{TS}(\text{PG}_1 \ ||| \ \text{PG}_2)$



$TS(PG_1 \ ||| PG_2)$



Both processes **cannot** be in critical section **simultaneously**

Concurrent systems

Independent

Interleaving

$\text{TS}_1 \parallel \text{TS}_2 \parallel \dots \parallel \text{TS}_n$

Shared variables

$\text{TS}(\text{PG}_1 \parallel \text{PG}_2 \parallel \dots \parallel \text{PG}_n)$

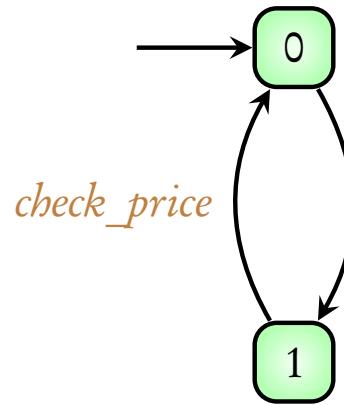
Mutual Exclusion

Shared actions

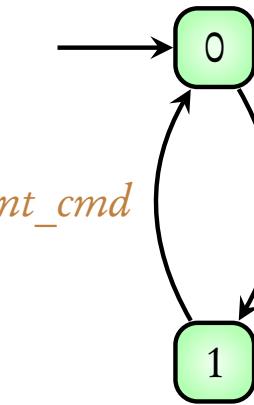
Handshaking or Synchronous Message Passing

Book-keeping system in a supermarket

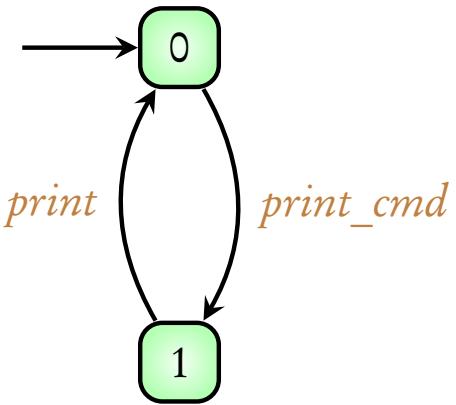
Bar-Code Reader (BCR)



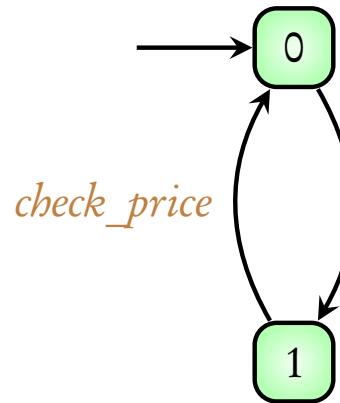
Booking Program (BP)



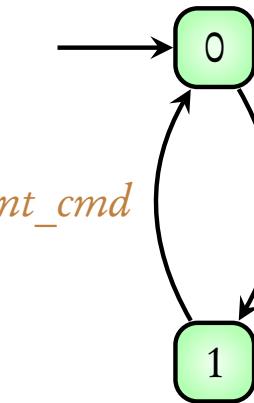
Printer (P)



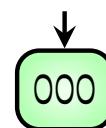
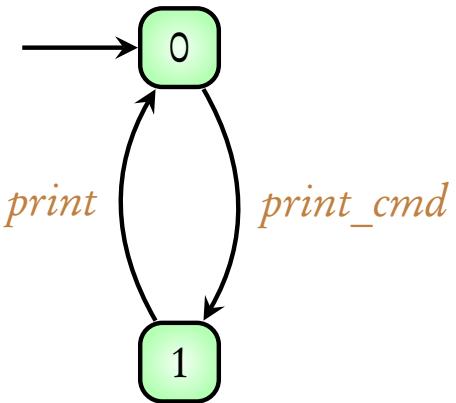
Bar-Code Reader (BCR)



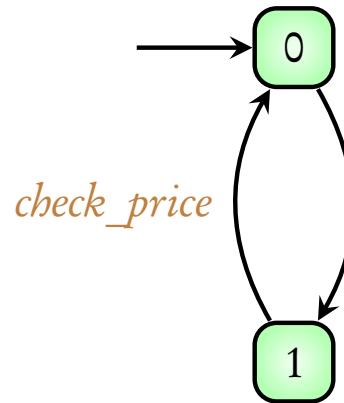
Booking Program (BP)



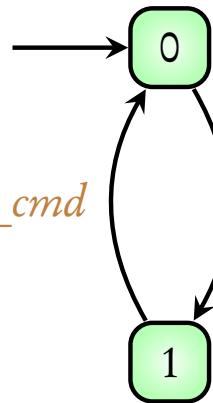
Printer (P)



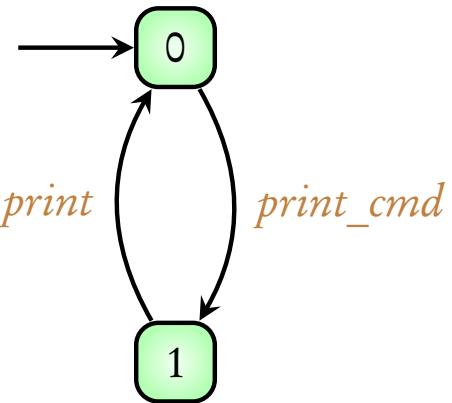
Bar-Code Reader (BCR)



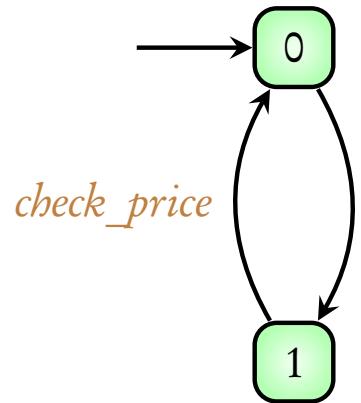
Booking Program (BP)



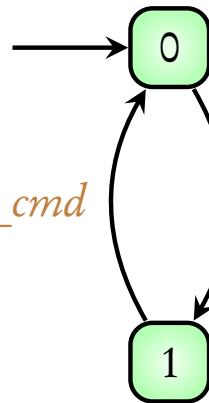
Printer (P)



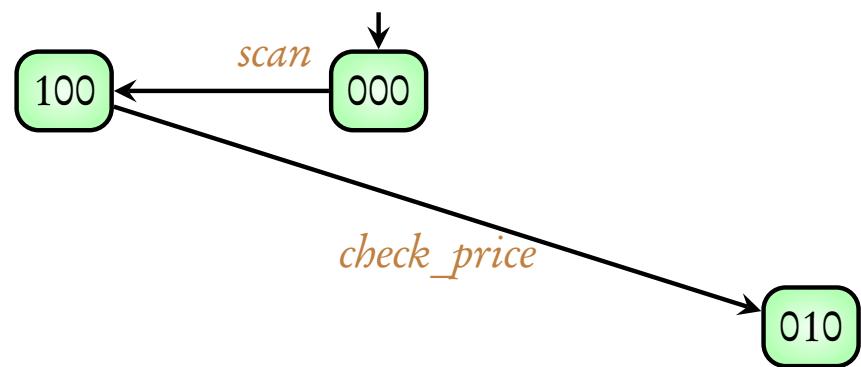
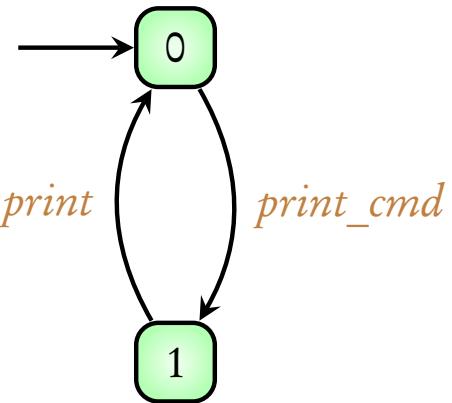
Bar-Code Reader (BCR)



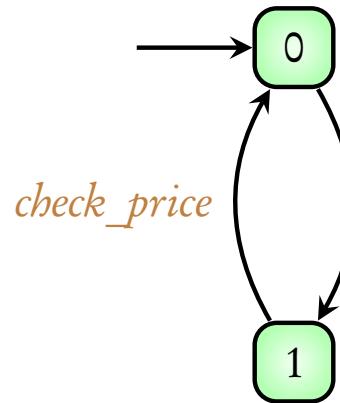
Booking Program (BP)



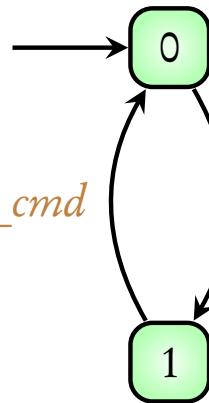
Printer (P)



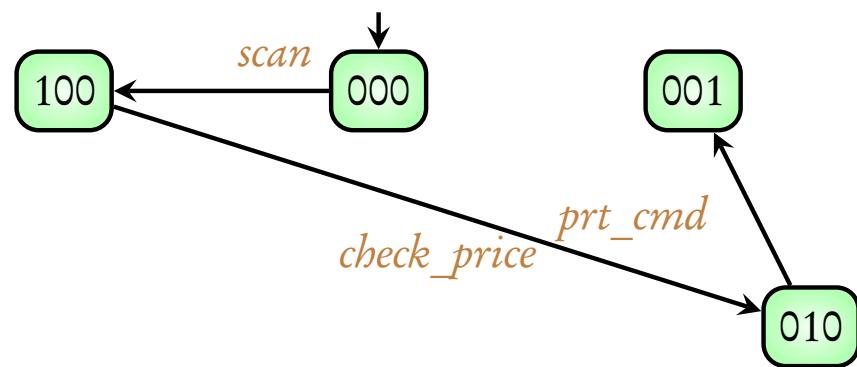
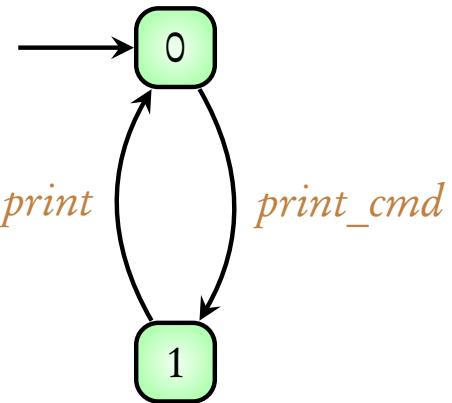
Bar-Code Reader (BCR)



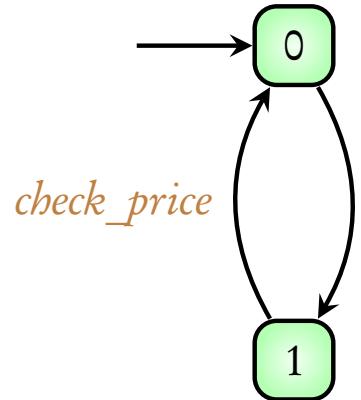
Booking Program (BP)



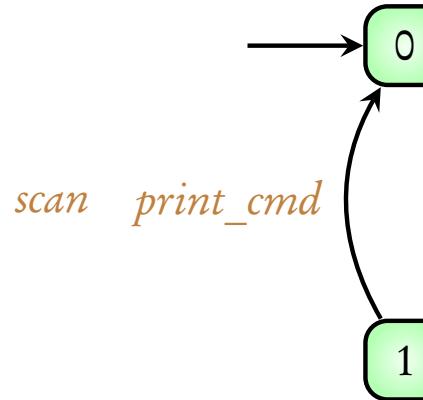
Printer (P)



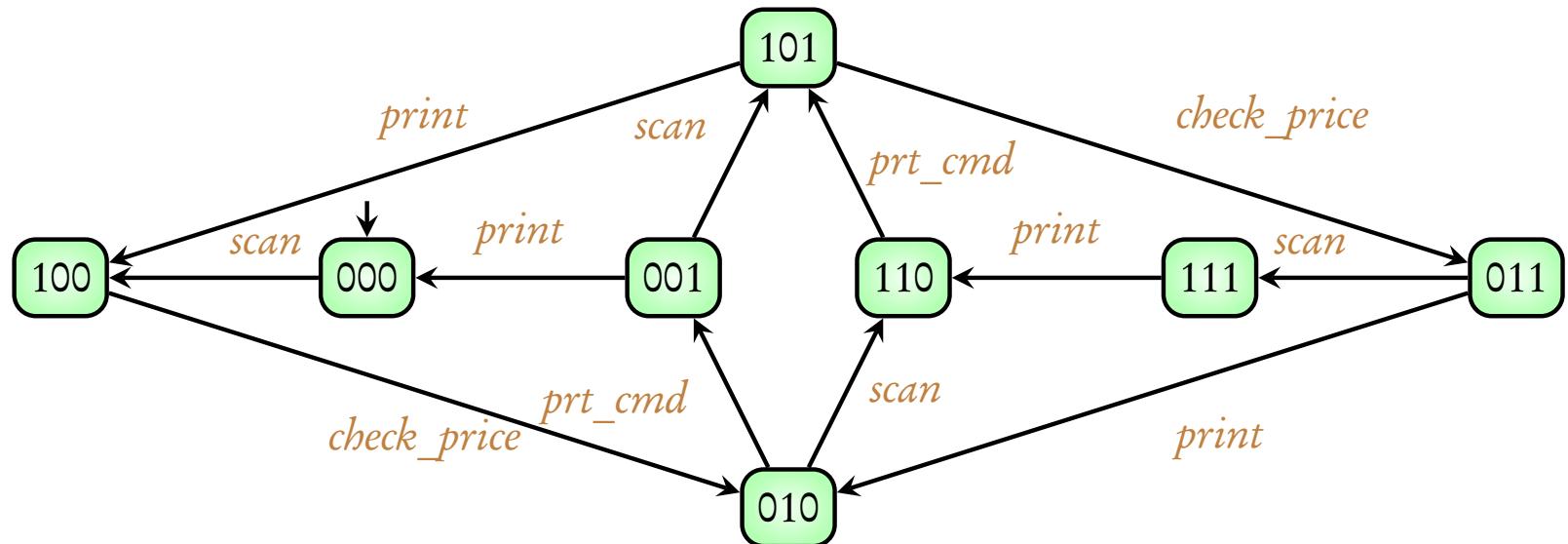
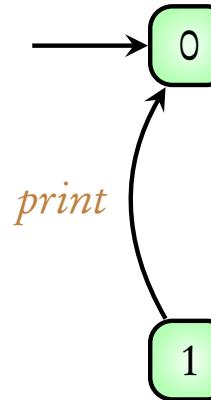
Bar-Code Reader (BCR)



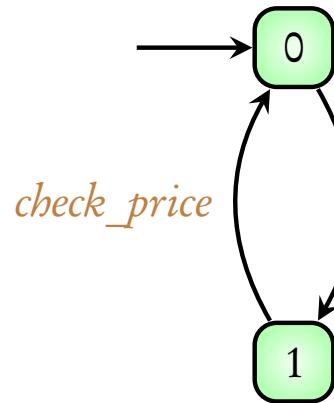
Booking Program (BP)



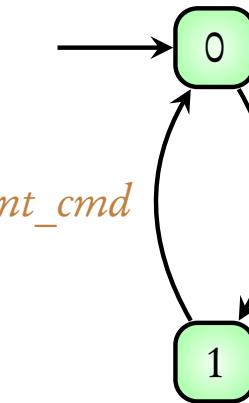
Printer (P)



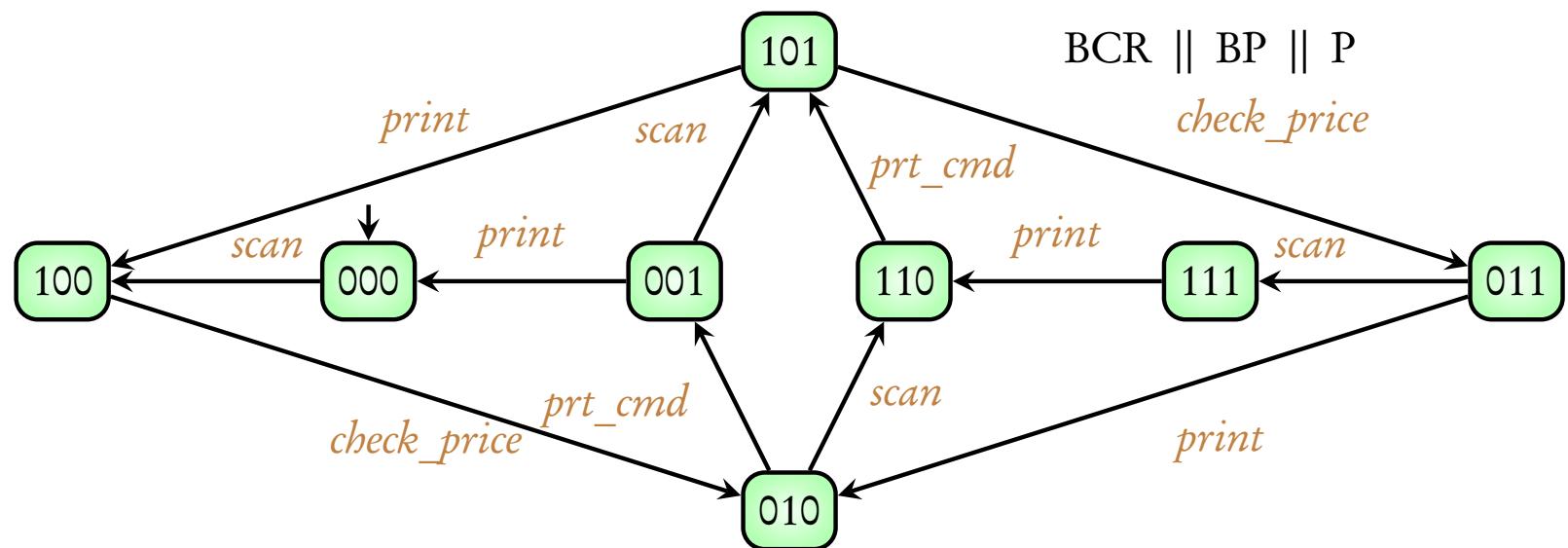
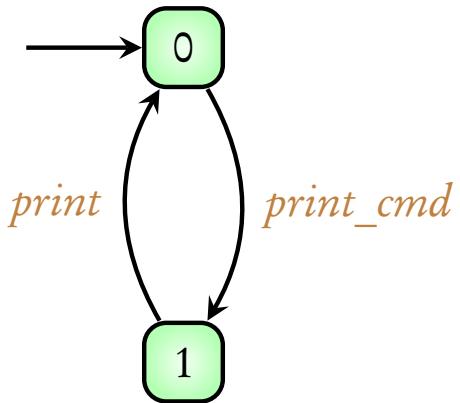
Bar-Code Reader (BCR)



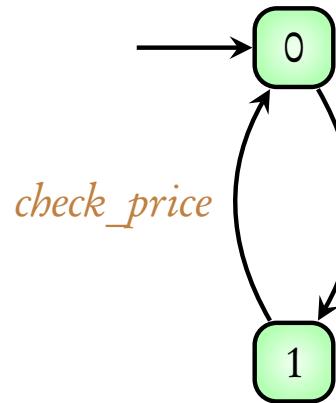
Booking Program (BP)



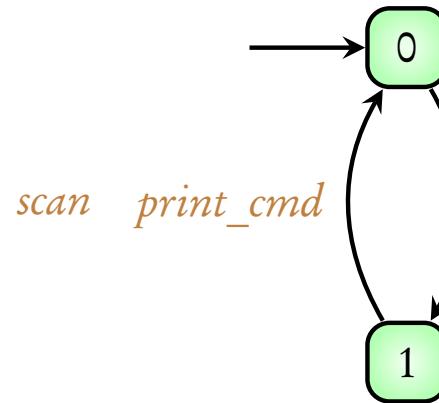
Printer (P)



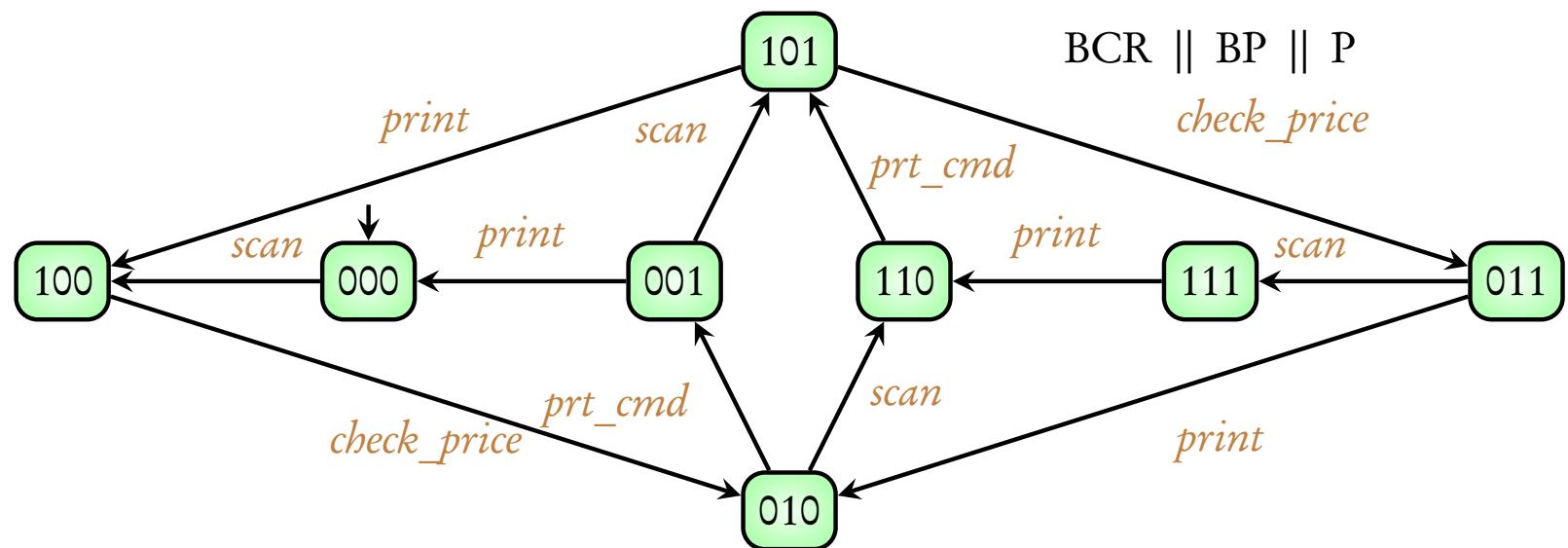
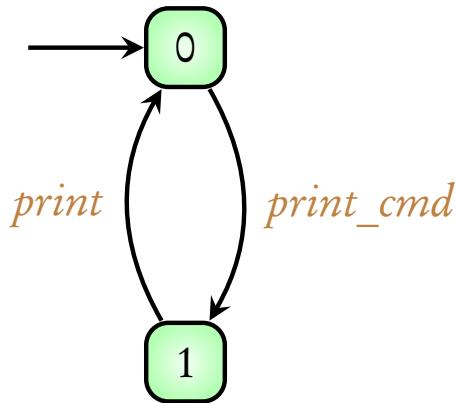
Bar-Code Reader (BCR)



Booking Program (BP)

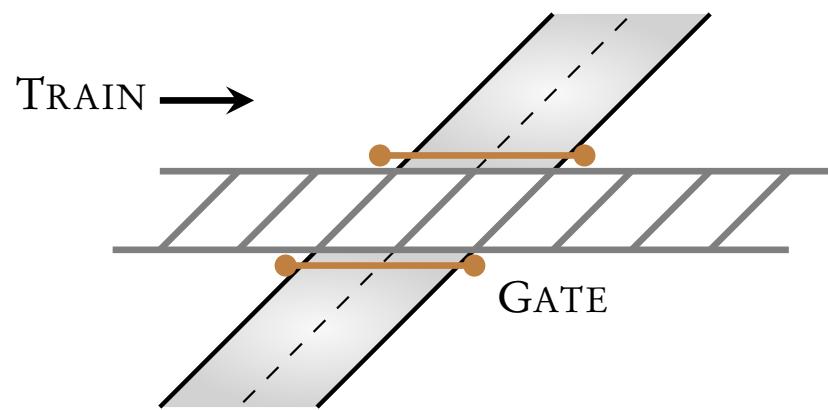


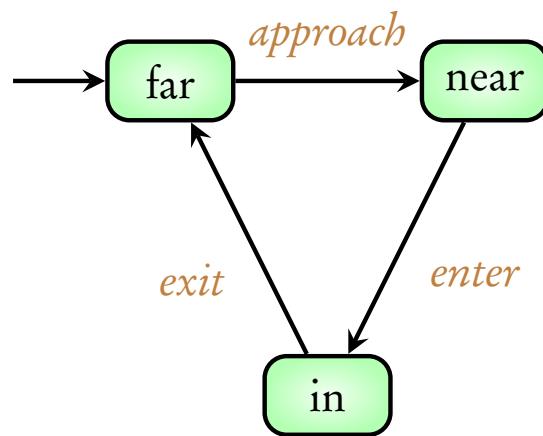
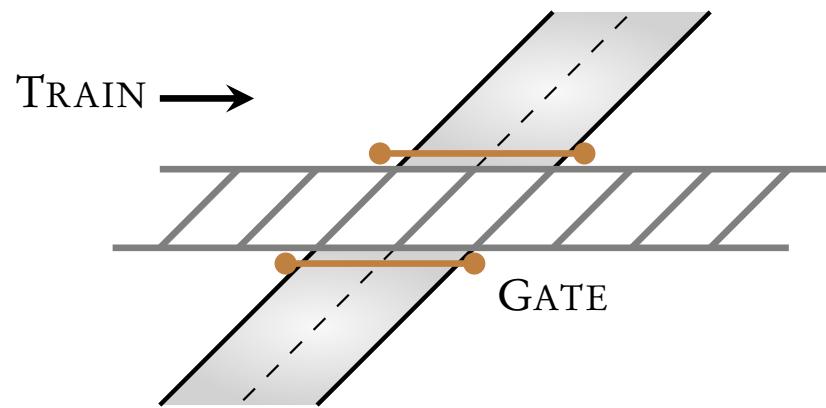
Printer (P)



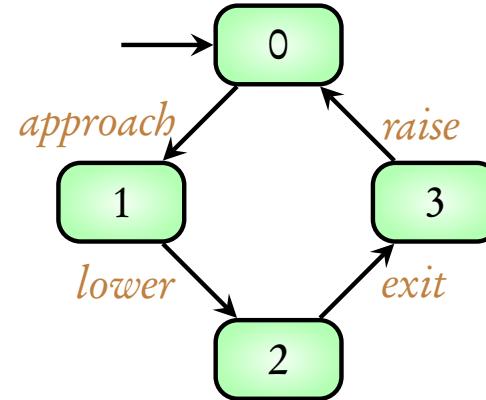
check_price, print_cmd: Shared actions (also called handshaking actions)

Next example: Train-Gate-Controller

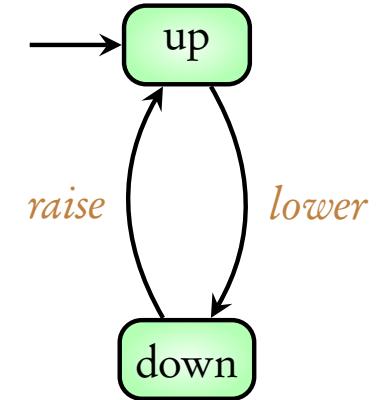




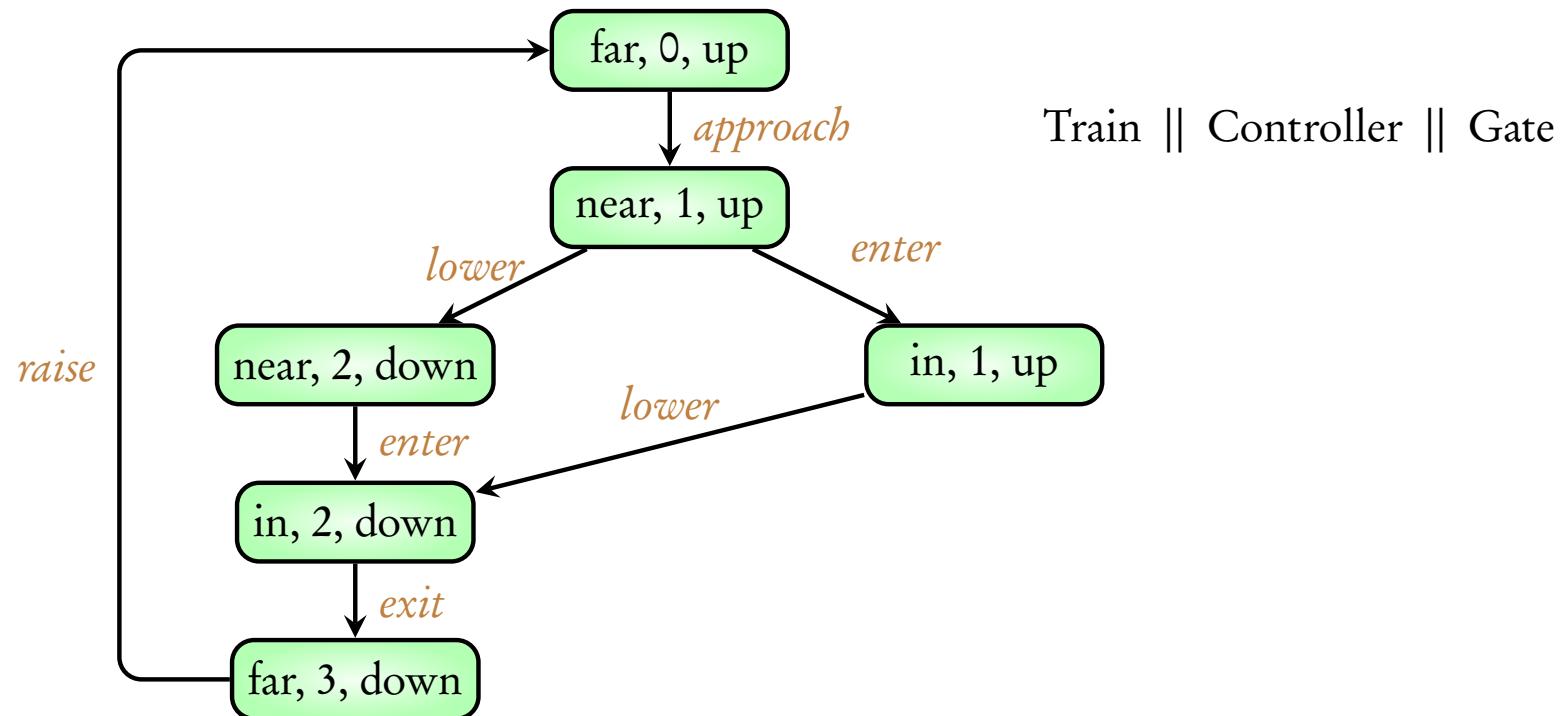
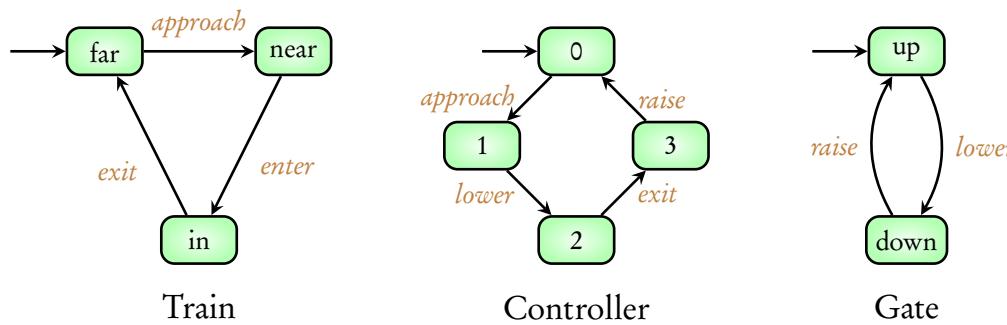
Train



Controller



Gate



|| : Handshake operator

Independent

Interleaving

$\text{TS}_1 \parallel\!||\!| \text{TS}_2 \parallel\!||\!| \dots \parallel\!||\!| \text{TS}_n$

Shared variables

$\text{TS}(\text{PG}_1 \parallel\!||\!| \text{PG}_2 \parallel\!||\!| \dots \parallel\!||\!| \text{PG}_n)$

Mutual Exclusion

Shared actions

$\text{TS}_1 \parallel \text{TS}_2$

Reference: Principles of Model Checking, *Baier and Katoen*, MIT Press (2008)
Pages 35 - 53

Summary

- ▶ **Module 1:** Transition systems, Modeling simple sequential programs
- ▶ **Module 2:** Modeling sequential hardware circuits
- ▶ **Module 3:** Modeling data-dependent programs
- ▶ **Module 4:** Modeling concurrent systems

Important concepts: Non-determinism, program graphs, interleaving and handshake operators