**Eiji Iki**

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**Professional Experience**

**Freelance Patent Translator -** Vancouver, Washington

(January 2011 – current)

* Translated patent documents from Japanese to English which are highly technical and complex.
* Provided technical advisory services to make certain that the translated of Patent documents is technically correct.
* Performed patent searches in which, based on a clients description of a technical idea, patents similar or related to the client’s idea are searched from patent databases worldwide.

**Grifphon LLC.** - Portland, Oregon

(September 2003 – December 2010) - Technical Analyst / Portfolio Manager

* Served as an industry analyst evaluating such sectors as computing, electronics, automotive, defense, and information technologies in order to seek out companies with strong technological advantages suitable for a mid to long term investment.
* Created, and managed a portfolio of financial products which are hand-picked through their fundamental data and technical tenets.
* Developed and implemented investment strategies emphasizing on a minimized downside loss utilizing a mixture of technical and fundamental analysis.

**Internap Network Services Corporation** - Seattle, Washington

(January 2003 – August 2003 \* due to a large scale lay-off) - Network Support Engineer

* Monitored, maintained, and performed troubleshooting on commercial data networks ranging from T1 to an OC12 lines at datacenters worldwide providing internet access to corporate clients.
* Trained to monitor and trouble shoot data networks by remotely logging onto Cisco 7600 routers and Catalyst switches to check and perform analyses of error logs.

**TSK America, Inc. (Accretech)** - Portland, Oregon

(June 2000 – September 2002 \* left position due to the branch closure) - Design Engineer

* Designed mechanical interfaces which interconnect semiconductor wafer testing equipment and wafer handling devices. Many were custom designed specifically for particular customer requirement. Managed tight lead times and unexpected emergency design projects requiring immediate attention while keeping high design qualities of interfaces at the same time.
* Worked with testing equipment companies to accommodate their new product lines and their interlocking mechanisms while accommodated for any changes made to the existing equipment.

**AVX / Kyocera** - Vancouver, Washington

(January 1996 – June 2000) - Mechanical Engineer

* Worked extensively with equipment development engineers from Kyocera in Japan to transfer and implement manufacturing technologies and processes for ceramic capacitors from Japan to the US.
* Continuously improved and optimized existing ceramic capacitor manufacturing equipment in order to achieve higher quality products and their production yield in corroboration with process engineers. Familiar with equipments including ceramic sheet casting machines, fully automated ceramic sheet stacking and printing machines, batch and continuous sintering kilns.
* Planned and executed capital equipment purchases as well as peripheral jigs and tooling.
* Managed an in house machine shop.

**Matsushita Electric Works, Ltd. (Panasonic)** - Tsu, Japan

(May 1994 – December 1995) - Manufacturing Process Development Engineer

* Performed an implementation of 3 high volume manufacturing lines for signal relays from the planning stage to the final production qualification stage. The work involved: budgeting, scheduling, generating, generating a detailed proposal, parts procurement, parts designing, modifying and retrofitting of existing design, PLC programming, programming of a robotic-arms, coordinating installation with the facility personnel and safety officers.
* Implemented a vision inspection system by designing mounting hardware for its cameras and performed programming on a miniature circuit connector assembly line.
* Improved an automated laser welding process for a signal relay manufacturing line by designing, implementing and characterizing a nitrogen injection system.
* Performed initial set up of 3 injection molding presses and copper sheet stamping presses.

**Education**

**State University of New York at Buffalo (graduated in 1994)**

Bachelor of Science / Mechanical Engineering