

## **Individual Project (COMSM0125)**

**Requirement:** The project should cover one of the topics (Fault tolerant design/Testing) covered in the course.

### **Sample Survey Titles:**

Investigation of Fault Tolerance in Multi-core Systems

Fault Exceptions and Fault Handling in ARM processors

A survey of Fault tolerant approaches in Avionics

Survey of Fault Tolerant techniques in Network on chip designs

Survey of Fault Tolerant Methodologies for Field Programmable Gate Array (FPGA)

Hardware Fault tolerance in Multi-core Design

Survey of Fault tolerant architectures and approaches for automotive systems

Fault Tolerance in Real-Time Embedded Systems

### **Design Project example:**

For the given MIPS processor design, make one or more unit fault tolerant and evaluate the design (area/power/timing)

Submission:

Your submission must contain:

(1) A Detailed report of the survey/Design.

(For the design project: also need the source code of your design/testbench if any - reasonably well documented).

Submission is through SAFE

<https://wwwa.fen.bris.ac.uk/COMSM0125/>