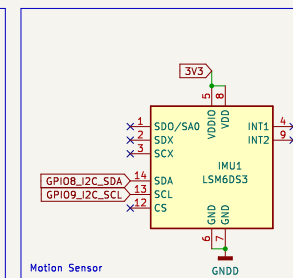
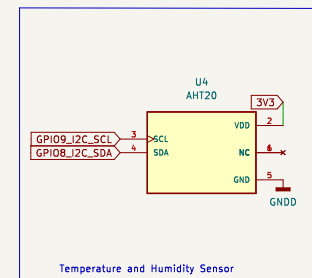
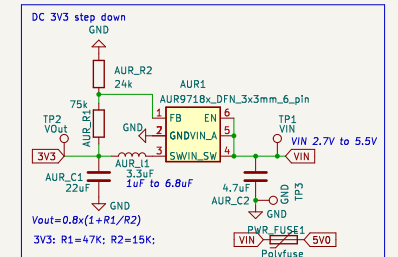
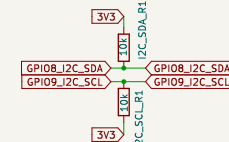
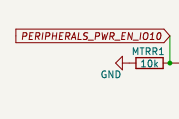
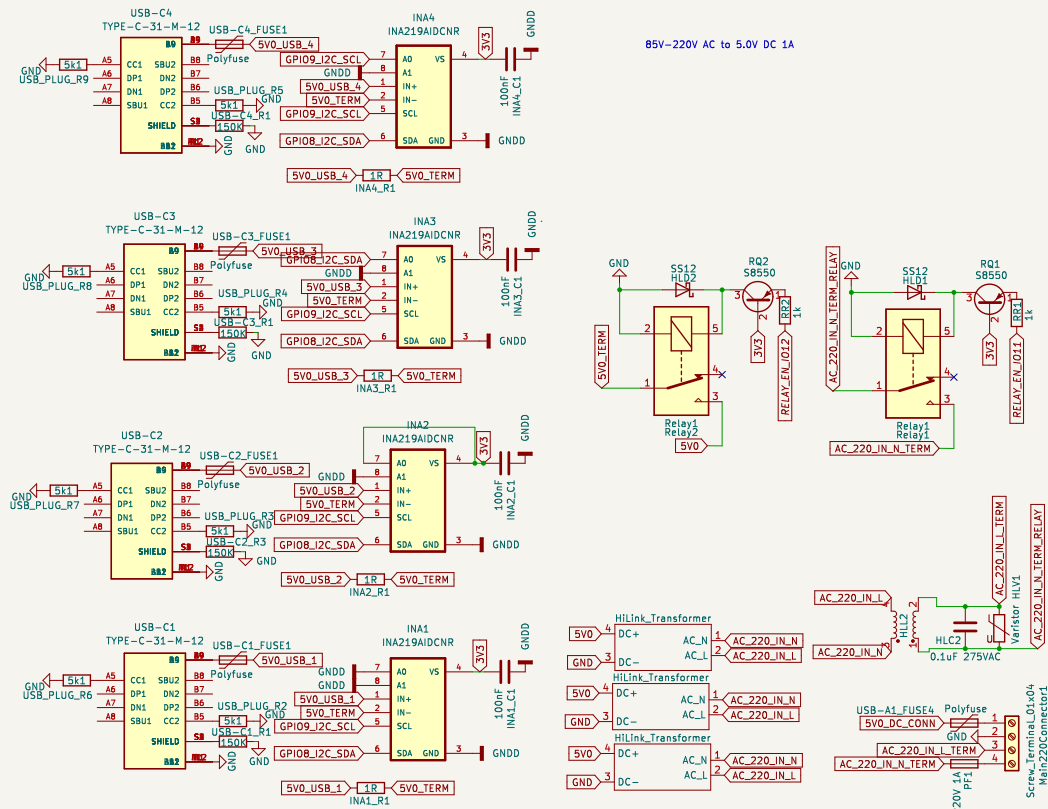
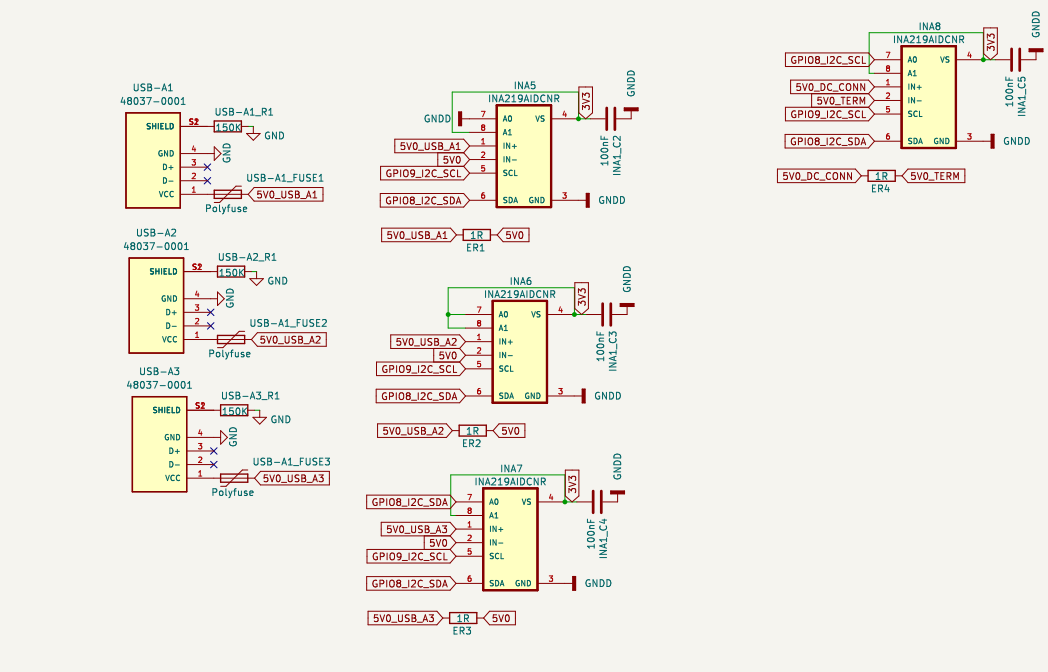


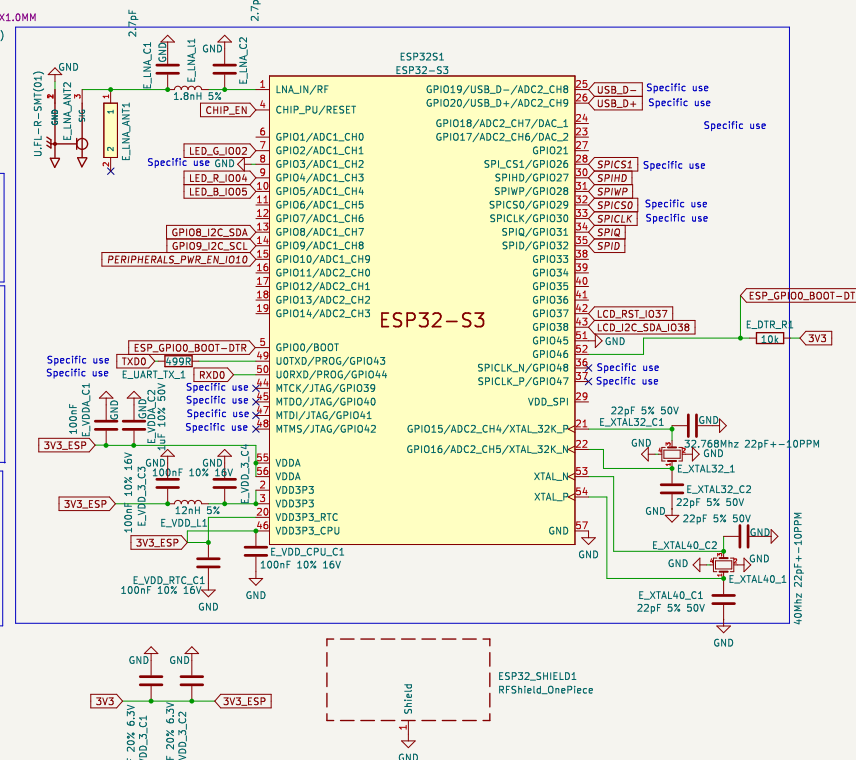
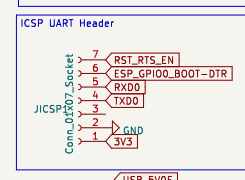
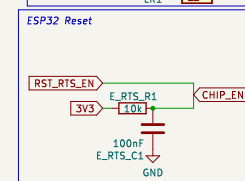
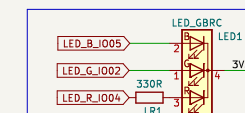
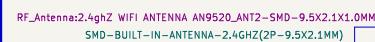
peripherals



Power Management



MCU ESP32 S3



Storage and Authentication

