## PINOUT DRIVERINO v1 ATMega2560

## **DRIVERS VNH5019**

#### **INPUT CHANNEL A**

			, .	 . ^ _		
	INA0	PA0	$\overline{}$		22	$\overline{}$
	INA1	PA1			23	
	INA2	PA2			24	
$\overline{/}$	INA3	PA3	$\overline{}$		25	$\overline{}$
	INA4	PA4			26	
7	INA5	PA5	7		27	7

#### **INPUT CHANNEL B**

	INB0	PC0		37	
$\overline{/}$	INB1	PC1		36	$\overline{}$
	INB2	PC2		35	
$\overline{/}$	INB3	PC3		34	
7	INB4	PC4		33	7
$\overline{}$	INB5	PC5		32	

#### **ENABLE CHANNEL A**

ENA0 PD2		19	
ENA1 PD3		18	
ENA2 PD4		-	
ENA3 PD5	7	-	
ENA4 PD6		-	
ENA5 PD7		38	

### **ENABLE CHANNEL B**

	ENB0	PE2		-	
Z	ENB1	PE3		5	
$\overline{Z}$	ENB2	PE4		2	
	ENB3	PE5		3	
$\overline{/}$	ENB4	PE6		-	
7	ENB5	PE7		-	$\overline{}$

#### **DRIVER PWM**

	PWM0	PH3		OC4A	7/	6	
$\overline{Z}$	PWM1	PH4		OC4B		7	
7	PWM2	PH5		OC4C		8	$\overline{}$
7	PWM3	PL3		OC5A	///	46	$\overline{}$
7	PWM4	PL4	7	OC5B		47	$\overline{}$
7	PWM5	PL5	7	OC5C	//	44	$\overline{}$

#### **CURRENT SENSE** 7.14 A/V

	CS0	PF0	ADC0	7/	Α0	
	CS1	PF1	ADC1		<b>A</b> 1	
	CS2	PF2	ADC2		<b>A2</b>	
$\overline{/}$	CS3	PF3	ADC3		А3	$\overline{}$
	CS4	PF4	ADC4		<b>A</b> 4	
	CS5	PF5	ADC5		<b>A</b> 5	

## **ENCODERS**

# CHANNEL A (Pin Change Interrupt 1)

•				
ENCA0	PJ0	PCINT9	15	
ENCA1	PJ1	PCINT10	14	
ENCA2	PJ2	PCINT11	-	
ENCA3	PJ3	PCINT12	-	
ENCA4	PJ4	PCINT13	-	7
ENCA5	PJ5	PCINT14	-	

# CHANNEL B (Pin Change Interrupt 2)

2 /
3 /
4 /
5 /
6
7 /

## **CONECTOR SIMOLASE**

1		TX2	Serial	2 /		Seria	I2 /	RX2		2
3	7	SDA	I2C		7	I2C		SCL	$\overline{}$	4
5		GND						+12V	$\overline{}$	6
7		SS	SPI			SPI		MISO	$\overline{Z}$	8
9	7	SCK	SPI	$\overline{}$	7	SPI		MOSI	7	10

## **CONECTOR MOTOR**



## **OTROS**

VSENSE	PF6	ADC	·c //	A.C.	$-\!\!\!/-$
LED	PB7			13	$\overline{}$

## **LEYENDA**

