



JLC06161H-3313 Stackup

Layer Stack Legend

Material	Layer	Thickness	Dielectric Material	Type	Gerber
	Top Overlay			Legend	GTO
Surface Material	Top Solder	0.030mm	SM-001	Solder Mask	GTS
PbSn	Top Surface Finish	0.020mm		Surface Finish	
JLC-1/2oz+1/2Plating	Top Layer	0.035mm		Signal	GTL
Prepreg		0.099mm	PP3313-0994	Dielectric	
JLC-1/2oz	Int1 (GND)	0.015mm		Signal	G1
Core		0.550mm	JLC_5500	Dielectric	
JLC-1/2oz	Int2 (PWR)	0.015mm		Signal	G2
Prepreg		0.109mm	PP2116-1088	Dielectric	
JLC-1/2oz	Int3 (Sign)	0.015mm		Signal	G3
Core		0.550mm	JLC_5500	Dielectric	
JLC-1/2oz	Int4 (GND)	0.015mm		Signal	G4
Prepreg		0.099mm	PP3313-0994	Dielectric	
JLC-1/2oz+1/2Plating	Bottom Layer	0.035mm		Signal	GBL
PbSn	Bottom Surface Finish	0.020mm		Surface Finish	
Surface Material	Bottom Solder	0.030mm	SM-001	Solder Mask	GBS
	Bottom Overlay			Legend	GBO
Total thickness: 1.639mm					

Transmission Line Structure Table

Impedance Id	Transmission Line	Target Impedance	Calculated Impedance	Trace layer	Wide Trace Width	Narrow Trace Width	Gap	Reference layers	Substack	Clearance	Target Tolerance
1	Coated Microstrip	50	50.02	Top Layer	0.15mm	0.15mm		Int1 (GND)	Board Layer Stack	0.13mm	10%
2	Edge-Coupled Coated Microstrip	90	89.97	Top Layer	0.14mm	0.14mm	0.15mm	Int1 (GND)	Board Layer Stack	0.13mm	10%
3	Edge-Coupled Coated Microstrip	100	100.00	Top Layer	0.13mm	0.13mm	0.24mm	Int1 (GND)	Board Layer Stack	0.13mm	10%
4	Coated Microstrip	55	55.02	Top Layer	0.12mm	0.12mm		Int1 (GND)	Board Layer Stack	0.13mm	10%
5	Edge-Coupled Coated Microstrip	120	120.02	Top Layer	0.15mm	0.15mm	0.17mm	Int2 (PWR)	Board Layer Stack	0.13mm	10%
6	Coated Microstrip	50	50.02	Bottom Layer	0.15mm	0.15mm		Int4 (GND)	Board Layer Stack	0.13mm	10%
7	Edge-Coupled Coated Microstrip	90	89.97	Bottom Layer	0.14mm	0.14mm	0.15mm	Int4 (GND)	Board Layer Stack	0.13mm	10%
8	Edge-Coupled Coated Microstrip	100	100.00	Bottom Layer	0.13mm	0.13mm	0.24mm	Int4 (GND)	Board Layer Stack	0.13mm	10%
9	Coated Microstrip	55	55.02	Bottom Layer	0.12mm	0.12mm		Int4 (GND)	Board Layer Stack	0.13mm	10%
10	Edge-Coupled Coated Microstrip	120	120.02	Bottom Layer	0.15mm	0.15mm	0.17mm	Int3 (Sign)	Board Layer Stack	0.13mm	10%

