ECED 3202

LAB #6 BUILD AND TEST Multistage amplifiers

Benjamin Jarrin B00846633 Aethan Cubitt B00877256

July 17th, 2023

Problem Statement:

It is required to build the multistage amplifier circuit designed in the previous lab to drive sound from an 80hm speaker. Furthermore, the purpose of the lab includes learning about power management and dissipation considerations in a real-world circuit.

Architectural Requirements:

- Circuit should be powered by a 5V supply.
- The circuit has to drive an 8ohm speaker load coupled to the amplifier through a 100uF capacitor.

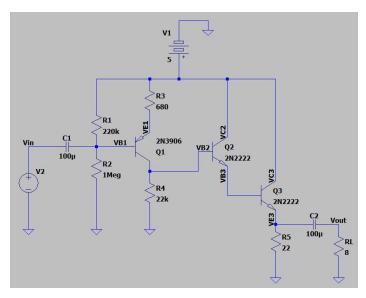


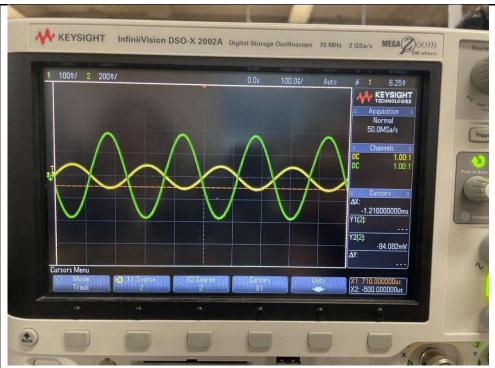
Figure 1: Schematics of circuit to build

Performance Requirements

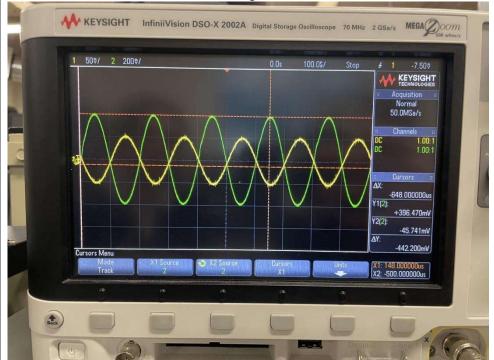
Test R6-03	
Requirements being tested	The input impedance of the amplifier shall be ¿ 80 k Ω between 500Hz and 5KHz 5
Output files	N/A
Description of simulation Configuration	In the simulation, similar values were used to achieve an input impedance gain of over 80k ohm over a frequency range of 500Hz to 5kHz which was successful.
Description of test output	A 205 mV p-p signal was sent through a circuit containing a resistor of known value in series with the amplifier and a voltage drop was measured. The Rin was then calculated through a modified version of voltage divider.
Acceptance Criterion	Input impedance was greater than 80k ohm over a frequency range of 500Hz to 5kHz
Test Result	Calculations show an input impedance of around 140kohm
Objective Evidence	KEYSIGHT InfinitVision DSO-X 2002A Digital Storage Oscilloscope 70 MHz 2 GSa/s MEBA 2000 1 5087 2 2087 -40 000: 1,0001x/ Stop # 1 91 95

Vi	Vimeas = Vin Rin Runown+Rin
	REMOUN RIN Vin - REMOUN + Rin
Rin	vneas Rin (Vin _ 1) = Rknown
	Rin = Rknown Vin vines - 1
	Vinees -1
we	know RKnown = 100 K
	Vin = 205mV P-P
	V meas = 120mV P-P
50	$Rin = \frac{100 \times 0^3}{208 - 1}$
	Rin= 14117652 which 13
	greater than 80KD

Test R6-05	
Requirements	The gain of the amplifier shall be 8x over the frequency range from 1000Hz to 5kHz
being tested	
Output files	Test3.asc submitted in submission.
Description of	In the simulation, very similar values were used to achieve a gain of over 15x over a
simulation	frequency range of 500Hz to 5kHz. This was successful and what lab six's values are
Configuration	based off of.
Description of	After ranging the function generator from 1kHz to 5kHz we want to observe a
test output	consistent gain of around 8x or 18dB or larger.
Acceptance	The gain is equal to or above 8x over the ranged of frequencies 1-5kHz
Criterion	
Test Result	After ranging the function generator from 1kHz to 5kHz we were able to observe a
	consistent gain of around 8x or 18dB or larger.
Objective	1kHz wave gain. Yellow wave is signal from signal generator. Green signal is from
Evidence	the output of the circuit. The signal appears to only be 4x bigger, this is because the
	scale is at a ½ therefor the gain is 8x at 1kHz.



5kHz wave gain below. Yellow wave is signal from signal generator. Green signal is from the output of the circuit. The signal appears to only be 2x bigger, this is because the scale is at a ¼ therefor the gain is 8x at 1kHz.



Test R6-06

Requirements being tested Output files The amplifier shall produce audible sound output. This is verified by the video attached.

Description of simulation	N/A
Configuration	
Description of	N/A
test output	
Acceptance	Video is audible and clear music can be heard
Criterion	
Test Result	After playing music from an ipad Audio jack music was audible
Objective	Attached in the video
Evidence	