1.	For legacy systems, explain why we would need a VBR if we already have an MBR.	
		Score
		/ 3
2.	What is the difference between a hypervisor and a typical virtual machine monitor.	
		Score
		1.5
		/ 3
3	Write one use that you can think of the /proc filesystem?	
٥.	White one use that you can think of the 7 p100 mesystem.	
		Score
		/ 3
		I .

4.	What is <i>preemption</i> and why is it necessary to properly support multitasking?	
		Score
		/1
5.	If the modular architecture and micro-kernel approaches both split the operating system in smaller modules, what is the benefit of the micro-kernel architecture over the modular structure?	
		Score
		/ 1
6.	Is it possible for a context switch to occur without a mode switch? Support your answer with a logical explanation.	
		Score
		/ 2
7.	List the three <i>types</i> of traps that can interrupt the normal execution of a program?	
		Score
		/ 2

٥.	Take a look at the following code and write the maximum number of processes that will will be present at any time.	
	<pre>void main(int argc, char **argv) {</pre>	
	int pid;	
	<pre>for (int i = 0; i < 4; i++) { pid = fork();</pre>	
	if(pid != 0) {	
	sleep(2000);	
	break;	
	} }	
	wait(NULL)	
	}	
		Score
		/ 2
		, -
9.	What is the need of the <i>created</i> state in the process life cycle? Mention at least one specific task that is performed in this state.	
		Score
		/ 2
10.	When we say that the kernel must 'save the state before switching context', what do we mean?	
		Score
		/ 1
		/ 1
11.	Explain the operations carried out by the kernel when the fork system call is issued by a process. Provide as much detail as you can in the allocated space.	
		C
		Score
		/ 2

12.	What is the <i>most important</i> limitation of using user-space threads. How is it remedied by modern operating systems.	
		Score
		/ 4
13.	Give an example from the programs you've coded where a race condition can lead to data inconsistency. (The example must <i>not</i> be of cash withdrawl and deposit.) Explain fully, the sequence of operations, which can lead to the data inconsistency.	
		Score
		/ 3
14.	In the example given above, suggest changes to ensure that the race condition is avoided.	
		Score
		/ 1
15.	If you initialize a semaphore with the value 5 and issue the down call on it from 7 different threads (t_1, \ldots, t_7) , <i>list</i> the state of each thread after the calls. (States can be 'ready', 'running', 'blocked', 'zombie').	
		Score
		/ 3

			_
ssume there's a thread-aware operating system. Thread t_1 from within proce time when the operating system has executed the corresponding syscal ampleted. Can the process p_1 still be selected by the scheduler to run at this	ll handler, but I/O	has not yet been	
e time when the operating system has executed the corresponding sysca	ll handler, but I/O	has not yet been	
e time when the operating system has executed the corresponding sysca	ll handler, but I/O	has not yet been	
e time when the operating system has executed the corresponding sysca	ll handler, but I/O	has not yet been	
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e time when the operating system has executed the corresponding sysca	ll handler, but I/O	has not yet been	
e time when the operating system has executed the corresponding sysca	ll handler, but I/O time? Give a reasor	has not yet been in for your answer.	