

# ANATOLY YAKOVENKO

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Hi,

I think I would be perfect for several positions you have open at Magic Leap. I am a software developer with over 10 years of experience working on embedded systems. Over the past 3 years I designed and lead development of a brand new technology for co-processor communication between ARM and DSP cores on Qualcomm chipsets, for which I was the lead inventor on several patents. We achieved a tenfold reduction in co-processor communication latency, which enabled cutting-edge uses of the DSP, and enabled technologies that would simply not be possible without leveraging the DSP's performance and power efficiency. We commercialized the technology on modern Android, Windows and Qnx mobile phones. I worked with internal developers and third party ISVs and OEMs with debugging, profiling and optimizing computer vision and image processing algorithms between ARM, Neon and Hexagon DSP processors.

I have many years of experience working with embedded operating systems, I was the lead developer of COM IPC implementation, which was the backbone of BREW, Qualcomm's mobile OS, commercialized on thousands of devices, millions of units, handling billion 3rd party app developer downloads. The IPC layer I developed provided a secure capability based framework for communication with kernel and userspace services. I worked on low level system call optimizations as well as user developer tools such as an IDL compiler, mock object generator, elf parser, system call trace tools. I wrote unit test coverage frameworks with an exception simulator, achieving 100% code coverage in critical pieces of the kernel. I lead interface and code reviews, enforcing strict requirements for API binary backwards compatibility, performance, application and kernel security as well as developer ease of use.

I would be an asset to any team trying to take full advantage of the computational resources available on Qualcomm processors. I would love to continue my role as a lead, taking on exciting engineering challenges and pushing the power and performance limits of mobile processors.