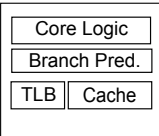
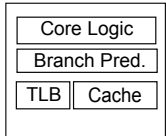


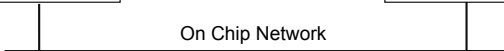
Core 1



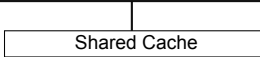
Core 2



On Chip Network



Shared Cache



System Bus



Memory Controller

