@ 8086 MD 0 his Coxen -1 @ 8086 pp chip (example > 14.7456 MHz). 3 KBIC > Key Board Interface Controllor Lunder the keypad - not visible DOW put device > LCD [Liquid Crystal Display] unit; LCDIC 150(2) 16 (Chan)×2 lines € (5) 72DD > 7-segment-display Device @CA7SDD -> Common Anord type 7-segment Display Device. (IC+) 8255 chip: programmable I/O device that acts as interface between PERIPHERAL Devices and MP. fore parallel data transer © LED → Light Emitting Diode. [LED-11-LED-13 in board (10)→ 8255 (chip U29 in pic). (2) RAM -> Randown Access Read and Write Memory Capacity (normally) -> RAM 20 20 (201 32Kbyte. (32×1024×8) bits means 32×1024 byte locations inside the RAM. address of memory > [00000H - OFFFFH] totally 64 xbxte. (IC) > 62256 (2x) ® EPROM → UV Erasable Electrically Programmable Random Access Read Only Memorey 32KB (c) →270256 (2x) They have the Monitore Proogram of the MDA-8086 trainer. \* priorides interface the user and computer F0000H~ at low level FFFFFH \* provides a way of Lading a preogram to preimary momercy (RAM), test momercy, examine registers, more or cpy memory, teun programs. \* provides all Basic Input Output [BIOS] functions. [FO809an] Largie latigital of larging signal to digital signal [ADCO804]

OUR CONVOTER - " digital " to analog "[DACD809] € @ D/A convorter > controls level moter

KeyBoard (26-keys) MPU control keys + Command keys = [2+7] = 9 keys. \*DRES -> MPU (MP) RESEX - key pressed: 8086 remains in ruset state. " released: " enters into working " message is shown on LCD monitor. jumpor (switch-to AM) must be set to left position[Left position-2 Imper ATERI ATTA Kit position] MON → Menitore
NMI→ Non Maskable Interrrupt [Pin (MICE 2001)] - key pressed: CPV receives external interuppt signal via NMI Pin \* Hardware interrupt (or signal to processor) that prioritizes a certain thread on process. \* Can't be interrupt by magking techniques. Example - Clicking reset button, turn off power supply. Command keys: () \* AD (set memory ADdress): allows to set 20 bit address of mem location. Foremat > "Segment: Offset = Seg: Oset" \* Press the key; LCD will show ] signent (Seg. Oset Data - >8 bit men location printed by xxxx yxxy zz seg: Cset \* Address is 5 digit hex. \* " obtained by placing 0 to segment's right then add with Oset. \* Example: Seg: Oset = 0000:1002. address: 00008 xaro addition of seg + Oset >00000+1002 = 0002 ans.

\* another example: seg: Oset = Froo: 0000.

address = OFOOOh.

0

& & &

- (2) DA [Dota entry]: brings current to the data field. + enterdata [0-7]
- 3)+ (di increment): more to next mem Location address.

  3)- (decrement): " " previous" "
- (parq ztixs) Op 3
- © REG → chacks + examinas contents of internal register
- (3) STP > Single stop executes linetructions at a frame.

Data Keys: 0-9, and A-F (hexa Keys)

## EMULATION KIT:

- \* software emulation of Intel's 8086 MP: Emusos6.
- \* A group of hardware devices that can be controlled by Emuso86 virtual central processing unit (CPU).
- \* Available hardware devices in it : ODO+ Matrix 27 Segment Display
  - 3) ASCIL LCD display (9) LEDs.

Total manager ( ) The second of the second o

-3' - 54 ( a , g

- 5 Push buttons (input) 6 Keyboord input.
- @Switches input & Thermometer input

  (3) Præssure Gauge input

1 18 KOM

## STEPS to control MP:

- @ Emulatore will get the HEX code.
- 1) Press RES to reset.
- 2) Press DA to update seg: Oset.
- 3 Type HEX code.
- (1) Press + to go to next address.
- (5) After typing (finishing) Pross STP-to execute
- 6 Press PEG to see the result on Display of MDA Wim 8086

7 H 0 4 ....

```
Instruction Set:
   mor: copy's values from one reg to another
   ADD: adds 2 numbers.
                                               INT 3 :
  SUB: subtracts
                                                * special Ibyteinstruction
                                                *Greade = CCH
   MUL: multiplies
                                                *used at breakpoints
  DIV: divides
                                                * when hit, interrupt handler
                                                 breaks into debugger.
   Ax: Accumulator reg
                                                - replaces oreiginal instruction
   BX! Base req
                                                - lets execute when user is ready
   CX: Counter 11
   DX: Data "
  Example: [HEX CODE
@ converse of to ok.; temp = 39°C; [1°K=1°C+273] forenula.
                             suppose AX address 0404
          AX, 39 ; AX=017H-
    MOY
                                  BX 11 (0404+3)= 0407
                                                                                 mov BX, 273; BX=111 H-
                                   > tosulf 11
                                               (0407 Hg) = 1000 040A
          AX, BX ; AX = 0138H=
    ADD
                             [39] 27H loaded in AX -> 0909
   TNI
                             [273] 111H " " BX ->0407.
                                ans (add tion of AX+BX) -> 04 A after STP and REC.
                                  AX = 0138H - 1
@ converse "K to "C; temp = 270" K. 1°C = 1°K - 273.
                  ; AX = 10EH load tam; address = 0404
  MOV AX, 270.
                   ; BX -9 111 H · 11 11; 11
        8x, 273.
  mor
                    ; AX-2 subtreact area and load som ; address = 040A.
  SUB AX, BX
                      [executes AX = -3]
  INT 3
@ average of 3 numbers; (2+3+5)/3 = for 2 20th curves
                ; AX=2 or 2H \longrightarrow 0.404
; BX=3 or 3H \longrightarrow 0.407
        AX, 2
   MOY
         BX, 3
                ; AX = $ (2+3) = 5 or 5H -> 040A.
  mor
        AX, BX
  ADD
       BX, 5
                  AX = (5+5) = 10 STC AH --> 040F
  MOV
        AX, BX;
  ADD
                  BX= 3.---
        BX, 3.;
  mor
                                       → 0413
                  10/3 -
        Br
  DIV
```

EC

6,-

65

6

6

6

65 6

6

D

```
mai southon => tiles needed = (20×20) (2×2) [ Hes size]
                                   Normal solution =>
€ Floor 820 = 20 × 20
    Tiles 11 = 2 \times 2
     How many tiles needed?
 Mov AX, 20
                ; AX = 20 on 14H
  mor BX, 20
                 ; BX=20 on 14H
                 ; AX = AX x BX = 400 one 1904.
 MUL BL
 mos ex, Ax.
                 3 \text{ CX} = AX = 190H \text{ arc } 400.
 mov Ax, 2
                ; AX = 2
 mor BX, 2
                ; BX=2
                 AX= 4 orc 4H4 (2x2)
 MONT BT
 mer BX, AX
                ; BX = AX = 414
                 AX = CX = 400 or 190 H.
 wer
      E AUB XA
                  Divide -> AX/BX = 400 ÷ 4 or, 190H/4H = 100 or 064H
 DIV BL
INT 3
               ; break.
Factoreid of 5! -3! 21(a (5×4×3×2×1) - (3×2×1) = 114
   mox AX, 1
   mov cl, 5; cl=5
   L1:
                ; AX = SEX AX X CL = 1 x 5.
   mul CL
                ; CL -- so loop-complete - 22 MO AX = 5 × 4 × 3 × 2 × 1. = 120 en 078
   LOOP LI
                 ; DX = AX = 120 or 078H.
   MEY DX, AX
  MOY AX, 1
  1
  mor et, 3
                  ; Ax = 3x 2 x1 = 6
  L2:
  MUL CL
  LOOP L2
               1; BX = 6
  MUSY BX, AX
              3. AX = DX = 120.
  mov AX, DX
                 ; AX = AX - BX = 120 - 6 = 114 on 724.
  SUB AX, BX
   TNT3.
```

MULTEL : AXX NXX BX - 2x(e)2

CH = XA = XO : XO XO YOUT

```
€ (5! /3!)+4!
     mov Ax, I
    mor CL,5
                   : AX = 5.4.3.2.1 = 120
    LI:
    MUL CL
    LOOP LI
                  ; DX = AX = 120
    MOV DX, AX
    mor AX, 1
    MOVCL, 3.
                    ; AX = 3.2.1 = 6
    L2:
    MUL CL
    LOOP L2
    mor BX, AX .; BX = AX = 6.
                 ', AX = DX = 120
    MOY AX, DX
                 ; AX = 120/G = 20.
    DIV BT
    MOY DX, AX
                 ; DX = AX = 20.
    mor Ax, 1
    mor CL, 4
                     AX = 4.3.2.1 = 24
    L3:
    MUL CL
    LOOP L3.
    ADD AX, DX.
                   Ax = Ax + Dx = 24 + 20 = 44 on
                                                       2CH .
   INT 3.
   (21 * 31 * 41)+1!
(3)
                                                 mov AX, 1
   AMOV AX, 1
                                                 mor CL, 4
                                                                   AX =4.3.2.1=94
     mor CL 2
                                                 'L3:
                AX=2.1=9
     LI:
                                                  mu L CL
    mul a
                                                  LOOP L3
    roop rt
                                                  MOV BX, AX. ; BX = AX = 24
    MOV DX, AX; DX = AX = 2
                                                  mov Ax, & DX; AX=DX= 12
    moy Ax, I
                                                  MUL BL. 3AX = AX \times BX = 12 \times 24
    mor ce, 3
                AX = 3.2.1=6
                                                  ADD AX, BX ; AX = AX+BX = 288+24
    L2:
    mul CL
                                                  INT 3.
                                                                            = 312
    LOOP L2
    mor AX, DABX, AX; BX = AX = 6.
    mor AX, DX ; AX = DX = 2
                 ; AX = AX \times BX = 2 \times 6 = 12
    MUL BL
   mov \mathbf{B}^{X}, \mathbf{A}^{X} ; \mathbf{D}^{X} = \mathbf{A}^{X} = 12
```

(p) EN

Eps.

6

C

Byte with Byte Division.

ORG 100h

: MODEL SMALL

· DATA hum\_L DB OF2H num\_2 DB 4H

· CODE

Mor BH, num-2

mor AL, num\_1

DIV BH; BH/AL.

RET ; return

B. World by world amen

DATA - CO DW ZCOT

onia code -2

mor ax, num-1

DIV num\_2 ; AX/num\_2.

Division-9: numeratore (20 coll 4044 byte-20 constant 11 11 world 11

byte-1 AL=quotient AH=remainder world-2 Dx=remainder Ax=quotient

②ORG = ORigin → directive not instruction.

defines where the machine is to place in memory.

URG 100H ZITCH 1 segment - 2 64 KB max space OTITOS and machine code starts from 100H (offset address). Effective address -> C5:100H.

(2) model small -> you get a progreame greater of Code segment] - I max space 69 KB.

DS [data 11 ] 11 " " "

. MODE MEDIUM: CS can exceed X4KB

DS = 64KB.

· MODEL COMPACT: DS EXCUEDS > 64 KB.

Fan at Gon if 1 09 or our Alles be

than burgers of all bot collections are and about brushed a

· MODEL LARGE : DS., CS > 64KB. but signle set of DATA < 64KB.

. MODEL HUGE: DS, DS > 64 KB; curray data > 64 KB too.

, MODEL TINY : DS, CS = G4KB.

## Lab 2:

- 8051 Mc simulation tool -> keil C51.
- \* me is called computer on ehip. as it includes 'MP. with RAM, ROM, parallel and serial ports.
- \*Applications: Washing Machine, ved player, over, trobotics.
- \* 8051 -> 8 bit MicroController and 8bit Data bus:
  recods, writes, process 8 bit data.
  executes code from embedded MASKED ROM.
- \* Original Mes-51 family (Intel's) de was developed with N-mos (Ntype metal Oxide - servi conductore) consumes morce power
  - but theire predecessors mes-48 fancily used complementary miss (emos)

    > 89051)

    > merce suitable fore battery powered devices.
  - AT89C52 Atmel's 8051 family shit Me.
    - -has 8KB of flash preogrammable errasable read only memorry (PEROM)
    - -256 bytes of RAM.
    - Endurance of 1000 write/Errase aycle. antet erased/prægrammed to a max 1000 times.
  - Oscillatore: provides clock to 8051 mc [pins XTAL2, XTALI]
    decides the speed of mc.
    Cystal oscillatore -> noremally 11.0592 mHz frequency.
  - each point is 8 bit and bit addressable [can be set on reset by bit instructions ->> SETB (High), CLR (Low)
    - porct 0 -> dual worth; lower order address (AO to A7)

      multiplexed with PO.O to PO.7 is ADO to AD7

      address bus and douta bus is demultiplex by ALE signal and latch.

```
PI -> true 1/0 porch (PI.O to PI.7) [general I/O only]
  P2 > I/O as well as higher oreder address bus A8 to A15.
   P3 > dual fune. as I/O; also has specific function.
fore AT89X51 -> headen: # include <AT89X51,h>
Lab-3
ARDUINO: open source electriconics platform
          - reads inputs [light on a sensore]
          - turns into an output [activates motore]
     →UNO> 14 pins (digital) + 6 pins [analog] = 20
                                              = 70
     →MEGA→ 54 "
                             + 16
  UNO: DI3 - SPI SCK.
                                RXO->DO
         D12-> "
                    MISO
                               TXO > DI
                   IZOM
  MEGATO analog pins digital behaviour 3 (72) [oscillator = 16 MHZ]
 Andrino larg: 3 parts 1. Strencture
                         2. values [variables + constants]
                          3. functions
 Strencture: Shotch > 100p()
                        setup()
             Further > # define (define)
                      # include (include).
              Contral strace -> break ..
                              continue
                              do .... while
              Arcitmetic > % remainder
                            * mul
```

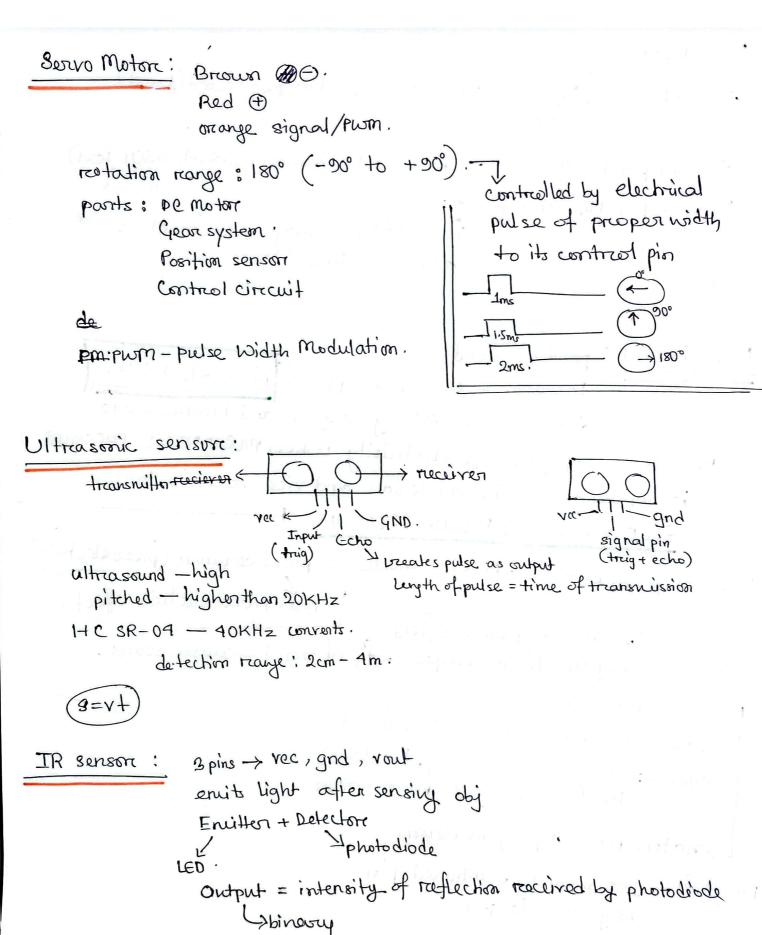
+ add

Functions:	
( digital Read (pin-no) : reads and returns high one low	
2 digital Wrate (pin-ro); writes.	
@digitalWrote (pin, value); wrotes high or low to a pin.	*
3 pinMode (*pin, node) returns nothing.	
configures pin's behaviour	
mode might be input output.	
Basic syntax:	
void setup(){	
11 to trun once	
<i>}</i>	
void loop () {	
// to reun respectedly	
À .	
Keypad	
1 4x3: R(4t1) C(3t1) 4x4: R(4x) C(4x) Left to right.	
func -> make Keymap (heys)> initilizes internal keymap to be equal to user defined keymap	
Livitale Vi 2	
syntax: Keypad kpd = Keypad (make Keymay (keys), rowlins, collins,	
class obj initialize defined and uino	
class obj  1 initialize defined anduino 4+4 pin	•
Social Communication: used fore communication both.	٠ د
Andeino board and other devices.	
10,1 pins are used.	

```
Social Functions:
  ① Serial. begin (9600) → sets data πade in bits por second (bound)
               typic > on 2002JT
 2 Sorial. proint ("Straing") -> proints data (human readable ASCII text)
 (3) Serial, proint In ("Strain") ->: "
                            followed by carriage recturn char 'vo' and a newline 'n'
                             and a newline
  >sktup(){}
Piezo Buzzer : produces sound.
               - based on inverse preinciple of piezo electricity
                discovered in 1880 by Jacques and Pierare Curve.
   Phenomena of generality electricity when MECHANICAL PRESSURE
      is applied to certain morterials and vice versa is true
        ELECTRICITY & MECHA PRESSURE .
Heart of piezo buzzer (DISC) > made of piezoceranic (piezoelectric)
                                          poses piezo electric effect.
           in alternating electric field
           they streech one correpross - for of signal - causes sound.
 Buzzer Functions:
    →tone(): → generates square wave.
    syntax: tone (pin, frequency, duration)
            ton (", ") aisso strok
   - notone (Ain) stops generation.
Buzzen: - pos si pin to mentioned pin
```

to gnd.

neg 11



6

6

es es

Intrared Sonson: - contains active enritter. - detects exact-position of obj - used: - garage door sys, industrial settings. PIR sensor: - constains no active enviller 3pms - car detects motion - used : security alorms, automatic lighting setups. VCC gnd Vout has 2 slot - each slot has IR made of most that is sensitive to IR. when sensore is idle both stol- detect- same amount of IR.

+detects warm objects one half-causes a positive differential chry both the 2 halves -> objet leaves . with reverse reaction . - regulive differential chry boths the halves.

\*\* Do not detect or measure heat

detects IR emitted by objects

thermal readiation. in infrared reage. purpose of lens -> widering sensing area; output "binarry.