Spring25 CS598YP

21.2: vLLM: PagedAttention

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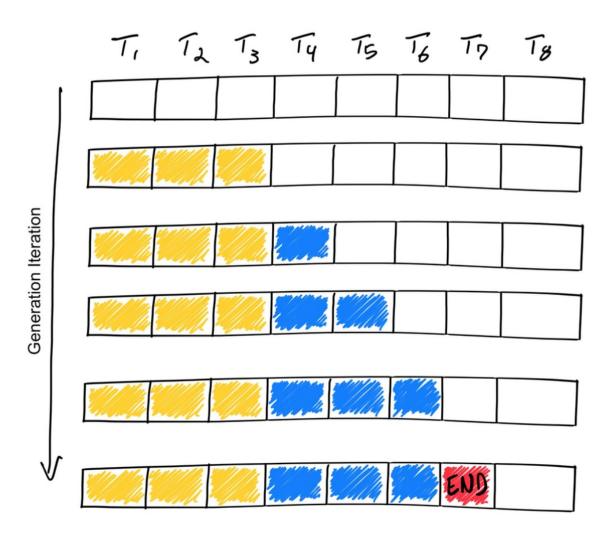
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Outline

- Recap: Orca's continuous batching
- What is **KV cache**?
- vllm's PagedAttention
- Scheduling inside vllm

Recap: Continuous batching

LLM inference basics



How does text generation work?

Iterative: each forward pass generates a single token

Autoregressive: generation consumes prompt tokens + previously generated tokens

Completion potentially decided by model: A generated token can be the end-of-sequence token

Legend:

- Yellow: prompt token
- Blue: generated token
- Red: end-of-sequence token

Static batching

- Batching multiple sequences on GPU, aka "static batching"
- Problem: GPU utilization drops as sequences complete

T_{i}	Tz	T3	Ty	Ts	T6	To	Tg
Si	Si	Si	SALL				
Sz	Si	SX					
Sz	S	Si	S				
Sy	Sy	Sy	Sy	Sy			

T_{i}	Tz	T3	Ty	Ts	16	To	Tg
Sil	Si	Si	SALL	S	end	,	
Sa	Sa	SA	SX	\$2/1	SH	SAL	END
Si	Si	S	S	END			
Sy	Sy	Sy	Sy	Sy	Sy	END	

Legend:

- Yellow: prompt token
- Blue: generated token
- Red: end-of-sequence token

Continuous batching

Top: static batching Bottom: continuous batching

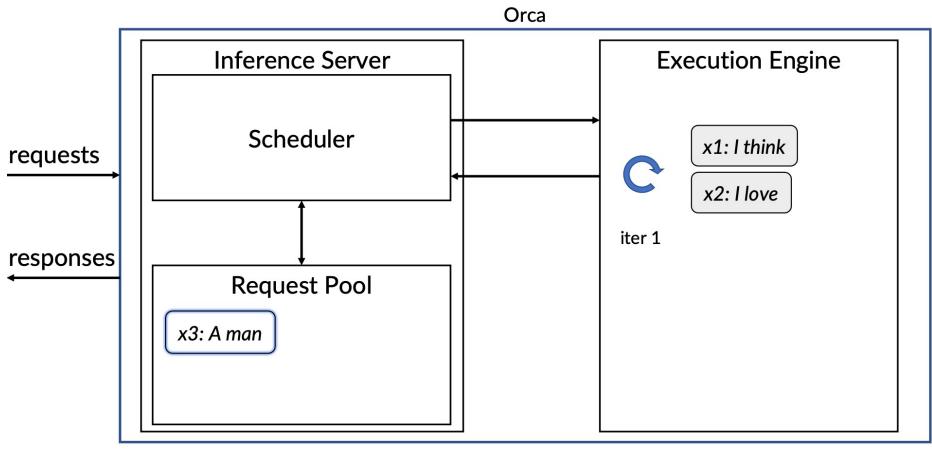
Legend:

Yellow: prompt token

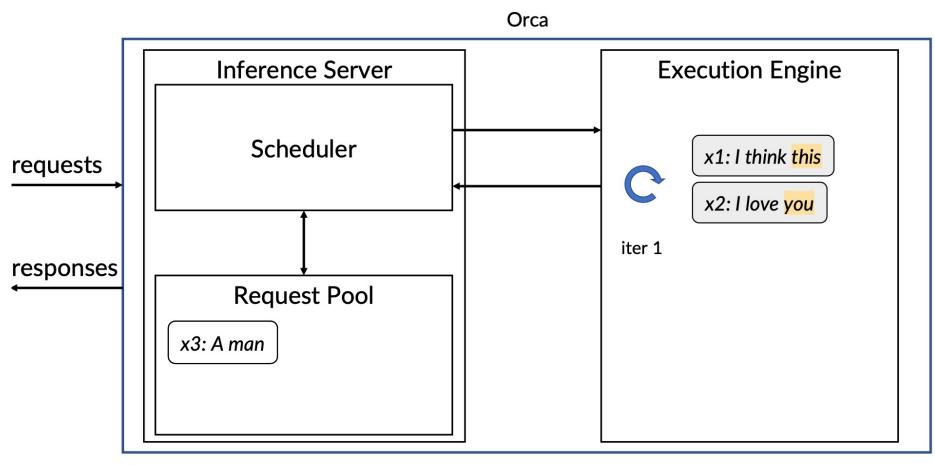
Blue: generated token

• Red: end-of-sequence token

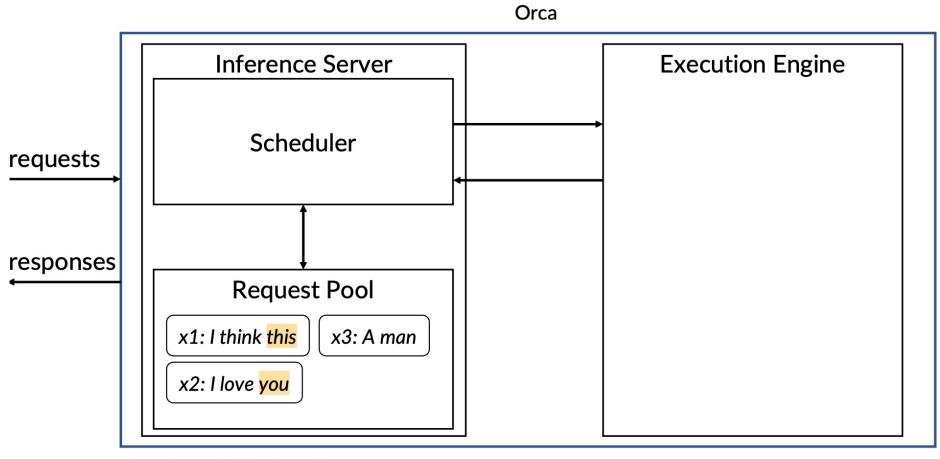
T, T2 T3 T4 T5 T6 T7 T8	T, T2 T3 T4 T5 T6 T7 T8
S. S. S.	S. S. S. S. S. EUS
Si Si Si	SI SI SI SI SI SI SI END
Sy Sy Sy	S3 S3 S3 END
Sy Sy Sy Sy	Sy Sy Sy Sy END
	T f f f f f f f f
T, T2 T3 T4 T5 T6 T7 T8	T, T2 T3 T4 T5 T6 T7 T8
T, T2 T3 T4 T5 T6 T7 T8	T, T2 T3 T4 T5 T6 T7 T8 S, S, S, S, S, S, S6
T, T ₂ T ₃ T ₄ T ₅ T ₆ T ₇ T ₈ S, S	week with a will not a suite of the suite with the suite of the suite
T ₁ T ₂ T ₃ T ₄ T ₅ T ₆ T ₇ T ₈ S ₁ S ₁ S ₁ S ₃ S ₃ S ₃ S ₃ S ₃	week with a will not a suite of the suite with the suite of the suite



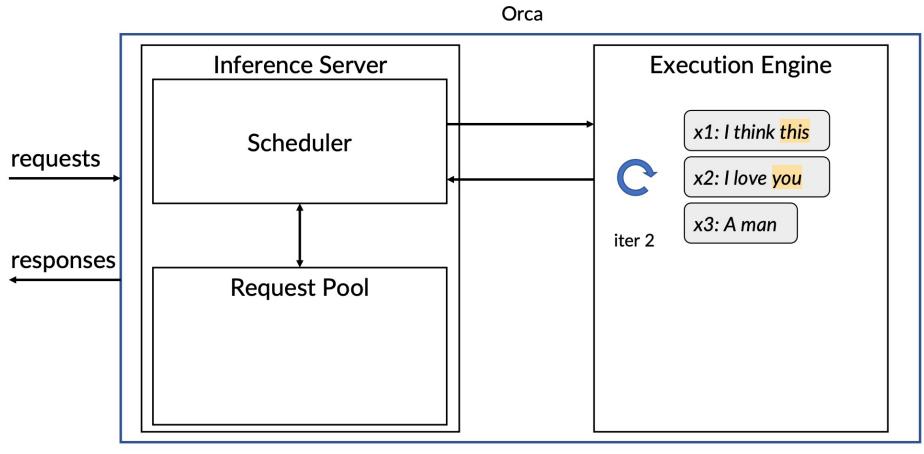
^{*} maximum batch size = 3



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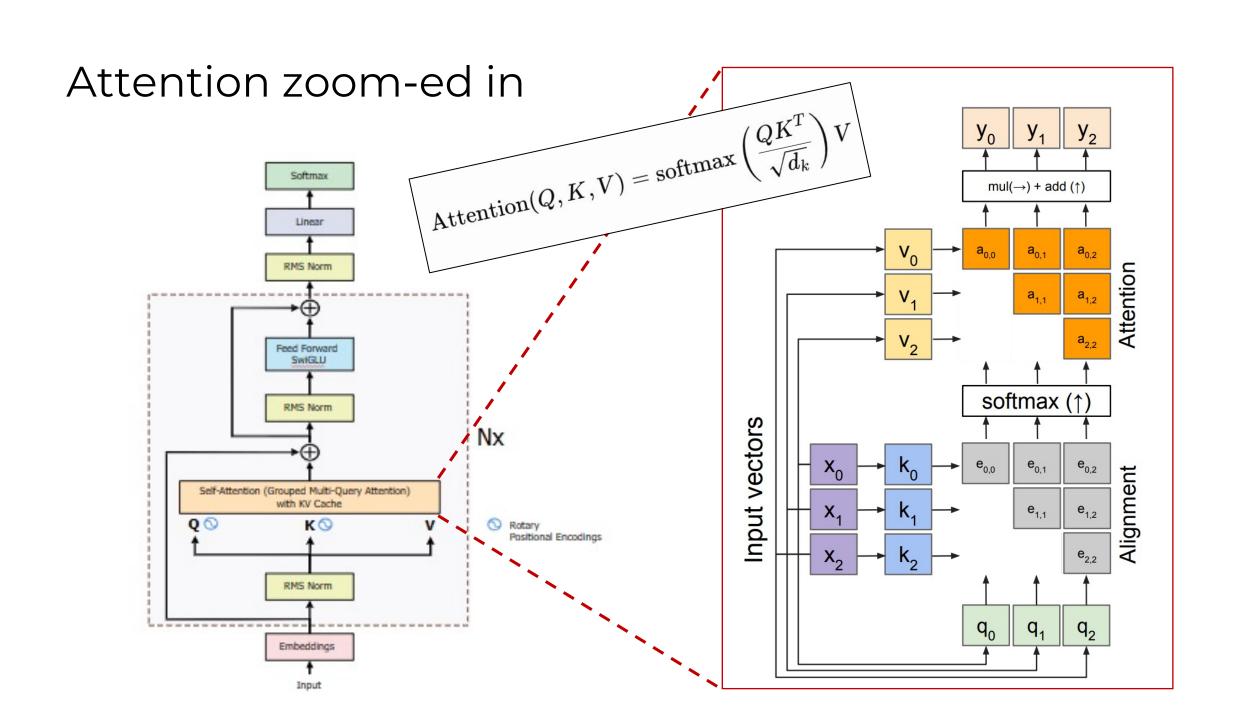


^{*} maximum batch size = 3

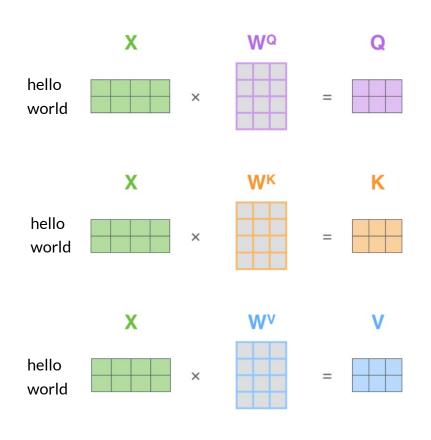


^{*} maximum batch size = 3

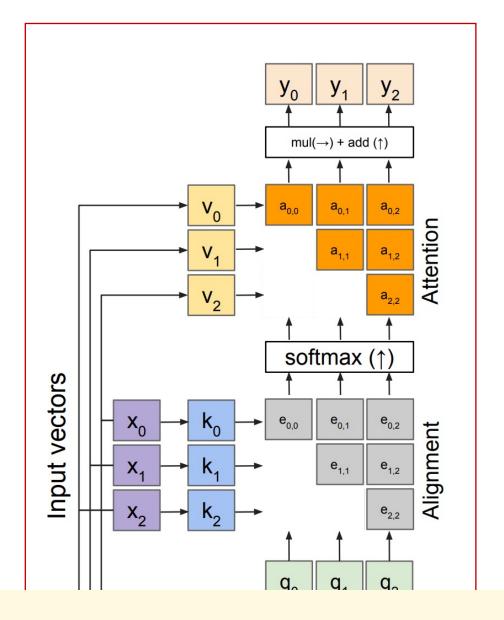
Attention and KV Cache



Getting K, Q, V is expensive



Dimension: 4,096 for Llama 3-8B



We can re-use K and V for previous tokens -> **KV Cache**

Continuous batching

Top: static batching Bottom: continuous batching

Legend:

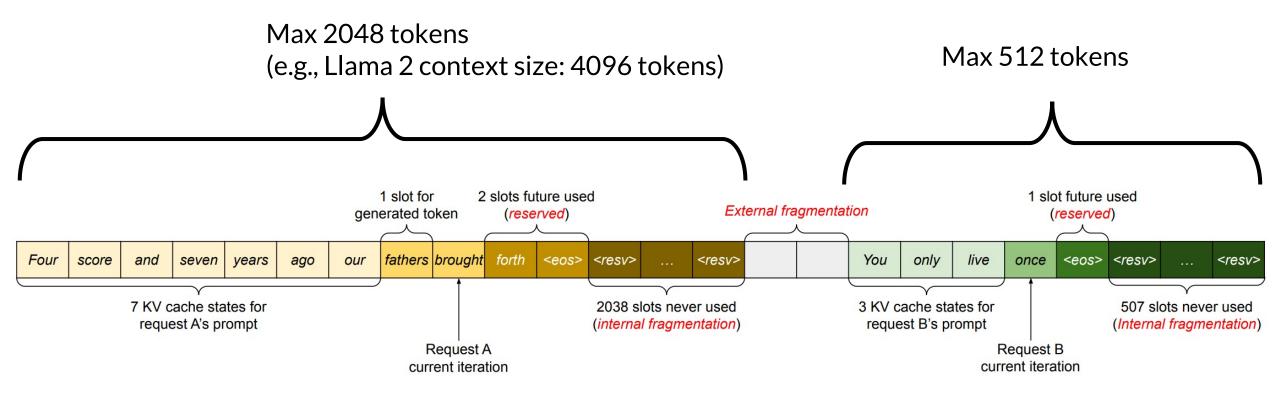
Yellow: prompt tokenBlue: generated token

Red: end-of-sequence token

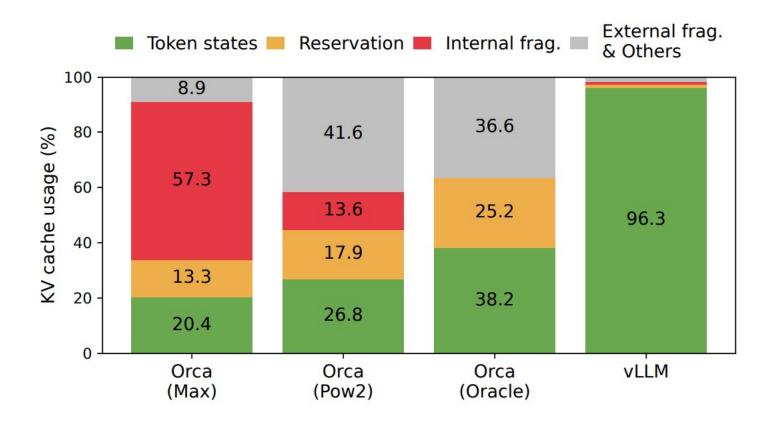
T,	Tz	T3	Ty	Ts	T6	To	TB
Sil	Si	Si	SNI				
Si	Sz	SX					
S_3	S	S	S				
Sy	Sy	Sy	Sy	Sy			
T,	Tz	T3	Ty	Ts	T6	To	Tg

T,	Tz	T3	Ty	Ts	76	To	Tg
Sil	Si	Si	SNI	S,	EN		
Sa	Sz	SXI	Sz	81	83	SA	END
Sz	Si	S	S	END			
Sy	Sy	Sy	Sy	Sy	Sy	ENI	
T,	Tz	T_3	Ty	Ts	76	To	Tg
Si	Si	Si	\$///	\$///	END	56	Sb
Sa	Sa	5/4/	Sal	\$4/1	8/	Shu	END
Si	Si	S	S	END	Ss	55	85/11
						. 44 . 10 10 40	

Fragmentation -> Wasted GPU memory

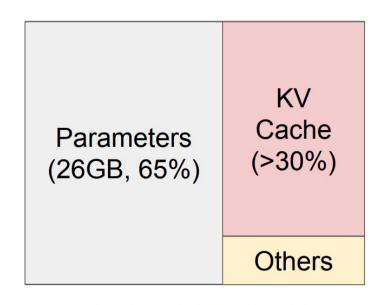


Much GPU memory is wasted

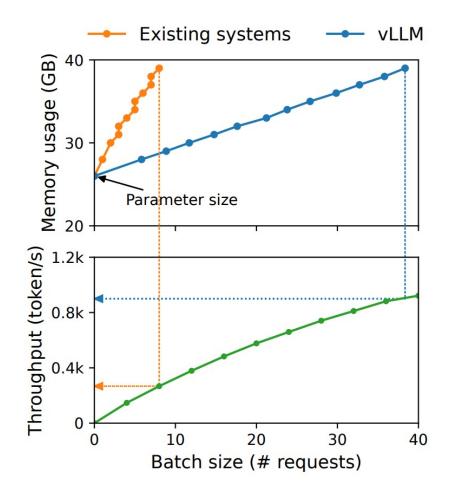


- Orca (Oracle). We assume the system has the knowledge of the lengths of the outputs that will be actually generated for the requests. This shows the upper-bound performance of Orca, which is infeasible to achieve in practice.
- Orca (Pow2). We assume the system over-reserves the space for outputs by at most 2×. For example, if the true output length is 25, it reserves 32 positions for outputs.
- Orca (Max). We assume the system always reserves the space up to the maximum sequence length of the model, i.e., 2048 tokens.

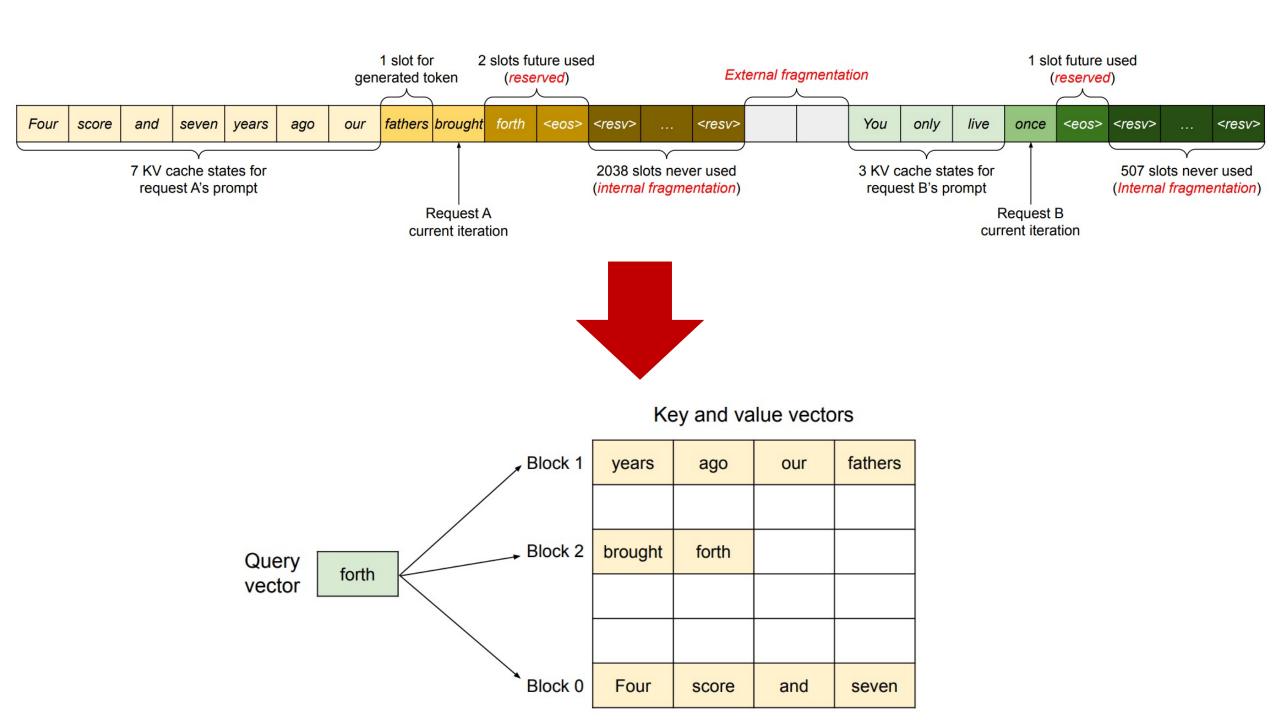
GPU memory is insufficient



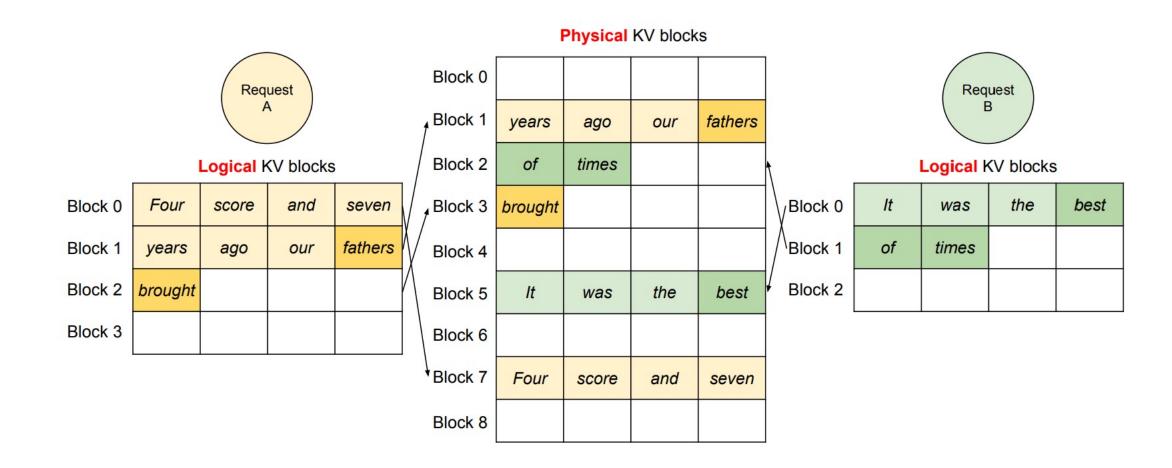
NVIDIA A100 40GB



PagedAttention and Scheduling



Pages can be shared by multiple prompts

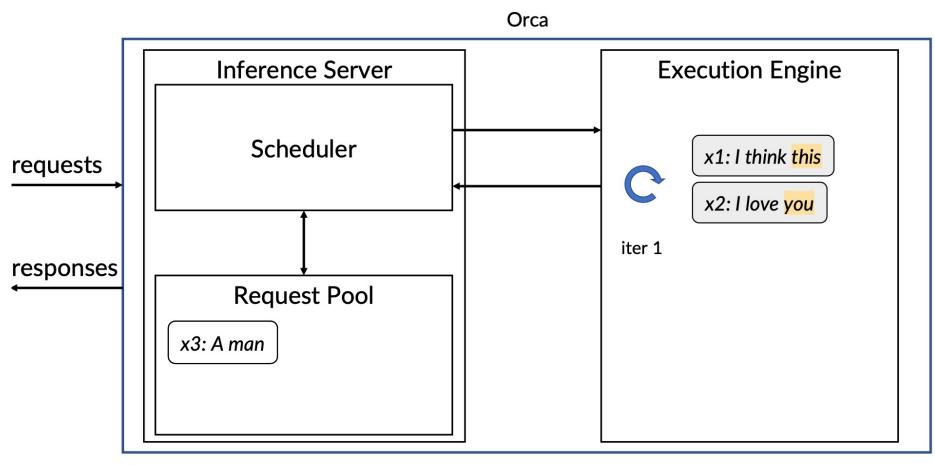


Attention computation needs additional lookups

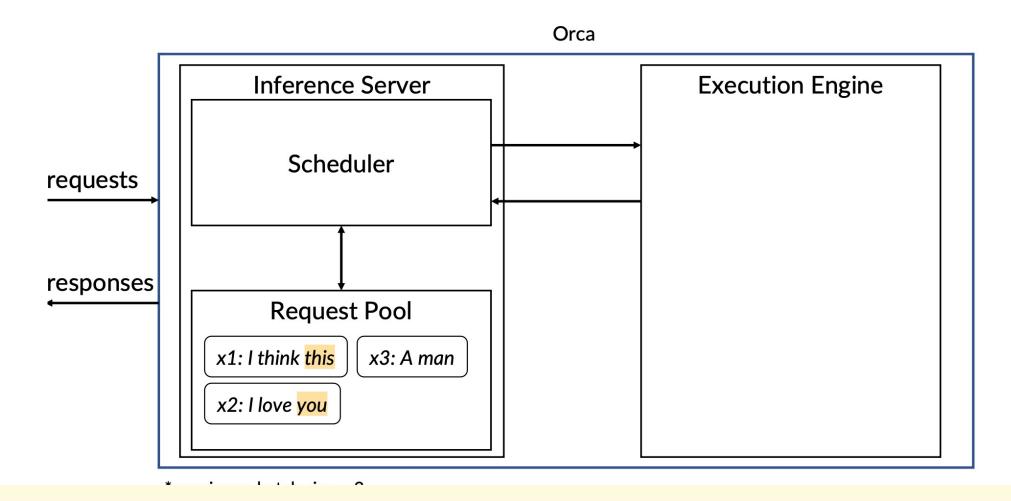
```
46
     namespace vllm {
                       TION_SIZE - U/ // Zero means no partitioning.
90
      device void paged attention kernel(
91
         float* __restrict__ exp_sums, // [num_seqs, num_heads, max_num_partitions]
         float* __restrict__ max_logits, // [num_seqs, num_heads,
92
93
                                       // max_num_partitions]
94
         scalar_t* __restrict__ out, // [num_seqs, num_heads, max_num_partitions,
95
                                    // head size]
96
         97
         const cache_t* __restrict__ k_cache, // [num_blocks, num_kv_heads,
98
                                            // head size/x, block size, x]
         const cache t* restrict__ v_cache, // [num_blocks, num_kv_heads,
99
                                            // head size, block size]
100
101
         const int num kv heads,
                                            // [num heads]
102
         const float scale,
         const int* restrict block tables, // [num seqs, max num blocks per seq]
103
104
         const int∗ restrict seg lens,
                                            // [num seqs]
105
         const int max_num_blocks_per_seq,
106
         const float* __restrict __alibi slopes, // [num_heads]
         const int q stride, const int kv block stride, const int kv head stride,
107
         const float* k_scale, const float* v_scale, const int tp_rank,
108
109
         const int blocksparse_local_blocks, const int blocksparse_vert_stride,
         const int blocksparse_block_size, const int blocksparse_head_sliding_step) {
110
```

No more blocks -> Preempt (i.e., kick out) requests

```
vllm > v1 > core > sched > ♦ scheduler.py > ★ Scheduler > ♦ schedule
      class Scheduler(SchedulerInterface):
115
           def schedule(self) -> SchedulerOutput:
 TQQ
189
                   while True:
                       new_blocks = self.kv_cache_manager.allocate_slots(
190
191
                           request, num_new_tokens)
192
                       if new_blocks is None:
193
                           # The request cannot be scheduled.
                           # Preempt the lowest-priority request.
194
                           preempted_reg = self.running.pop()
195
196
                           self.kv_cache_manager.free(preempted_req)
                           preempted_req.status = RequestStatus.PREEMPTED
197
                           preempted_req.num_computed_tokens = 0
198
                           if self.log_stats:
199
200
                               preempted_req.record_event(
                                    EngineCoreEventType.PREEMPTED, scheduled_timestamp)
201
202
                           self.waiting.appendleft(preempted reg)
203
                           preempted_reqs.append(preempted_req)
204
205
                           if preempted reg == request:
                               # No more request to preempt.
206
207
                               can_schedule = False
208
                               break
209
                       else:
                           # The request can be scheduled.
210
211
                           can_schedule = True
212
                           break
                   if not can schedule:
213
                       break
214
                   assert new blocks is not None
215
```



^{*} maximum batch size = 3



X2 may be pre-empted for the next iteration, processing only X1

Summary

- KV cache allows avoiding expensive operations for previous tokens
- vllm proposes *PagedAttention*, which incrementally allocates memory
- For each iteration, determines what requests to preempt / run

Questions?