AUSTIN FISK

Http://pages.cs.wisc.edu/~fisk/ | 608-393-5902 | fiskaustin.af@gmail.com

Education and Training

University of Wisconsin - Madison — Madison, WI, USA Bachelor of Science in Computer Science Graduated: May 2018

University of Wisconsin Baraboo/Sauk County — Baraboo, Wisconsin, USA

September 2013 - December 2015 Deans List: Spring 2014, Spring 2015

Overview

- https://pages.cs.wisc.edu/~fisk/: Automated Chicken Door, Addressable Motorcycle Headlights, Thermostat, Crowd Gate Circuit
- **Test Automation:** Client and Internal DAI and Eggplant Functional Automations, Client Support, Client Training, Employee Training, Best Practices Training

Experience

Embedded Software Engineer

Jan 2021 to Current

Reconyx — Verona, WI

- Bus Traffic Configuration
- Persisting User Config
- SDK Modifications

Test Automation Developer

Jan 2019 to Current

MCANTA — Madison, WI

- Robot Process Automation and Robot Test Automation
- Working with clients to ensure understanding of automation best practices

Network Services Student Jun 2017 to May 2018

DolT (Division of Information Technologies) — Madison, WI

Math Tutor Mar 2014 to Dec 2015

University of Wisconsin - Baraboo/Sauk County — Baraboo, WI

Soldering Specialist Nov 2013 to Jan 2015

Private Contracting — Madison, WI

Skills

- Certifications: ISTQB Foundation Level
 1 | Eggplant: Functional Expert,
 Functional Genius, Al Genius,
 Monitoring Insights Expert, Monitoring
 Insights Genius | Qentinel Pace:
 Foundation, Advanced
- Programming Languages: Python, Java, Visual Basic, Assembly, C, HTML, JavaScript, CSS, SenseTalk, PaceWords
- Object Detection: YOLO V3 & V5
 Object Detection, Google Vision, Azure
 Vision API & Evaluation, Firebase
 Queries
- Optimization/Speed: Multi Thread on Embedded Systems, Bus Traffic Configuration, Power Saving, Interrupts, Measuring Speed with Ossilloscope
- Failure Evaluation: Photo Metadata, Firebase Queries
- User Interface Design: UI Mock Up, UI Design, Persisting Options
- Hardware Skills: Oscilloscope
 Debugging, Simple Circuit Design