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	APJ ABDUL KALAM TECHNOLOGICAL UNIVERSITY FIFTH SEMESTER B.TECH DEGREE EXAMINATION, DECEMBER 2017	
	Course Code: EE305	
	Course Name: POWER ELECTRONICS (EE)	
May N	Marks: 100 Duration: 3	Hours
Max. N	Graph sheets will be supplied.	
	PART A	
/	Answer all questions, each carries 5 marks.	Marks
1/	Draw the circuit for two transistor analogy of silicon controlled rectifier and	(5)
1 /	briefly describe the working.	
2 /	Derive the expression for the output voltage of half wave controlled rectifier with R load.	(5)
3	Draw the input and output voltage waveforms of $3\emptyset$ half controlled rectifier with R load for a firing angle of 30° .	(5)
1/	What are the different classifications of inverters?	(5)
₹ ⁷	Explain the terms modulation index and frequency modulation ratio related to pulse width modulation.	(5)
6	What are the control strategies for the regulation of output voltage in ac	(5)
8	voltagecontrollers? Explain time ratio control method to vary the output voltage in choppers. Derive an expression for average output voltage in terms of input dc voltage and duty cycle for a step up chopper.	(5) (5)
	PART B	
/ -	* Answer any twofull questions, each carries 10 marks.	
a)	Derive the expression for resistance used for static voltage equalisation for a	(5)
b)	series connected string. In a power circuit, 4 SCRs are to be connected in series in a string to handle 6kV	(5)
U)	and 1kA. The voltage and current ratings of SCRs are 1800V and 1000A and have a maximum difference in their blocking currents of 10mA. Difference in recovery charge is 10µC. Design a suitable equalizing circuit with figure.	
0, (A single phase semi converter delivers a constant load current Io . Express its	
·······································	source current in Fourier Series and derive the expressions for displacement	(10)
· / Y	factor and current distortion factor.	
(1 a)	Explain the structure & principle of operation of IGBT.	(5)
/ b)	Draw RC triggering circuit for SCR and explain with relevant wave forms.	(5)