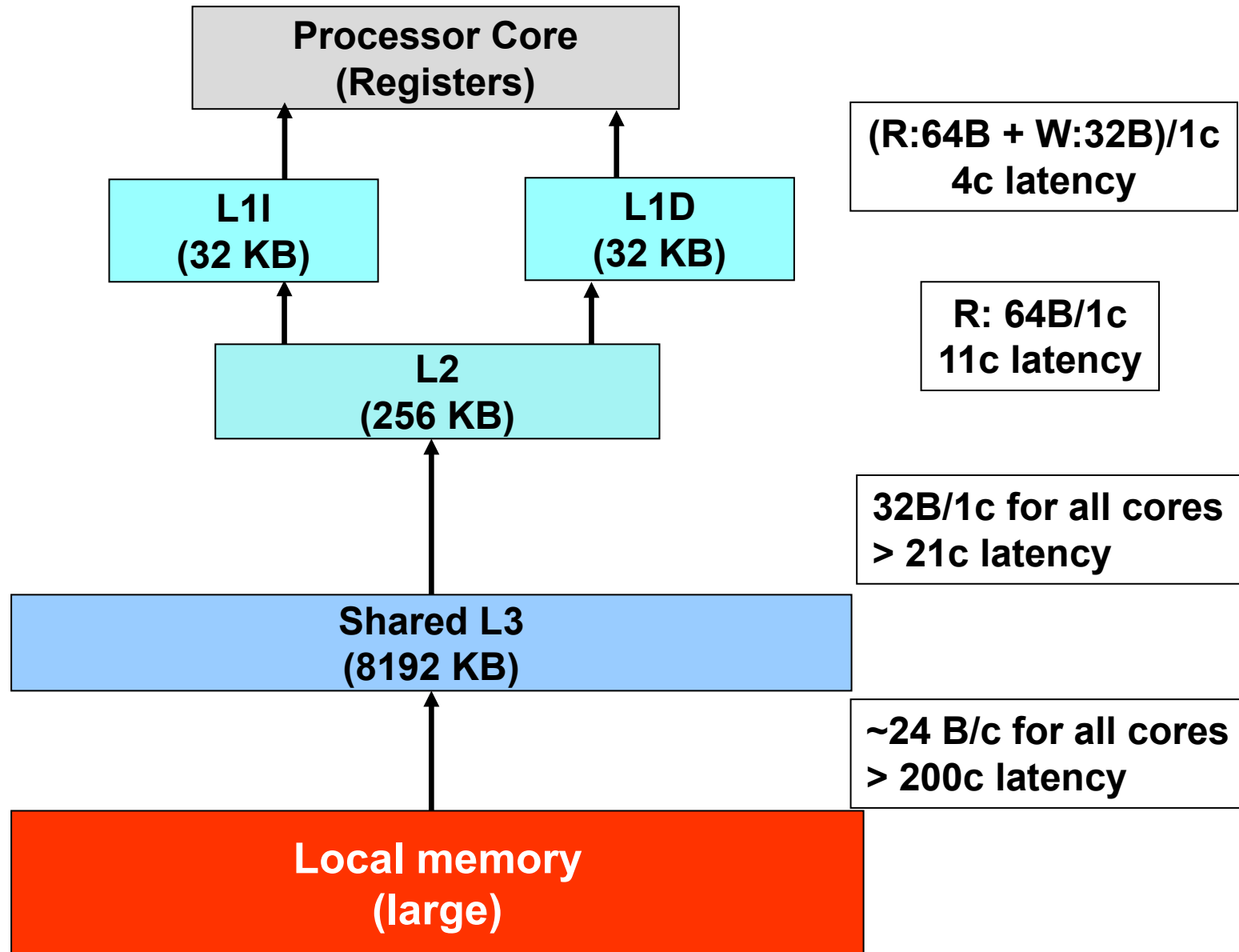


Cache Hierarchy

- From CPU to main memory on a **Haswell** processor
 - With multicore, memory bandwidth is shared between cores in the same processor (socket)



c = cycle