				PM	od Inte	erface	9										120	C Intoufood			
		Sing	le Width	PMod		Double Width PMod								I2C Interface							
Hos	Host Peripheral						Host				Peripheral										
1	1 1					1 7					7	1									
2	2	E	2			2	8		Е		8	2		1	2		E	SCL	SCL		
3	3	d	3			3	9		d		9	3		3	4		d	SDA	SDA		
4	4	g	4			4	10		g		10	4		5	6		g	GND	GND		
5	5	е	5			5	11		е		11	5		7	8		е	VCC	VCC		
6	6		6			6	12				12	6									
	0.40	0.1	15"			Тор	Bot	0.45	"	0.20'	Bot	Тор									
Hos	st			Peripheral		Host Peripheral								,	l ²	² C Mo	dule PCB	3			
		 - -		☐ 1 ☐ 2 ☐ 3 ☐ 4 ☐ 5 ☐ 6 ☐ 6 ☐ Right angle male connector		2	7 8 9 10 11 12 12 14 at angle f	15" < emale		B			7	0.80"	→ 0.30" → →	7 (→ 0.20'	t angle ma	le	_		
	Fig 1: Standard Six-Pin Connector Placement						Fig 2: Standard Twelve-Pin Connector Placement							connector Fig 3: Standard Module Dimensions							

Pmod Pin	Standard Pin				ndard Pins		Pm Pi	od ns
1	2	Ю		1	2		7	1
2	4	Ю	Е	3	4	Е	8	2
3	6	Ю	d	5	6	d	9	3
4	8	Ю	g	7	8	g	10	4
5	10	GND	е	9	10	е	11	5
6	12	VCC		11	12		12	6
7	1	Ю						
8	3	Ю						
9	5	Ю						
10	7	Ю						
11	9	GND						
12	11	VCC						
Pmod	Standard			Ctou	ndard		Pm	
Pmoa Pin	Pin				ins			ioa ins
1	11	Ю		12	11		7	1
2	9	Ю	Е	10	9	Е	8	2
3	7	Ю	d	8	7	d	9	3
4	5	Ю	g	6	5	g	10	4
5	3	GND	е	4	3	е	11	5
6	1	VCC		2	1		12	6
7	12	Ю						
	10	IO						
8	10							
8 9	8	Ю						
		10 10						
9	8							

Type 1 GPIO		Type 1 Expanded	A GPIO	Type SPI	2		Type 2	A SPI	Type UAR	3 T		Type -	4 T		Type 4 Expanded	IA UART		Type 5 H-Bridg			Type Dual H-E	6 Bridge
Pin Signal	Direction	Pin Signal	Direction	Pin Signal	Direction	Pin	Signal	Direction	Pin Signal	Direction	Pin	Signal	Direction	Pin	Signal	Direction	Pin	Signal	Direction	Pin	Signal	Direction
1 101	In/Out	1 IOA1	In/Out	1 SS	Out	1 S	S	Out	1 CTS	Out	1	CTS	In	1	CTS	In	1	DIR	Out	1 I	DIR1	Out
2 102	In/Out	2 IOA2	In/Out	2 MOSI	Out	2 N	MOSI	Out	2 RTS	In	2 TXD Out		Out	2 TXD Out		2 EN Out		2 1	EN1	Out		
3 IO3	In/Out	3 IOA3	In/Out	3 MISO	In	3 N	MISO	In	3 RXD	In	3	RXD	. In		RXD	In	3 SA In		3 I	3 DIR2 Out		
4 104	In/Out	4 IOA4	In/Out	4 SCK	Out	4 S	CK	Out	4 TXD	Out	4	RTS	Out	4	RTS	Out	4	SB	In	4 I	EN2	Out
5 GND		5 GND		5 GND		5 G	SND		5 GND		5	GND		5	GND		5	GND		5 (GND	
6 VCC		6 VCC		6 VCC		6 V	CC 22		6 VCC		6	VCC		6	VCC		6	VCC		6 1	vcc	
		7 IOB1	In/Out			7 11	NT	In						7	INT	In						
		8 IOB2	In/Out				RESET	Out							RESET	Out						
		9 IOB3	In/Out			9 N		N/S						-	N/S	N/S						
		10 IOB4	In/Out			10 N		N/S							N/S	N/S						
		11 GND				11 G									GND							
		12 VCC				12 V	CC C								VCC							
				SS - Slave Select. Act slave device					CTS - Permission for send data to host	peripheral to	signal is asserted		transmit when thi	nis CTS - Device will only transmit when this signal is asserted		DIR - Motor direction		DIR1 - high	DIR1 - Motor/Phase 1 direction, active high			
				MOSI - Master Out Sia master to slave	ave In. Data from				RTS - Request from plata to host	peripheral to send	nd RTS - Device is ready to receive data		RTS - Device is ready to receive data		EN - Motor enable, active high		EN1 - Motor/Phase 1 enable					
				MISO - Master In Slav slave to master	e Out. Data from				RXD - Data from peri	pheral to host	ost RXD - Data from peripheral to host		heral to host	RXD - Data from peripheral to host		SA - feedback sense A		DIR2 - Motor/Phase 2 direction, active high				
				SCK - Serial clock. Da master to slave	ta clock from						TXD - Data from host to peripheral		TXD - Data from host to peripheral		SB - fee	edback sense B		EN2 - Motor/Phase 2 enable		enable		
														INT - Inte host	errupt signal fr	om peripheral to						
														RESET -		for host to reset						
														module t	hese pins may	pending on the be unconnected pecific inputs or						

	Pmod "High Sp	peed"		XADC Pm	od		Low Spee	d
Pin	Signal	LVDS Pair	Pin	Signal	LVDS Pair	Pin	Signal	LVDS Pair
1	JA1_P	10.4	1	JA1_P	JA1	1	IO1	
2	JA1_N	JA1	2	JA2_P	JA2	2	IO2	NA
3	JA2_P	JA2	3	JA3_P	JA3	3	IO3	INA
4	JA2_N	UNZ	4	JA4_P	JA4	4	IO4	
5	GND		5	GND		5	GND	
6	VCC		6	VCC			VCC	
7	JA3_P	JA3	7	JA1_N	JA1	7	IO1	
8	JA3_N	JA4	8	JA2_N	JA2	8	IO2	NA
9	JA4_P		9	JA3_N	JA3	9	IO3	147.
	JA4_N	67 ()		JA4_N	JA4	10	IO4	
11	GND		11	GND		11	GND	
12	VCC		12	VCC		12	VCC	
* 0 Ohm * ESD pr	series resistor otection		* No seri * ESD pr	es resistor otection			hm series resisto mes ESD protect	
	TMDS_33 input	+ output		~XADC Pm	od		LVDS_25 input/o	output
	LVDS_25 input		Pin	Signal	LVDS Pair			
				JA1_N	JA1			
				JA2_N	JA2			
				JA3_N	JA3			
				JA4_N	JA4			
				GND				
				VCC				
				JA1_P	JA1			
				JA2_P	JA2			
				JA3_P	JA3			
				JA4_P	JA4			
				GND				
			12	VCC				

Digilent Arty	Artix 7		100E				
JA	Low Speed						
JB	High Speed	0 4	No ESD?, JB1 - SRCC, JB2 - MRCC				
	High Speed	Group 1	No ESD?				
JD	Low Speed		Uses mostly clock pins				
Digilent Nexys Video	Artix 7	GigE, HDMI In, HD	MI Out, DP Out FMC(2 x HDMI In)				
JA	Low Speed	Group 1					
JB	High Speed	Crown 2					
JC	High Speed	Group 2					
JXADC	XADC	Group 3	100 Ohm Resistor				
Digilent Nexys 4 DDR	Artix 7						
JA	Low Speed						
JB	Low Speed						
JC	Low Speed						
JD	Low Speed						
JXADC	XADC						
<u>Digilent Genesys 2</u>	Kintex 7	GigE, HDMI In, HDMI Out, [DP In, DP Out, VGA Out FMC(2 x HDMI In)				
	Kintex 7 High Speed		DP In, DP Out, VGA Out FMC(2 x HDMI In)				
JA		GigE, HDMI In, HDMI Out, [DP In, DP Out, VGA Out FMC(2 x HDMI In)				
JA JB	High Speed	Group 1	DP In, DP Out, VGA Out FMC(2 x HDMI In)				
JA JB JC	High Speed High Speed		DP In, DP Out, VGA Out FMC(2 x HDMI In)				
JA JB JC	High Speed High Speed Low Speed Low Speed	Group 1	DP In, DP Out, VGA Out FMC(2 x HDMI In)				
JA JB JC JD	High Speed High Speed Low Speed Low Speed XADC	Group 1 Group 2 Group 3	DP In, DP Out, VGA Out FMC(2 x HDMI In) HDMI BiDir, VGA Out				
JA JB JC JD JXADC Digilent Zybo	High Speed High Speed Low Speed Low Speed XADC	Group 1 Group 2 Group 3					
JA JB JC JD JXADO Digilent Zybo JA	High Speed High Speed Low Speed Low Speed XADC Zynq	Group 1 Group 2 Group 3 GigE,					
JA JB JC JD JXADC Digilent Zybo JA JB	High Speed High Speed Low Speed Low Speed XADC Zynq XADC	Group 1 Group 2 Group 3 GigE, Group 1					
JA JB JC JD JXADC Digilent Zybo JA JB JB JC	High Speed High Speed Low Speed Low Speed XADC Zynq XADC High Speed	Group 1 Group 2 Group 3 GigE,					
JA JB JC JD JXADC Digilent Zybo JA JB JC JD JXADC DIGILENT Zybo	High Speed High Speed Low Speed Low Speed XADC Zynq XADC High Speed High Speed	Group 1 Group 2 Group 3 GigE, Group 1	HDMI BiDir, VGA Out				
JA JB JC JD JXADC Digilent Zybo JA JB JC JD JS JD JS JC JD JD JE	High Speed High Speed Low Speed Low Speed XADC Zynq XADC High Speed High Speed High Speed	Group 1 Group 2 Group 3 GigE, Group 1	HDMI BiDir, VGA Out				
JA JB JC JD JXADC Digilent Zybo JA JB JC JD JS JD JS JC JD JD JE	High Speed High Speed Low Speed Low Speed XADC Zynq XADC High Speed High Speed High Speed Low Speed Low Speed Low Speed	Group 1 Group 2 Group 3 GigE, Group 1 Group 2 Group 3	HDMI BiDir, VGA Out JD3 - SRCC				
JA JB JC JD JXADC Digilent Zybo JA JB JC JD JB JC JD JE JE ZedBoard	High Speed High Speed Low Speed Low Speed XADC Zynq XADC High Speed High Speed High Speed Low Speed Low Speed Low Speed	Group 1 Group 2 Group 3 GigE, Group 1 Group 2 Group 3	HDMI BiDir, VGA Out JD3 - SRCC MIO PMOD?				

104	11: 1 0 1							
	High Speed							
	High Speed	Group 2						
JE1	Low Speed		MIO PMOD?					
<u>Digilent PYNQ</u>	Zynq	GigE	E, HDMI In, HDMI Out					
PMODA	High Speed	Group 1	200 Ohm Resistor, JA3 - MRCC					
PMODB	High Speed	Oloup 1	200 Ohm Resistor					
TOFE LowSpeedIC			Check Spacing					
P2	XADC							
P1	XADC	Group 1						
P4	Low Speed							
P3	Low Speed	Group 2						
Numato Mimas V2	Spartan 6		Check Spacing					
P6	~XADC							
P7	~XADC							
P8	~XADC							
P9	~XADC							
Numato Waxwing	Spartan 6		Check Spacing					
P3	XADC							
P5	XADC							
P4	XADC							
MicroZed	Zynq							
J5			MIO PMOD???					
MyIRTech Z-turn with IO Cape	eZynq							
XADC?								
PMOD1?								
PMOD2?								
PMOD3?								
Pipistrello	Spartan 6							
PMOD	Low Speed		No resistors or ESD					
icoBoard	Lattice??							
P1	Low Speed		Check					
P3	Low Speed		Check					

P2 Low Speed	Check
P4 Low Speed	Check

	Numato Numato			^	lumato			Numato			Numato		Digilent					
HDM	II Transmitter			Gigabit Ethe														
	nsion Module		Expan	sion Module)	•	oansion Mod			lay Expansio			D Expansion			Full-sized SE		
	mi-transmitter-expa	ansion-mo			et-expansion	http://numato.com		on-module	http://numato.co		pansion-module/		om/micro-sd-exp	ansion-module/	://store.digilentin	•	ull-sized-sd-card-	
2)	x Dual PMOD		3 x	Dual PMOD		1 x I	Dual PMOD			2 x Dual PMOD			1 x Dual PMOD			1 x Dual PMOD		
Module	~XADC	P1	Module	Low Speed	P1	Module	Low Speed	P1	Module	Low Speed	P1	Module	Low Speed	P1	Module	Low Speed	P1	
PMod Pin	Signal	Numato Pin	PMod Pin	Signal	Numato Pin	PMod Pin	Signal	Numato Pin	PMod Pin	Signal	Numato Pin	PMod Pin	Signal	Numato Pin	PMod Pin	Signal	Direction	
1	TMDS-TX-2_P	2	1	NC	2	1	CBUS0	2	1	R1	2	1	DAT2	2	1	SS	Out	
2	TMDS-TX-1_P	4	2	ETH_RXCTL	4	2	NC	4	2	G2	4	2	CMD	4	2	MOSI	Out	
3	TMDS-TX-0_P	6	3	ETH_RXD1	6	3	NC	6	3	G1	6	3	DAT0	6	3	MISO	In	
4	TMDS-TX-CLK_F	8	4	ETH_RXD3	8	4	NC	8	4	B1	8	4	NC	8	4	SCK	Out	
5	GND	10	5	GND	10	5	GND	10	5	GND	10	5	GND	10	5	GND		
6	VCC	12	6	VCC	12	6	VCC	12	6	VCC	12	6	VCC	12	6	VCC		
7	TMDS-TX-2_N	1	7	NC	1	7	TXD	1	7	R2	1	7	DAT3	1	7	DAT1		
8	TMDS-TX-1_N	3	8	ETH_RXD0	3	8	RTS	3	8	R0	3	8	CLK	3	8	DAT2		
9	TMDS-TX-0_N	5	9	ETH_RXD2	5	9	RXD	5	9	G0	5	9	DAT1	5	9	CD		
10	TMDS-TX-CLK_N	7	10	ETH_INT_B	7	10	CTS	7	10	В0	7	10	NC	7	10	WP		
11	GND	9	11	GND	9	11	GND	9	11	GND	9	11	GND	9	11	GND		
12	VCC	11	12	VCC	11	12	VCC	11	12	VCC	11	12	VCC	11	12	VCC		
Module	Low Speed	P2	Module	Low Speed	P2				Module	Low Speed	P2							
PMod Pin	Signal	Numato Pin	PMod Pin	Signal	Numato Pin				PMod Pin	Signal	Numato Pin							
1	TMDS-TX-SCL	2	1	NC	2				1	Vsync	2							
2	TMDS-TX-HOT	4	2	ETH_TXCLK	4				2	NC	4							
3	NC	6	3	ETH_TXD1	6				3	NC	6							
4	NC	8	4	ETH_TXD3	8				4	NC	8							
5	GND	10	5	GND	10				5	GND	10							
6	VCC	12	6	VCC	12				6	VCC	12							
7	TMDS-TX-CEC		7	NC	1				7	Hsync	1							
8	TMDS-TX-SDA	3	8	ETH_RXCLK	3				8	NC	3							
9	NC	5	9	ETH_TXD0	5				9	NC	5							
10	NC	7	10	ETH_TXD2	7				10	NC	7							
11	GND	9	11	GND	9				11	GND	9							
12	VCC	11	12	VCC	11				12	VCC	11							
			Module	Low Speed	P3													
			PMod Pin	Signal	Numato Pin													
			1	ETH_RESET_E	3 2													
			2	ETH_MDIO	4													
			3	MAC_SDA	6													
			4	FPGA_CLK	8													
			5	GND	10													
			6	VCC	12													
			7	ETH_TXCTL	1													
			8	ETH_MDC	3													
			9	MAC_SCL	5													
			10	SYSCLK	7													
			11	GND	9													
			12	VCC	11													