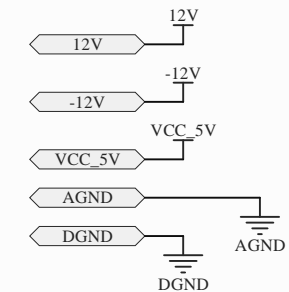


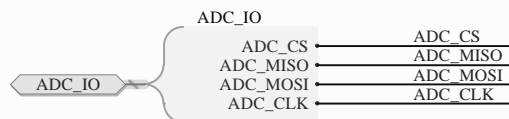
A

A



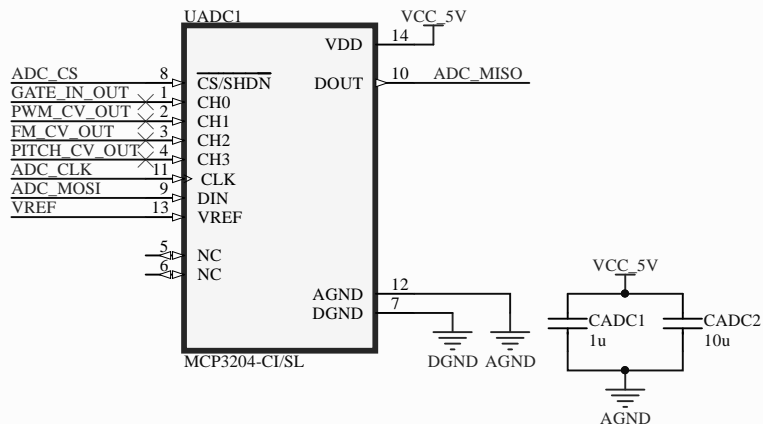
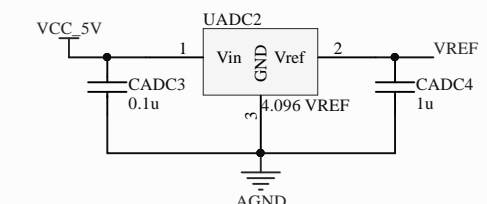
B

B



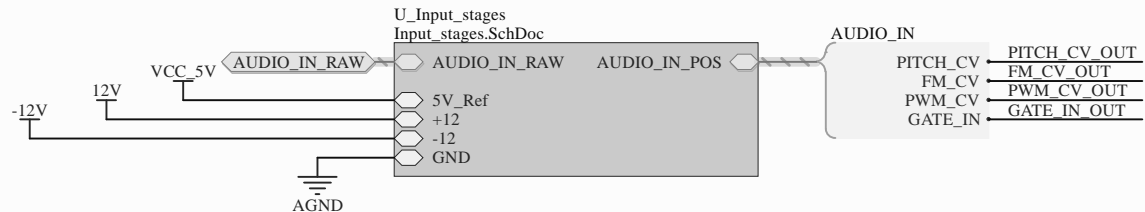
C

C



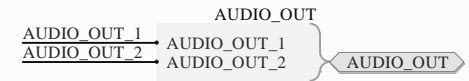
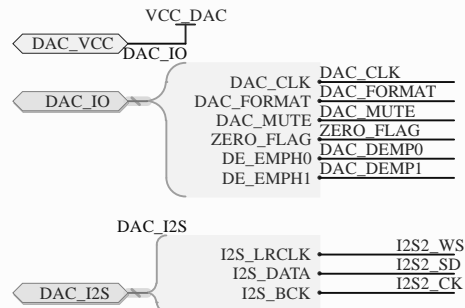
D

D



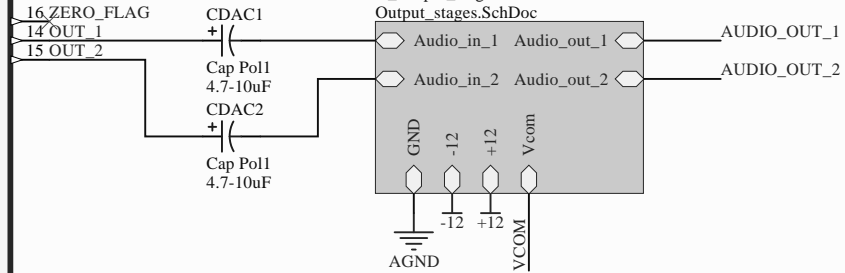
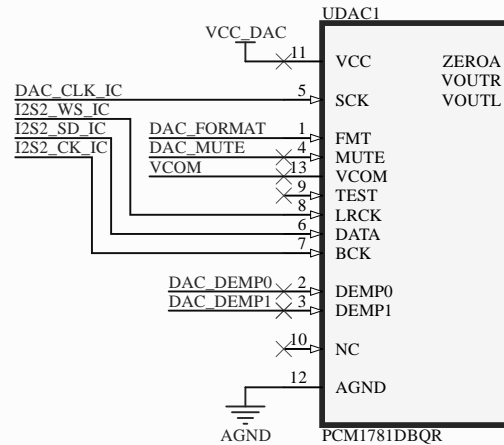
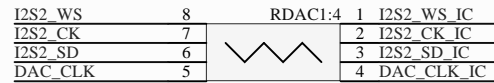
Title		
Size	Number	Revision
A4		
Date:	6/14/2020	Sheet of
File:	F:\Documents\...\ADC.SchDoc	Drawn By:

A



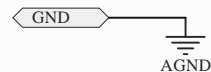
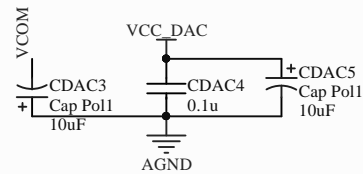
A

B



B

C



C

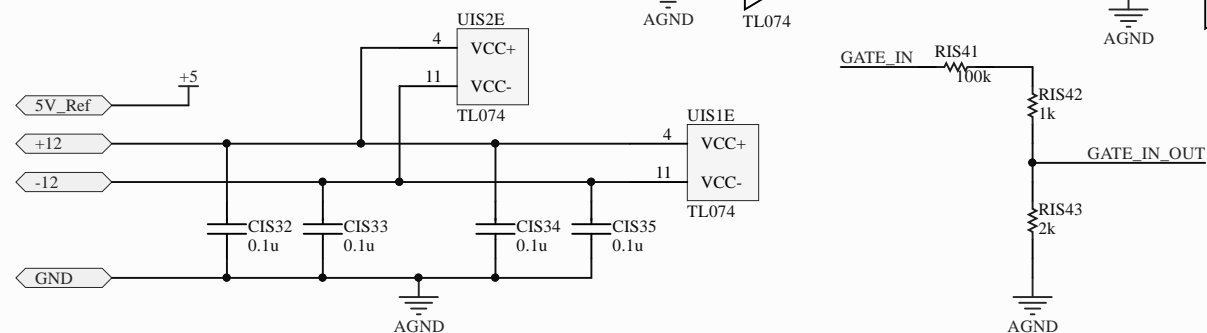
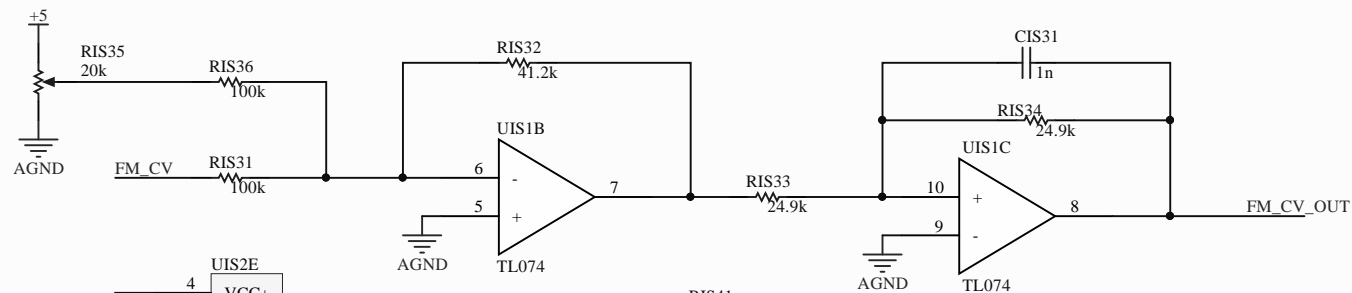
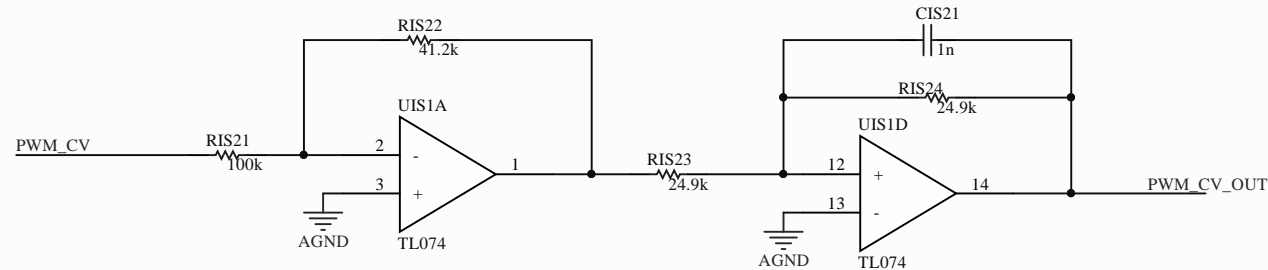
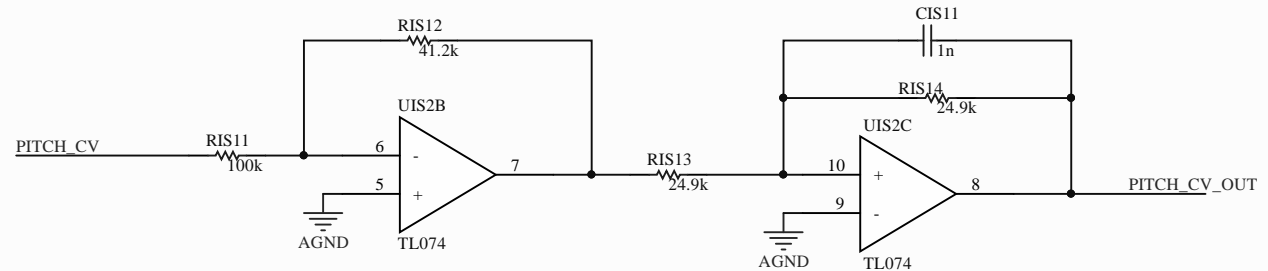
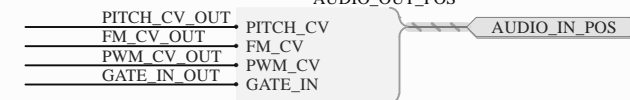
D

Title			
Size	Number		Revision
A4			
Date:	6/14/2020		Sheet of
File:	F:\Documents\...\DAC.SchDoc		Drawn By:

D



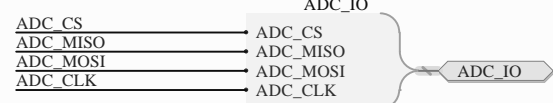
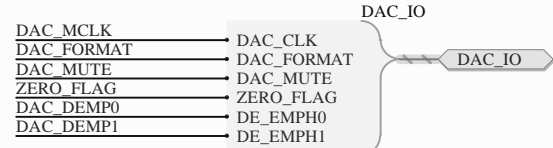
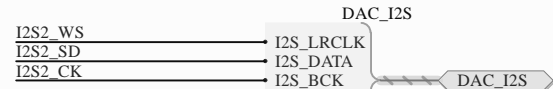
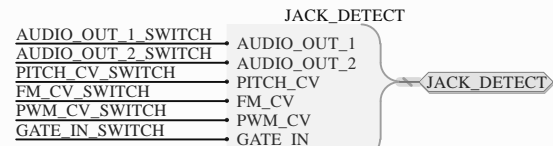
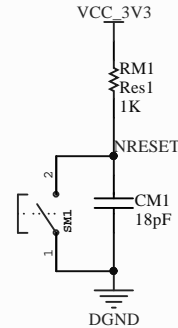
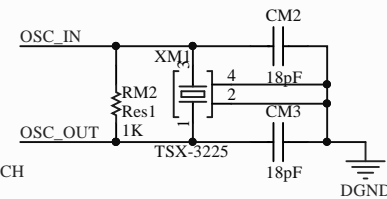
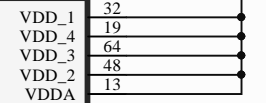
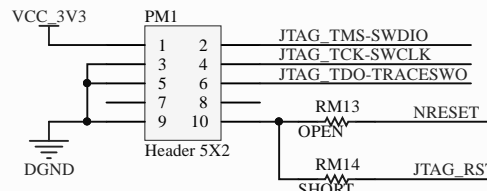
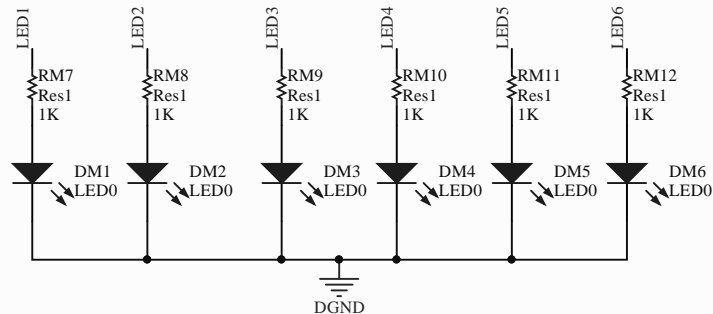
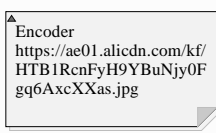
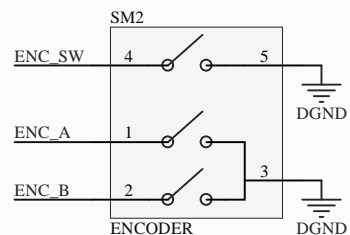
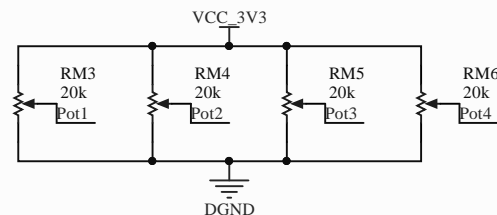
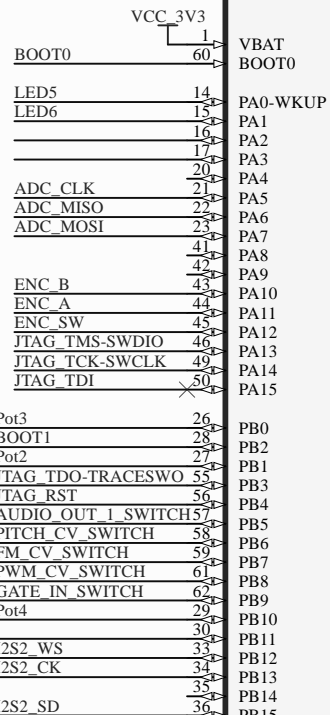
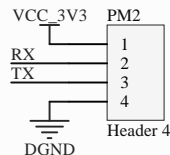
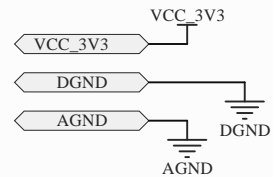
V/oct CV should be 0 - 10v  
 FM CV should be +/-5V  
 PWM CV should be 0 - 10 v  
 Gates should be 5V



Title			
Size	Number		Revision
A4			
Date:	6/14/2020	Sheet	of
File:	F:\Documents\...\Input_stages.SchDoc	Drawn By:	



A



D

Title		
Size	Number	Revision
A4		
Date:	6/14/2020	Sheet of
File:	F:\Documents\...\Micro.SchDoc	Drawn By:



A

B

C

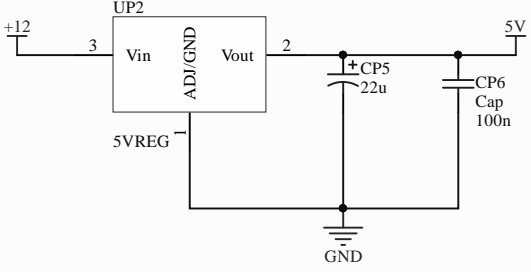
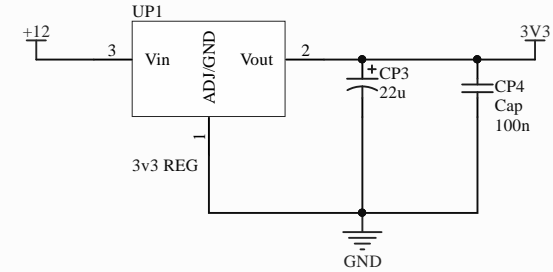
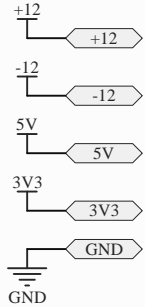
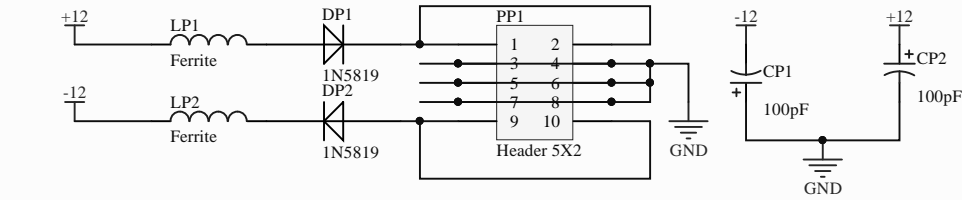
D

A

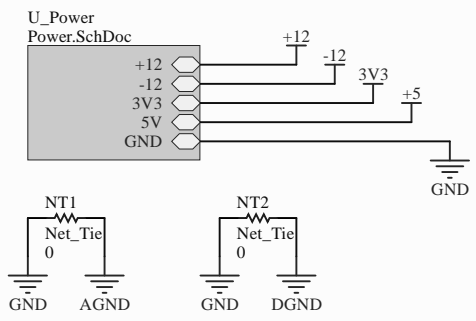
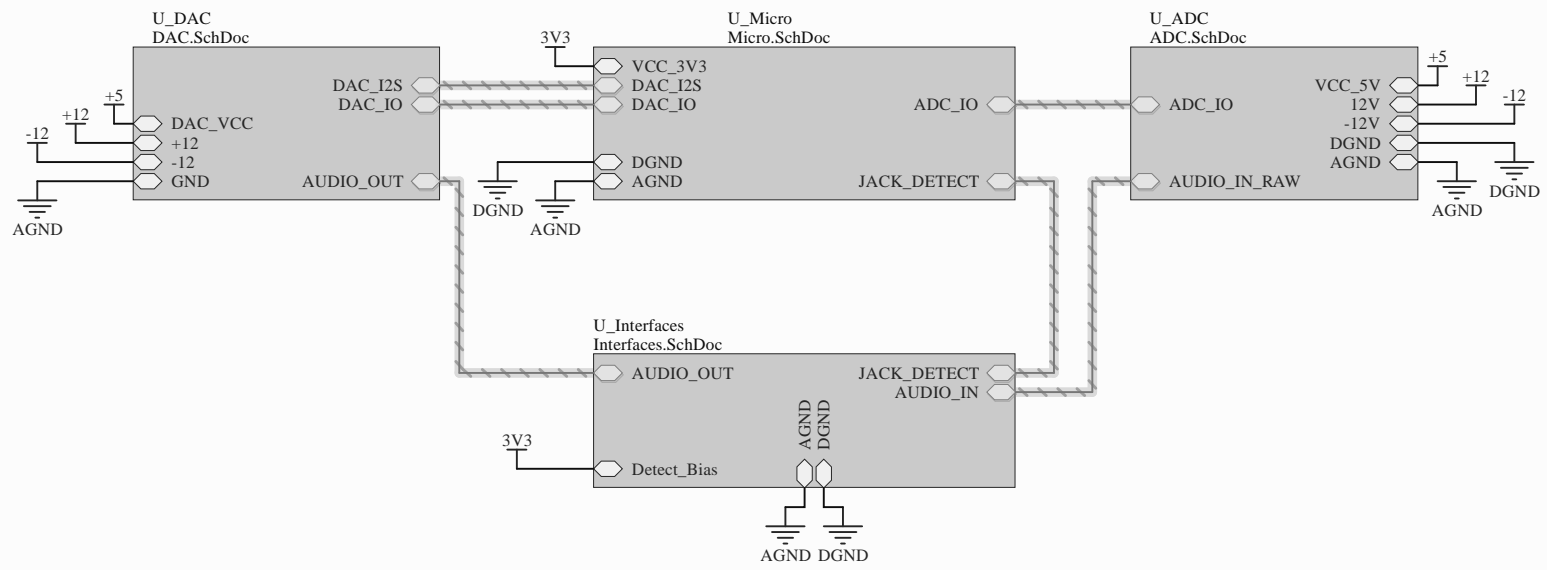
B

C

D



Title		
Size	Number	Revision
A4		
Date:	6/14/2020	Sheet of
File:	F:\Documents\...\Power.SchDoc	Drawn By:



Title		
Size	Number	Revision
A4		
Date:	6/14/2020	Sheet of
File:	F:\Documents\...\VCO_TOP.SchDoc	Drawn By: