N-bit Adder

User Guide

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1   Introduction

This is a simple implementation of an N-bit adder.

2   Block Diagram

The Nbit adder basic block takes two inputs, computes its sum and outputs it on the output.
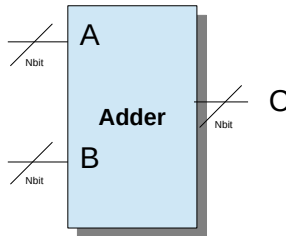

Figure 1: Block Diagram

3   Interface Signals

The N-bit adder features 2 inputs, A and B and one output, C. All of these can be configured to have Nbits.

4   Assembler

The N-bit adder can be build using iverilog by executing the make command.

The "AdderN_tb" testbench file can be edited in order to change the number of bits of the adder by editiong the ".NBIT" parameter. A and B can be replaced with constants in order to test the block's funcionality.