

# Sequencer

## User Guide



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# Contents

<b>1</b>	<b>Introduction</b>	<b>7</b>
<b>2</b>	<b>Sequencer operation</b>	<b>7</b>
<b>3</b>	<b>Implementation</b>	<b>7</b>
3.1	Sequencer Loop Controller . . . . .	8
3.2	Picoversat . . . . .	10
3.2.1	General Purpose Register File . . . . .	10
3.2.2	Debug Printer . . . . .	10
3.3	Block Diagram . . . . .	11
<b>4</b>	<b>Interface Signals</b>	<b>12</b>
<b>5</b>	<b>Memory Map</b>	<b>12</b>
<b>6</b>	<b>Implementation Results</b>	<b>12</b>
<b>7</b>	<b>Conclusions</b>	<b>12</b>



# List of Tables

1	Sequencer Loop Controller Inputs . . . . .	8
2	Sequencer Loop Controller Outputs . . . . .	8
3	Interface Signals . . . . .	12
4	Memmory map . . . . .	12

## List of Figures

1	Basys 2 board peripherals . . . . .	7
2	Basys 2 board peripherals . . . . .	8
3	PicoVersat SoC with two peripherals . . . . .	8
4	PicoVersat SoC with two peripherals . . . . .	9
5	Sequencer Loop controler fluxogram. . . . .	9
6	PicoVersat SoC with two peripherals . . . . .	10
7	Picoversat code fluxogram. . . . .	11
8	Block Diagram . . . . .	11
9	Peripherals interconnections. . . . .	11



## 1 Introduction

A sequencer is a device that can produce rhythmic loops programmed by the user. The loop is divided into 8 equally spaced steps and each step can be activated in order to produce a sound. The sequencer will go through the loop and will play a sound on the activated steps. The loop period and the sound frequency can be set by the user.

The sequencer hardware is implemented in Verilog and uses the Picoversat SoC as the basic processing unit. Refer to the Picoversat manual for more information. This sequencer implementation is meant to be used on the Basys 2 FPGA by Digilent, for that, custom-made peripherals are developed in order to use the board's features (e.g. LEDs, Switches, etc...).

## 2 Sequencer operation

In normal operation, the sequencer will loop through all the steps in a cyclic fashion, the current step can be seen by the indicated LED. In order to create a rhythm, the user can activate the desired steps by activating the corresponding switch. The activated steps are indicated by their corresponding LEDs being on.

The sound output of the signal is a DC biased square wave outputted by an IO pin of the Basys 2 Board, this should be connected to an external powered speaker in order for the sound to be amplified and filtered.

Figure 1 shows the Basys 2 peripherals that the user can use in order to interact with the sequencer.

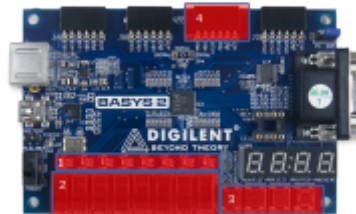


Figure 1: Basys 2 board peripherals

## 3 Implementation

Since the sequencer depends on multiple time-dependent routines and there is no trivial way to deal with this on the picoversat controller (because of the lack of interrupts), the main logic is divided into two routines: one for the main sequencer loop, implemented as a standalone peripheral, the "Sequencer loop controller", and other for the reading and debouncing of the pushbuttons and for keeping track of the frequency and loop values.

All the peripherals and modules are interconnected as described in the following picture:

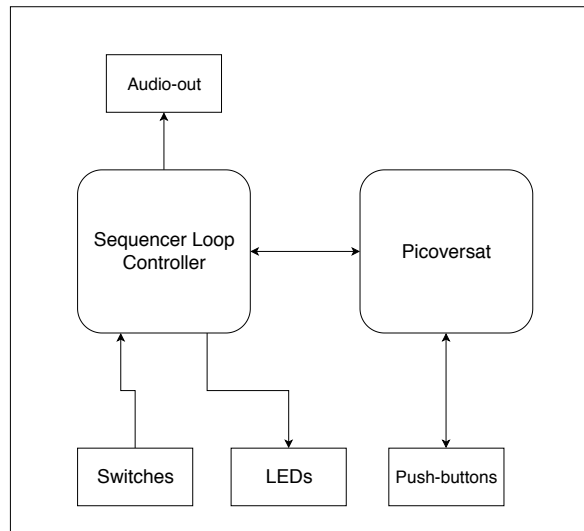


Figure 2: Basys 2 board peripherals

### 3.1 Sequencer Loop Controller

The sequencer loop controller is used to generate the sequencer loop. The loop and note frequency can be set by using the *freq* input and by selecting the according selector signal. The Sequencer loop controller will output a square wave corresponding to the loop output. The led outputs are directly connected to the LED driver peripheral and send information about the current and selected note.

The sequencer loop controller operation is described by the fluxogram on Picture 7.

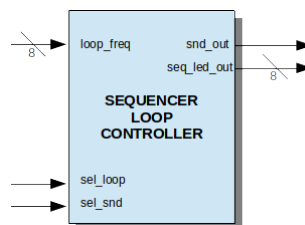


Figure 3: PicoVersat SoC with two peripherals

Table 1: Sequencer Loop Controller Inputs

Name	#bits	Description
freq	8	Loop Period / Note Frequency
sel_loop	1	Loop period select signal (address decoder)
sel_snd	1	Note frequency select signal (address decoder)

Table 2: Sequencer Loop Controller Outputs

Name	#bits	Description
snd_out	1	Audio Output
seq_led_out	8	Current note led output



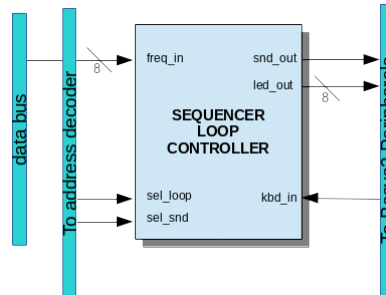


Figure 4: PicoVersat SoC with two peripherals

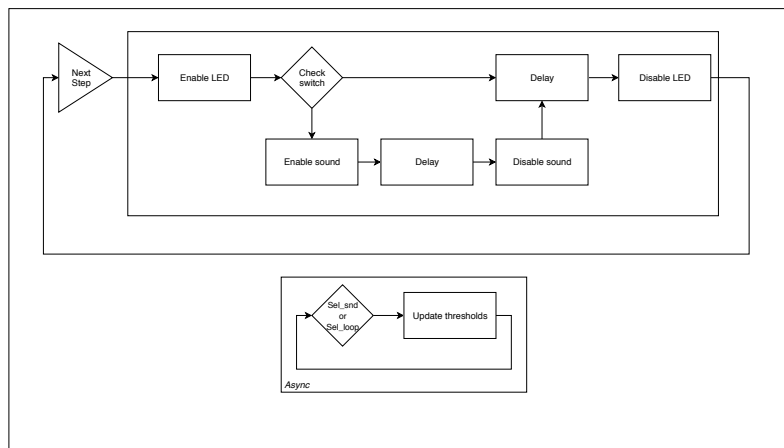


Figure 5: Sequencer Loop controller fluxogram.

## 3.2 Picoversat

For reading the pushbuttons, debouncing and managing loop and sound frequencies, the picoversat SoC was used. The Sequencer Loop Controller is connected to the picoversat data bus and the loop and sound frequency registers are mapped in memory. For this, the picoversat address decoder was altered so that the sequencer loop controller's registers can be accessed. This was also done for the push-buttons.

Some auxiliary modules were also added to the picoversat:

### 3.2.1 General Purpose Register File

This peripheral contains a 16x32bit register file that can be used by user programs. Refer to the picoversat manual for more information about this peripheral.

### 3.2.2 Debug Printer

This peripheral can be used by user programs to print characters, mainly for debug purposes. Refer to the picoversat manual for more information about this peripheral.

All of the aforementioned peripherals connect to the picoversat as described below:

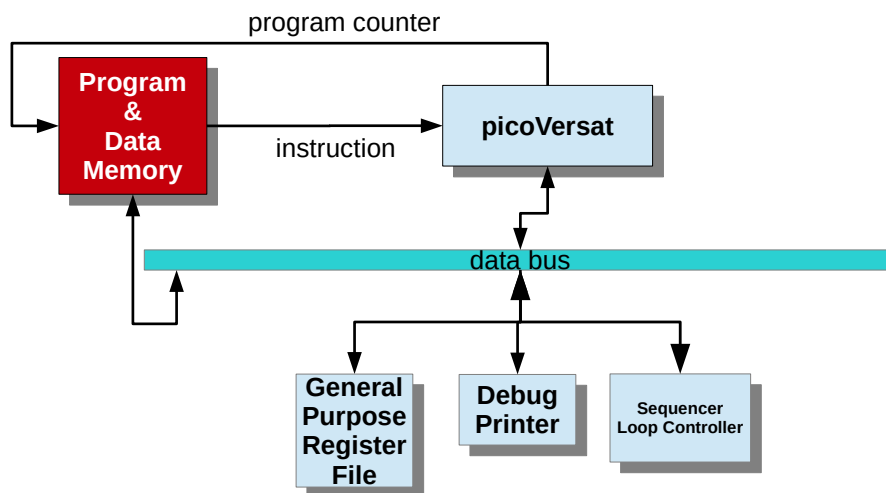


Figure 6: PicoVersat SoC with two peripherals

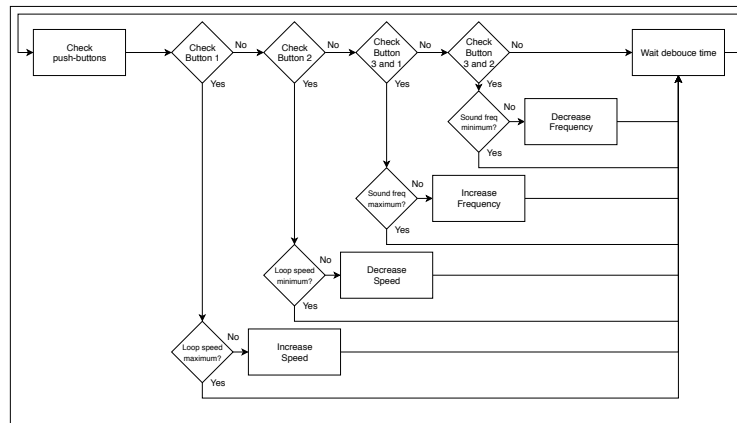


Figure 7: Picoversat code fluxogram.

### 3.3 Block Diagram

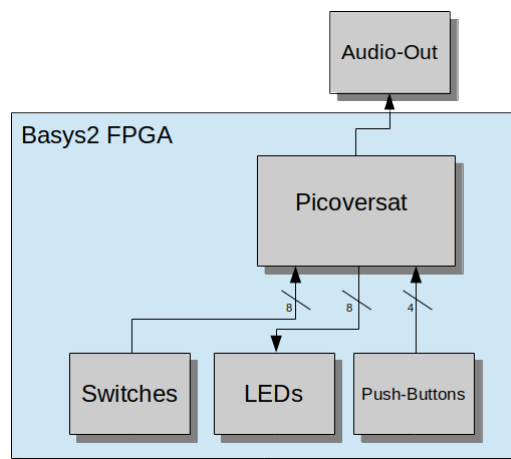


Figure 8: Block Diagram

The peripheral are interconnected according to the following picture:

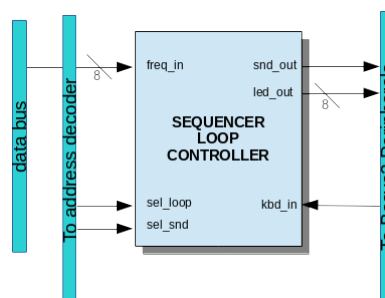


Figure 9: Peripherals interconnections.

## 4 Interface Signals

The interface signals of the sequencer controller peripherals Table 3..

Table 3: Interface Signals				
Name	#bits	Direction	Peripheral	Description
clk	1	IN	FPGA clock	Clock Signal
led_out	8	OUT	LED driver	Reset Signal
sw_in	4	IN	Push-button driver	Push Buttons
kbd_in	8	IN	Switch driver	Slide Switches
snd_out	1	OUT	Sequencer loop controller	Sound output

## 5 Memory Map

The sequencer peripherals are mapped in memory according to the following table:

Table 4: Memory map				
Mnemonic	Address	Read/Write	Read/Write Latency	Description
SND_BASE	0x25D	Write only	0	Sound frequency address for sequencer loop controller
LOOP_BASE	0x25C	Write only	0	Loop frequency address for sequencer loop controller
SW_BASE	0x25B	Read only	0	Slide switch peripheral
PUSH_BASE	0x25A	Read only	0	Push-button peripheral
CPRT_BASE	0x258	Read only	N/A	Debug printer peripheral
REGF_BASE	0x200	Read+Write	0	Register file peripheral
PROG_BASE	0x0	Read+Write	1	User program and data

## 6 Implementation Results

## 7 Conclusions