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Control Unit And ALU Control Table:

Opcode	Operations	ALUOp	msb	lsb	Binv	Cin	WC	Mem2Reg	ALUsrc	RegDst	Str	Ld	Branch	MEMen	Jump
000000	beq	xx	x	x	0	0	0	0	0	0	0	0	1	0	0
000001	add	11	1	1	0	0	1	0	0	0	0	0	0	0	0
000010	and	01	0	1	0	0	1	0	0	0	0	0	0	0	0
000011	sw	11	1	1	0	0	0	0	1	0	1	0	0	1	0
000100	addi	11	1	1	0	0	1	0	1	1	0	0	0	0	0
000101	nop	xx	x	x	0	0	0	0	0	0	0	0	0	0	0
000110	sub	11	1	1	1	1	1	0	0	0	0	0	0	0	0
000111	slt	10	1	0	0	0	1	0	0	0	0	0	0	0	0
001000	jmp	xx	x	x	0	0	0	0	0	0	0	0	0	0	1
001001	lw	11	1	1	0	0	1	1	1	1	0	1	0	1	0
001010	sll	00	0	0	0	0	1	0	0	0	0	0	0	0	0