

WORST-FIT algorithm:

Question 1 Select one or more:

- a.
Requires sorting the cut part into the list of free blocks
- b.
It is designed to reduce external fragmentation
- c.
It allows for fast determining whether there is a free block of the required size
- d.
Requires a descending sort of the list of free blocks

Feedback

Twoja odpowiedź jest częściowo poprawna.

You have correctly selected 2.

The correct answers are: Requires a descending sort of the list of free blocks, It allows for fast determining whether there is a free block of the required size, It is designed to reduce external fragmentation, Requires sorting the cut part into the list of free blocks

Question 2

Incorrect

Mark 0.00 out of 1.00

Flag question

Question text

The hardware mechanisms necessary for paging are ("frame error" also called "page fault", "frame miss"):

Question 2 Select one or more:

- a.
address translation, page index tables, "frame error" interrupt
- b.
address translation, page index tables, page reference bits, "frame error" interrupt
- c.
address translation, page index tables, "frame error" interrupt, associative memory, page swapper
- d.
address translation, page index tables, page reference bits, "frame error" interrupt, associative memory

Feedback

Twoja odpowiedź jest niepoprawna.

The correct answer is: address translation, page index tables, "frame error" interrupt

Question 3

Incorrect

Mark 0.00 out of 1.00

Flag question

Question text

For address translation, the following is used:

Question 3 Select one or more:

- a.
translation register
- b.
reference and protection bits
- c.
index table
- d.
associative translation buffer

Feedback

Twoja odpowiedź jest niepoprawna.

The correct answers are: index table, associative translation buffer

Question **4**

Correct

Mark 1.00 out of 1.00

Flag question

Question text

The hardware resources of a computer system are:

Question 4 Select one or more:

- a.
Virtual memory
- b.
Peripheral devioecs
- c.
Processor time
- d.
Windows on the screen

Feedback

Twoja odpowiedź jest poprawna.

The correct answers are: Processor time, Peripheral devioecs

Question **5**

Correct

Mark 1.00 out of 1.00

Flag question

Question text

The following paging exceptions allow for returning to the program after they occur:

Question 5 Select one or more:

- a.
interrupt on write (IOW)
- b.
attempting to write to a write-protected frame
- c.
attempt to read from the frame storing the code
- d.
page fault

Feedback

Twoja odpowiedź jest poprawna.

The correct answers are: page fault, interrupt on write (IOW)

Question 6

Correct

Mark 1.00 out of 1.00

[Flag question](#)

Question text

The content of the last (lowest in the hierarchy) page index table is:

Answer: Question 6

Feedback

The correct answer is: frame

Question 7

Correct

Mark 1.00 out of 1.00

[Flag question](#)

Question text

Using the Test-And-Set instruction in synchronization:

Question 7 Select one or more:

- a.
It requires processes to actively wait
- b.
Stops the processor if 0 is read
- c.
Requires the use of semaphores
- d.
Requires organizing inactive waiting in queues

Feedback

Twoja odpowiedź jest poprawna.

The correct answer is: It requires processes to actively wait

Question 8

Incorrect

Mark 0.00 out of 1.00

Flag question

Question text

On UNIX, the number of files is directly limited by:

Question 8 Select one or more:

- a.
The size of the space allocated for files
- b.
Allocation unit size
- c.
The number of bits of the field describing the size of the file
- d.
FAT size
- e.
The number of bits of the disk address

Feedback

Twoja odpowiedź jest niepoprawna.

The correct answer is: The size of the space allocated for files

Question 9

Incorrect

Mark 0.00 out of 1.00

Flag question

Question text

Having two-level page index tables:

Question 9 Select one or more:

- a.
the content of the level I table element is concatenated (combined) with the content of the level II table element
- b.
the content of a level I table element is added to the content of a level II table element
- c.
the content of a level I table element points to a level II table
- d.
the content of a level I table element is an index in a level II table

Feedback

Twoja odpowiedź jest niepoprawna.

The correct answer is: the content of a level I table element points to a level II table

Question 10

Incorrect

Mark 0.00 out of 1.00

Flag question

Question text

Which swapping algorithms use information about a reference to pages in the last k periods of time?

Question 10 Select one or more:

- a.
FIFO
- b.
working set clock
- c.
optimal
- d.
working set
- e.
LRU
- f.
drugiej szansy
- g.
LFU
- h.
no algorithm
- i.
NRU

Feedback

Twoja odpowiedź jest niepoprawna.

The correct answers are: working set, working set clock

Question 11

Correct

Mark 1.00 out of 1.00

Flag question

Question text

When a suspended program is moved to auxiliary memory, its process state is called:

Question 11 Answer

- a.
rinsed out
- b.
swept away
- c.
exchanged
- d.
moved out

Feedback

The correct answer is: swept away

Question **12**

Correct

Mark 1.00 out of 1.00

Flag question

Question text

The common allocation queue to fixed blocks of memory of equal size causes:

Question 12 Select one or more:

- a.
need for compaction
- b.
external fragmentation
- c.
internal fragmentation

Feedback

Twoja odpowiedź jest poprawna.

The correct answer is: internal fragmentation

Question **13**

Incorrect

Mark 0.00 out of 1.00

Flag question

Question text

The effective address is also:

Question 13 Select one or more:

- a.
Indirect address
- b.
Absolute address
- c.
Logical address

d.
Physical address

Feedback

Twoja odpowiedź jest niepoprawna.

The correct answer is: Logical address

Question **14**

Incorrect

Mark 0.00 out of 1.00

Flag question

Question text

In which swapping algorithms is it necessary to collectively clear the M-bits?

Question 14 Select one or more:

- a.
second chance
- b.
NRU
- c.
LFU
- d.
no algorithm
- e.
LRU
- f.
working set
- g.
working set clock
- h.
clock
- i.
FIFO

Feedback

Twoja odpowiedź jest niepoprawna.

The correct answer is:
no algorithm

Question **15**

Incorrect

Mark 0.00 out of 1.00

Flag question

Question text

Dynamic relocation requires the use of:

Question 15 Select one or more:

- a.
limit register
- b.
base register (DATUM)
- c.
status register
- d.
program counter

Feedback

Twoja odpowiedź jest niepoprawna.

The correct answer is: base register (DATUM)

Question **16**

Incorrect

Mark 0.00 out of 1.00

Flag question

Question text

In UNIX, access rights are attributes:

Question 16 Select one or more:

- a.
An entry in the I-node table
- b.
User descriptor
- c.
A special table specifying access rights
- d.
A directory entry for a file

Feedback

Twoja odpowiedź jest niepoprawna.

The correct answer is: An entry in the I-node table

Question **17**

Correct

Mark 1.00 out of 1.00

Flag question

Question text

Which of the following facilities or abilities are required to provide mutual exclusion support?

Question 17 Select one or more:

- a.
A task that is performed outside the critical section must not affect the behavior of a task in the critical section.
- b.
The relative speeds of the tasks must be taken into account.
- c.
The task stays in its critical section only for a finite amount of time.
- d.
Task scheduling must be considered.

Feedback

Twoja odpowiedź jest poprawna.

The correct answers are: A task that is performed outside the critical section must not affect the behavior of a task in the critical section., The task stays in its critical section only for a finite amount of time.

Question **18**

Correct

Mark 1.00 out of 1.00

Flag question

Question text

How is exception identification performed?

Question 18 Select one or more:

- a.
the specification of hardware interrupts and errors is given over the data bus
- b.
the specification of hardware interrupts and traps is given over the data bus
- c.
all exceptions are specified using the data bus
- d.
the specification of hardware interrupts is given over the data bus

Feedback

Twoja odpowiedź jest poprawna.

The correct answer is: the specification of hardware interrupts is given over the data bus

Question **19**

Correct

Mark 1.00 out of 1.00

Flag question

Question text

Paging is in thrashing if:

Question 19Answer

- a.
the system spends less time paging than execution
- b.
page cannot be swapped
- c.
the system spends more time paging than execution
- d.
page faults occur

Feedback

The correct answer is: the system spends more time paging than execution

Question 20

Incorrect

Mark 0.00 out of 1.00

Flag question

Question text

Allocation unit for file storage:

Question 20Answer

- a.
may differ between files in a partition
- b.
it should be selected to match the characteristics of the data
- c.
may be a variable in a partition
- d.
may vary between partitions

Feedback

The correct answer is: may vary between partitions

Question 21

Correct

Mark 1.00 out of 1.00

Flag question

Question text

If a linker prepares a program with absolute addresses to be loaded in a fixed address space, it is called:

Question 21Answer

- a.
Dynamic linking
- b.
Dynamic relocation

- c.
Static linking
- d.
Static relocation

Feedback

The correct answer is: Static relocation

Question 22

Correct

Mark 1.00 out of 1.00

Flag question

Question text

At the blocked state is a process that:

Question 22 Select one or more:

- a.
occupies a processor
- b.
waits for a processor
- c.
fills the processor idle time
- d.
waits for an I/O operation to complete

Feedback

Twoja odpowiedź jest poprawna.

The correct answer is: waits for an I/O operation to complete

Question 23

Correct

Mark 1.00 out of 1.00

Flag question

Question text

The optimal scheduling algorithm in terms of minimizing the average time in the system of a given task is:

Question 23 Select one or more:

- a.
priority
- b.
FCFS
- c.
time slicing
- d.

SJF

Feedback

Twoja odpowiedź jest poprawna.

The correct answer is: SJF

Question **24**

Incorrect

Mark 0.00 out of 1.00

Flag question

Question text

Allocation unit for storing files:

Question 24 Answer

- a.
may vary between partitions
- b.
it should be selected to match the characteristics of the data
- c.
must be constant across the disk partition
- d.
may differ between files in a partition

Feedback

The correct answers are: must be constant across the disk partition, may vary between partitions

Question **25**

Correct

Mark 1.00 out of 1.00

Flag question

Question text

Is IOW bit:

Question 25 Select one or more:

- a.
protection bit
- b.
enable bit
- c.
reference bit

Feedback

Twoja odpowiedź jest poprawna.

The correct answer is: protection bit

Question **26**

Correct

Mark 1.00 out of 1.00

[Flag question](#)

Question text

The Test-And-Set or Compare-And-Swap instruction has the following characteristics:

Question 26 Select one or more:

a.

It is possible to use only in 1-processor systems

b.

Requires active waiting from tasks

c.

It is possible to deadlock with TAS/CAS operations on multiple variables

d.

It is possible to use only on computers with common memory

Feedback

Twoja odpowiedź jest poprawna.

The correct answers are: Requires active waiting from tasks, It is possible to use only on computers with common memory, It is possible to deadlock with TAS/CAS operations on multiple variables

Question **27**

Correct

Mark 1.00 out of 1.00

[Flag question](#)

Question text

Collective interrupt mask is:

Question 27 Select one or more:

a.

a register that blocks or unblocks all interrupts

b.

a combinational circuit that calculates the number of the interrupt received

c.

register in which the address of the interrupt controller is stored

d.

a register that blocks or unblocks a non-maskable interrupt

Feedback

Twoja odpowiedź jest poprawna.

The correct answer is: a register that blocks or unblocks all interrupts

Question **28**

Incorrect
Mark 0.00 out of 1.00

Flag question

Question text

Which swapping algorithms can be implemented based on hardware support in the form of a collective reading of reference bits and collective clearing of these bits?

Question 28 Select one or more:

- a.
LRU
- b.
FIFO
- c.
working set clock
- d.
optimal
- e.
no algorithm
- f.
LFU
- g.
NRU
- h.
working set
- i.
second chance

Feedback

Twoja odpowiedź jest niepoprawna.

The correct answers are: LRU, LFU

Question 29

Correct
Mark 1.00 out of 1.00

Flag question

Question text

The scheduler decisions take the form:

Question 29 Select one or more:

- a.
change from active to ready state
- b.
change from waiting to ready state

- c.
change from ready to active state
- d.
change from waiting to active state

Feedback

Twoja odpowiedź jest poprawna.

The correct answer is: change from ready to active state

Question 30

Incorrect

Mark 0.00 out of 1.00

Flag question

Question text

Address translation aims to:

Question 30 Select one or more:

- a.
Generating a "frame error" interrupt when the page is out of memory
- b.
detecting the phenomenon of locality of references
- c.
converting a virtual address to a physical one
- d.
converting a physical address to a virtual one

Feedback

Twoja odpowiedź jest niepoprawna.

The correct answer is: converting a virtual address to a physical one

Question 31

Incorrect

Mark 0.00 out of 1.00

Flag question

Question text

Fragmentation is implied by:

Question 31 Select one or more:

- a.
Internal by partitioning into quantized blocks
- b.
External by resizing allocated blocks
- c.
Internal by paging

d.

External by freeing blocks in a deallocation order not reverse to allocation

e.

External by segmentation

Feedback

Twoja odpowiedź jest niepoprawna.

The correct answers are: Internal by paging, Internal by partitioning into quantized blocks, External by freeing blocks in a deallocation order not reverse to allocation, External by resizing allocated blocks

Question **32**

Correct

Mark 1.00 out of 1.00

Flag question

Question text

"Soft" real-time system:

Question 32 Select one or more:

a.

Guarantees interrupt handling time

b.

Guarantees average response time

c.

Guaranteed response time

Feedback

Twoja odpowiedź jest poprawna.

The correct answer is: Guarantees average response time

Question **33**

Correct

Mark 1.00 out of 1.00

Flag question

Question text

During the interrupt handling:

Question 33 Select one or more:

a.

other interrupts may or may not be accepted at the discretion of the programmer

b.

other interrupts can be accepted

c.

other interrupts are disabled

Feedback

Twoja odpowiedź jest poprawna.

The correct answer is: other interrupts may or may not be accepted at the discretion of the programmer

Question 34

Incorrect

Mark 0.00 out of 1.00

Flag question

Question text

A multiprogram system is one that, in principle:

Question 34 Select one or more:

- a.
Requires relocation or equivalent mechanism
- b.
It allows for storing many programs in primary memory
- c.
Requires dynamic relocation
- d.
It allows for running programs only one after another

Feedback

Twoja odpowiedź jest niepoprawna.

The correct answers are: It allows for storing many programs in primary memory, Requires relocation or equivalent mechanism

Question 35

Incorrect

Mark 0.00 out of 1.00

Flag question

Question text

A multiuser system is one that, in principle:

Question 35 Select one or more:

- a.
Must be concurrent
- b.
Requires the use of timer interrupts
- c.
Must be multi-threaded
- d.
Allows multiple processes to actually run simultaneously

Feedback

Twoja odpowiedź jest niepoprawna.

The correct answers are: Must be concurrent, Requires the use of timer interrupts

Question **36**

Correct

Mark 1.00 out of 1.00

Flag question

Question text

The scheduling goal, which is to occupy processors as efficiently as possible, is:

Question 36 Select one or more:

- a.
response time
- b.
throughput
- c.
utilization

Feedback

Twoja odpowiedź jest poprawna.

The correct answer is: utilization

Question **37**

Correct

Mark 1.00 out of 1.00

Flag question

Question text

In a multitasking environment, the operating system decides which task the CPU gets, when, and for how long. This feature is called:

Question 37 Answer

- a.
traffic control
- b.
task timetable management
- c.
task management
- d.
task shedding

Feedback

The correct answer is: task shedding

Question **38**

Correct

Mark 1.00 out of 1.00

Flag question

Question text

Which of the following statements is true for system level threads?

Question 38 Answer

- a. Multithreaded applications cannot use multiprocessing.
- b. User-level threads can be synchronized by the kernel.
- c. The threading implementation at the kernel level is done by the thread library attached to the program.
- d. Kernel-level threads require their descriptors in the kernel.

Feedback

The correct answer is: Kernel-level threads require their descriptors in the kernel.

Question **39**

Correct

Mark 1.00 out of 1.00

Flag question

Question text

Interrupt mask:

Question 39 Select one or more:

- a. causes interrupts to be disabled when an interrupt is accepted
- b. must be part of the status register
- c. can be set and reset by special processor instructions
- d. can unblock interrupts when the interrupt handler completes

Feedback

Twoja odpowiedź jest poprawna.

The correct answers are: causes interrupts to be disabled when an interrupt is accepted, can unblock interrupts when the interrupt handler completes, can be set and reset by special processor instructions

Question **40**

Correct

Mark 1.00 out of 1.00

Flag question

Question text

What does the kernel do when there is no task (process) to run?

Question 40 Select one or more:

- a. switches off the power supply
- b. resets the entire system
- c. starts the idle task
- d. executes an infinite loop in the kernel until a task arrives

Feedback

Twoja odpowiedź jest poprawna.

The correct answer is: starts the idle task

Question **41**

Correct

Mark 1.00 out of 1.00

[Flag question](#)

Question text

Is memory protection useless in a non-concurrent system?

Question 41 Answer

True

False

Feedback

The correct answer is 'False'.

Question **42**

Correct

Mark 1.00 out of 1.00

[Flag question](#)

Question text

The program supervisor layer in the operating system has the following role:

Question 42 Select one or more:

- a. Deals with the management of primary memory (memory allocation to programs)
- b. Intercepts all program system calls and routes them to the appropriate layers
- c. Runs programs
- d.

Deals with buffering data written and read from mass memory

Feedback

Twoja odpowiedź jest poprawna.

The correct answers are: Intercepts all program system calls and routes them to the appropriate layers, Runs programs, Deals with the management of primary memory (memory allocation to programs)

Question **43**

Incorrect

Mark 0.00 out of 1.00

Flag question

Question text

A process always transitions from the "user" state to the "system" state as a result of:

Question 43 Select one or more:

- a.
Debugger trap
- b.
Hardware interrupt
- c.
Process suspension
- d.
A software interrupt that calls a system function

Feedback

Twoja odpowiedź jest niepoprawna.

The correct answers are: A software interrupt that calls a system function, Debugger trap

Question **44**

Correct

Mark 1.00 out of 1.00

Flag question

Question text

The key in associative memory is:

Question 44 Select one or more:

- a.
The page number concatenated with the frame number
- b.
The frame number concatenated with the page number
- c.
Frame number
- d.
Page number

Feedback

Twoja odpowiedź jest poprawna.

The correct answer is: Page number

Question **45**

Correct

Mark 1.00 out of 1.00

[Flag question](#)

Question text

At the ready state is a process that:

Question 45 Select one or more:

- a.
fills the processor idle time
- b.
occupies a processor
- c.
waits for a processor
- d.
waits for an I/O operation to complete

Feedback

Twoja odpowiedź jest poprawna.

The correct answer is: waits for a processor

Question **46**

Incorrect

Mark 0.00 out of 1.00

[Flag question](#)

Question text

Between fork and exec operations, the following operations are performed:

Question 46 Select one or more:

- a.
Loading the program to be started into memory
- b.
Allocating the appropriate amount of memory for the program being started
- c.
Opening the appropriate input/output files

Feedback

Twoja odpowiedź jest niepoprawna.

The correct answer is: Opening the appropriate input/output files

Question **47**

Correct

Mark 1.00 out of 1.00

Flag question

Question text

How does the operating system call the task completion subroutine?

Answer 1 Question 47 Choose... sets the trace in the terminating subroutine to the current position, builds an interrupt vector on the system stack pointing to the terminating subroutine code, builds the frame of the terminating subroutine on the task stack, recreates the context programmatically and executes the IRET instruction

Answer 2 Question 47 Choose... sets the trace in the terminating subroutine to the current position, builds an interrupt vector on the system stack pointing to the terminating subroutine code, builds the frame of the terminating subroutine on the task stack, recreates the context programmatically and executes the IRET instruction

Answer 3 Question 47 Choose... sets the trace in the terminating subroutine to the current position, builds an interrupt vector on the system stack pointing to the terminating subroutine code, builds the frame of the terminating subroutine on the task stack, recreates the context programmatically and executes the IRET instruction

Answer 4 Question 47 Choose... sets the trace in the terminating subroutine to the current position, builds an interrupt vector on the system stack pointing to the terminating subroutine code, builds the frame of the terminating subroutine on the task stack, recreates the context programmatically and executes the IRET instruction

Feedback

Twoja odpowiedź jest poprawna.

The correct answer is: 4. → recreates the context programmatically and executes the IRET instruction, 2. → sets the trace in the terminating subroutine to the current position, 1. → builds the frame of the terminating subroutine on the task stack, 3. → builds an interrupt vector on the system stack pointing to the terminating subroutine code

Question 48

Partially correct

Mark 0.50 out of 1.00

Flag question

Question text

Which of the following memory allocation schemes causes external fragmentation?

Question 48 Answer

- a. Multiple contiguous fixed partitions of equal size
- b. Paging
- c. Segmentation
- d. Multiple contiguous fixed partitions of various sizes

Feedback

The correct answers are: Segmentation, Multiple contiguous fixed partitions of various sizes

Question **49**

Incorrect

Mark 0.00 out of 1.00

Flag question

Question text

The kernel is _____ user-level threads.

Question 49 Answer

- a.
unaware
- b.
aware
- c.
part
- d.
creator

Feedback

The correct answer is: unaware

Question **50**

Correct

Mark 1.00 out of 1.00

Flag question

Question text

Context switch is:

Question 50 Select one or more:

- a.
calling the kernel of the operating system
- b.
switching to the system stack
- c.
extracode execution
- d.
writing registers to the task stack and retrieving them from another task stack

Feedback

Tvoja odpowiedź jest poprawna.

The correct answer is: writing registers to the task stack and retrieving them from another task stack

Question **51**

Correct

Mark 1.00 out of 1.00

Flag question

Question text

Operation V on a raised binary semaphore:

Question 51 Select one or more:

- a.
It does not change the value of the semaphore
- b.
Increases semaphore value by 1
- c.
It is stored in order to be able to perform as many operations P as there were V

Feedback

Twoja odpowiedź jest poprawna.

The correct answer is: It does not change the value of the semaphore

Question **52**

Incorrect

Mark 0.00 out of 1.00

Flag question

Question text

The combination of paging and segmentation consists in:

Question 52 Select one or more:

- a.
identifying segments with pages
- b.
the use of a segment table or a pool of segment registers treated as an additional, superior level of paging
- c.
Generating a "frame error" interrupt when the frame containing the page does not belong to the current segment
- d.
dividing pages into segments

Feedback

Twoja odpowiedź jest niepoprawna.

The correct answer is: the use of a segment table or a pool of segment registers treated as an additional, superior level of paging

Question **53**

Correct

Mark 1.00 out of 1.00

Flag question

Question text

Scheduling algorithms can be:

Question 53 Answer

- a. indulgent
- b. interrupting
- c. term changing
- d. preemptive

Feedback

The correct answers are: indulgent, preemptive

Question **54**

Incorrect

Mark 0.00 out of 1.00

[Flag question](#)

Question text

MMU uses index tables to:

Question 54 Select one or more:

- a. generate an effective address
- b. generate a relative address
- c. generate a logical address
- d. generate a physical address

Feedback

Twoja odpowiedź jest niepoprawna.

The correct answer is: generate a physical address

Question **55**

Correct

Mark 1.00 out of 1.00

[Flag question](#)

Question text

When is the scheduler called?

Question 55 Select one or more:

- a.

at the request of a user task

- b.
when the scheduler decides itself
- c.
at the end of the execution of each kernel procedure
- d.
at the start of execution of each kernel procedure

Feedback

Twoja odpowiedź jest poprawna.

The correct answer is: at the end of the execution of each kernel procedure

Question 56

Correct

Mark 1.00 out of 1.00

[Flag question](#)

Question text

After performing the mount(/dev/hd5,/usr/x/bin/hd5) operation, the file /usr/z/a on the mounted disk should be referenced by:

Question 56 Select one or more:

- a.
/dev/hd5/usr/z/a
- b.
/usr/x/bin/hd5/usr/z/a
- c.
/usr/x/bin/dev/hd5/usr/z/a

Feedback

Twoja odpowiedź jest poprawna.

The correct answer is: /usr/x/bin/hd5/usr/z/a

Question 57

Correct

Mark 1.00 out of 1.00

[Flag question](#)

Question text

Interrupt vector is saved in a case of:

Question 57 Select one or more:

- a.
jump with trace
- b.
accepting a non-maskable interrupt
- c.

subroutine call

- d.
accepting a hardware interrupt

Feedback

Twoja odpowiedź jest poprawna.

The correct answers are: accepting a hardware interrupt, accepting a non-maskable interrupt, jump with trace

Question **58**

Correct

Mark 1.00 out of 1.00

Flag question

Question text

What are the functions of the kernel?

Question 58 Answer

- a.
File management
- b.
Memory management
- c.
Interrupt handling
- d.
Program management

Feedback

The correct answer is: Interrupt handling

Question **59**

Correct

Mark 1.00 out of 1.00

Flag question

Question text

By definition, a deadlock is a situation where:

Question 59 Select one or more:

- a.
at least two processes are waiting for conditions that cannot be met
- b.
any greater than zero number of processes are waiting for conditions that cannot be met
- c.
any greater than one number of processes are waiting for conditions that cannot be met
- d.
exactly two processes are waiting for conditions that cannot be met

Feedback

Twoja odpowiedź jest poprawna.

The correct answer is: any greater than zero number of processes are waiting for conditions that cannot be met

Question **60**

Correct

Mark 1.00 out of 1.00

[Flag question](#)

Question text

In a concurrent environment, the operating system decides which task the CPU gets, when, and for how long. This feature is called:

Question 60 Answer

- a.
task scheduling
- b.
traffic management
- c.
task management
- d.
task timetable planning

Feedback

The correct answer is: task scheduling

Question **61**

Correct

Mark 5.00 out of 5.00

[Flag question](#)

Question text

What is the average time in the system for tasks in the batch, using SJF algorithm?

task	1	2	3	4
processing time	3.1	6.7	2.8	1.1

Answer: Question 61

Feedback

The correct answer is: 6.4

Question **62**

Incorrect

Mark 0.00 out of 5.00

Flag question

Question text

In RAID 4, data is placed in Strips that are "scattered" over the data disks, so that each subsequent strip is on the next data disk, modulo the number of disks. For this, there is a parity disk that holds the parity bits of zeroth bits, first bits, second bits, etc., equal-numbered strips divided by the number of data disks, for example, strips 0-3, 4-7, 8-11, etc.:

the start of strips 0,1,2,3 looks like this:

0	1	0	0	1	Parity disk
0	0	0	0	0	Disk 3
1	0	1	1	0	Disk 2
1	1	0	0	1	Disk 1
0	1	1	0	0	Disk 0

In the parity strip, the values are placed so that the parity bit keeps the corresponding strip bits 0-3 odd.

Disk 3 has been damaged and reads only 0. After replacing the disk with a new one, what values should be put in the strip on disk 3?

Answer:Question 62

Feedback

The correct answer is: 10101

Question 63

Incorrect

Mark 0.00 out of 5.00

Flag question

Question text

Assuming memory cells are 1-byte, the page number in the address field is 12 bits, the offset is 12 bits, the frame number is 10 bits, and all entries in the TIS page index table are on a 16-bit word boundary, enter:

- the maximum size of the program's virtual memory in MB

Answer:Question 63

Feedback

The correct answer is: 4