Started on	Thursday, 27 June 2024, 12:36 PM
State	Finished
Completed on	Thursday, 27 June 2024, 1:48 PM
Time taken	1 hour 11 mins
Marks	53.58/75.00
Grade	<b>22.86</b> out of 32.00 ( <b>71.44</b> %)
Question <b>1</b>	
Correct	
Mark 1.00 out of 1.00	

Which swapping algorithms can be implemented based on hardware support in the form of a collective reading of reference bits and collective clearing of these bits?

Select one or more:

- 🕜 a. LFU 🗸
- b. FIFO
- ☑ c. LRU 🗸
- d. optimal
- e. second chance
- f. NRU
- g. working set clock
- h. no algorithm
- i. working set

Twoja odpowiedź jest poprawna.

The correct answers are: LRU, LFU

1.00
e following sequence of address references:
00, 1234, 76, 96.
size is 100, the order of page references is as follows:
2,6,12
2,6,12,0,0
2,6,12,0,0 🗸
,21,60,123,7,9
answer is: 1,2,6,12,0,0
1.00
1.00
nterrupts can be handled:
nterrupts can be handled:
nterrupts can be handled: or more:
nterrupts can be handled: or more: oly when the program allows accepting interrupts
) 2 2

Twoja odpowiedź jest poprawna.

The correct answers are: after clearing the pipeline from the instructions, after saving the full state of the pipeline

Question <b>4</b>		
Correct		
Mark 1.00 out of 1.00		
Address translation mechanism:		
Select one or more:		
a. concatenates the frame number and page number		
☐ b. concatenates the page number and page offset		
☑ c. concatenates the frame number and offset on the page  ✓		
d. adds the frame number to the page number		
Twoja odpowiedź jest poprawna.		
The correct answer is: concatenates the frame number and offset on the page		
Question 5		
Correct  Market 100 part of 100		
Mark 1.00 out of 1.00		
FIRST-FIT algorithm:		
Select one or more:		
a. Requires an ascending sorting of the list of free blocks		
□ b. Avoids external fragmentation		
☑ c. Causes external fragmentation ✔		
d. It allows for fast determining whether there is a free block of the required size		

Twoja odpowiedź jest poprawna.

The correct answer is: Causes external fragmentation

Question <b>6</b>	
Correct	
Mark 1.00 out of	f 1.00
SJF selects	the task:
551 5616613	
a. w	hich was first placed in the queue
✓ b. w	ith the least CPU requirement ❤
	hich was last placed in the queue
d. w	ho waited the longest in the queue
<b>T</b> I.	
The correc	t answer is: with the least CPU requirement
Question <b>7</b>	
Partially correct	
Mark 0.67 out of	f 1.00
AARL ALCO LA	e for simultaneous execution in the same context?
wnat is tru	e for simultaneous execution in the same context?
a. T	he use of threads ensures concurrency within the process.
☑ b. A	multiprocessor kernel can be concurrent ❤
	hreads minimize context switch time. 🗸
d. T	he shared context forces threads to run on the same processor

The correct answers are: Threads minimize context switch time., The use of threads ensures concurrency within the process., A multiprocessor kernel can be concurrent

, 12:21 PM	Exam 2: Attempt review   LeON archiwum 23/24
Question <b>8</b>	
Correct	
Mark 1.00 out of 1.00	
The file system layer plays the following role in the ope	rating system:
Select one or more:	
☑ a. Controls file access rights  ✓	
b. Performs file opening and closing operations	<b>~</b>
$\  \  \  \  \  \  \  \  \  \  \  \  \  $	of disk files ❤
d. It runs programs stored in files	
Twoja odpowiedź jest poprawna.	
The correct answers are: Performs file opening and clos file access rights	sing operations, It performs directory services in the hierarchy of disk files, Controls

Question 9

Correct

Mark 1.00 out of 1.00

Processor access scheduling decisions may be made under which of the following circumstances?

Select one or more:

- $extbf{ iny}$  a. When a task transitions from the active state to the ready state  $extbf{ iny}$
- ☑ b. When a task terminates ✔
- ☑ c. When a task goes from the waiting state to the ready state 
  ✓
- ☑ d. When a task goes from the active state to the waiting state 
  ✓

Twoja odpowiedź jest poprawna.

The correct answers are: When a task goes from the active state to the waiting state, When a task transitions from the active state to the ready state, When a task goes from the waiting state to the ready state, When a task terminates

12.21 PIVI	Exam 2. Attempt review   Leon archiwum 23/24		
Question <b>1</b> (	Question 10		
Correct	prrect		
Mark 1.00 ou	Mark 1.00 out of 1.00		
In which	swapping algorithms is it necessary to collectively clear the M-bits?		
Select o	ne or more:		
✓ a.	no algorithm 🗸		
<ul><li>□ b.</li></ul>	working set clock		
_ c.	NRU		
d.	working set		
_ e.	LFU		
f.	FIFO		
g.	clock		
h.	LRU		
_ i.	second chance		
Twoja o	dpowiedź jest poprawna.		
	rect answer is:		
no algor			
Question <b>1</b> 1			
Correct			
Mark 1.00 ou	at of 1.00		
Static re	location is performed by:		
a.	Special registers (DATUM)		
<ul><li>b.</li></ul>	Loader ❤		
_ c.	Paging system		
_ d.	Segment descriptors		

The correct answer is: Loader

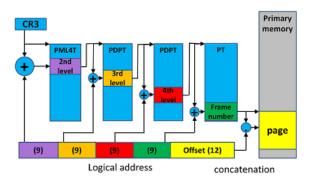
Question 12	2
Correct	
Mark 1.00 o	ut of 1.00
What m	echanism is part of time-sharing systems?
	short-term scheduler ❤
<ul><li>□ b.</li></ul>	long-term scheduler
c.	swapping
_ d.	medium-term scheduler
The cor	rect answer is: short-term scheduler
THE COL	rect answer is, short-term scheduler
Question 13	3
Correct	
Mark 1.00 o	rt of 1.00
Context	switch is:
Select o	ne or more:
a.	switching to the system stack
_ b.	calling the kernel of the operating system
✓ c.	writing registers to the task stack and retrieving them from another task stack ❤
_ d.	extracode execution

Twoja odpowiedź jest poprawna.

The correct answer is: writing registers to the task stack and retrieving them from another task stack

Question 14
Correct
Mark 1.00 out of 1.00

In the four-level page index table, the frame number is:



## Select one or more:

- a. in the first level table the leftmost in the figure
- b. part in the table of each level
- ☑ c. in the table of the last level the rightmost in the figure 
  ✓
- d. none of the above answers

Twoja odpowiedź jest poprawna.

The correct answer is: in the table of the last level - the rightmost in the figure

Question 15
Correct
Mark 1.00 out of 1.00

Virtual memory consists of:

## Select one or more:

- ☑ a. primary memory and storage memory 
  ✓
- b. primary memory and cloud storage
- c. main memory and cache
- d. cache and storage memory

Twoja odpowiedź jest poprawna.

The correct answer is: primary memory and storage memory

,	z.z.r PW Exam z. Attempt review   Leon arthwam z.s/z4
	Question 16
	ncorrect
	Mark 0.00 out of 1.00
	What type of code can execute on multiple datasets in parallel:
	Select one or more:
	a. binary
	Ø b. dynamically relocated       X
	c. reentrant
	Twoja odpowiedź jest niepoprawna.
	The correct answer is: reentrant
	Question 17
	Mark 1.00 out of 1.00
	What mechanism is used to desynchronize processes with different relative speeds?
	Select one or more:
	a. scheduling
	b. preempting
	☑ c. buffer ✔
	d. interrupts
	Twoja odpowiedź jest poprawna.
	The correct answer is: buffer

Question 18		
Incorrect		
Mark 0.00 out of 1.00		
Address translation mechanism:		
Select one or more:		
a. Concatenates the frame number and offset on the page		
□ b. Concatenates the frame number and page number		
☑ c. Concatenates the page number and page offset   ※		
d. Adds the frame number to the page number		
Twoja odpowiedź jest niepoprawna.		
The correct answer is: Concatenates the frame number and offset on the page		
The correct answer is, conteatenates the name number and onset on the page		
Question 19		
Correct		
Mark 1.00 out of 1.00		
When starting a program, how is control passed to it from the operating system?		
Select one or more:		
a. jump		
b. extracode		
d. jump with trace		
Twoja odpowiedź jest poprawna.		
rwoja oupowieuz jest poprawna.		

The correct answer is: return from interrupt handler IRET

Correct Mark 1.00 out of 1.00	
The sweeping criteria include:	
Select one or more:	
☑ a. Analysis of program execution history  ✓	
☑ b. Program state ✔	
c. Segment referencing frequency	
☑ d. Priority ✔	
d. Thorty	
Twoja odpowiedź jest poprawna.	
The correct answers are: Priority, Program state, Analysis of program execution history	
uestion 21	
Correct	
○ True	
Correct  Mark 1.00 out of 1.00  Is memory protection useless on a single-user system?	
Sorrect  Mark 1.00 out of 1.00  Is memory protection useless on a single-user system?  ☐ True  ☐ False ✔	
Correct Mark 1.00 out of 1.00  Is memory protection useless on a single-user system?  True	
Sorrect  Mark 1.00 out of 1.00  Is memory protection useless on a single-user system?  ☐ True  ☐ False ✔	
Sorrect  Mark 1.00 out of 1.00  Is memory protection useless on a single-user system?  ☐ True  ☐ False ✔	
Is memory protection useless on a single-user system?  True  False ✓  The correct answer is 'False'.	
Sorrect Mark 1.00 out of 1.00  Is memory protection useless on a single-user system?  ☐ True ☐ False ✔  The correct answer is 'False'.	
Is memory protection useless on a single-user system?  ☐ True ☐ False ✓  The correct answer is 'False'.  Duestion 22  orrect  lark 1.00 out of 1.00	
orrect  lark 1.00 out of 1.00  Is memory protection useless on a single-user system?  True  False ✓  The correct answer is 'False'.  puestion 22 orrect	
Is memory protection useless on a single-user system?  ☐ True ☐ False ✓  The correct answer is 'False'.  Duestion 22  Forrect  Fark 1.00 out of 1.00	
orrect lark 1.00 out of 1.00  Is memory protection useless on a single-user system?  True False ✓  The correct answer is 'False'.  It is the correct answer is 'False'.  Paging - the key in associative memory is:	
orrect lark 1.00 out of 1.00  Is memory protection useless on a single-user system?  True  False ✓  The correct answer is 'False'.  The correct answer is 'False'.  Paging - the key in associative memory is:  Select one or more:	
Is memory protection useless on a single-user system?  ☐ True ☐ False ✔  The correct answer is 'False'.  Duestion 22  Forrect  Hark 1.00 out of 1.00  Paging - the key in associative memory is:  Select one or more: ☐ a. frame number	

TZ.ZTTW EXCIT.	2. Accompered to Leon archiwani 23/24
Question 23	
Correct	
Mark 1.00 out of 1.00	
What is a scheduler?	
Select one or more:	
a. a kernel routine that selects a task to execute   ✓	
☐ b. the system process that allocates the processor	
c. procedure that schedules frame release in the page rep	lace algorithm
d. memory allocation procedure	
Twoja odpowiedź jest poprawna.	
The correct answer is: a kernel routine that selects a task to exec	ute
Question <b>24</b>	
Correct	
Mark 1.00 out of 1.00	
Using Test-And-Set or Compare-And-Swap in synchronization:	
Select one or more:	
a. Stops the processor if 0 is read	
<ul> <li>a. Stops the processor in ors read</li> <li>b. It can only be applied in systems with shared memory of</li> </ul>	•
Ø d. It requires processes to actively wait   ✓	
Twoja odpowiedź jest poprawna.	
The correct answers are: It requires processes to actively wait,	
It can only be applied in systems with shared memory	

Question <b>25</b>		
Incorrect		
Mark 0.00 out	Mark 0.00 out of 1.00	
System/u	iser threads:	
Select on	e or more:	
	User-level thread descriptors are stored in the operating system kernel. 🔀	
_ b.	User-level threads share the same stack.	
✓ c.	User-level threads share the same execution context. ❤	
Twoja od	powiedź jest niepoprawna.	
The corre	ect answer is: User-level threads share the same execution context.	
Question <b>26</b>		
Partially corre	ect .	
Mark 0.75 out	of 1.00	
The envi	ronment in which the process is executed includes:	
Select on	e or more:	
a.	Open files	
_ b.	Contents of the interrupt request register	
<ul><li>✓ c.</li></ul>	General purpose registers content ❤	
✓ d.	Process address space ❤	
<ul><li>✓ e.</li></ul>	A set of environment variables ❤	
f.	The content in the memory management unit	
	powiedź jest częściowo poprawna.	
You have	correctly selected 3.	

The correct answers are: A set of environment variables, Process address space, General purpose registers content, Open files

12:21 F	PM Exam 2: Atte	empt review   LeON archiwum 23/24
Questio	n <b>27</b>	
Correct		
Mark 1.0	00 out of 1.00	
The	sequence of actions in interrupt handling:	
1.	saving a copy of the interrupt vector (PC and SR)	<b>~</b>
2. sv	witching to system mode (modification of PC and status register)	<b>✓</b>
3.	programmatic context saving	<b>✓</b>
4.	switching to the system stack	<b>✓</b>
Two	ja odpowiedź jest poprawna.	
The	correct answer is:	
The	sequence of actions in interrupt handling:	
1.[sa	aving a copy of the interrupt vector (PC and SR)]	
2.[sv	vitching to system mode (modification of PC and status register)]	
3.[pr	rogrammatic context saving]	
4.[sv	vitching to the system stack]	
Questio	n 28	
Partially	y correct	
Mark 0.5	50 out of 1.00	
Wha	t does the file system layer do?	

- a. Manages remote files
- ☐ b. Tracks the status of information
- c. Manages remote file systems
- ✓ d. Manages files 
  ✓

The correct answers are: Tracks the status of information, Manages files

Question 29
Incorrect
Mark 0.00 out of 1.00
When is the scheduler called?
Select one or more:
·
b. when the scheduler decides itself
☑ c. at the start of execution of each kernel procedure ×
d. at the end of the execution of each kernel procedure
Twoja odpowiedź jest niepoprawna.
The correct answer is: at the end of the execution of each kernel procedure
Question 30
Correct
Mark 1.00 out of 1.00
Twins algorithm:
Select one or more:
☑ a. Causes external fragmentation  ✓
☑ b. Causes less fragmentation than static division into equal blocks
☑ c. Causes internal fragmentation ❤
Twoja odpowiedź jest poprawna.
The correct answers are: Causes internal fragmentation, Causes external fragmentation, Causes less fragmentation than static division

into equal blocks

uestion <b>31</b>		
Correct		
ark 1.00 out of 1.00		
On UNIX, the number of files is <u>directly limited</u> by:		
Select one or more:		
☑ a. The size of the space allocated for files  ✔		
☐ b. Allocation unit size		
c. The number of bits of the field describing the size of the file		
d. The number of bits of the disk address		
e. FAT size		
Twoja odpowiedź jest poprawna.		
The correct answer is: The size of the space allocated for files		
uestion 32		
prrect		
ark 1.00 out of 1.00		
Between fork and exec operations, the following operations are performed:		
Select one or more:		
☑ a. Opening the appropriate input/output files		
■ b. Allocating the appropriate amount of memory for the program being started		
c. Loading the program to be started into memory		
Twoja odpowiedź jest poprawna.		
Twoja oupowieuz jest poprawna.		

The correct answer is: Opening the appropriate input/output files

Question 33 Correct Mark 1.00 out of 1.00  Devices report their readiness by:  Select one or more:  a. system call  b. setting a status bit   c. issuing an interrupt   d. unblocking the interrupts  Twoja odpowiedź jest poprawna. The correct answers are: issuing an interrupt, setting a status bit  Question 34 Correct Mark 1.00 out of 1.00  The page fault exception is specific in that:  Select one or more:  a. execution of the instruction may require decrementing the program counter   b. is reported in the "middle" of an instruction execution   c. continuation of the instruction execution may require the saving of internal processor registers storing intermediate values   d. there is no return to the program from it	۰,	12:21 PM Exam 2: Attempt review   LeON archiwum 23/24
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<ul> <li>c. issuing an interrupt ✓</li> <li>d. unblocking the interrupts</li> </ul> Twoja odpowiedź jest poprawna. The correct answers are: issuing an interrupt, setting a status bit Question 34 Correct Mark 1.00 out of 1.00 The page fault exception is specific in that: Select one or more: <ul> <li>a. execution of the instruction may require decrementing the program counter ✓</li> <li>b. is reported in the "middle" of an instruction execution ✓</li> <li>c. continuation of the instruction execution may require the saving of internal processor registers storing intermediate values ✓ <ul> <li>d. there is no return to the program from it</li> </ul></li></ul>		a. system call
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Question 34 Correct  Mark 1.00 out of 1.00  The page fault exception is specific in that:  Select one or more:  □ a. execution of the instruction may require decrementing the program counter  □ b. is reported in the "middle" of an instruction execution  □ c. continuation of the instruction execution may require the saving of internal processor registers storing intermediate values  □ d. there is no return to the program from it		
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Select one or more:  ☑ a. execution of the instruction may require decrementing the program counter ✔  ☑ b. is reported in the "middle" of an instruction execution ✔  ☑ c. continuation of the instruction execution may require the saving of internal processor registers storing intermediate values ✔  ☐ d. there is no return to the program from it		
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<ul> <li>☑ a. execution of the instruction may require decrementing the program counter ✓</li> <li>☑ b. is reported in the "middle" of an instruction execution ✓</li> <li>☑ c. continuation of the instruction execution may require the saving of internal processor registers storing intermediate values ✓</li> <li>☐ d. there is no return to the program from it</li> </ul>		The page fault exception is specific in that:
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<ul> <li>☑ c. continuation of the instruction execution may require the saving of internal processor registers storing intermediate values ✓</li> <li>☐ d. there is no return to the program from it</li> </ul>		☑ a. execution of the instruction may require decrementing the program counter  ✓
d. there is no return to the program from it		☑ b. is reported in the "middle" of an instruction execution
		c. continuation of the instruction execution may require the saving of internal processor registers storing intermediate values 🗸
		d. there is no return to the program from it
		Twois adnowled first pagrawns

The correct answers are: is reported in the "middle" of an instruction execution, execution of the instruction may require decrementing the program counter, continuation of the instruction execution may require the saving of internal processor registers storing intermediate values

12.21111	Exam 2.7 Mempereview   Leon dichiwan 23/24
Question 35	5
Correct	
Mark 1.00 ou	ut of 1.00
Two suc	cessive executions of operation V in one process on a binary semaphore in down state:
Select o	ne or more:
✓ a.	If the semaphore guards a critical region, it can lets two processes enter the critical region $\checkmark$
✓ b.	Raises the semaphore if there are no suspended processes ✔
_ c.	Increases semaphore value by 2
d.	It doesn't change anything
rwoja o	dpowiedź jest poprawna.
The core	rect answers are: If the semaphore guards a critical region, it can lets two processes enter the critical region, Raises the semaphore
	are no suspended processes
Question 36	
Correct	4-51.00
Mark 1.00 ou	IT OT 1.00
Multipro	ogram systems:
a.	They are only used on large mainframe computers
<ul><li>□ b.</li></ul>	It does any job faster
<ul><li>☑ b.</li><li>☑ c.</li></ul>	It holds more than one program in primary memory at the same time ❤
d.	They are easier to develop than single-program systems

The correct answer is: It holds more than one program in primary memory at the same time

Question 3	7
Incorrect	
Mark 0.00 or	rt of 1.00
Multipro	ogramming is a technique in which <u>, as a rule</u> :
Select o	ne or more:
<ul><li>□ a.</li></ul>	only addresses that can be generated by the processor when performing calculations are used
_ b.	is a memory allocation method by which a program is divided into equal parts
✓ c.	is a method of allocating processor time 🗶
✓ d.	many programs can be stored in primary memory 🖍
Twoja o	dpowiedź jest niepoprawna.
	rect answer is: many programs can be stored in primary memory
Question 3	3
Correct	
Mark 1.00 o	ut of 1.00
The effe	ective address is also:
Select o	ne or more:
a.	Physical address
_ b.	Absolute address
_ c.	Indirect address
✓ d.	Logical address ❤
Twois	dpowiedź jest poprawna.
_	
rne cor	rect answer is: Logical address

′,	Example View   Lean and Linux   Lean and
	Question 39
	Correct
	Mark 1.00 out of 1.00
	Page thrashing is a phenomenon involving:
	Select one or more:
	☑ a. frequent downloading of pages that have just been swapped out from memory ✔
	□ b. frequent context changes that require page index tables to be reloaded
	c. frequently changing the values of bits describing pages in frames
	d. loading the same page over and over again
	Twoja odpowiedź jest poprawna.
	The correct answer is: frequent downloading of pages that have just been swapped out from memory
	Question 40
	Correct Mark 100 and 4100
	Mark 1.00 out of 1.00
	What mechanism is part of batch systems?
	☑ a. high-level scheduler ❤
	b. low-level scheduler
	c. no scheduler is needed
	☑ d. medium-tevel scheduler ❤

The correct answers are: high-level scheduler, medium-tevel scheduler

	i i i i i i i i i i i i i i i i i i i
Question <b>4</b>	
Correct	
Mark 1.00 ou	rt of 1.00
The swe	eping mechanism consists in:
Select o	ne or more:
	Loading segments into primary memory and sending them to disk ❤
<ul><li>□ b.</li></ul>	Loading pages into the primary memory and sending them to the disk
_ c.	Merging adjacent free memory blocks
_ d.	Moving programs around in memory to eliminate fragmentation
Twoia o	dpowiedź jest poprawna.
	rect answer is: Loading segments into primary memory and sending them to disk
THE CON	ect answer is. Loading segments into primary memory and sending them to disk
Question <b>4</b> 2	2
Correct Mark 1.00 ou	t of 1.00
Mark 1.00 00	1.00
	ry management technique in which the system divides memory into equal-sized chunks with virtual base addresses divisible by a zero to easily manage relocation, is called:
	ne or more:
	swapping
☐ b.	mapping
_ c.	fragmentation
✓ d.	paging 🗸
Twoja o	dpowiedź jest poprawna.
The cor	rect answer is: paging

https://leon2023.pw.edu.pl/mod/quiz/review.php?attempt=145071&cmid=70585&showall=1

Question 43	
Correct	
Mark 1.00 out of 1.00	
In indulgent scheduling, the process keeps the CPU until:	
Select one or more:	
a. next interrupt from the timer	
□ b. next interrupt from the device	
☑ c. waiving ❤	
☑ d. termination ❤	
· · · · · · · · · · · · · · · · · · ·	
Twoja odpowiedź jest poprawna.	
The correct answers are: waiving, termination	
Question 44	
Correct Mark 1.00 out of 1.00	
Allocation unit for file storage:	
Allocation unit for the storage.	
☑ a. must be constant across the disk partition  ✓	
☑ b. may vary between partitions ❤	
c. may differ between files in a partition	
d. is any integer multiple of a sector (>0)	
The covered angulars are much be constant parcent by disk position, many law between positions	
The correct answers are: must be constant across the disk partition, may vary between partitions	
Question 45	
Incorrect  Mark 0.00 out of 1.00	
Mark 0.00 Out of 1.00	
Allocation unit for file storage:	
a. may be a variable in a partition	
☑ b. may differ between files on a partition	
c. It should be selected to match the characteristics of the data	
☑ d. must be constant across the disk partition ✔	
☑ d. must be constant across the disk partition  ✓	

The correct answer is: must be constant across the disk partition

Question 40	
Question 1	6
Correct	
Mark 1.00 o	ut of 1.00
	n exception is raised in user mode, the operating system switches to the kernel system stack, and what happens when an exception I in system mode?
Select o	one or more:
✓ a.	nothing special, it builds the context on the kernel system stack 🗸
<ul><li>□ b.</li></ul>	initializes the kernel system stack from the scratch
_ c.	switches to the next kernel system stack
d.	switches back to the application program stack
Twoia o	dnowiedź jest poprawna
_	dpowiedź jest poprawna.
_	dpowiedź jest poprawna. rect answer is: nothing special, it builds the context on the kernel system stack
_	
_	rect answer is: nothing special, it builds the context on the kernel system stack
The cor  Question 4  Correct	rect answer is: nothing special, it builds the context on the kernel system stack
The cor	rect answer is: nothing special, it builds the context on the kernel system stack
The cor  Question 4  Correct	rect answer is: nothing special, it builds the context on the kernel system stack
The cor  Question 4  Correct  Mark 1.00 or	rect answer is: nothing special, it builds the context on the kernel system stack
The cor  Question 4  Correct  Mark 1.00 or	rect answer is: nothing special, it builds the context on the kernel system stack  7  ut of 1.00
The cor  Question 4  Correct  Mark 1.00 or	rect answer is: nothing special, it builds the context on the kernel system stack  7  ut of 1.00  onal variables in a monitor
Question 4' Correct Mark 1.00 or	rect answer is: nothing special, it builds the context on the kernel system stack  7  ut of 1.00  onal variables in a monitor  one or more:

Twoja odpowiedź jest poprawna.

The correct answer is: They are used to suspend processes that cannot run because the conditions for their continuation are not met

Question 4	8
Correct	
Mark 1.00 o	ut of 1.00
<u>By defir</u>	nition, a deadlock is a situation where:
Select o	one or more:
	any greater than zero number of processes are waiting for conditions that cannot be met ❤
_ b.	any greater than one number of processes are waiting for conditions that cannot be met
_ c.	at least two processes are waiting for conditions that cannot be met
_ d.	exactly one process is waiting for a condition that cannot be met
Twoia	odpowiedź jest poprawna.
_	
The cor	rect answer is: any greater than zero number of processes are waiting for conditions that cannot be met
Question 4	9
Incorrect	
Mark 0.00 o	ut of 1.00
How is	exception identification performed?
Select o	one or more:
_ a.	all exceptions are specified using the data bus
_ b.	the specification of hardware interrupts is given over the data bus
<ul><li>✓ c.</li></ul>	the specification of hardware interrupts and traps is given over the data bus 🗶
_ d.	the specification of hardware interrupts and errors is given over the data bus
	2

Twoja odpowiedź jest niepoprawna.

The correct answer is: the specification of hardware interrupts is given over the data bus

Question 50
Correct
Mark 1.00 out of 1.00
In a multiprocessor operating system, interrupt blocking is sufficient to prevent the microkernel from executing its routines simultaneously
○ True
False   ✓
nie
The correct answer is 'False'.
Question <b>51</b>
Correct
Mark 1.00 out of 1.00
After performing the mount(/dev/hd5,/usr/x/bin/hd5) operation, the file /usr/z/a on the mounted disk should be referenced by:
Select one or more:
a. /dev/hd5/usr/z/a
☑ b. /usr/x/bin/hd5/usr/z/a ❖
c. /usr/x/bin/dev/hd5/usr/z/a
Twoja odpowiedź jest poprawna.
The correct answer is: /usr/x/bin/hd5/usr/z/a
Question <b>52</b>
Correct
Mark 1.00 out of 1.00
The scheduling goal, which is to occupy processors as efficiently as possible, is:
Select one or more:
a. response time
□ b. productivity
☑ c. utilization ❖
Twoja odpowiedź jest poprawna.

https://leon2023.pw.edu.pl/mod/quiz/review.php?attempt=145071&cmid=70585&showall=1

The correct answer is: utilization

of 1.00
uspended program is moved to auxiliary memory, its process state is called:
moved out
rinsed out
swept away 🗸
exchanged
ect answer is: swept away
of 1.00
translation aims to:
ne or more:
detecting the phenomenon of locality of references
detecting the phenomenon of locality of references

The correct answer is: converting a virtual address to a physical one

Question 55			
Partially correct			
Mark 0.67 out of 1.00			
The hardware resources of a computer system are:			
Select one or more:			
☑ a. Primary memory 🛩			
☑ b. Peripheral devices ❤			
c. Windows on the screen			
d. Processor time			
Twoja odpowiedź jest częściowo poprawna.			
You have correctly selected 2.			
Tou have correctly selected 2.			
The correct answers are: Processor time, Primary memory, Peripheral devices			
Question <b>56</b>			
Incorrect			
Mark 0.00 out of 1.00			
The return from interrupt instruction:			
Select one or more:			
a. always jumps to the process that was interrupted			
b. restores the interrupt vector			
☑ d. restores the stack pointer			
Twoja odpowiedź jest niepoprawna.			

The correct answer is: restores the interrupt vector

Correct  Mark 1.00 out of 1.00  Dynamic relocation is performed by:  a. Loader  b. Linker  c. Compiler  d. Special registers (DATUM) ✓  The correct answer is: Special registers (DATUM)	
Dynamic relocation is performed by:  a. Loader  b. Linker  c. Compiler  d. Special registers (DATUM) ✓  The correct answer is: Special registers (DATUM)	
<ul> <li>a. Loader</li> <li>b. Linker</li> <li>c. Compiler</li> <li>☑ d. Special registers (DATUM)</li> </ul> The correct answer is: Special registers (DATUM) Question 58	
<ul> <li>a. Loader</li> <li>b. Linker</li> <li>c. Compiler</li> <li>☑ d. Special registers (DATUM) ✓</li> </ul> The correct answer is: Special registers (DATUM) Question 58	
<ul> <li>b. Linker</li> <li>c. Compiler</li> <li>d. Special registers (DATUM) ✓</li> <li>The correct answer is: Special registers (DATUM)</li> </ul>	
<ul> <li>c. Compiler</li> <li>☑ d. Special registers (DATUM)</li> <li>✓</li> <li>The correct answer is: Special registers (DATUM)</li> <li>Question 58</li> </ul>	
<ul> <li>☑ d. Special registers (DATUM)</li> <li>The correct answer is: Special registers (DATUM)</li> <li>Question 58</li> </ul>	
The correct answer is: Special registers (DATUM)  Question 58	
Question 58	
Question 58	
Incorrect	
montee	
Mark 0.00 out of 1.00	
Cloning a process with a <i>fork</i> operation results in (not taking to account the numerical result of <i>fork</i> ):	
Select one or more:	
$\ \square$ a. Duplication of code segment, initialization of new data segment and stack segment	
☐ b. Duplication of data segment and stack segment	
☑ c. Duplication of code, data and stack segments ×	

Twoja odpowiedź jest niepoprawna.

The correct answer is: Duplication of data segment and stack segment

, 12:21 PM	Exam 2: Attempt review   LeON archiwum 23/24
Question <b>59</b>	
Incorrect	
Mark 0.00 out	of 1.00
Paging - t	wo-level index tables mode of operation
Select on	e or more:
✓ a.	The content of a level 1 table element points to a level 2 table 🔀
b.	The content of the level 1 table element is concatenated (combined) with the content of the level 2 table element
_ c.	The content of a level 1 table element points to a level 2 table
✓ d.	The content of a level 1 table element is an index in a level 2 table 🗶
Twoja od	powiedź jest niepoprawna.
The corre	ect answer is: The content of a level 1 table element points to a level 2 table
Question <b>60</b>	
Correct	
Mark 1.00 out	of 1.00
Address t	ranslation is handled by a unit called (give the abbreviation):
(	
Answer:	MMU ~

The correct answer is: MMU

Question **61** Incorrect Mark 0.00 out of 5.00

What is the average time in the system for tasks in the batch, using SJF algorithm?

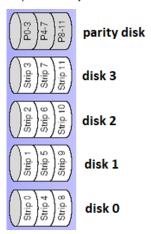
task	1	2	3	4
processing time	3.4	5.3	2.3	1.1

4.3 Answer:

The correct answer is: 5.8

Question 62
Correct
Mark 5.00 out of 5.00

In RAID 4, data is placed in Strips that are "scattered" over the data disks, so that each subsequent strip is on the next data disk, modulo the number of disks. For this, there is a parity disk that holds the parity bits of zeroth bits, first bits, second bits, etc., equal-numbered strips divided by the number of data disks, for example, strips 0-3, 4-7, 8-11, etc.:



the start of strips 0,1,2,3 looks like this:

1	1	1	0	0	Parity disk
0	0	0	0	0	Disk 3
1	0	0	1	0	Disk 2
1	0	1	0	1	Disk 1
0	1	1	0	0	Disk 0

In the parity strip, the values are placed so that the parity bit keeps the corresponding strip bits 0-3 even.

Disk 3 has been damaged and reads only 0. After replacing the disk with a new one, what values should be put in the strip on disk 3?



The correct answer is: 10111

Question 63
Incorrect
Mark 0.00 out of 5.00

The virtual address consists of 7b page number and 9b offset. The page index table is shown below (index, content). For decimal address 2690, binary 0000 1010 1000 0010, specify the physical address in the form: frame number.offset (as decimal numbers, offset in 3 digits). For example, for a physical address consisting of frame 0 and offset 18, specify 0.018. If there is no physical address for the given virtual address, then -1 should be specified.

7	12
6	0
5	-1
4	4
3	9
2	5
1	-1
0	2



The correct answer is: -1.000