

- ▶ The logic gates we've built are *higher*-level components than the low-level transistors we started off with ...
- ▶ ... now we can start to build genuinely *high*-level components:
 - ▶ We'd like components that are *clos(er)* to what we might regard as useful computation.
 - ▶ The first step is to build high-level components that perform some computation continuously, i.e., continuously compute an output given some inputs.
 - ▶ We call such designs **combinatorial** logic; they just combine low-level logic gates together.
- ▶ **Challenge**: given the specification (e.g., a truth table) for some Boolean function f , produce a Boolean expression e (i.e., implement a circuit) that can compute it.

Notes:

An Aside: Design patterns

- ▶ **Decomposition**:
 - ▶ Any n -input, m -output Boolean function

$$f : \mathbb{B}^n \rightarrow \mathbb{B}^m$$

can be rewritten as m *separate* n -input, 1-output Boolean functions, say

$$\begin{array}{ll} f_0 & : \mathbb{B}^n \rightarrow \mathbb{B} \\ f_1 & : \mathbb{B}^n \rightarrow \mathbb{B} \\ & \vdots \\ f_{m-1} & : \mathbb{B}^n \rightarrow \mathbb{B} \end{array}$$

- ▶ How? We simply let

$$f(x) \mapsto f_0(x) \parallel f_1(x) \parallel \dots \parallel f_{m-1}(x).$$

Notes:

- Strictly speaking, f is a **vectorial** Boolean function whereas each f_i is a genuine Boolean function (in the sense they each have a 1-bit output).

► Sharing:

- Imagine, for example, that we are given a 2-input, 1-bit AND gate.
- If, within some larger circuit, we compute

$$r = x \wedge y$$

and then, somewhere else,

$$r' = x \wedge y$$

then we can replace the two AND gates with one since clearly $r = r'$; this essentially shares the gate between two usage points.

Notes:

An Aside: Design patterns

► Replication:

- Imagine, for example, that we are given a 2-input, 1-bit AND gate.
- A 2-input, m -bit AND gate is simply replication of 2-input, 1-bit AND gates; if x and y are m -bit values then

$$r = x \wedge y$$

is computed by

$$r_i = x_i \wedge y_i$$

for $0 \leq i < m$, i.e., m separate instantiations of the 2-input, 1-bit gate.

Notes:

► Cascading:

- Imagine, for example, that we are given a 2-input, 1-bit AND gate.
- An n -input, 1-bit AND gate is simply a cascade of 2-input, 1-bit AND gates, i.e.,

$$r = \bigwedge_{i=0}^{n-1} x_i$$

for $n = 4$ is the same as

$$r = (x_0 \wedge x_1) \wedge (x_2 \wedge x_3).$$

- Notice we've built an AND tree, and that the balanced tree above is more attractive than equivalents such as

$$r = x_0 \wedge (x_1 \wedge (x_2 \wedge x_3)).$$

Notes:

Mechanical Derivation (1): Method #1

Algorithm

Input: A truth table for some Boolean function f , with n inputs and 1 output

Output: A Boolean expression e that implements f

First let I_j denote the j -th input for $0 \leq j < n$ and O denote the single output:

1. Find a set T such that $i \in T$ iff. $O = 1$ in the i -th row of the truth table.
2. For each $i \in T$, form a term t_i by AND'ing together all the variables while following two rules:

- 2.1 if $I_j = 1$ in the i -th row, then we use

$$I_j$$

as is, but

- 2.2 if $I_j = 0$ in the i -th row, then we use

$$\neg I_j.$$

3. An expression implementing the function is then formed by OR'ing together all the terms, i.e.,

$$e = \bigvee_{i \in T} t_i,$$

which is in SoP form.

Notes:

Mechanical Derivation (2): Method #1

Example

Consider the example of deriving an expression for XOR, i.e.,

$$r = f(x, y) = x \oplus y,$$

a function described by the following truth table:

f		
x	y	r
0	0	0
0	1	1
1	0	1
1	1	0

Following the algorithm produces:

Notes:

Mechanical Derivation (2): Method #1

Example

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a function described by the following truth table:

f		
x	y	r
0	0	0
0	1	1
1	0	1
1	1	0

$\rightsquigarrow i = 1$
 $\rightsquigarrow i = 2$

Following the algorithm produces:

1. Looking at the truth table, it is clear there are

- ▶ $n = 2$ inputs that we denote $I_0 = x$ and $I_1 = y$, and
- ▶ one output that we denote $O = r$.

Clearly $T = \{1, 2\}$ since $O = 1$ in rows 1 and 2, while $O = 0$ in rows 0 and 3.

Notes:

Mechanical Derivation (2): Method #1

Example

Consider the example of deriving an expression for XOR, i.e.,

$$r = f(x, y) = x \oplus y,$$

a function described by the following truth table:

f		
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$\rightsquigarrow t_1 = \neg x \wedge y$
 $\rightsquigarrow t_2 = x \wedge \neg y$

Following the algorithm produces:

2. Each term t_i for $i \in T = \{1, 2\}$ is formed as follows:

► For $i = 1$, we find

- $I_0 = x = 0$ and so we use $\neg x$,
- $I_1 = y = 1$ and so we use y

and hence form the term $t_1 = \neg x \wedge y$.

► For $i = 2$, we find

- $I_0 = x = 1$ and so we use x ,
- $I_1 = y = 0$ and so we use $\neg y$

and hence form the term $t_2 = x \wedge \neg y$.

Notes:

Mechanical Derivation (2): Method #1

Example

Consider the example of deriving an expression for XOR, i.e.,

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a function described by the following truth table:

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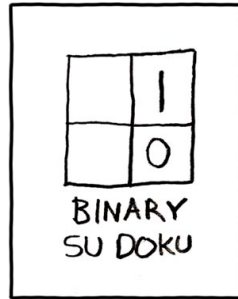
Following the algorithm produces:

3. The expression implementing the function is therefore

$$\begin{aligned} e &= \bigvee_{i \in T} t_i \\ &= \bigvee_{i \in \{1, 2\}} t_i \\ &= (\neg x \wedge y) \vee (x \wedge \neg y) \end{aligned}$$

which is in SoP form.

Notes:



<http://xkcd.com/74/>

Mechanical Derivation (4): Method #2 (Karnaugh map)

Algorithm (Karnaugh map)

Input: A truth table for some Boolean function f , with n inputs and 1 output

Output: A Boolean expression e that implements f

1. Draw a $(p \times q)$ -element grid, st. $p \cdot q = 2^n$ and each row and column represents one input combination; order rows and columns according to a **Gray code**.
2. Fill the grid elements with the output corresponding to inputs for that row and column.
3. Cover *rectangular* groups of adjacent 1 elements which are of total size 2^m for some m ; groups can “wrap around” edges of the grid and overlap.
4. Translate each group into one term of an SoP form Boolean expression e where
 - 4.1 *bigger* groups, and
 - 4.2 *less* groupsmean a simpler expression.

Notes:

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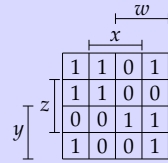
- Note that the typesetting of Karnaugh maps in these slides is a bit awkward, and arguably not standard, basically due to the difficulty of typesetting small fonts.
Rather than write the binary values of each variable along the top and side, instead the rendered image shows where they are 1 via a range marked by a line: the variable is 1 within cells inside the range, and 0 otherwise.

Mechanical Derivation (5): Method #2 (Karnaugh map)

Example

Consider an example 4-input, 1-output function:

w	x	y	z	r
0	0	0	0	1
0	0	0	1	1
0	0	1	0	1
0	0	1	1	0
0	1	0	0	1
0	1	0	1	1
0	1	1	0	0
0	1	1	1	0
1	0	0	0	1
1	0	0	1	0
1	0	1	0	1
1	0	1	1	1
1	1	0	0	0
1	1	0	1	0
1	1	1	0	0
1	1	1	1	1



Each group translates into one term of the SoP form expression

$$r =$$

Notes:

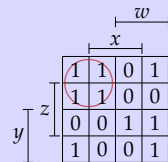
- The first two steps wrt. use of a Karnaugh map are simple: drawing a grid of an appropriate size then filling it with entries from the truth table are both trivial.
- Forming the groups is a little harder, in the sense that their “quality” (i.e., number and size) dictates how optimised the resulting expression will be. This example has some non-intuitive cases: the blue group might not be obvious for example, *but* is within the rules (although it is sort of inside-out, it is rectangular and a power-of-two in size).
- The last step is the hardest: we need to translate each group into a term that covers it. Put another way, we want a term that specifies just the cells in that group. In a sense, the more variables that exist within a term place more restrictions on which cells we specify; this highlights the fact that larger groups therefore contain fewer variables, and are therefore simpler. This example has three groups:
 - The red group spans columns 0 and 1 and rows 0 and 1; provided $w = 0$ and $y = 0$ we specify *just* those cells, so the expression is $\neg w \wedge \neg y$. That is, $w = 0$ restricts us to columns 0 and 1 (columns 2 and 3 have $w = 1$) and $y = 0$ restricts us to rows 0 and 1 (rows 2 and 3 have $y = 1$). Note that the values of x and z don't matter: cells in the group hold the value 1 regardless of x and y .
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Mechanical Derivation (5): Method #2 (Karnaugh map)

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0	0	0	1	1
0	0	1	0	1
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0	1	0	1	1
0	1	1	0	0
0	1	1	1	0
1	0	0	0	1
1	0	0	1	0
1	0	1	0	1
1	0	1	1	1
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1	1	0	1	0
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$$r = (\neg w \wedge \neg y)$$

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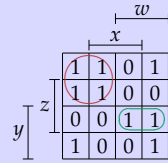
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Mechanical Derivation (5): Method #2 (Karnaugh map)

Example

Consider an example 4-input, 1-output function:

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0	0	1	1	0
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1	0	0	1	0
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Notes:

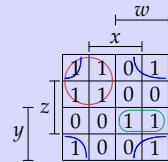
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Each group translates into one term of the SoP form expression

$$r = (\neg w \wedge \neg y) \vee (w \wedge y \wedge z) \vee (\neg x \wedge \neg z)$$

Notes:

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Mechanical Derivation (6): Method #2 (Karnaugh map)

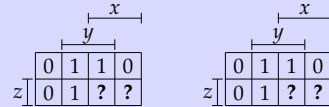
► Karnaugh maps can accommodate **don't care** states:

- Sometimes we don't care whether the output for a given input combination is 0 or 1; we mark this with ? rather than 0 or 1.
- Although we don't care what value a ? element takes, treating it like a 1 allows us to include it in a group and as 0 to avoid covering it.

Example

Consider an example 3-input, 1-output function:

x	y	z	r
0	0	0	0
0	0	1	0
0	1	0	1
0	1	1	1
1	0	0	0
1	0	1	?
1	1	0	1
1	1	1	?



Each group translates into one term of the SoP form expressions

$$r =$$

$$r =$$

where effective use of don't care states yields a clear improvement!

Notes:

- Note the differing shape of this grid versus the previous example: since there are $n = 3$ variables, we set $p = 2$ and $q = 4$ st. the grid contains $2 \cdot 4 = 2^3 = 8$ cells.
- Adopting the same approach as the previous example, by not ignoring the don't care states (i.e., assuming they are 0) we have two groups. However, opting to treat one of them as a 1 (which is fine: by definition we don't care what the output is) we only have one:
 - The red group spans column 1 and rows 0 and 1; provided $x = 0$ and $y = 1$ we specify *just* those cells, so the expression is $\neg x \wedge y$. That is, $x = 0$ and $y = 1$ restricts us to column 1 (columns 0, 2 and 3 have at least one of $x = 1$ or $y = 0$) which is all we need because the group spans *all* rows. Note that the value of z doesn't matter: cells in the group hold the value 1 regardless of z .
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Mechanical Derivation (6): Method #2 (Karnaugh map)

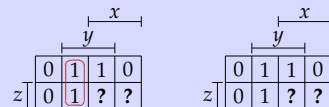
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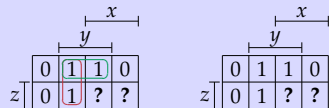
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- Adopting the same approach as the previous example, by not ignoring the don't care states (i.e., assuming they are 0) we have two groups. However, opting to treat one of them as a 1 (which is fine: by definition we don't care what the output is) we only have one:
 - The red group spans column 1 and rows 0 and 1; provided $x = 0$ and $y = 1$ we specify *just* those cells, so the expression is $\neg x \wedge y$. That is, $x = 0$ and $y = 1$ restricts us to column 1 (columns 0, 2 and 3 have at least one of $x = 1$ or $y = 0$) which is all we need because the group spans *all* rows. Note that the value of z doesn't matter: cells in the group hold the value 1 regardless of z .
 - The green group spans columns 1 and 2, in row 0; provided $y = 1$ and $z = 0$ we specify *just* those cells, so the expression is $y \wedge \neg z$. That is, $y = 1$ restricts us to columns 1 and 2 (columns 0 and 3 have $y = 0$) and $z = 0$ restricts us to row 0 (row 1 has $z = 1$). Note that the value of x doesn't matter: cells in the group hold the value 1 regardless of x .
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Mechanical Derivation (6): Method #2 (Karnaugh map)

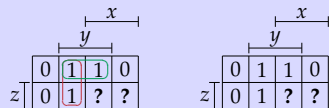
► Karnaugh maps can accommodate **don't care** states:

- Sometimes we don't care whether the output for a given input combination is 0 or 1; we mark this with ? rather than 0 or 1.
- Although we don't care what value a ? element takes, treating it like a 1 allows us to include it in a group and as 0 to avoid covering it.

Example

Consider an example 3-input, 1-output function:

x	y	z	r
0	0	0	0
0	0	1	0
0	1	0	1
0	1	1	1
1	0	0	0
1	0	1	?
1	1	0	1
1	1	1	?



Each group translates into one term of the SoP form expressions

$$r = (\neg x \wedge y) \vee (y \wedge \neg z)$$

where effective use of don't care states yields a clear improvement!

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- Note the differing shape of this grid versus the previous example: since there are $n = 3$ variables, we set $p = 2$ and $q = 4$ st. the grid contains $2 \cdot 4 = 2^3 = 8$ cells.
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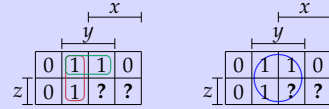
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Each group translates into one term of the SoP form expressions

$$r = (\neg x \wedge y) \vee (y \wedge \neg z) \quad r = y$$

where effective use of don't care states yields a clear improvement!

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Conclusions

► Take away points:

1. There are a *huge* number of issues to consider when designing even simple components, e.g.,
 - how do we describe what the circuit should do?
 - what sort of standard library do we use?
 - do we aim for the fewest gates?
 - do we aim for shortest critical path?
 - how do we cope with propagation delay and fan-out?
2. The design patterns, and mechanical techniques are the key concepts to focus on: these allow you to produce an effective implementation most easily.
3. In many cases, use of appropriate **Electronic Design Automation (EDA)** tools can provide (semi-)automatic solutions.

Notes:

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- [7] A.S. Tanenbaum.
[Section 3.2: Basic digital logic circuits](#).
In *Structured Computer Organisation* [8].

Notes:

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- [8] A.S. Tanenbaum.
Structured Computer Organisation.
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