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1200/1152/1080/960-Output TFT LCD Gate Driver

1. GENERAL DESCRIPTION

The EK73217BCGA is a 1200/1152/1080/960-Output gate driver used for driving the gate electrode of TFT LCD panel. It is designed for 2-level output with maximum +400 output driving voltage.

2. FEATURES

- 2-level output gate driver for TFT LCD panek
- 1200/1152/1080/960-Output gate driver with 2 dummy outputs which are fixed to VGL
- Maximum +40V output driving voltage
- Logic operating voltage (VCC): 1.7-\2.5\
- Bi-directional data shift capability
- 200 KHz maximum operation frequency
- High voltage CMOS process technology
- COG package
- Chip size=33650*670µm
- Output bump pitch=18km

3. BLOCK DIAGRAM

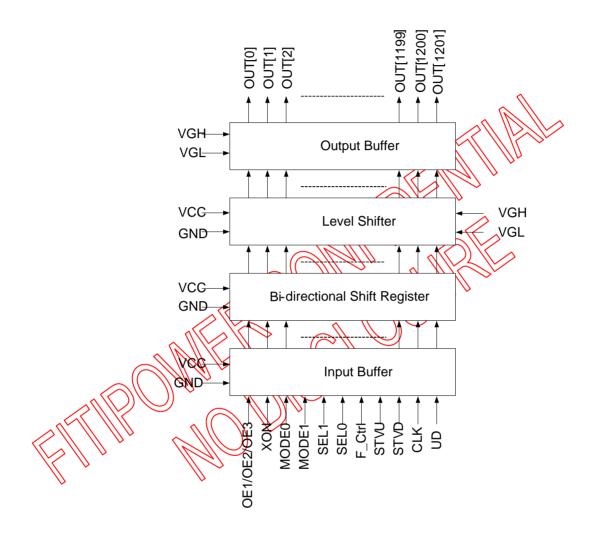


Figure 1. Block Diagram

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4. PIN DESCRIPTION

Pin Name	Pin Type	Description						
CLKR/CLKL	1	This is the clock input for chip internal shift register. Data is shifted at						
CLKR/CLKL	! 	each rising edge of this clock.						
		This pin controls the output shifting direction as listed below.						
UDR/UDL	I	$UD = H \colon STVD(input) \to OUT[1] \to OUT[2] \to \bullet \bullet \bullet \to OUT[1200] \to STVU$						
		$UD = L: STVU(input) \rightarrow OUT[1200] \rightarrow \cdots \rightarrow OUT[2] \rightarrow OUT[1] \rightarrow STVD$						
		These two pins are the device start pulse input or output pin. The						
		function of these two pins depends on the status of UD pin.						
STVD	I/O	STVD STVU						
STVU		UD = H output output						
		UD = A output input						
		The OE1 signal controls the OUT1, QUT4, QUT7OUT1195, OUT1198						
		output enable						
OE1R/OE1L		QE1,2,3="H" outputs are fixed to VCLregardless of CLK, However, the						
		content of shift register is not cleared.						
		OE1;2,3="L":Normal operation.						
_		The OE1 signal controls the OUT2, OUT5, OUT8OUT1196, OUT1199						
\bigcirc		output enable:						
OE2R/OE2L		OE1,2,3="H":outputs are fixed to VGLregardless of CLK,However,the						
		content of shift register is not cleared.						
		OE1,2,3="L":Normal operation.						
V		The OE1 signal controls the OUT3, OUT6,OUT9OUT1197, OUT1200						
		output enable.						
OE3R/OE3L	I	OE1,2,3="H":outputs are fixed to VGLregardless of CLK,However,the						
		content of shift register is not cleared.						
		OE1,2,3="L":Normal operation.						
		When XON input pin is L, all the output pins are forced to VGH level.						
		Note that this pin has higher priority than OE1/OE2/OE3.						
XONR/XONL	IPH	Also it has an internal pull high resistor, keep it to VCC is preferred when						
		unused. The chip internal shift register is not cleared when XON input is						
		active.						

		Output chan	nels select ir	nput. MODE0/MODE1	are internally pulled high.	
MODE0R		Note: This	oin should be	e connected to either	"VCC" or "GND".	
MODE0L		MODE1	MODE0	Output Channels	Disable channel	
MODE1R	IPH	1	1	1200	-	
MODE1L		1	0	1152	OUT[577] ~ OUT[624]	
		0	1	1080	OUT[541] ~ OUT[660]	
		0	0	960	OUT[48] ~ OUT[720]	
		Output sec	uence cont	rol inputs. These two	pins control the driver ou	utput
SEL0R		sequence.	Internally p	ulled low.		
SEL0L	IPL		SEI	L1 SELO S	Scan Type	
SEL1R	II L		1		Z+ Bow	
SEL1L			10	MY NY	Bow	
			\mathbf{x}	9	1	
F_CtrlL		Erame con	troll input. T	his pin decides to in	nverse the output sequenc	re or
F_CtrlR	IPL	(())		ne Internally pulled I		,0 01
OUT[1] ~		~ ~			or driving the gate electroo	
OUT[1200]	(B) 11 .	11 11	(())	nding on the data s	stored in shift register and	I the
		state of OF)			
OU/[0]//	9	LCD panel	auxiliary pir	ns, these pins always	s output VGL level.	
OUT(1201]	11/1/					

Pin Name	Pin	Description
	Туре	
VCC	Р	Digital power
GND	Р	Digital ground
VGH	Р	Power supply for OUT[1] ~ OUT[1200] drive output High
VGL	Р	Power supply for OUT[1] ~ OUT[1200] drive output Low.
PATH1R,PATH1L		
PATH2R,PATH2L	-	Linked together internal.
PATH3R,PATH3L		
DUM2~DUM220	-	This pin is connected to GND internally. Not connected.

note:

I: Input, IPH: Input with internal pull high, IPL: Input with internal low, O: Output, P: Power.

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Pass line name

Pass line No.	Pad n	ame		
1	OE1R	OE1L		
2	OE2R	OE2L		
3	OE3R	OE3L		
4	UDR	UDL		
5	CLKR	CLKL	Λ.	
6	PATH1R	PATHAL	?	
7	PATH2R	PATH2		
8	PATH3R	PATH3L		
9	VGH	VGH		
10	KGT ///	VGL		
11	Wacl	VOC		
12	(()(GND	// GND		
13	MODEOR	MODEOL		
14	MQDETR	MODE0L		
15	SELOR	SEL0L		
16 1	SELIR	SEL1L		
17	F_CtrlR	F_CtrlL		
18	XONR	XONL		
	1.0			

5. FUNCTION DESCRIPTION

5.1. Device operation

In the condition of UD=H, the STVD start pulse input is sensed at the rising edge of CLK and stored in the first stage of shift register, which causes the first scan signal is output from the X1 output pin. While stored data is transferred to the next stage shift register at the rising edge of next CLK, new data of STVD is sensed and stored simultaneously.

The output pin (OUT[1] to OUT[1200]) supplies VGH voltage or VGL voltage to the LCD panel depending on the data stored in the shift register. For normal operation a VGH voltage is output one by one from OUT[1] to OUT[1200] in sync with CLK pulse.

After 1200 CLK rising edge are past, the STVU goes up to high level at the 1200th falling edge of CLK and goes down to low level at the 1201th falling edge of CLK. This STVU output signal becomes the STVD start pulse input of next cascaded gate driver device.

During any "H" state of OE, the corresponding output channels are forced to VGL level regardless of CLK. The channel output returns to normal status as soon as OE to back to "L".

5.2. Device power supply

The EK73217BCGA must be used by the following conditions.

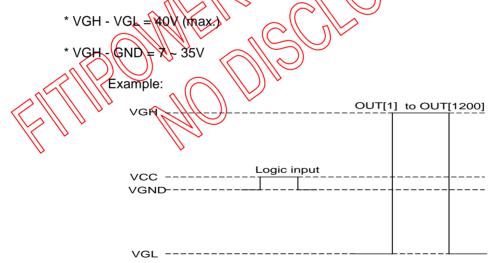


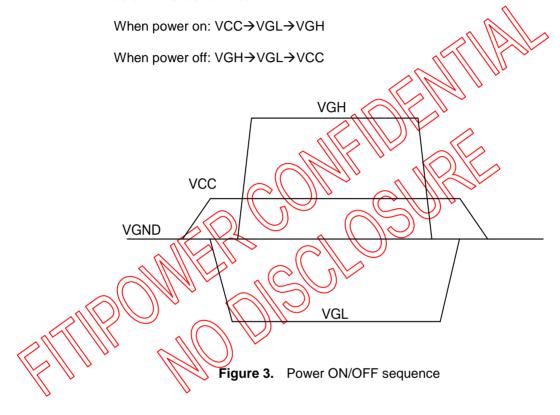
Figure 2. Device power supply

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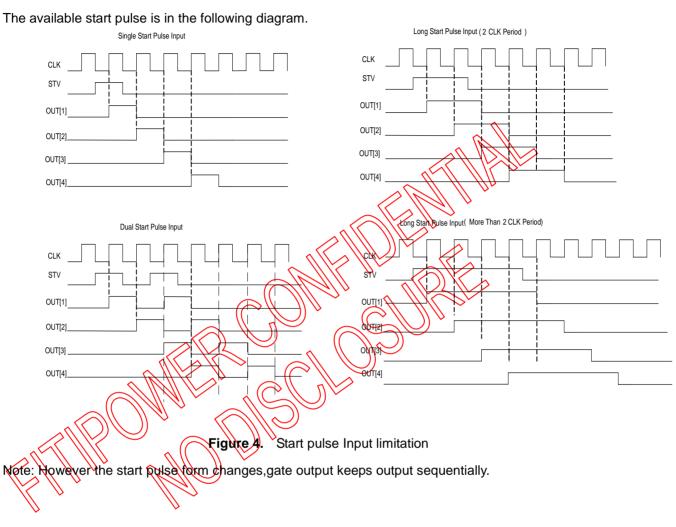
The input signal level of CLK, UD, OE, STVD, STVU, MODE0, MODE1, SEL1, SEL0, and F_ctrl have to swing between VCC and GND. The signal output level of start pulse (STVU or STVD) to the next stage cascaded device is VCC for "H" and GND for "L".

5.3. Power ON/OFF sequence

To prevent the device from damage due to latch up, the power ON/OFF sequence shown below must be followed.



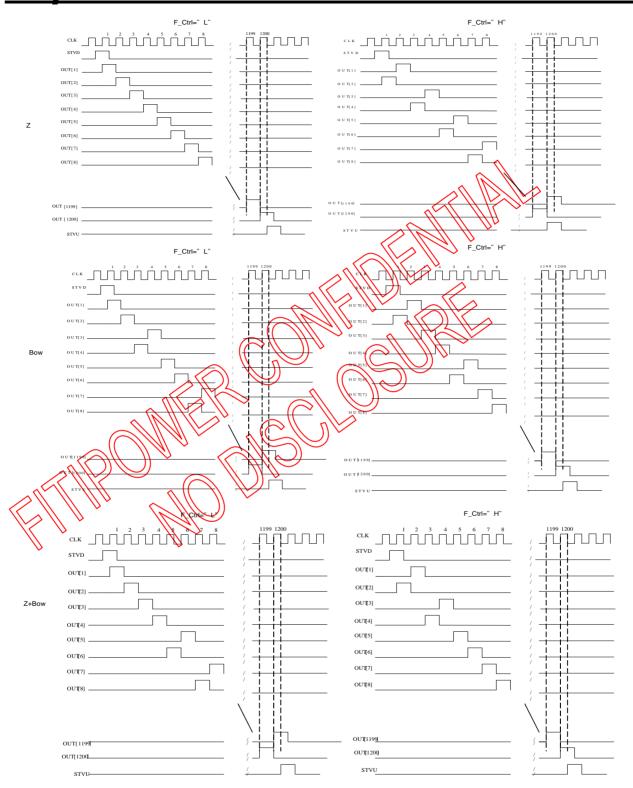
5.4. Start Pulse LIMITATION



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5.5. Output Sequence and Frame Control

			UD = H	
SEL0	SEL1	F_Ctrl	Scan Type	Output Sequence
1	1	0	Z + BOW	1→2→3→4→6→5→8→7→ (Note1)
		1	Inverse (Z+BOW)	2→1→4→3→5→6→7→8→
1	0	0	BOW	1→2→4→3→5→6→8→7→
		1	Inverse BOW	2→1→3→4→6→5→7→8→
0	Х	0	z	$7 \rightarrow 2 \rightarrow 3 \rightarrow 4 \rightarrow 5 \rightarrow 6 \rightarrow 7 \rightarrow 8 \rightarrow \dots$ (Note 3)
		1	Inverse Z	$2 \rightarrow 1 \rightarrow 4 \rightarrow 3 \rightarrow 6 \rightarrow 5 \rightarrow 8 \rightarrow 7 \rightarrow \dots$ (Note 4)
			UD = L	
SEL0	SEL1	F_Ctrl	Scan Type	Output Sequence
1	1		Z + BOW Inverse Z+BOW	7→8→5→6→4→3→2→1
1 6	Whole	V O	BOW	8→7→5→6→4→3→1→2
	11/11/2		Inverse BOW	7→8→6→5→3→4→2→1
	×	0	Z Inverse Z	8\rightarrow7\rightarrow6\rightarrow5\rightarrow4\rightarrow3\rightarrow2\rightarrow17\rightarrow8\rightarrow5\rightarrow6\rightarrow3\rightarrow4\rightarrow1\rightarrow2
1111	M			I



6. ELECTRICAL SPECIFICATION

6.1. Absolute Maximum Ratings

Absolute Maximum Ratings (GND = 0 V)

Parameter	Symbol	Rating	Unit
Power supply voltage (1)	VGH	-0.3 to +42.0	V
Power supply voltage (2)	VCC	-0.3 to +7.0	V
Power supply voltage (3)	VGL	-20 to +0.3	V
Power supply voltage (4)	VGH - VGL	10.82+40	V
Input voltage	V _{IN}	-0.3 to VCC+0.3	V
Storage temperature	T _{STG}	-55 to +125	°C

Note 1: All of the voltages listed above are with respective to QND = 0V.

Note 2: Device is subject to be damaged permanently if stresses beyond those absolute maximum ratings listed above.

6.2. Recommended Operating Range

Recommended Operating Range (VGND = 0V)

Parameter	Symbol	Conditions		Unit		
Farameter	Syllibol	Conditions	Min.	Тур.	Max.	Offic
Power supply voltage (1)	/ KGH//	リ・	7	-	VGL+40	V
Power supply voltage (2)	//VÇC)	-	1.7	-	2.5	V
Power supply voltage (3)	VGL	VCC=1.7V~2.5V	-10	ı	-5	V
Power supply voltage (4)	VGH −VGL	•	15	ı	40	V
Operation frequency	FCLK	-	1	1	200	KHz
Operation temperature	Ta	-	-20	-	+85	°C



6.3. DC Characteristics

DC Characteristic (VGH = 25V, VGL = -15V, VCC = 1.8V, GND = 0V, Ta = 25° C)

					Rat	ina	Unit	Application	
	Parameter	Symbol	Condition		ivai	Oilit	pin		
				Min.	Тур.	Max.			
	Input H voltage	ViH	-	0.7VCC	-	VCC	V	All input	
	Input L voltage	VIL	-	0	-	0.3VCC	V	All input	
	Output H voltage	Vон	IOH=40µA	VCC-0.4	- /	VdC	W .	STVU,D	
	Output L voltage	Vol	IOL=40μA	0	2/1/1	0.4/	V	STVU,D	
	Output H	Rон	Vx =		1/4	1000	Ω	OUT[1] ~	
	resistance	NOH	VGH -0.5V			1000	2.2	OUT[1200]	
	Output L	Rol	Vx =		- (1000	Ω	OUT[1] ~	
	resistance	I KOL	VGL+0.5V	1,	$\langle \bigcirc \rangle$	1000	22	OUT[1200]	
	Input leakage	lin (\approx	-1.0		+1.0	μA	Note ⁽²⁾	
	current				\bigcirc	11.0	μπ	NOLE	
			VCC=1.8(X)			XON,	
	Pull high / low	Remo	VIN=VGND	220		4000	1.0	SEL1,	
	resistance	WI TYCO	220	-	1080	kΩ	SEL0,		
								F_CTRL	
	VOIT Power	Ivgh	Note ⁽¹⁾	_	50	250	μA	_	
	consumption		710.0			200	μ, ,		
	VGL Power	Tvgh	Note ⁽¹⁾	_	-50	-250	μA	_	
	consumption	V Wen	110.0			200	μ, ,		
	VCC Power	Ivcc	Note ⁽¹⁾	_	15	100	μA	_	
	consumption						μ,,		
•									

Note 1: Power consumption with the following condition: Output no load, VGH=25V, VGL=-15V, VCC=1.8V, VIH = VCC, VIL=GND, F_{Clk} = 50 KHz, OE = VIL, XON= VIH.

Note 2: All input except XON, SEL1, SEL0, FCTRL

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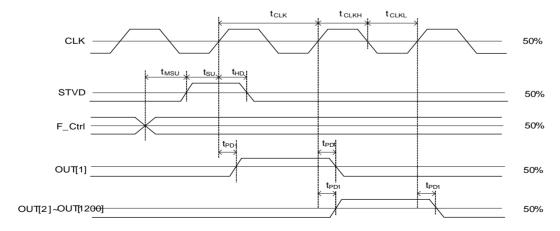
6.4. AC Characteristics

AC Characteristics (VGH = 25V, VGL = -15V, VCC = 1.8V, VGND = 0V, Ta = 25°C)

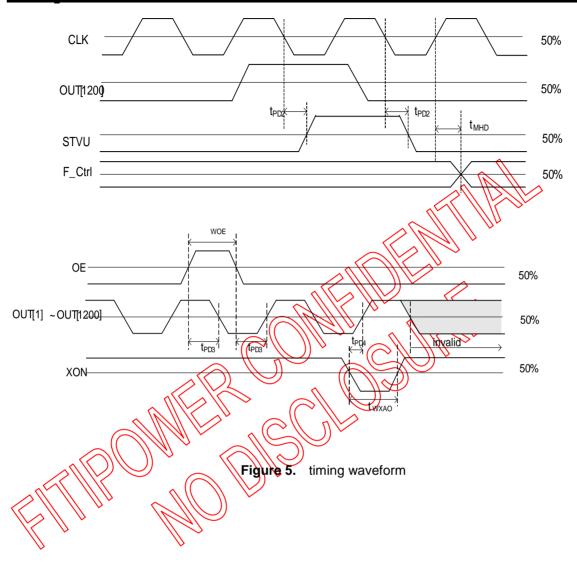
Parameter	Symbol	Condition		Rating	Unit	
i didilietei	Symbol	Condition	Min.	Тур.	Max.	
CLK period	t _{CLK}	-	5	-	-	μs
CLK pulse width	t _{CLKH} , t _{CLKL}	50% duty cycle	2.5	-	-	μs
OE pulse width	t _{WOE}	VCC=1.7V~2.5V	1.3	\ -	-	μs
C Paido maii.	-WOE		11/10		>	
XON pulse width	t _{WXAO}	- 2	100	7	-	μs
Data setup time	t _{SU}		0,7	-	-	μs
Data hold time	t _{HD}		0.7	-	-	μs
CLK to output delay	t _{PD1}	CL=300pA VCC=1.7V-2.5V	<u> </u>	_	1.5	μs
time	*PD1		$\langle \langle \rangle \rangle$		1.0	μο
Start pulse output	t _{PD2}	QL=30pR; VCC=1,74-2,5V	<u>\</u>	-	1.3	μs
delay time	1902					p.o
OE to output delay	t _{PD3}	CL=300pF, VCC=1)7V~2.5V	_	_	1.3	μs
time		\$ 62 664,100			1.0	μο
XON to output delay	t _{PD4}	=300pF	_	_	100	μs
time	1704				. 30	P.O
F Cth setup time	Denk		1.0			μs
F_Otrl hold time	T WHD		1.0			μs

Note 1: The measurement point for all of above signals is at 50% of input/output amplitude.

6.5. Timing Waveform



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6.6. Operation Timing

UD="H"

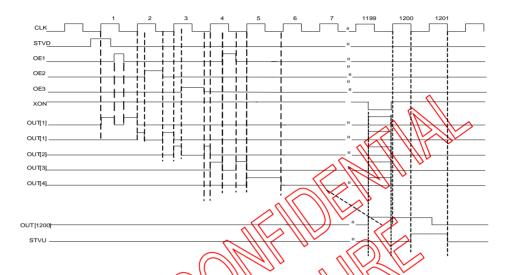


Figure 6. Example of input output timing (UD = H with OE and XON)

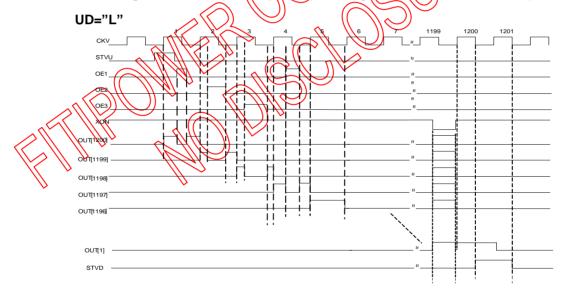
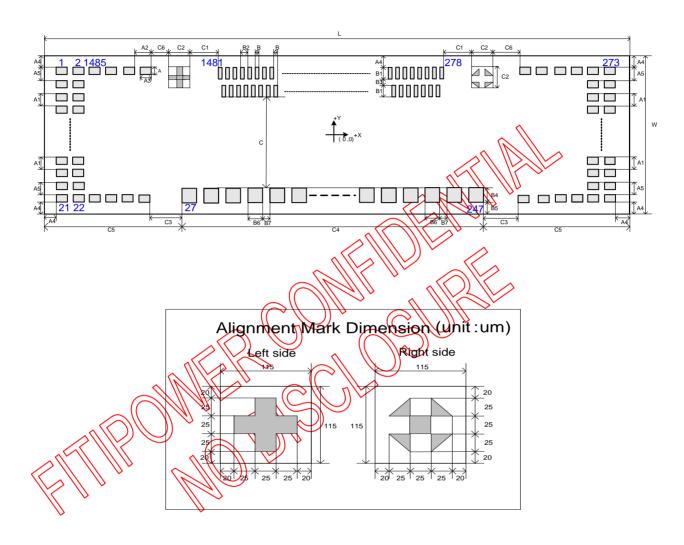


Figure 7. Example of input/output timing (UD = H with OE and XON)

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7. CHIP OUTLINE DIMENSIONS AND ALIGNMENT MARK



Symbol	Dimensions	Symbol	Dimensions	Symbol	Dimensions	
Α	32	B2	36	C2	115	
A1	52	В3	25	C3	208	
A2	90	B4	70	C4	22080	
A3	70	B5	57	C5	785	
A4	57	B6	80	C6	89	
A5	54	B7	20	L	23650	
В	18	С	291	W	670	
B1	85	C1	199	Unit : um		

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8. BUMP CENTER COORDINATE

		Pad Coo	rdinate	ı			Pad Cod	ordinate	l			Pad Coo	rdinate
Pad #	Pad Name	X	Y		Pad #	Pad Name	X	Y		Pad #	Pad Name	X	Y
1	OE1L	-11733	262		81	DUM55	-5600	-243		161	DUM135	2400	-243
2	OE1L	-11643	262		82	DUM56	-5500	-243		162	DUM136	2500	-243
3	OE2L	-11733	208		83	DUM57	-5400	-243		163	DUM137	2600	-243
5	OE2L	-11643	208		84 85	DUM58	-5300	-243		164	DUM138	2700	-243 -243
6	OE3L OE3L	-11733 -11643	156 156		86	DUM59 DUM60	-5200 -5100	-243 -243		165 166	DUM139 DUM140	2800 2900	-243
7	UDL	-11733	104		87	DUM61	-5000	-243		167	DUM141	3000	-243
8	UDL	-11643	104		88	DUM62	-4900	-243		168	DUM142	3100	-243
9	CLKL	-11733	52		89	DUM63	-4800	-243		169	DUM143	3200	-243
10	CLKL	-11643	52		90	DUM64	-4700	-243		170	DUM144	3300	-243
11	PATH1L	-11733	0		91	DUM65	-4600	-243		171	DUM145	3400	-243
12	PATH1L	-11643	0		92	DUM66 DUM67	-4500	-243		172	DUM146	3500	-243
13 14	STVUL STVUL	-11733 -11643	-52 -52		93 94	DUM68	-4400 -4300	-243 -243		173 174	DUM(47)	3600 3700	-243 -243
15	VGH	-11733	-104		95	DUM69	-4200	-243		175	DUM149	3800	-243
16	VGH	-11643	-104		96	DUM70	-4100	-243		176	DUM 50	3900	-243
17	VGL	-11733	-156		97	DUM71	-4000	-243		177	QUM\51	4000	-243
18	VGL	-11643	-156		98	DUM72	-3900	-243		178	Q UM15 2	4100	-243
19	VCC	-11733	-208		99	DUM73	-3800	-243	//	179	DUM153	4200	-243
20	VCC	-11643	-208		100	DUM74	-3700	-243		180	DUM154	4300	-243
21	GND	-11733	-262		101	DUM75	-3600	-2243	//	181	DUM155	4400	-243
22	GND MODE1L	-11643 -11553	-262 -262		102	DUM76 DUM77	-3500 -34 0 0	1243 243	١)	183	DUM156 DUM157	4500 4600	-243 -243
24	MODE 1L MODE 0L	-11463	-262		103	DUM78	-3300	7-243 \\	$\prime\prime$	184	DUM157	4700	-243
25	SELOL	-11373	-262		105	DUM79	-3200	-243		185	DUM159	4800	-243
26	SEL1L	-11283	-262		106	DUM80	113469	-243		186	DUM160	4900	-243
27	PATH2L	-11000	-243		107	DUM81	1-8000	V-243		1/587	DUMJEN	5000	-243
28	DUM2	-10900	-243		108	DUM82	11-2900	-243	0	188	DUM162	5100	-243
29	DUM3	-10800	-243		109	DRIW88	2800	-243	٥,	189	DUM163	5200	-243
30 31	DUM4 DUM5	-10700 -10600	-243 -243		110	DUM84	-2600	-24β(-243	<u> </u>	190	DUM164 DUM165	5300 5400	-243 -243
32	DUM6	-10500	-243		11/2	DUM86	-2500	-243		192	DUM166	5500	-243
33	DUM7	-10400	-243	//	71/3	DUM87	-2400	-243)	193	DUM167	5600	-243
34	DUM8	-10300	-843	`	(114)	DUM88	2300	-243	/	194	DUM168	5700	-243
35	DUM9	-10200	-243	\geq	115	DUM89 /	2200	243		195	DUM169	5800	-243
36	DUM10	-10100	\\ <u>2</u> \43_		<u>√</u> №6	DUM90	-2100	243		196	DUM170	5900	-243
37	DUM11	-10000	1/-548 /	\leq	117	DMM94> \	-2000	-243		197	DUM171	6000	-243
38	DUM12	9900	11 -248	_	118	DUM92	-1900	-243		198	DUM172	6100	-243
39 40	DUM13 DUM14	-9800	243		119	DUM93 DUM94	-1800 -1700	-243 -243		199 200	DUM173 DUM174	6200 6300	-243 -243
41	DUM15	9600	-243		121	DUM95	-1600	-243		201	DUM175	6400	-243
42	DUM'N6	-9500	-243		122	SeMUD	-1500	-243		202	DUM176	6500	-243
43	MUMAY \	-9400	-243		123	M97 لارض	-1400	-243		203	DUM177	6600	-243
44	\ \ \\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\	-9300	-243		124	DUM98	-1300	-243		204	DUM178	6700	-243
45	DUM19 \	-9200	-243		125	DUM99	-1200	-243		205	DUM179	6800	-243
46	DUM20 DUM21	-9100 -9000	1243	//	1/26 127	DUM100 DUM101	-1100 -1000	-243 -243		206 207	DUM180 DUM181	6900 7000	-243 -243
48	DUM22	-8900	-243		128	DUM101 DUM102	-900	-243		208	DUM181	7100	-243
49	DUM23	-8800	-243		129	DUM103	-800	-243		209	DUM183	7200	-243
50	DUM24	-8700	-243		130	DUM104	-700	-243		210	DUM184	7300	-243
51	DUM25	-8600	-243		131	DUM105	-600	-243		211	DUM185	7400	-243
52	DUM26	-8500	-243		132	DUM106	-500	-243		212	DUM186	7500	-243
53	DUM27	-8400	-243		133	DUM107	-400 200	-243		213	DUM187	7600	-243
54 55	DUM28 DUM29	-8300 -8200	-243 -243		134 135	DUM108 DUM109	-300 -200	-243 -243		214 215	DUM188 DUM189	7700 7800	-243 -243
56	DUM30	-8200	-243		136	DUM109 DUM110	-100	-243		216	DUM189	7900	-243
57	DUM31	-8000	-243		137	DUM111	0	-243		217	DUM191	8000	-243
58	DUM32	-7900	-243		138	DUM112	100	-243		218	DUM192	8100	-243
59	DUM33	-7800	-243		139	DUM113	200	-243		219	DUM193	8200	-243
60	DUM34	-7700	-243		140	DUM114	300	-243		220	DUM194	8300	-243
61	DUM35	-7600 7500	-243		141	DUM115	400	-243		221	DUM195	8400	-243
62 63	DUM36 DUM37	-7500 -7400	-243 -243		142 143	DUM116 DUM117	500 600	-243 -243		222	DUM196 DUM197	8500 8600	-243 -243
64	DUM38	-7300	-243		144	DUM117 DUM118	700	-243		223	DUM197	8700	-243
65	DUM39	-7200	-243		145	DUM119	800	-243		225	DUM199	8800	-243
66	DUM40	-7100	-243		146	DUM120	900	-243		226	DUM200	8900	-243
67	DUM41	-7000	-243		147	DUM121	1000	-243		227	DUM201	9000	-243
68	DUM42	-6900	-243		148	DUM122	1100	-243		228	DUM202	9100	-243
69 70	DUM43 DUM44	-6800 -6700	-243 -243		149 150	DUM123 DUM124	1200 1300	-243 -243		229	DUM203	9200 9300	-243 -243
70	DUM44 DUM45	-6600	-243 -243		150	DUM124 DUM125	1400	-243 -243		230	DUM204 DUM205	9400	-243 -243
72	DUM46	-6500	-243		152	DUM126	1500	-243		232	DUM205 DUM206	9500	-243
73	DUM47	-6400	-243		153	DUM127	1600	-243		233	DUM207	9600	-243
74	DUM48	-6300	-243		154	DUM128	1700	-243		234	DUM208	9700	-243
75	DUM49	-6200	-243		155	DUM129	1800	-243		235	DUM209	9800	-243
76	DUM50	-6100	-243		156	DUM130	1900	-243		236	DUM210	9900	-243
77	DUM51	-6000	-243		157	DUM131	2000	-243		237	DUM211	10000	-243
78 79	DUM52 DUM53	-5900 -5800	-243 -243		158 159	DUM132 DUM133	2100 2200	-243 -243		238	DUM212 DUM213	10100 10200	-243 -243
80	DUM54	-5700	-243		160	DUM133 DUM134	2300	-243		239	DUM213 DUM214	10200	-243
	SOIVIOT	3,00	2-10		100	20111107	_000				IVIE IT	.0000	2-10

Pad #	Pad Name	Pad Coo	1	1	Pad #	Pad Name	Pad Coo			Pad#	Pad Name	Pad Coo	
241	DUM215	10400	-243		321	OUT[42]	X 10044	Y 235.5		401	OUT[122]	8604	Y 235.5
242	DUM216	10500	-243		322	OUT[42]	10044	125.5		402	OUT[123]	8586	125.5
243	DUM217	10600	-243		323	OUT[44]	10020	235.5		403	OUT[124]	8568	235.5
244	DUM218	10700	-243		324	OUT[45]	9990	125.5		404	OUT[125]	8550	125.5
245	DUM219	10800	-243		325	OUT[46]	9972	235.5		405	OUT[126]	8532	235.5
246	DUM220	10900	-243		326	OUT[47]	9954	125.5		406	OUT[127]	8514	125.5
247	PATH2R	11000	-243		327	OUT[48]	9936	235.5		407	OUT[128]	8496	235.5
248	SEL1R	11283	-262		328	OUT[49]	9918	125.5		408	OUT[129]	8478	125.5
249	SEL0R	11373	-262		329	OUT[50]	9900	235.5		409	OUT[130]	8460	235.5
250	MODE0R	11463	-262		330	OUT[51]	9882	125.5		410	OUT[131]	8442	125.5
251	MODE1R	11553	-262		331	OUT[52]	9864	235.5		411	OUT[132]	8424	235.5
252	GND	11643	-262		332	OUT[53]	9846	125.5		412	OUT[133]	8406	125.5
253	GND	11733	-262		333	OUT[54]	9828	235.5		413	OUT[134]	(8388	235.5
254	VCC	11643	-208		334	OUT[55]	9810	125.5		414	OUT[135]	8370	125.5
255	VCC	11733	-208		335	OUT[56]	9792	235.5		415	OUT[136]	8352	235.5
256	VGL	11643	-156		336	OUT[57]	9774	125.5		416	OUT[137]	8334	125.5
257	VGL	11733	-156		337	OUT[58]	9756	235.5		417	OUT[138]	8316	235.5
258	VGH	11643	-104		338	OUT[59]	9738	125.5		418	OUT[139]	8298	125.5
259 260	VGH STVDR	11733 11643	-104 -52		339 340	OUT[60] OUT[61]	9720 9702	235.5 125.5		419	\\OUT(140] \ \QUT(141)	8280 8262	235.5 125.5
261	STVDR	11733	-52		341	OUT[62]	9684	235.5		421	QUT[142]	8244	235.5
262	PATH1R	11643	0		342	OUT[62]	9666	125.5	$\langle \langle$	422	OUT[142]	8226	125.5
263	PATH1R	11733	0	ł	343	OUT[64]	9648	235.5	$^{\prime\prime}$	423	OUT[144]	8208	235.5
264	CLKR	11643	52	l	344	OUT[65]	9630	125,5	11,	424	OUT[144]	8190	125.5
265	CLKR	11733	52	1	345	OUT[66]	9612	235,5	١,	425	OUT[146]	8172	235.5
266	UDR	11643	104	1	346	OUT[67]	9594	125.5	ノ	426	OUT[147]	8154	125.5
267	UDR	11733	104	1	347	OUT[68]	9 \$ \\	235,5		427	OUT[148]	8136	235.5
268	OE3R	11643	156]	348	OUT[69]	9558	125.5		428	QUT[148]	8118	125.5
269	OE3R	11733	156		349	OUT[70]	1/9640	235.5		429	DUTNS 0]	3 8100	235.5
270	OE2R	11643	208		350	OUT[[*1]	9522	125.5	_	430	OUT[15]	8082	125.5
271	OE2R	11733	208		351	OUT(\$2]	19504	235.5	/	431	(OUT(152)	8064	235.5
272	OE1R	11643	262		352	(οὐτ[λ3]	9486	125,5	<i>></i> '	432	QUT[153]	8046	125.5
273	OE1R	11733	262	١,	353	OUT[74]	9468	235 5	_	\433	OUT[154]	8028	235.5
274	F_CTRLR	11553	262	//	354	VOUT[]5]	9450	125,5		434	OUT[155]	8010	125.5
275 276	F_CTRLR XONR	11463 11373	262 262	//	356 356	OUT[78]	9432 9414	235.5)	435 436	OUT[156] OUT[157]	7992 7974	235.5 125.5
277	XONR	11283	1268	`	357	OUT[78]	9396	235.5	/	437	OUT[157]	7974	235.5
278	PATH3R	10836 ^	235.6	2	368	OUT[79] /	9378	125.5		438	OUT[150]	7938	125.5
279	OUT[0]	10800	235.5	١.	359	OUT[80]	9360	235.5		439	OUT[160]	7920	235.5
280	OUT[1]	10782	1 425.5	\leq	360	OV7[81)	9342	125.5		440	OUT[161]	7902	125.5
281	OUT[2]	10764	235,5	~	361	QU[182]	9324	235.5		441	OUT[162]	7884	235.5
282	OUT[3]	10746	125.5		362	QV1[83]	9306	125.5		442	OUT[163]	7866	125.5
283	OUT[4]	(10728)	235.5		363	\QUT[84]	9288	235.5		443	OUT[164]	7848	235.5
284	QUT(5)	10710	125.5		364	OUT[85]	9270	125.5		444	OUT[165]	7830	125.5
285 286	QUT(N)	106 92 10674	235.5 125.5		365	QUT[86] OUT[87]	9252 9234	235.5 125.5		445 446	OUT[166] OUT[167]	7812 7794	235.5 125.5
287	OUT[8]	10656	235,5		367	OUT[88]	9216	235.5		447	OUT[167]	7776	235.5
288	// 001/61	10638	125.5		368	OUT[89]	9198	125.5		448	OUT[169]	7758	125.5
289	OUTIVOI	10620	1235.51		369	OUT[90]	9180	235.5		449	OUT[170]	7740	235.5
290	QUT[11]	10602	125.5	\	370	OUT[91]	9162	125.5		450	OUT[171]	7722	125.5
291	OUT[12]	10584	235,5		371	OUT[92]	9144	235.5		451	OUT[172]	7704	235.5
292	OUT[13]	10566	125.5		372	OUT[93]	9126	125.5		452	OUT[173]	7686	125.5
293	OUT[14]	10548	235.5		373	OUT[94]	9108	235.5		453	OUT[174]	7668	235.5
294	OUT[15]	10530	125.5		374	OUT[95]	9090	125.5		454	OUT[175]	7650	125.5
295	OUT[16]	10512	235.5	l	375	OUT[96]	9072	235.5		455	OUT[176]	7632	235.5
296	OUT[17]	10494	125.5	ł	376	OUT[97]	9054	125.5		456	OUT[177]	7614	125.5
297	OUT[18]	10476	235.5	l	377	OUT[98]	9036	235.5		457	OUT[178]	7596	235.5
298	OUT[19]	10458 10440	125.5	ł	378	OUT[99]	9018	125.5		458 459	OUT[179]	7578	125.5
299 300	OUT[20]	10440	235.5 125.5	l	379 380	OUT[100] OUT[101]	9000 8982	235.5 125.5		459 460	OUT[180] OUT[181]	7560 7542	235.5 125.5
300	OUT[21] OUT[22]	10422	235.5	l	381	OUT[101]	8964	235.5		461	OUT[181]	7524	235.5
302	OUT[22]	10386	125.5	ł	382	OUT[102]	8946	125.5		462	OUT[182]	7506	125.5
303	OUT[24]	10368	235.5	l	383	OUT[103]	8928	235.5		463	OUT[183]	7488	235.5
304	OUT[25]	10350	125.5	1	384	OUT[105]	8910	125.5		464	OUT[185]	7470	125.5
305	OUT[26]	10332	235.5	1	385	OUT[106]	8892	235.5		465	OUT[186]	7452	235.5
306	OUT[27]	10314	125.5]	386	OUT[107]	8874	125.5		466	OUT[187]	7434	125.5
307	OUT[28]	10296	235.5		387	OUT[108]	8856	235.5		467	OUT[188]	7416	235.5
308	OUT[29]	10278	125.5		388	OUT[109]	8838	125.5		468	OUT[189]	7398	125.5
309	OUT[30]	10260	235.5		389	OUT[110]	8820	235.5		469	OUT[190]	7380	235.5
310	OUT[31]	10242	125.5	l	390	OUT[111]	8802	125.5		470	OUT[191]	7362	125.5
311	OUT[32]	10224	235.5	l	391	OUT[112]	8784	235.5		471	OUT[192]	7344	235.5
312	OUT[33]	10206	125.5	ł	392	OUT[113]	8766	125.5		472	OUT[193]	7326	125.5
313 314	OUT[34] OUT[35]	10188 10170	235.5 125.5	l	393 394	OUT[114] OUT[115]	8748 8730	235.5 125.5		473 474	OUT[194] OUT[195]	7308 7290	235.5 125.5
314	OUT[36]	10170	235.5	ł	394	OUT[115]	8712	235.5		474	OUT[195]	7290	235.5
316	OUT[37]	10134	125.5	l	396	OUT[110]	8694	125.5		476	OUT[190]	7254	125.5
317	OUT[38]	10116	235.5	1	397	OUT[118]	8676	235.5		477	OUT[198]	7236	235.5
318	OUT[39]	10098	125.5	1	398	OUT[119]	8658	125.5		478	OUT[199]	7218	125.5
319	OUT[40]	10080	235.5		399	OUT[120]	8640	235.5		479	OUT[200]	7200	235.5
320	OUT[41]	10062	125.5]	400	OUT[121]	8622	125.5		480	OUT[201]	7182	125.5

		Pad Coo	rdinate	ĺ			Pad Cod	ordinate	ı			Pad Coo	rdinate
Pad #	Pad Name	X	Y		Pad #	Pad Name	X	Y		Pad #	Pad Name	X	Y
481	OUT[202]	7164	235.5		561	OUT[282]	5724	235.5		641	OUT[362]	4284	235.5
482 483	OUT[203]	7146	125.5		562	OUT[283]	5706	125.5		642	OUT[363]	4266 4248	125.5 235.5
484	OUT[204] OUT[205]	7128 7110	235.5 125.5		563 564	OUT[284] OUT[285]	5688 5670	235.5 125.5		643 644	OUT[364] OUT[365]	4230	125.5
485	OUT[206]	7092	235.5		565	OUT[286]	5652	235.5		645	OUT[366]	4212	235.5
486	OUT[207]	7074	125.5		566	OUT[287]	5634	125.5		646	OUT[367]	4194	125.5
487	OUT[208]	7056	235.5		567	OUT[288]	5616	235.5		647	OUT[368]	4176	235.5
488 489	OUT[209] OUT[210]	7038 7020	125.5 235.5		568 569	OUT[289] OUT[290]	5598 5580	125.5 235.5		648 649	OUT[369] OUT[370]	4158 4140	125.5 235.5
490	OUT[210]	7020	125.5		570	OUT[291]	5562	125.5		650	OUT[370]	4122	125.5
491	OUT[212]	6984	235.5		571	OUT[292]	5544	235.5		651	OUT[372]	4104	235.5
492	OUT[213]	6966	125.5		572	OUT[293]	5526	125.5		652	OUT[373]	4086	125.5
493	OUT[214]	6948	235.5		573	OUT[294]	5508	235.5		653	OUT[374]	4068	235.5
494 495	OUT[215] OUT[216]	6930 6912	125.5 235.5		574 575	OUT[295] OUT[296]	5490 5472	125.5 235.5		654 655	OUT[875] \	4050	125.5 235.5
496	OUT[217]	6894	125.5		576	OUT[297]	5454	125.5		656	OUT 37 X	4014	125.5
497	OUT[218]	6876	235.5		577	OUT[298]	5436	235.5		657	OU T [378]	3996	235.5
498	OUT[219]	6858	125.5		578	OUT[299]	5418	125.5		658	QUT[879]	3978	125.5
499 500	OUT[220] OUT[221]	6840 6822	235.5 125.5		579 580	OUT[300] OUT[301]	5400 5382	235.5 125.5		659	OUT[380]	3960 3942	235.5 125.5
501	OUT[221]	6804	235.5		581	OUT[301]	5364	235.5	//	ø61	OUT[382]	3924	235.5
502	OUT[223]	6786	125.5		582	OUT[303]	5346	126.5		662	OUT[383]	3906	125.5
503	OUT[224]	6768	235.5		583	OUT[304]	5328	1235.6	$ \ \rangle $	683	OUT[384]	3888	235.5
504	OUT[225]	6750	125.5		584	OUT[305]	5310	125.5	ノ	664	OUT[385]	3870	125.5
505 506	OUT[226] OUT[227]	6732 6714	235.5 125.5		585 586	OUT[306] OUT[307]	5292\ 5274	235.5 125.5		665 666	QU7[386] QU7[387]	3852 3834	235.5 125.5
507	OUT[228]	6696	235.5	1	587	OUT[307]	15256	235.5		667	QU1(388]	3816	235.5
508	OUT[229]	6678	125.5	1	588	OUT[309]	\$238	125.5		668	OUT[389]	3798	125.5
509	OUT[230]	6660	235.5		589	OUT[310]	1 5220	235.5	0	669	ONTISON	3780	235.5
510 511	OUT[231] OUT[232]	6642 6624	125.5 235.5		590 591	OUT[312]	52 02 5184	125.5 235.5	2,	670	OUT[391]	3762 3744	125.5 235.5
512	OUT[232]	6606	125.5	١.	592	OUT[318]	5166	125.5	_	672	OUT[393]	3726	125.5
513	OUT[234]	6588	235.5	//	593	QUT[3/4]	5148	235.5		673	OUT[394]	3708	235.5
514	OUT[235]	6570	125,5		5 94	OUT[315]	5130	125.5))	674	OUT[395]	3690	125.5
515 516	OUT[236] OUT[237]	6552 6534	285.5 125.5	ゝ`	595 \$96	OUT[316] OUT[317] /	5/1(2 5/09/4	125.5		675 676	OUT[396] OUT[397]	3672 3654	235.5 125.5
517	OUT[237]	6516	285.5		597	OUT[318]	5076	235.5		677	OUT[398]	3636	235.5
518	OUT[239]	6498	1265		598	OU7[319]	505 6	125.5		678	OUT[399]	3618	125.5
519	OUT[240]	6480	1 23515	~	599	QUT[320]	5040	235.5		679	OUT[400]	3600	235.5
520 521	OUT[241] OUT[242	6462	285.5		600	OUT[321] OUT[322]	5022	125.5 235.5		680 681	OUT[401] OUT[402]	3582 3564	125.5 235.5
522	OUT[243]	6426	125.5		802	QU 1/323	4986	125.5		682	OUT[403]	3546	125.5
523	001(244)	6408	235.5		603	OUT[324]	4968	235.5		683	OUT[404]	3528	235.5
524	OUT[245]	6390	125.5		604	OUT[325]	4950	125.5		684	OUT[405]	3510	125.5
525 526	OUT[246] OUT[247]	6372 6354	235.5 125.6		605 606	OUT[326] OUT[327]	4932 4914	235.5 125.5		685 686	OUT[406] OUT[407]	3492 3474	235.5 125.5
527	OUT[248]	6336	1235.5		607	OUT[328]	4896	235.5		687	OUT[408]	3456	235.5
528	QUT[249]	6318	125.51	_	608	OUT[329]	4878	125.5		688	OUT[409]	3438	125.5
529	OVT[250] OUT[251]	6300 6282	235.5 125.5	1	609 610	OUT[330] OUT[331]	4860 4842	235.5 125.5		689 690	OUT[410] OUT[411]	3420 3402	235.5 125.5
531	OUT[251]	6264	235.5		611	OUT[331]	4824	235.5		691	OUT[411]	3384	235.5
532	OUT[253]	6246	125.5		612	OUT[333]	4806	125.5		692	OUT[413]	3366	125.5
533	OUT[254]	6228	235.5		613	OUT[334]	4788	235.5		693	OUT[414]	3348	235.5
534 535	OUT[255] OUT[256]	6210 6192	125.5 235.5		614 615	OUT[335] OUT[336]	4770 4752	125.5 235.5		694 695	OUT[415] OUT[416]	3330 3312	125.5 235.5
536	OUT[256]	6174	125.5		616	OUT[336]	4734	125.5		696	OUT[416]	3294	125.5
537	OUT[258]	6156	235.5		617	OUT[338]	4716	235.5		697	OUT[418]	3276	235.5
538	OUT[259]	6138	125.5		618	OUT[339]	4698	125.5		698	OUT[419]	3258	125.5
539 540	OUT[260] OUT[261]	6120 6102	235.5 125.5		619 620	OUT[340] OUT[341]	4680 4662	235.5 125.5		699 700	OUT[420] OUT[421]	3240 3222	235.5 125.5
541	OUT[262]	6084	235.5	1	621	OUT[341]	4644	235.5		701	OUT[421]	3204	235.5
542	OUT[263]	6066	125.5		622	OUT[343]	4626	125.5		702	OUT[423]	3186	125.5
543	OUT[264]	6048	235.5		623	OUT[344]	4608	235.5		703	OUT[424]	3168	235.5
544 545	OUT[265] OUT[266]	6030 6012	125.5 235.5		624 625	OUT[345] OUT[346]	4590 4572	125.5 235.5		704 705	OUT[425] OUT[426]	3150 3132	125.5 235.5
546	OUT[267]	5994	125.5	1	626	OUT[347]	4554	125.5		706	OUT[427]	3114	125.5
547	OUT[268]	5976	235.5		627	OUT[348]	4536	235.5		707	OUT[428]	3096	235.5
548 549	OUT[269] OUT[270]	5958	125.5 235.5	ł	628 629	OUT[349] OUT[350]	4518 4500	125.5 235.5		708 709	OUT[429] OUT[430]	3078 3060	125.5 235.5
550	OUT[270]	5940 5922	125.5		630	OUT[350]	4500 4482	125.5		709	OUT[430]	3060	125.5
551	OUT[272]	5904	235.5	1	631	OUT[352]	4464	235.5		711	OUT[432]	3024	235.5
552	OUT[273]	5886	125.5		632	OUT[353]	4446	125.5		712	OUT[433]	3006	125.5
553	OUT[274]	5868	235.5		633	OUT[354]	4428 4410	235.5		713 714	OUT[434] OUT[435]	2988	235.5
554 555	OUT[275] OUT[276]	5850 5832	125.5 235.5		634 635	OUT[355] OUT[356]	4392	125.5 235.5		714	OUT[435]	2970 2952	125.5 235.5
556	OUT[277]	5814	125.5	1	636	OUT[357]	4374	125.5		716	OUT[437]	2934	125.5
557	OUT[278]	5796	235.5		637	OUT[358]	4356	235.5		717	OUT[438]	2916	235.5
558	OUT[279]	5778 5760	125.5		638	OUT[359]	4338	125.5		718	OUT[439]	2898	125.5
559 560	OUT[280] OUT[281]	5760 5742	235.5 125.5		639 640	OUT[360] OUT[361]	4320 4302	235.5 125.5		719 720	OUT[440] OUT[441]	2880 2862	235.5 125.5
- 000		J1 72	.20.0		3-10	551[501]	1002	.20.0	•	, , _ 0		2002	.20.0

Dod#	Dad Nama	Pad Coo	ordinate		Dod#	Dod Nome	Pad Cod	ordinate		Bod #	Dod Nome	Pad Coo	rdinate
Pad #	Pad Name	X	Υ		Pad #	Pad Name	Χ	Υ		Pad#	Pad Name	X	Υ
721	OUT[442]	2844	235.5		801	OUT[522]	1404	235.5		881	OUT[602]	-36	235.5
722 723	OUT[443] OUT[444]	2826 2808	125.5 235.5		802 803	OUT[523] OUT[524]	1386 1368	125.5 235.5		882 883	OUT[603] OUT[604]	-54 -72	125.5 235.5
724	OUT[445]	2790	125.5		804	OUT[525]	1350	125.5		884	OUT[605]	-90	125.5
725	OUT[446]	2772	235.5		805	OUT[526]	1332	235.5		885	OUT[606]	-108	235.5
726	OUT[447]	2754	125.5		806	OUT[527]	1314	125.5		886	OUT[607]	-126	125.5
727	OUT[448]	2736	235.5		807	OUT[528]	1296	235.5		887	OUT[608]	-144	235.5
728	OUT[449]	2718	125.5		808	OUT[529]	1278	125.5		888	OUT[609]	-162	125.5
729 730	OUT[450] OUT[451]	2700 2682	235.5 125.5		809 810	OUT[530] OUT[531]	1260 1242	235.5 125.5		889 890	OUT[610] OUT[611]	-180 -198	235.5 125.5
731	OUT[452]	2664	235.5		811	OUT[532]	1224	235.5		891	OUT[612]	-216	235.5
732	OUT[453]	2646	125.5		812	OUT[533]	1206	125.5		892	OUT[613]	234	125.5
733	OUT[454]	2628	235.5		813	OUT[534]	1188	235.5		893	OUT[614]	-252	235.5
734	OUT[455]	2610	125.5		814	OUT[535]	1170	125.5		894	OUT[6(5]	-270	125.5
735 736	OUT[456] OUT[457]	2592 2574	235.5 125.5		815 816	OUT[536] OUT[537]	1152 1134	235.5 125.5		895 896	QUT[617)\	-306	235.5 125.5
737	OUT[458]	2556	235.5		817	OUT[538]	1116	235.5		897	OUT 618]	-324	235.5
738	OUT[459]	2538	125.5		818	OUT[539]	1098	125.5		8 98	OUT[619]	-342	125.5
739	OUT[460]	2520	235.5		819	OUT[540]	1080	235.5		899	QUT[620]	-360	235.5
740	OUT[461]	2502	125.5		820	OUT[541]	1062	125.5	\mathcal{N}	900	QU [621]	-378	125.5
741 742	OUT[462] OUT[463]	2484 2466	235.5 125.5		821 822	OUT[542] OUT[543]	1044 1026	235.5		901	OUT[622] OUT[623]	-396 -414	235.5 125.5
743	OUT[463]	2448	235.5	l	823	OUT[544]	1008 _^	235.5	//	903	OUT[624]	-432	235.5
744	OUT[465]	2430	125.5	1	824	OUT[545]	990	125.5)	904	OUT[621]	-450	125.5
745	OUT[466]	2412	235.5		825	OUT[546]	Q 72\\	235.5		905	QU7[626]	-468	235.5
746	OUT[467]	2394	125.5		826	OUT[547]	954	125.5	ĺ	906	OUT[627]	-486	125.5
747 748	OUT[468] OUT[469]	2376 2358	235.5 125.5		827 828	OUT[548]	936	235.5		907	001(628) 001(628)	-504 -522	235.5
748	OUT[469] OUT[470]	2358	235.5	l	828	OUT(5/49] OUT (550]	1 986	235.5	۸	908	ODT[638]	-522 -540	125.5 235.5
750	OUT[470]	2322	125.5	1	830	OUX[551]	1882	125.5	$\backslash \backslash$	910	QUT[631]	-558	125.5
751	OUT[472]	2304	235.5		831	OUT[552]	864	235 5	১	911	O UT[632]	-576	235.5
752	OUT[473]	2286	125.5		832	OUT[568]	846	125.5	\Box	8/12	OUT[633]	-594	125.5
753 754	OUT[474] OUT[475]	2268 2250	235.5	$\langle \langle$	833	OUT[55]	828 810	235.5		913	OUT[634] OUT[635]	-612 -630	235.5
755	OUT[476]	2232	285.5	//	834 835	OUT[556]	X9 2	125.5))	915	OUT[636]	-648	125.5 235.5
756	OUT[477]	2214	1255	ゝ`	836	OUT[557] /	₹	125.8		916	OUT[637]	-666	125.5
757	OUT[478]	2196	235.5	ľ	837	OUT[558]	756	235.5		917	OUT[638]	-684	235.5
758	OUT[479]	2178	1255		838	OU7[559]	7387	125.5		918	OUT[639]	-702	125.5
759	OUT[480]	2160	1 23515	~	839	OUT[560]	720	235.5		919	OUT[640]	-720	235.5
760 761	OUT[481] OUT[482	2142	285.5		840	OUT[561] OUT[562]	684	125.5 235.5		920 921	OUT[641] OUT[642]	-738 -756	125.5 235.5
762	OUT[483]	2106	125.5		842	QU 1/563	666	125.5		922	OUT[643]	-774	125.5
763	QUT(484)\	2088	235.5		843	OUT[564]	648	235.5		923	OUT[644]	-792	235.5
764	OUT[485] \	2070	125.5		844	OUT[565]	630	125.5		924	OUT[645]	-810	125.5
765 766	OUT[486]\\ OUT[486]\\	2052	235.5		845	OUT[566]	612 594	235.5		925 926	OUT[646]	-828 -846	235.5
767	OUT[488]	2034 2016	125.5		846 847	OUT[567] OUT[568]	576	125.5 235.5		927	OUT[647] OUT[648]	-864	125.5 235.5
768	OUT[489]	1998	125.51		848	OUT[569]	558	125.5		928	OUT[649]	-882	125.5
769	O ∀ T[490]	1980	235.5	>	849	OUT[570]	540	235.5		929	OUT[650]	-900	235.5
770	OUT[491]	1962	125.5		850	OUT[571]	522	125.5		930	OUT[651]	-918	125.5
771	OUT[492] OUT[493]	1944	235.5		851	OUT[572]	504	235.5		931	OUT[652]	-936	235.5
772 773	OUT[493] OUT[494]	1926 1908	125.5 235.5	ł	852 853	OUT[573] OUT[574]	486 468	125.5 235.5		932 933	OUT[653] OUT[654]	-954 -972	125.5 235.5
774	OUT[495]	1890	125.5	1	854	OUT[574]	450	125.5		934	OUT[655]	-990	125.5
775	OUT[496]	1872	235.5	1	855	OUT[576]	432	235.5		935	OUT[656]	-1008	235.5
776	OUT[497]	1854	125.5		856	OUT[577]	414	125.5		936	OUT[657]	-1026	125.5
777 778	OUT[498] OUT[499]	1836 1818	235.5		857 858	OUT[578] OUT[579]	396 378	235.5		937 938	OUT[658]	-1044 -1062	235.5 125.5
779	OUT[500]	1800	125.5 235.5	ł	859	OUT[579]	360	125.5 235.5		939	OUT[659] OUT[660]	-1082	235.5
780	OUT[500]	1782	125.5	1	860	OUT[581]	342	125.5		940	OUT[661]	-1098	125.5
781	OUT[502]	1764	235.5		861	OUT[582]	324	235.5		941	OUT[662]	-1116	235.5
782	OUT[503]	1746	125.5		862	OUT[583]	306	125.5		942	OUT[663]	-1134	125.5
783	OUT[504]	1728	235.5	l	863	OUT[584]	288	235.5		943	OUT[664] OUT[665]	-1152	235.5
784 785	OUT[505] OUT[506]	1710 1692	125.5 235.5		864 865	OUT[585] OUT[586]	270 252	125.5 235.5		944 945	OUT[665]	-1170 -1188	125.5 235.5
786	OUT[500]	1674	125.5	1	866	OUT[587]	234	125.5		946	OUT[667]	-1206	125.5
787	OUT[508]	1656	235.5	1	867	OUT[588]	216	235.5		947	OUT[668]	-1224	235.5
788	OUT[509]	1638	125.5		868	OUT[589]	198	125.5		948	OUT[669]	-1242	125.5
789	OUT[510]	1620	235.5	l	869	OUT[590]	180	235.5		949	OUT[670]	-1260	235.5
790 791	OUT[511] OUT[512]	1602 1584	125.5 235.5		870 871	OUT[591] OUT[592]	162 144	125.5 235.5		950 951	OUT[671] OUT[672]	-1278 -1296	125.5 235.5
791	OUT[512]	1566	125.5	l	872	OUT[592]	126	125.5		952	OUT[672]	-1314	125.5
793	OUT[514]	1548	235.5	1	873	OUT[594]	108	235.5		953	OUT[674]	-1332	235.5
794	OUT[515]	1530	125.5		874	OUT[595]	90	125.5		954	OUT[675]	-1350	125.5
795	OUT[516]	1512	235.5	l	875	OUT[596]	72	235.5		955	OUT[676]	-1368	235.5
796 797	OUT[517] OUT[518]	1494 1476	125.5 235.5		876 877	OUT[597] OUT[598]	54 36	125.5 235.5		956 957	OUT[677] OUT[678]	-1386 -1404	125.5 235.5
797	OUT[518]	1476	125.5	l	878	OUT[598]	18	125.5		957	OUT[678]	-1404	125.5
799	OUT[520]	1440	235.5	1	879	OUT[600]	0	235.5		959	OUT[680]	-1440	235.5
800	OUT[521]	1422	125.5		880	OUT[601]	-18	125.5		960	OUT[681]	-1458	125.5

		Pad Coo	rdinate	1			Pad Cod	ordinate	1			Pad Coo	rdinate
Pad #	Pad Name	Х	Υ		Pad #	Pad Name	Х	Υ		Pad #	Pad Name	Χ	Υ
961	OUT[682]	-1476	235.5		1041	OUT[762]	-2916	235.5		1121	OUT[842]	-4356	235.5
962	OUT[683]	-1494	125.5		1042	OUT[763]	-2934	125.5		1122	OUT[843]	-4374	125.5
963	OUT[684]	-1512	235.5		1043	OUT[764]	-2952	235.5		1123	OUT[844]	-4392	235.5
964 965	OUT[685] OUT[686]	-1530 -1548	125.5 235.5		1044 1045	OUT[765] OUT[766]	-2970 -2988	125.5 235.5		1124 1125	OUT[845] OUT[846]	-4410 -4428	125.5 235.5
966	OUT[687]	-1566	125.5		1045	OUT[767]	-3006	125.5		1126	OUT[847]	-4446	125.5
967	OUT[688]	-1584	235.5		1047	OUT[768]	-3024	235.5		1127	OUT[848]	-4464	235.5
968	OUT[689]	-1602	125.5		1048	OUT[769]	-3042	125.5		1128	OUT[849]	-4482	125.5
969	OUT[690]	-1620	235.5		1049	OUT[770]	-3060	235.5		1129	OUT[850]	-4500	235.5
970	OUT[691]	-1638	125.5		1050	OUT[771]	-3078	125.5		1130	OUT[851]	4518	125.5
971 972	OUT[692] OUT[693]	-1656 -1674	235.5 125.5		1051 1052	OUT[772] OUT[773]	-3096 -3114	235.5 125.5		1131 1132	OUT[852] OUT[853]	-4536 -4554	235.5 125.5
973	OUT[694]	-1692	235.5		1053	OUT[774]	-3132	235.5		1133	OUT(854)	4572	235.5
974	OUT[695]	-1710	125.5		1054	OUT[775]	-3150	125.5		1134	QUT[855]	4590	125.5
975	OUT[696]	-1728	235.5		1055	OUT[776]	-3168	235.5		1135 <	OU\\[856]\\	\\-4608	235.5
976	OUT[697]	-1746	125.5		1056	OUT[777]	-3186	125.5		1136	OUT(857]	V-4626	125.5
977 978	OUT[698] OUT[699]	-1764 -1782	235.5 125.5		1057 1058	OUT[778] OUT[779]	-3204 -3222	235.5 125.5		1138	OUT[858]	-4644 -4662	235.5 125.5
979	OUT[700]	-1762	235.5		1058	OUT[779]	-3240	235.5	$\langle \langle$	1139	OUT 860]	-4680	235.5
980	OUT[701]	-1818	125.5		1060	OUT[781]	-3258	125.5		1140	OUT[861]	-4698	125.5
981	OUT[702]	-1836	235.5		1061	OUT[782]	-3276	(\235\5	//	1141	OUT[862]	-4716	235.5
982	OUT[703]	-1854	125.5		1062	OUT[783]	-3294	125.5	١)	1142	OUT[863]	-4734	125.5
983	OUT[704]	-1872	235.5	l	1063	OUT[784]	-3312	285,5	レ	1143	Ou [[864]	-4752	235.5
984	OUT[705] OUT[706]	-1890	125.5	l	1064	OUT[785]	-3330	125.5		1144	OUT[865]	-4770 -4799	125.5
985 986	OUT[706] OUT[707]	-1908 -1926	235.5 125.5		1065 1066	OUT[786] OUT[787]	3348	235.5	l	1145	QUT[868] DUT[867]	-4788 -4806	235.5 125.5
987	OUT[707]	-1926	235.5		1067	OUT[788]	3384	235.5	1	1147	CD1[898]	-4824	235.5
988	OUT[709]	-1962	125.5		1068	OP1[489]	1-3402	125.5	1	1148	(CUTISES)	-4842	125.5
989	OUT[710]	-1980	235.5	1	1069	OUT[790]	3420	235/5	٥,	1149	OUT[870]	-4860	235.5
990	OUT[711]	-1998	125.5		1070	OUT[79]	-3438	125 5	_	1450	OUT[871]	-4878	125.5
991	OUT[712]	-2016	235.5	//	1071	QUT[792]	-3456	235.5		11151	OUT[872]	-4896	235.5
992 993	OUT[713] OUT[714]	-2034 -2052	125.5 235.5		1072 1073	OUT[793] OUT[794]	-3474 - 34 92	125.5		1152 1153	OUT[873] OUT[874]	-4914 -4932	125.5 235.5
994	OUT[715]	-2070	126.5	^\	1074	OUT[795]	-3510	125.5		1154	OUT[875]	-4950	125.5
995	OUT[716]	-2088 🦿	235.6	/	1075	OUT[796]	-3528	235.5		1155	OUT[876]	-4968	235.5
996	OUT[717]	-2106	125.5		1076	OUT[797]	-3546	25.5		1156	OUT[877]	-4986	125.5
997	OUT[718]	2124	234,5	~	1077	OUT[798]	-3564	235.5		1157	OUT[878]	-5004	235.5
998	OUT[719]	(-2142)	1125.5		1078	QU41499	3582	125.5		1158	OUT[879]	-5022	125.5
999 1000	OUT[720]	2160	235.5 125.5		1079	ODT[800]	-3600 -3618	235.5 125.5		1159 1160	OUT[880] OUT[881]	-5040 -5058	235.5 125.5
1000	OUT[721] OUT[722]	2196	235.5		1081	QUT[802]	-3636	235.5		1161	OUT[882]	-5076	235.5
1002	QUTIX23	-2214	125.5		1082	OUT[803]	-3654	125.5		1162	OUT[883]	-5094	125.5
1003 🗸	✓QU T[72 4) \	-2232	235.5	\	1083	T[804]	-3672	235.5		1163	OUT[884]	-5112	235.5
1003	OU 1725	-2250	125,5		1084	OUT[805]	-3690	125.5		1164	OUT[885]	-5130	125.5
1,005	//OUT(726] V	-2268	235.5		1085	OUT[806]	-3708	235.5		1165	OUT[886]	-5148	235.5
1006	OUT[727]	-2286 -2304	235.5	_	1086 1087	OUT[807] OUT[808]	-3726 -3744	125.5 235.5		1166 1167	OUT[887] OUT[888]	-5166 -5184	125.5 235.5
1008	OUT[729]	-2322	125.5	>	1088	OUT[809]	-3762	125.5		1168	OUT[889]	-5202	125.5
1009	OUT[730]	-2340	235.5		1089	OUT[810]	-3780	235.5		1169	OUT[890]	-5220	235.5
1010	OUT[731]	-2358	125.5		1090	OUT[811]	-3798	125.5		1170	OUT[891]	-5238	125.5
1011	OUT[732]	-2376	235.5		1091	OUT[812]	-3816	235.5		1171	OUT[892]	-5256	235.5
1012	OUT[733]	-2394	125.5	l	1092	OUT[813]	-3834	125.5		1172	OUT[893]	-5274	125.5
1013 1014	OUT[734] OUT[735]	-2412 -2430	235.5 125.5	ł	1093 1094	OUT[814] OUT[815]	-3852 -3870	235.5 125.5		1173 1174	OUT[894] OUT[895]	-5292 -5310	235.5 125.5
1014	OUT[736]	-2430	235.5		1094	OUT[816]	-3888	235.5	l	1174	OUT[896]	-5328	235.5
1016	OUT[737]	-2466	125.5	1	1096	OUT[817]	-3906	125.5	1	1176	OUT[897]	-5346	125.5
1017	OUT[738]	-2484	235.5		1097	OUT[818]	-3924	235.5		1177	OUT[898]	-5364	235.5
1018	OUT[739]	-2502	125.5		1098	OUT[819]	-3942	125.5		1178	OUT[899]	-5382	125.5
1019 1020	OUT[740] OUT[741]	-2520	235.5 125.5	l	1099 1100	OUT[820]	-3960	235.5 125.5		1179 1180	OUT[900]	-5400 -5418	235.5 125.5
1020	OUT[741] OUT[742]	-2538 -2556	235.5	ł	1100	OUT[821] OUT[822]	-3978 -3996	235.5		1180	OUT[901] OUT[902]	-5418 -5436	235.5
1021	OUT[742]	-2574	125.5	1	1101	OUT[823]	-4014	125.5		1182	OUT[902]	-5454	125.5
1023	OUT[744]	-2592	235.5	İ	1103	OUT[824]	-4032	235.5		1183	OUT[904]	-5472	235.5
1024	OUT[745]	-2610	125.5		1104	OUT[825]	-4050	125.5		1184	OUT[905]	-5490	125.5
1025	OUT[746]	-2628	235.5	l	1105	OUT[826]	-4068	235.5		1185	OUT[906]	-5508	235.5
1026	OUT[747] OUT[748]	-2646	125.5	l	1106	OUT[827]	-4086	125.5		1186	OUT[907]	-5526	125.5
1027 1028	OUT[748] OUT[749]	-2664 -2682	235.5 125.5	ł	1107 1108	OUT[828] OUT[829]	-4104 -4122	235.5 125.5	l	1187 1188	OUT[908] OUT[909]	-5544 -5562	235.5 125.5
1028	OUT[750]	-2700	235.5	1	1109	OUT[830]	-4140	235.5		1189	OUT[910]	-5580	235.5
1030	OUT[751]	-2718	125.5	1	1110	OUT[831]	-4158	125.5	1	1190	OUT[911]	-5598	125.5
1031	OUT[752]	-2736	235.5		1111	OUT[832]	-4176	235.5		1191	OUT[912]	-5616	235.5
1032	OUT[753]	-2754	125.5		1112	OUT[833]	-4194	125.5		1192	OUT[913]	-5634	125.5
1033	OUT[754]	-2772	235.5	l	1113	OUT[834]	-4212	235.5		1193	OUT[914]	-5652	235.5
1034 1035	OUT[755] OUT[756]	-2790 -2808	125.5 235.5	ł	1114 1115	OUT[835] OUT[836]	-4230 -4248	125.5 235.5		1194 1195	OUT[915] OUT[916]	-5670 -5688	125.5 235.5
1033	OUT[757]	-2826	125.5	l	1116	OUT[836]	-4248	125.5		1195	OUT[916]	-5706	125.5
1036				1	1117	OUT[838]	-4284	235.5	1	1197	OUT[918]	-5724	235.5
1036 1037	OUT[758]	-2844	235.5		1117	00110301	-4204						
		-2844 -2862	235.5 125.5		1118	OUT[839]	-4302	125.5		1198	OUT[919]	-5742	125.5
1037 1038 1039	OUT[758] OUT[759] OUT[760]	-2862 -2880	125.5 235.5		1118 1119	OUT[839] OUT[840]	-4302 -4320	125.5 235.5		1198 1199	OUT[919] OUT[920]	-5742 -5760	125.5 235.5
1037 1038	OUT[758] OUT[759]	-2862	125.5		1118	OUT[839]	-4302	125.5		1198	OUT[919]	-5742	125.5

Pad #	Pad Name	Pad Coo			Pad#	Pad Name	Pad Cod			Pad #	Pad Name	Pad Coo	
		X 5700	Y				X 7000	Y 225 5				X	Y
1201 1202	OUT[922] OUT[923]	-5796 -5814	235.5 125.5		1281 1282	OUT[1002] OUT[1003]	-7236 -7254	235.5 125.5		1361 1362	OUT[1082] OUT[1083]	-8676 -8694	235.5 125.5
1202	OUT[923]	-5832	235.5		1283	OUT[1003]	-7272	235.5		1363	OUT[1083]	-8712	235.5
1204	OUT[925]	-5850	125.5		1284	OUT[1005]	-7290	125.5		1364	OUT[1085]	-8730	125.5
1205	OUT[926]	-5868	235.5		1285	OUT[1006]	-7308	235.5		1365	OUT[1086]	-8748	235.5
1206	OUT[927]	-5886	125.5		1286	OUT[1007]	-7326	125.5		1366	OUT[1087]	-8766	125.5
1207	OUT[928]	-5904	235.5		1287	OUT[1008]	-7344	235.5		1367	OUT[1088]	-8784	235.5
1208	OUT[929]	-5922	125.5		1288	OUT[1009]	-7362	125.5		1368	OUT[1089]	-8802	125.5
1209 1210	OUT[930] OUT[931]	-5940 -5958	235.5 125.5		1289 1290	OUT[1010] OUT[1011]	-7380 -7398	235.5 125.5		1369 1370	OUT[1090] OUT[1091	-8838	235.5 125.5
1211	OUT[931]	-5976	235.5		1291	OUT[1011]	-7416	235.5		1371	OUT[1091]	8856	235.5
1212	OUT[933]	-5994	125.5		1292	OUT[1013]	-7434	125.5		1372	ORITH 10681	-88 7 4	125.5
1213	OUT[934]	-6012	235.5		1293	OUT[1014]	-7452	235.5		1373	OUT 1094	-8892	235.5
1214	OUT[935]	-6030	125.5		1294	OUT[1015]	-7470	125.5		1374	OUT(1095]	8910	125.5
1215	OUT[936]	-6048	235.5		1295	OUT[1016]	-7488	235.5	ر ا	1375	QUT[1096]	-8928	235.5
1216	OUT[937]	-6066	125.5		1296	OUT[1017]	-7506	125.5		1376	QUT[1093]	-8946	125.5
1217 1218	OUT[938] OUT[939]	-6084 -6102	235.5 125.5		1297 1298	OUT[1018] OUT[1019]	-7524 -7542	235.5 125.5	//	1377	OUT[1098] OUT[1099]	-8964 -8982	235.5 125.5
1219	OUT[940]	-6120	235.5		1299	OUT[1019]	-7560	235.5		1379	OUT[1100]	-9000	235.5
1220	OUT[941]	-6138	125.5		1300	OUT[1020]	-7578	125.5	//	1380	OUT[1101]	-9018	125.5
1221	OUT[942]	-6156	235.5		1301	OUT[1022]	-7596	235.5)	1381	OUT[1302]	-9036	235.5
1222	OUT[943]	-6174	125.5		1302	OUT[1023]	-8614	<u></u>		1382	O(J7[1103]	-9054	125.5
1223	OUT[944]	-6192	235.5		1303	OUT[1024]	-7632	235.5	ĺ	1383	QU7(1104)	-9072	235.5
1224	OUT[945]	-6210	125.5		1304	OUT[1025]	17650	125.5		1384	OUT[(105]	-9090	125.5
1225	OUT[946]	-6228	235.5		1305	OUT[1026]	7668	235.5	_	1385	OUT[1106]	-9108 -0126	235.5
1226 1227	OUT[947] OUT[948]	-6246 -6264	125.5 235.5		1306 1307	OUT[1027]	\\-\\\8686 \\7704	125.5 235.5	1	13 86	QUT[1108]	-9126 -9144	125.5 235.5
1228	OUT[948]	-6282	125.5		1307	OUT[1029]	77722	125.5	٥,	1388	OUT[1108]	-9162	125.5
1229	OUT[950]	-6300	235.5	ر ا	1309	QUT[1030]	-7740	235.5		1389	OUT[1110]	-9180	235.5
1230	OUT[951]	-6318	125.5		1310	ODT[1031]	-7758	125,5		1390	OUT[1111]	-9198	125.5
1231	OUT[952]	-6336	235.8	//	13/1	OUT[1032]	-7776	235.5))	1391	OUT[1112]	-9216	235.5
1232	OUT[953]	-6354	185.5	^ \	1312	OUT[1033]	-X794	125.5		1392	OUT[1113]	-9234	125.5
1233	OUT[954]	-6372	285.5		1313	OUT[1034]	78/2	235.5		1393	OUT[1114]	-9252	235.5
1234 1235	OUT[955] OUT[956]	-6390 -64 68	125.5 235.5	/	1314	OUT[1035] OUT[1035]	-7830 -78 48	125.5 235.5		1394 1395	OUT[1115] OUT[1116]	-9270 -9288	125.5 235.5
1236	OUT[956]	-6426	12616	\ <u>\</u>	1316	QUT[1037]	-7866	125.5		1396	OUT[1116]	-9306	125.5
1237	OUT[958]	(-6444	235.5		1317	OUT 1038	-7884	235.5		1397	OUT[1118]	-9324	235.5
1238	OUT[959]	-6462	125.5		1318	QU7(1039)	7902	125.5		1398	OUT[1119]	-9342	125.5
1239	OUT(960)	6480	235.5		1819	QUTY1040]	-7920	235.5		1399	OUT[1120]	-9360	235.5
1240	00T(961)\	-6498	125.5	1	1320	OUT[1041]	-7938	125.5		1400	OUT[1121]	-9378	125.5
1241	OUT[962] \	-6516	235.5	\ \ \	1321	OUT[1042]	-7956	235.5		1401	OUT[1122]	-9396	235.5
1242	OUT[963]\\ OUT[964]\\	-6534 -6552	125.5 235.5		1322	OUT[1043] OUT[1044]	-7974 -7992	125.5 235.5		1402 1403	OUT[1123] OUT[1124]	-9414 -9432	125.5 235.5
1244	QUT[965]	-6570	1255.5		1824	OUT[1044]	-8010	125.5		1404	OUT[1124]	-9450	125.5
1245	QUT[966]	-6588	235.6	\backslash	1325	OUT[1046]	-8028	235.5		1405	OUT[1126]	-9468	235.5
1246	OVT[967]	-6606	125.5	>	1326	OUT[1047]	-8046	125.5		1406	OUT[1127]	-9486	125.5
1247	OUT[968]	-6624	235,5		1327	OUT[1048]	-8064	235.5		1407	OUT[1128]	-9504	235.5
1248	OUT[969]	-6642	125.5		1328	OUT[1049]	-8082	125.5		1408	OUT[1129]	-9522	125.5
1249	OUT[970]	-6660	235.5		1329	OUT[1050]	-8100	235.5		1409	OUT[1130]	-9540	235.5
1250 1251	OUT[971] OUT[972]	-6678 -6696	125.5 235.5		1330 1331	OUT[1051] OUT[1052]	-8118 -8136	125.5 235.5		1410 1411	OUT[1131] OUT[1132]	-9558 -9576	125.5 235.5
1252	OUT[972]	-6714	125.5		1332	OUT[1052]	-8154	125.5		1411	OUT[1132]	-9594	125.5
1253	OUT[974]	-6732	235.5		1333	OUT[1054]	-8172	235.5		1413	OUT[1134]	-9612	235.5
1254	OUT[975]	-6750	125.5		1334	OUT[1055]	-8190	125.5		1414	OUT[1135]	-9630	125.5
1255	OUT[976]	-6768	235.5		1335	OUT[1056]	-8208	235.5		1415	OUT[1136]	-9648	235.5
1256	OUT[977]	-6786	125.5		1336	OUT[1057]	-8226	125.5		1416	OUT[1137]	-9666	125.5
1257	OUT[978]	-6804	235.5		1337	OUT[1058]	-8244	235.5		1417	OUT[1138]	-9684 -9702	235.5
1258 1259	OUT[979] OUT[980]	-6822 -6840	125.5 235.5		1338 1339	OUT[1059] OUT[1060]	-8262 -8280	125.5 235.5		1418 1419	OUT[1139] OUT[1140]	-9702 -9720	125.5 235.5
1260	OUT[980]	-6858	125.5		1340	OUT[1060]	-8298	125.5		1420	OUT[1140]	-9738	125.5
1261	OUT[982]	-6876	235.5		1341	OUT[1062]	-8316	235.5		1421	OUT[1142]	-9756	235.5
1262	OUT[983]	-6894	125.5		1342	OUT[1063]	-8334	125.5		1422	OUT[1143]	-9774	125.5
1263	OUT[984]	-6912	235.5		1343	OUT[1064]	-8352	235.5		1423	OUT[1144]	-9792	235.5
1264	OUT[985]	-6930	125.5		1344	OUT[1065]	-8370	125.5		1424	OUT[1145]	-9810	125.5
1265 1266	OUT[986] OUT[987]	-6948 -6966	235.5 125.5		1345 1346	OUT[1066] OUT[1067]	-8388 -8406	235.5 125.5		1425 1426	OUT[1146] OUT[1147]	-9828 -9846	235.5 125.5
1267	OUT[987]	-6984	235.5		1346	OUT[1067]	-8424	235.5		1426	OUT[1147]	-9864	235.5
1268	OUT[989]	-7002	125.5		1348	OUT[1069]	-8442	125.5		1427	OUT[1146]	-9882	125.5
1269	OUT[990]	-7020	235.5		1349	OUT[1070]	-8460	235.5		1429	OUT[1150]	-9900	235.5
1270	OUT[991]	-7038	125.5		1350	OUT[1071]	-8478	125.5		1430	OUT[1151]	-9918	125.5
1271	OUT[992]	-7056	235.5		1351	OUT[1072]	-8496	235.5		1431	OUT[1152]	-9936	235.5
1272	OUT[993]	-7074	125.5		1352	OUT[1073]	-8514	125.5		1432	OUT[1153]	-9954	125.5
1273	OUT[994]	-7092	235.5		1353	OUT[1074]	-8532	235.5		1433	OUT[1154]	-9972	235.5
1274	OUT[995]	-7110 -7128	125.5		1354	OUT[1075]	-8550 -8568	125.5		1434 1435	OUT[1155]	-9990 -10008	125.5
1275 1276	OUT[996] OUT[997]	-7128 -7146	235.5 125.5		1355 1356	OUT[1076] OUT[1077]	-8568 -8586	235.5 125.5		1435	OUT[1156] OUT[1157]	-10008 -10026	235.5 125.5
1277	OUT[998]	-7146	235.5		1357	OUT[1077]	-8604	235.5		1430	OUT[1157]	-10026	235.5
1278	OUT[999]	-7182	125.5		1358	OUT[1079]	-8622	125.5		1438	OUT[1159]	-10044	125.5
1279	OUT[1000]	-7200	235.5		1359	OUT[1080]	-8640	235.5		1439	OUT[1160]	-10080	235.5
1280	OUT[1001]	-7218	125.5		1360	OUT[1081]	-8658	125.5		1440	OUT[1161]	-10098	125.5

5 1 "	5 111	Pad Coo	rdinate
Pad #	Pad Name	X	Υ
1441	OUT[1162]	-10116	235.5
1442	OUT[1163]	-10134	125.5
1443	OUT[1164]	-10152	235.5
1444	OUT[1165]	-10170	125.5
1445	OUT[1166]	-10188	235.5
1446	OUT[1167]	-10206	125.5
1447	OUT[1168]	-10224	235.5
1448	OUT[1169]	-10242	125.5
1449	OUT[1170]	-10260	235.5
1450 1451	OUT[1171]	-10278	125.5
	OUT[1172]	-10296	235.5
1452 1453	OUT[1173] OUT[1174]	-10314 -10332	125.5 235.5
1453	OUT[1174]	-10332	125.5
1455	OUT[1176]	-10350	235.5
1456	OUT[1176]	-10386	125.5
1456	OUT[1177]	-10300	235.5
1458	OUT[1178]	-10404	125.5
1459	OUT[1180]	-10440	235.5
1460	OUT[1181]	-10458	125.5
1461	OUT[1182]	-10476	235.5
1462	OUT[1183]	-10494	125.5
1463	OUT[1184]	-10512	235.5
1464	OUT[1185]	-10530	125.5
1465	OUT[1186]	-10548	235.5
1466	OUT[1187]	-10566	125.5
1467	OUT[1188]	-10584	235.5
1468	OUT[1189]	-10602	125.5
1469	OUT[1190]	-10620	235.5
1470	OUT[1191]	-10638	125.5
1471	OUT[1192]	-10656	235.5
1472	OUT[1193]	-10674	125.5
1473	OUT[1194]	-10692	235.5
1474	OUT[1195]	-10710	1265
1475 1476	OUT[1196] OUT[1197]	-10728 -10746	235.6 125.5
1476	OUT[1197]	-10746	236.5
1477	OUT[1198]	(10788	1125,5
1479	OUT[1200]	-10800	235.5
1480	OUT[1201]	10818	125.5
1481	PATN3L	-10836	235.5
1482	XONL	-11283	262
1483	XONL	-11373	262
1484	F CTRLL	-11463	262
1,485	/F_CTRLL	-11553	262
1486	AL_MARK_L	-11101.5	280:61
1487	AL_MARK_R	11101.5	220.5
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9. DEFINITIONS

9.1 Data Sheet Status

Preliminary Data Sheet	This data sheet contains preliminary data; supplementary data may be published later.
Data Sheet	This data sheet contains final product specifications.

Contents in the document are subject to change without notice.

9.2 Life Support Application

These products are not designed for use in life support appliances, devices, or systems where malfunction of these products can reasonably be expected to result in personal injury. fitipower customers using or selling these products for use in such applications do so at their own risk and agree to fully indemnify fitipower for any damages resulting from such improper use or sale.

10 REVISION HISTORY

Revision	Content	Page	Date
1.0	New Issue		2017/07/28

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