

EDF PROJECT REPORT

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1. Using analytical methods calculate the following for the given set of tasks:

Calculate the system hyperperiod

Our system has 6 tasks as follow

Task	Period	Execution time	Deadline
Button_1_Monitor	50	0.0233	50
Button_2_Monitor	50	0.0233	50
Periodic_Transmitter	100	0.16	100
Uart_Receiver	20	WCE = 4.92	20
Load_1_Simulation	10	5.01	10
Load_2_Simulation	100	12.075	100

These execution times was extracted from real time simulation from both trace hooks counter calculations and logic analyzer of (GPIO) pins.

According to periodicity we can see that all periods can be a fraction of the largest periodic task .

Largest periodic function is 100 ms.

Button_1_Monitor and Button_2_Monitor occur 2 times in 100 ms.

Uart_Receiver occur 5 times in 100 ms.

Load_1_Simulation occur 10 times in 100 ms.

So the **hyperperiod is 100 ms** in which all tasks have to execute at this time.

Calculate the CPU load:

	Occurrence in 100 ms	Execution time	Total execution time in hyper period	Percentage
Button_1_Monitor	2	0.0233	0.0466	0%
Button_2_Monitor	2	0.0233	0.0466	0%
Periodic_Transmitter	1	0.16	0.16	0%
Uart_Receiver	5	WCE = 4.9 ms	24.5	24.5%
Load_1_Simulation	10	5	50	50%
Load_2_Simulation	1	12	12	12%
Total			In WCE = 87	87%

Note* this calculation depends on the worst case execution time (WCET) as the function Uart_Receiver not always takes 4.9 ms at the worst case the function will wait until all queue "3" messages will be sent ,and if there is no messages in QUEUE it will not wait to send all messages and it will return immediate

During real time simulation the average CPU load is 73%.

Check system schedulability using URM and time demand analysis techniques (Assuming the given set of tasks are scheduled using a fixed priority rate -monotonic scheduler)

1- By URM

The total utilization (U) = Sum(Execution time / Period)

$$= (0.0233/50) + (0.0233/50) + (0.16/100) + (4.9/20) + (5/10) + (12/100)$$

$$= 0.000466 + 0.000466 + 0.0016 + 0.245 + 0.5 + 0.12 = 0.86753 \text{ (it is } < 1)$$

URM (U_{lub}) = $6(2^{(1/6)} - 1) = 0.734$

According to the calculation the total utilization time (0.86799) > URM (0.734) so the system (may or may not be schedulable) and cannot be grantee as schedulable and we need more analysis

2- By Time demand analysis

Schedulable system is grantee when Time provided > Time demand

Time Provided is the hyperperiod = 100 ms

The time demand for each task according to priority "However priority depends on deadline"

1- Load_1_Simulation

$W(10)(\text{Load_1_Simulation}) = 5 + 0 = 5 \text{ ms} \dots < 10 \text{ ms}$ so the system is feasible for Load_1_Simulation task

2- Uart_Receiver

$W(20)(\text{Uart_Receiver}) = 4.9 + (20/10)*5 = 14.9 \text{ ms} \dots < 20 \text{ ms}$ so the system is feasible for Uart_Receiver task

3- Button_1_Monitor

$W(50)(\text{Button_1_Monitor}) = 0.0233 + (50/50)*0.0233 + (50/10)*5 + (50/20)*4.9 = 0.0466 + 5*5 + 2.5 \rightarrow 3*4.9 = 39.466 \text{ ms} \dots < 50 \text{ ms}$ so the system is feasible for Button_1_Monitor task

4- Button_2_Monitor

$W(50)(\text{Button_2_Monitor}) = 0.0233 + (50/50)*0.0233 + (50/10)*5 + (50/20)*4.9 = 0.0466 + 5*5 + 2.5 \rightarrow 3*4.9 = 39.466 \text{ ms} \dots < 50 \text{ ms}$ so the system is feasible for Button_2_Monitor task

5- Periodic_Transmitter

$W(100)(\text{Periodic_Transmitter}) = 0.16 + (100/100)*12 + (100/50)*0.0233 + (100/50)*0.0233 + (100/10)*5 + (100/20)*4.9 = 0.16 + 12 + 0.0466 + 0.0466 + 50 + 24.5 = 86.753 \text{ ms} \dots < 100 \text{ ms}$ so the system is feasible for Periodic_Transmitter task

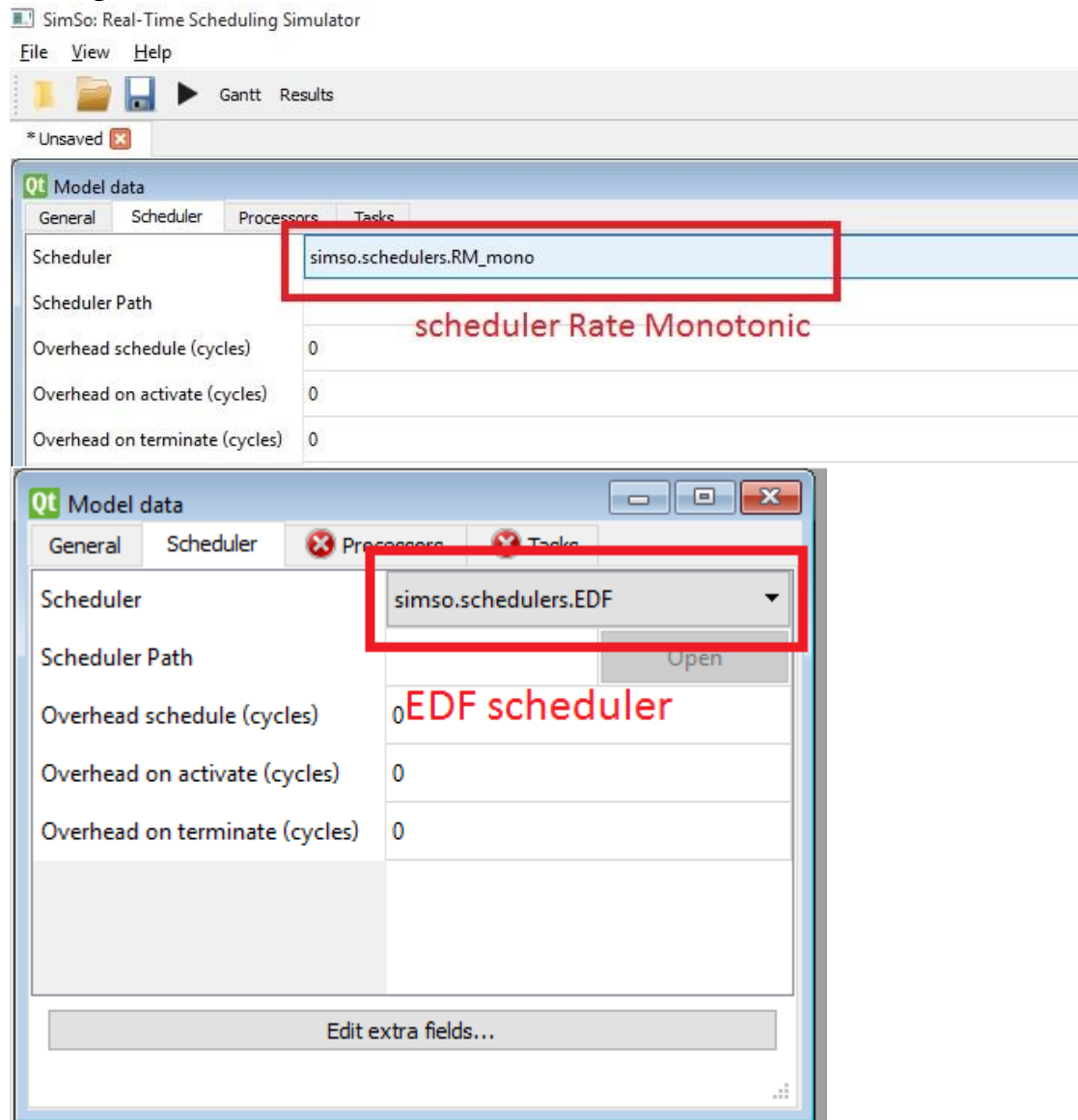
6- Load_2_Simulation

$W(100)(\text{Load_2_Simulation}) = 12 + (100/100)*0.16 + (100/50)*0.0233 + (100/50)*0.0233 + (100/10)*5 + (100/20)*4.9 = 12 + 0.16 + 0.0466 + 0.0466 + 50 + 24.5 = 86.753 \text{ ms} \dots < 100 \text{ ms}$ so the system is feasible for Load_2_Simulation task

In conclusion by analytic method the system is schedulable

2. Using Simso offline simulator, simulate the given set of tasks assuming: Fixed priority rate monotonic scheduler

1- Configuration



The scheduler we choose either Rate Monotonic or EDF both schedulers give the same results.

2- Tasks

SimSo: Real-Time Scheduling Simulator

File View Help

Unsaved

Model data

General Scheduler Processors Tasks

Worst case execution time

id	Name	Task type	Abort on miss	Act. Date (ms)	Period (ms)	List of Act. dates (ms)	Deadline (ms)	WCET (ms)	Followed by
1	Button_1_Monitor	Periodic	<input type="checkbox"/> No	0	50	-	50	0.0333	
2	Button_2_Monitor	Periodic	<input type="checkbox"/> No	0	50	-	50	0.0333	
3	Periodic_Transmitter	Periodic	<input type="checkbox"/> No	0	100	-	100	0.167	
4	Uart_Receiver	Periodic	<input type="checkbox"/> No	0	20	-	20	4.9	
5	Load_1_Simulation	Periodic	<input type="checkbox"/> No	0	10	-	10	5	
6	Load_1_Simulation	Periodic	<input type="checkbox"/> No	0	100	-	100	12	

We entered the task parameters as shown and we entered worst execution time "WCET" according to real time simulation (data extracted from keil simulation from logic analyser mainly and other methods so please refer to section >> 3. Using Keil simulator in run-time and the given set of tasks)

3- Result

a- CPU load

Qt Results

General Logs Tasks Scheduler Processors

Observation Window:

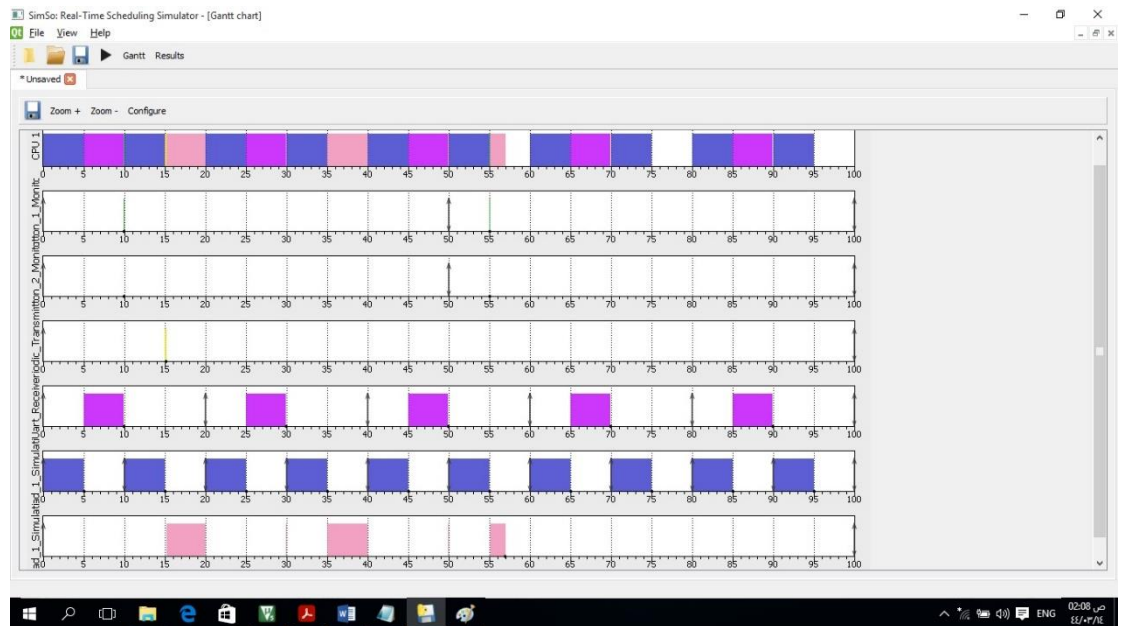
from 0.00 to 100.00 ms

Configure...

	Total load	Payload	System load
CPU 1	0.8678	0.8678	0.0000
Average	0.8678	0.8678	0.0000

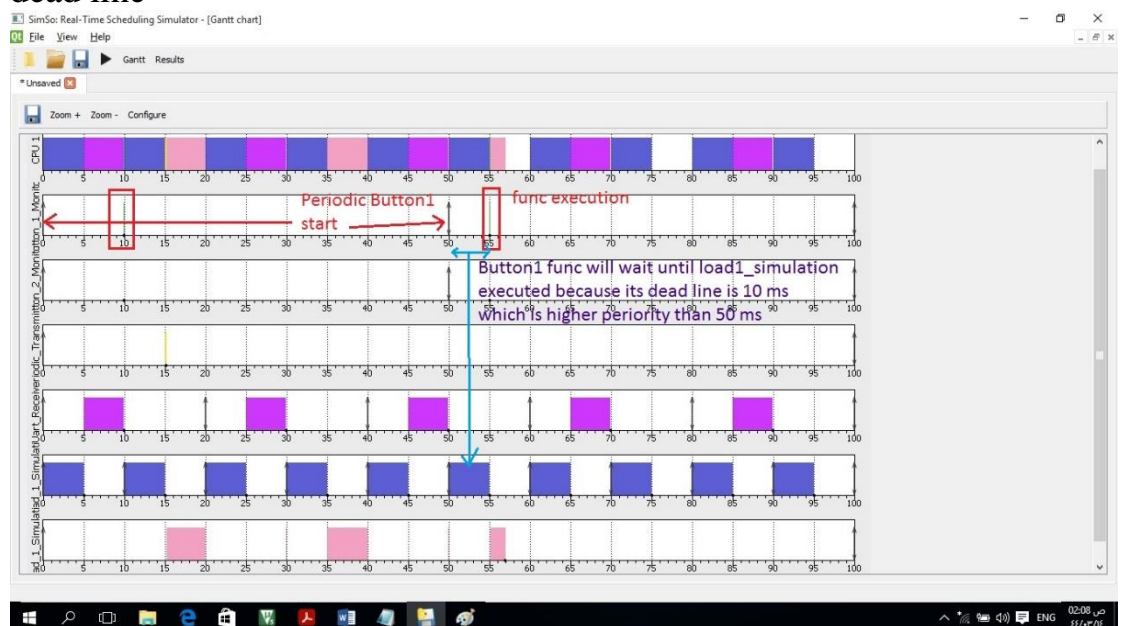
As we can see that CPU load is in the range of 86.8% in WCET of all tasks.

b- Chart simulation "Gantt"



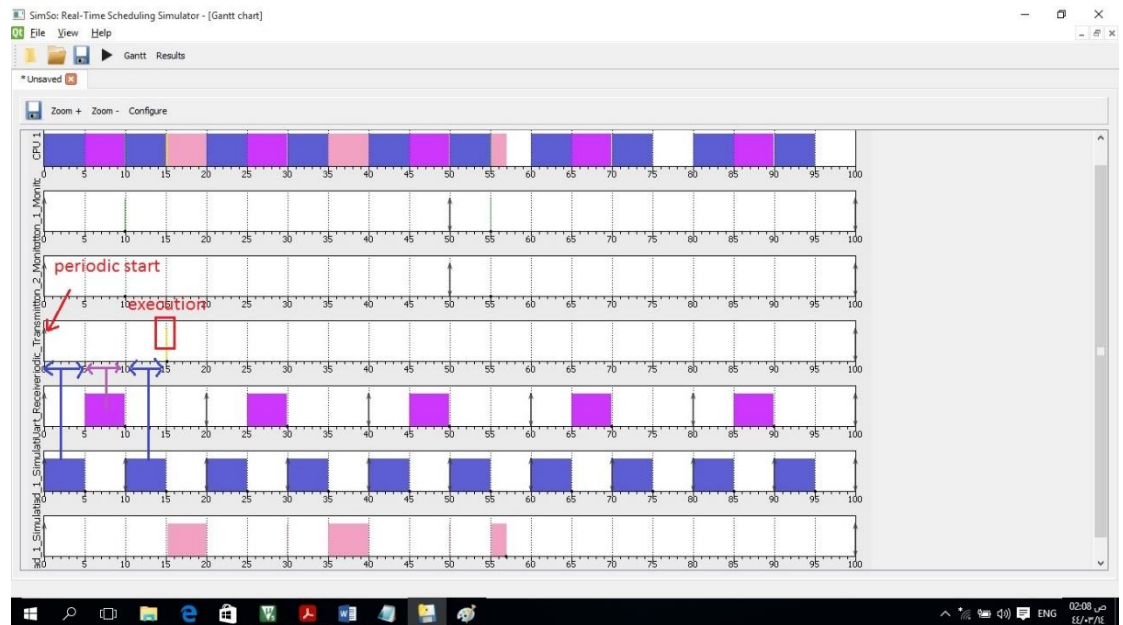
As we can see all tasks don't break their deadlines

*As regard **Button1** and **Button2** tasks they are very short period occurring 2 times in 100 ms scale and didn't breaks its dead line



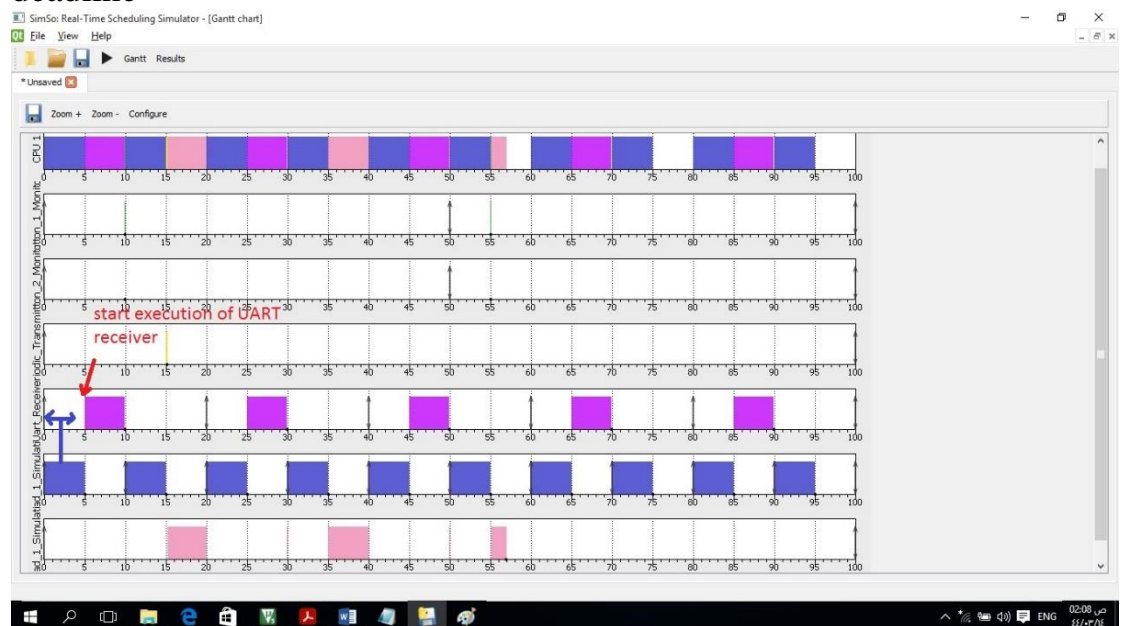
From the chart its execution will be delayed until **load1simulation** task end its work which will take 5 ms because its dead line is 10 ms < 50 ms of the **button task** deadline.

*as regard **periodic_transmitter** it occurs at period of 100 ms with WCET 0.167 ms and didn't missed the dead line

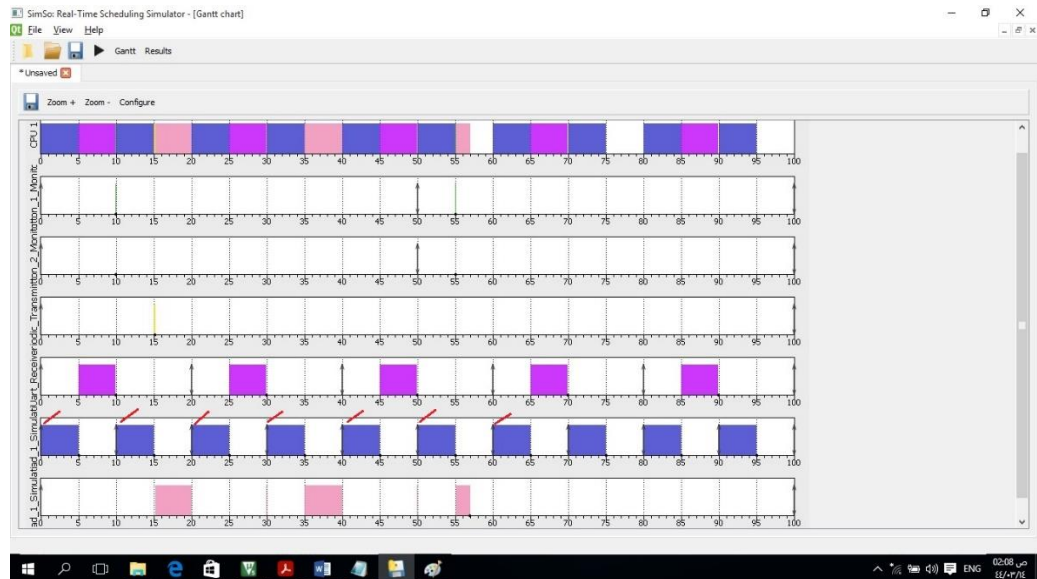


As we can see the **periodic_transmitter func** execution will be delayed for 15 ms this is because its deadline is 100 ms seconds so it will be delayed until **load1 func** (10 ms deadline) executed and finished then **UART receiver func** (20 ms dedline) executed and fished, again **load1 func**.

*as regard **UART receiver func** (20 ms deadline) it will be executed immediately (5 ms) after **load1 func** (10 ms deadline) because the priority depends on deadline and it didn't break its deadline

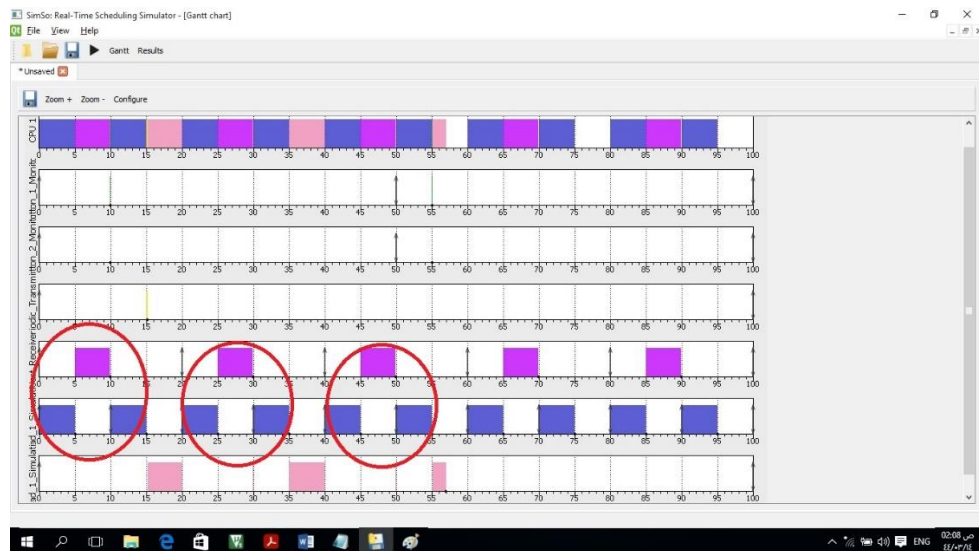


*as regard **load1 func** (10 ms deadline) it will preempt any other function and will delay all functions until its execution and its very clear preemption of the **load2 func** (100 ms deadline) at different time frame .



As we can see no delay in execution of the function every time because it is the least deadline

*as regard **load2 func** (100 ms deadline and execution time of 12 ms) it will be delayed and preempted by all other functions and it will finish its execution time before deadline

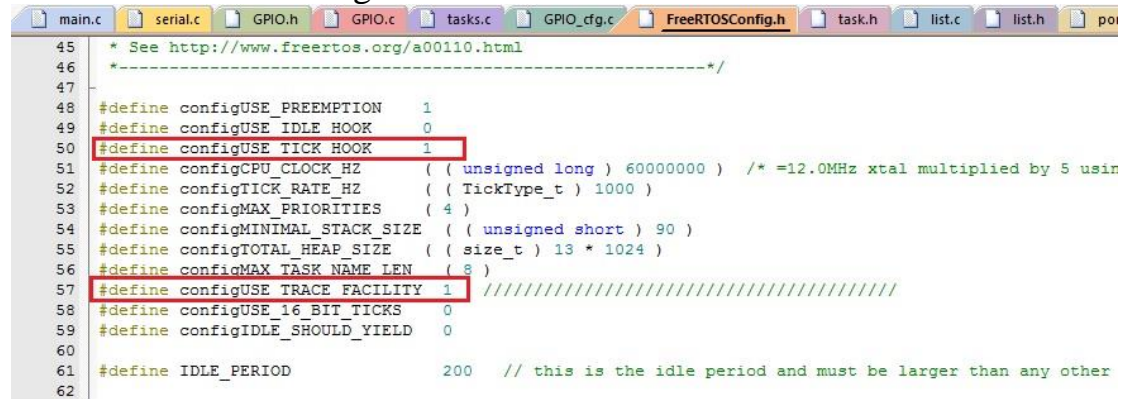


In conclusion according to SIMSO simulation program all tasks didn't break their deadlines and the nearest deadline will preempt other tasks with high cpu load (86.8%) and the system is schedulable.

3. Using Keil simulator in run-time and the given set of tasks:

1- Implementing trace and Tick macro hooks and GPIO

In the FreeRTOSConfig.h file



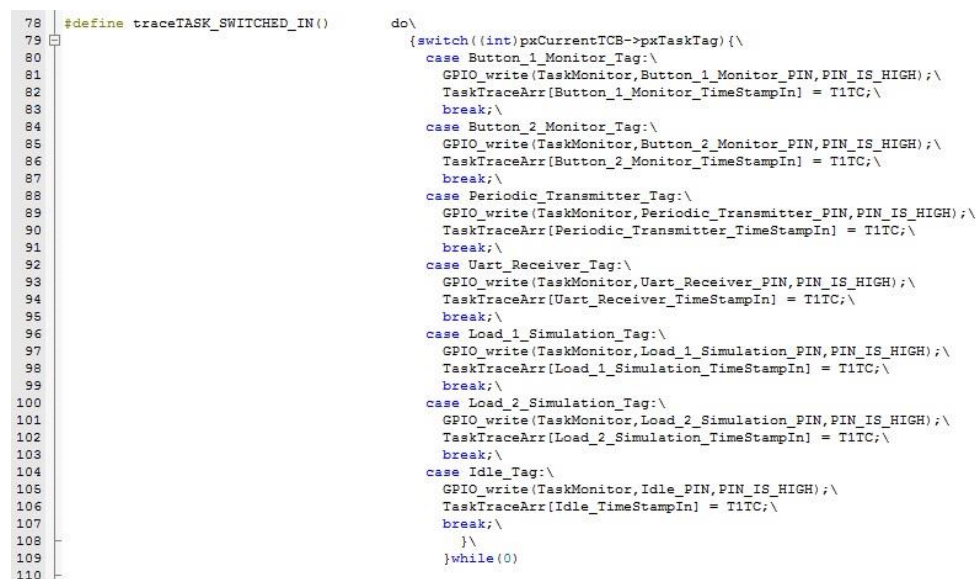
```
45 * See http://www.freertos.org/a00110.html
46 *-----*/
47
48 #define configUSE_PREEMPTION 1
49 #define configUSE_IDLE_HOOK 0
50 #define configUSE_TICK_HOOK 1
51 #define configCPU_CLOCK_HZ ( ( unsigned long ) 60000000 ) /* =12.0MHz xtal multiplied by 5 usin
52 #define configTICK_RATE_HZ ( ( TickType_t ) 1000 )
53 #define configMAX_PRIORITIES ( 4 )
54 #define configMINIMAL_STACK_SIZE ( ( unsigned short ) 90 )
55 #define configTOTAL_HEAP_SIZE ( ( size_t ) 13 * 1024 )
56 #define configMAX_TASK_NAME_LEN ( 8 )
57 #define configUSE_TRACE_FACILITY 1 //////////////////////////////////////////////////
58 #define configUSE_16_BIT_TICKS 0
59 #define configIDLE_SHOULD_YIELD 0
60
61 #define IDLE_PERIOD 200 // this is the idle period and must be larger than any other
62
```

We will set the macros
configUSE_TICK_HOOK and configUSE_TRACE_FACILITY to
1 to enable idle hook and traceability



```
73
74 //////////////////////////////////////////////////
75 // Task Tags and trace hooks
76 #define configUSE_APPLICATION_TASK_TAG 1
77
78 #define traceTASK_SWITCHED_IN() do\
79 {switch((int)pxCurrentTCB->pxTaskTag){\
80 case Button_1_Monitor_Tag:\
81 GPIO_write(TaskMonitor,Button_1_Monitor_PIN,PIN_IS_HIGH);\
82 TaskTraceArr[Button_1_Monitor_TimeStampIn] = T1TC;\
83 break;\
84 case Button_2_Monitor_Tag:\
85 GPIO_write(TaskMonitor,Button_2_Monitor_PIN,PIN_IS_HIGH);\
86 TaskTraceArr[Button_2_Monitor_TimeStampIn] = T1TC;\
87 break;\
88 case Periodic_Transmitter_Tag:\
89 GPIO_write(TaskMonitor,Periodic_Transmitter_PIN,PIN_IS_HIGH);\
90 TaskTraceArr[Periodic_Transmitter_TimeStampIn] = T1TC;\
91 break;\
92 case Uart_Receiver_Tag:\
93 GPIO_write(TaskMonitor,Uart_Receiver_PIN,PIN_IS_HIGH);\
94 TaskTraceArr[Uart_Receiver_TimeStampIn] = T1TC;\
95 break;\
96 case Load_1_Simulation_Tag:\
97 GPIO_write(TaskMonitor,Load_1_Simulation_PIN,PIN_IS_HIGH);\
98 TaskTraceArr[Load_1_Simulation_TimeStampIn] = T1TC;\
99 break;\
100 case Load_2_Simulation_Tag:\
101 GPIO_write(TaskMonitor,Load_2_Simulation_PIN,PIN_IS_HIGH);\
102 TaskTraceArr[Load_2_Simulation_TimeStampIn] = T1TC;\
103 break;\
104 case Idle_Tag:\
105 GPIO_write(TaskMonitor,Idle_PIN,PIN_IS_HIGH);\
106 TaskTraceArr[Idle_TimeStampIn] = T1TC;\
107 break;\
108 }\
109 }while(0)
110
```

Also we will set the (configUSE_APPLICATION_TASK_TAG) to
1 to give tasks name tags to trace each task



```
78 #define traceTASK_SWITCHED_IN() do\
79 {switch((int)pxCurrentTCB->pxTaskTag){\
80 case Button_1_Monitor_Tag:\
81 GPIO_write(TaskMonitor,Button_1_Monitor_PIN,PIN_IS_HIGH);\
82 TaskTraceArr[Button_1_Monitor_TimeStampIn] = T1TC;\
83 break;\
84 case Button_2_Monitor_Tag:\
85 GPIO_write(TaskMonitor,Button_2_Monitor_PIN,PIN_IS_HIGH);\
86 TaskTraceArr[Button_2_Monitor_TimeStampIn] = T1TC;\
87 break;\
88 case Periodic_Transmitter_Tag:\
89 GPIO_write(TaskMonitor,Periodic_Transmitter_PIN,PIN_IS_HIGH);\
90 TaskTraceArr[Periodic_Transmitter_TimeStampIn] = T1TC;\
91 break;\
92 case Uart_Receiver_Tag:\
93 GPIO_write(TaskMonitor,Uart_Receiver_PIN,PIN_IS_HIGH);\
94 TaskTraceArr[Uart_Receiver_TimeStampIn] = T1TC;\
95 break;\
96 case Load_1_Simulation_Tag:\
97 GPIO_write(TaskMonitor,Load_1_Simulation_PIN,PIN_IS_HIGH);\
98 TaskTraceArr[Load_1_Simulation_TimeStampIn] = T1TC;\
99 break;\
100 case Load_2_Simulation_Tag:\
101 GPIO_write(TaskMonitor,Load_2_Simulation_PIN,PIN_IS_HIGH);\
102 TaskTraceArr[Load_2_Simulation_TimeStampIn] = T1TC;\
103 break;\
104 case Idle_Tag:\
105 GPIO_write(TaskMonitor,Idle_PIN,PIN_IS_HIGH);\
106 TaskTraceArr[Idle_TimeStampIn] = T1TC;\
107 break;\
108 }\
109 }while(0)
110
```

```

111 #define traceTASK_SWITCHED_OUT() do\
112 {switch((int)pxCurrentTCB->pxTaskTag){\
113     case Button_1_Monitor_Tag:\
114         GPIO_write(TaskMonitor,Button_1_Monitor_PIN,PIN_IS_LOW);\
115         TaskTraceArr[Button_1_Monitor_TimeStampOut] = TlTC;\
116         TaskTraceArr[Button_1_Monitor_EcutionTime] = TaskTraceArr[Button_1_Monitor_TimeStampOut]-TaskTraceArr[Butt\
117         TaskTraceArr[Button_1_Monitor_TotalEcutionTime] += TaskTraceArr[Button_1_Monitor_EcutionTime];\
118         break;\
119     case Button_2_Monitor_Tag:\
120         GPIO_write(TaskMonitor,Button_2_Monitor_PIN,PIN_IS_LOW);\
121         TaskTraceArr[Button_2_Monitor_TimeStampOut] = TlTC;\
122         TaskTraceArr[Button_2_Monitor_EcutionTime] = TaskTraceArr[Button_2_Monitor_TimeStampOut]-TaskTraceArr[Butt\
123         TaskTraceArr[Button_2_Monitor_TotalEcutionTime] += TaskTraceArr[Button_2_Monitor_EcutionTime];\
124         break;\
125     case Periodic_Transmitter_Tag:\
126         GPIO_write(TaskMonitor,Periodic_Transmitter_PIN,PIN_IS_LOW);\
127         TaskTraceArr[Periodic_Transmitter_TimeStampOut] = TlTC;\
128         TaskTraceArr[Periodic_Transmitter_EcutionTime] = TaskTraceArr[Periodic_Transmitter_TimeStampOut]-TaskTrace\
129         TaskTraceArr[Periodic_Transmitter_TotalEcutionTime] += TaskTraceArr[Periodic_Transmitter_EcutionTime];\
130         break;\
131     case Uart_Receiver_Tag:\
132         GPIO_write(TaskMonitor,Uart_Receiver_PIN,PIN_IS_LOW);\
133         TaskTraceArr[Uart_Receiver_TimeStampOut] = TlTC;\
134         TaskTraceArr[Uart_Receiver_EcutionTime] = TaskTraceArr[Uart_Receiver_TimeStampOut]-TaskTraceArr[Uart_Recei\
135         TaskTraceArr[Uart_Receiver_TotalEcutionTime] += TaskTraceArr[Uart_Receiver_EcutionTime];\
136         break;\
137     case Load_1_Simulation_Tag:\
138         GPIO_write(TaskMonitor,Load_1_Simulation_PIN,PIN_IS_LOW);\
139         TaskTraceArr[Load_1_Simulation_TimeStampOut] = TlTC;\
140         TaskTraceArr[Load_1_Simulation_EcutionTime] = TaskTraceArr[Load_1_Simulation_TimeStampOut]-TaskTraceArr[Lc\
141         TaskTraceArr[Load_1_Simulation_TotalEcutionTime] += TaskTraceArr[Load_1_Simulation_EcutionTime];\
142         break;\
143     case Load_2_Simulation_Tag:\
144         GPIO_write(TaskMonitor,Load_2_Simulation_PIN,PIN_IS_LOW);\
145         TaskTraceArr[Load_2_Simulation_TimeStampOut] = TlTC;\

```

We defined both trace TASK_SWITCHED_IN() & traceTASK_SWITCHED_OUT(). this function like macros are prone for errors and it is better to define it as a function call however we want to make the least time delay to accurately calculate the performance of the system

```

78 #define traceTASK_SWITCHED_IN() do\
79 {switch((int)pxCurrentTCB->pxTaskTag){\ // going through task tags
80     case Button_1_Monitor_Tag:\
81         GPIO_write(TaskMonitor,Button_1_Monitor_PIN,PIN_IS_HIGH);\
82         TaskTraceArr[Button_1_Monitor_TimeStampIn] = TlTC;\
83         break;\
84     case Button_2_Monitor_Tag:\
85         GPIO_write(TaskMonitor,Button_2_Monitor_PIN,PIN_IS_HIGH);\
86         TaskTraceArr[Button_2_Monitor_TimeStampIn] = TlTC;\
87         break;\
88     case Periodic_Transmitter_Tag:\

```

// if the function tag is ...
// make gpio specifc pin high
// store the current timer1 counter
// inside the trace array

```

111 #define traceTASK_SWITCHED_OUT() do\
112 {switch((int)pxCurrentTCB->pxTaskTag){\ // going though task tags
113     case Button_1_Monitor_Tag:\
114         GPIO_write(TaskMonitor,Button_1_Monitor_PIN,PIN_IS_LOW);\
115         TaskTraceArr[Button_1_Monitor_TimeStampOut] = TlTC;\
116         TaskTraceArr[Button_1_Monitor_EcutionTime] = TaskTraceArr[Button_1_Monitor_TimeStampOut]-TaskTraceArr[Button\
117         TaskTraceArr[Button_1_Monitor_TotalEcutionTime] += TaskTraceArr[Button_1_Monitor_EcutionTime];\
118         break;\
119     case Button_2_Monitor_Tag:\

```

// if current taks tag is ..
// make specifc pin low
// read current timer1 counts
// calculate difference
// accumulate this calacultions

Inside FreeRTOSConfig.h after we have to include my "TraceDef.h" which have defintions .. TaskTraceArr[] present inside main.c

In Main.c file

```
132 //////////////////////////////////////////////////
133 uint32_t TaskTraceArr[MAX_TRACE_NUMB];
134
135 uint32_t Button_1_Monitor_ExcutionTime ,Button_2_Monitor_ExcutionTime,Periodic_Transmitter_ExcutionTime,
136 Uart_Receiver_ExcutionTime , Load_1_Simulation_ExcutionTime , Load_2_Simulation_ExcutionTime;
137
138 uint8_t CPU_Load,Button_1_Monitor_LoadPercent,Button_2_Monitor_LoadPercent,Periodic_Transmitter_LoadPercent,
139 Uart_Receiver_LoadPercent,Load_1_Simulation_LoadPercent,Load_2_Simulation_LoadPercent;
140
141 //////////////////////////////////////////////////
142 #define WCETcounterThreshold 20 // wait 20 cycle befor start sampling periodic excution time
143 int WCET_counter =0;
144 int WCET_Monitor = 0; // to monitor max periodic tasks load for selecting item = worst case excution time
145 //////////////////////////////////////////////////
```

Defining TaskTraceArr[] , Button_1_Monitor_ExcutionTime ,Button_2_Monitor_ExcutionTime,

And CPU_Load,Button_1_Monitor_LoadPercent, Button_2_Monitor_LoadPercent,

Variables to monitor them during run-time of the system both we will obtain absolute and percentage values

Also we define WCET_Monitor variable which stands for worst case execution time to monitor specific task WCET according to hash defined macros inside the "TraceDef.h"

```
271 //
272 void Button_1_Monitor(void* pvparams){
273     vTaskSetApplicationTaskTag( NULL, (void*) Button_1_Monitor_Tag ); // giving tag number to the task
274
275
276
```

We will give each task its specific tag number

```
307 // used to calculate worst case excution time and periodic excution time
308 #if (configTaskExecutionTimePeriodic ==1)
309 {
310     #ifdef Task1_WCET
311     if(WCET_counter >= WCETcounterThreshold){
312         if(WCET_Monitor < TaskTraceArr[Button_1_Monitor_TotalEcutionTime]){
313             WCET_Monitor = TaskTraceArr[Button_1_Monitor_TotalEcutionTime];
314             WCET_counter = WCETcounterThreshold;
315         }
316         WCET_counter++;
317     }
318     TaskTraceArr[Button_1_Monitor_TotalEcutionTime] =0;
319 }
320
321 #endif
322
323 }
324 }
325
```

To calculate WCET for specific task you have to set "configTaskExecutionTimePeriodic" and "Task1_WCET" macros to 1 inside "TraceDef.h" file

As you can see we will not sample WCET except after counting n number of task execution because during start execution of simulator it will pass very large number to WCET_Monitor variable which is not correct

Inside "TraceDef.h"

```

9  #define MAX_TRACE_NUMB 35 defining numb of elements in trace array
10
11 #define TaskMonitor PORT_0 we will monitor tasks through PORT0
12
13 ///////////////////////////////////////////////////
14 // INPUTS BUTTONS
15 #define Button1_in PIN0 Defining input pins
16 #define Button2_in PIN1
17 ///////////////////////////////////////////////////
18 // OUTPUT MONITORING
19 #define Button_1_Monitor_PIN PIN2 defining output pins
20 #define Button_2_Monitor_PIN PIN3
21 #define Periodic_Transmitter_PIN PIN4
22 #define Uart_Receiver_PIN PIN5
23 #define Load_1_Simulation_PIN PIN6
24 #define Load_2_Simulation_PIN PIN7
25 #define Idle_PIN PIN8
26 #define Tick_PIN PIN9
27
28
29 ///////////////////////////////////////////////////
30 // Task Tags that will be traced during running
31 #define Idle_Tag 0
32 #define Button_1_Monitor_Tag 1 defining tag number for
33 #define Button_2_Monitor_Tag 2 each task
34 #define Periodic_Transmitter_Tag 3
35 #define Uart_Receiver_Tag 4
36 #define Load_1_Simulation_Tag 5
37 #define Load_2_Simulation_Tag 6
38
39
40 ///////////////////////////////////////////////////
41 // Array elements whcih will trace the time of tasks excution
42 // present in TaskTraceArr[MAX_TRACE_NUMB]
43 // Task1
44 #define Button_1_Monitor_TimeStampIn 0
45 #define Button_1_Monitor_TimeStampOut 1
46 #define Button_1_Monitor_EcutionTime 2
47 #define Button_1_Monitor_TotalEcutionTime 3
48 // Task2
49 #define Button_2_Monitor_TimeStampIn 4
50 #define Button_2_Monitor_TimeStampOut 5
51 #define Button_2_Monitor_EcutionTime 6
52 #define Button_2_Monitor_TotalEcutionTime 7
53 // Task3
54 #define Periodic_Transmitter_TimeStampIn 8
55 #define Periodic_Transmitter_TimeStampOut 9
56 #define Periodic_Transmitter_EcutionTime 10
57 #define Periodic_Transmitter_TotalEcutionTime 11
58 //Task4
59 #define Uart_Receiver_TimeStampIn 12
60 #define Uart_Receiver_TimeStampOut 13
61 #define Uart_Receiver_EcutionTime 14
62 #define Uart_Receiver_TotalEcutionTime 15

```

Defining elements of the trace array

```

92
93 //////////////////////////////////////////////////
94 #define configTaskExecutionTimePeriodic 1
95
96 //////////////////////////////////////////////////
97 // uncomment only (one) definition to monitor the variable WCET_Monitor
98 // according to task number the code to get the worst case excution time will added
99 // to enable this variable you must define configTaskExecutionTimePeriodic = 1
100
101 //#define Task1_WCET
102 //#define Task2_WCET
103 #define Task3_WCET
104 //#define Task4_WCET
105 //#define Task5_WCET
106 //#define Task6_WCET
107 //#define Task7_WCET
108 //#define Task8_WCET

```

To calculate task specific WCET to be monitored by value of WCET_Monitor .

////////////////////////////////////
For accurate results we will set TIMER1 devision to 100 instead of 1000

```

238 /* Function to initialize and start timer 1 */
239 static void configTimer1(void)
240 {
241     T1PR = 100; //////////////////////////////////////////////////
242     T1TCR |= 0x1; // enable
243 }
244

```

Then inside "TraceDef.h"

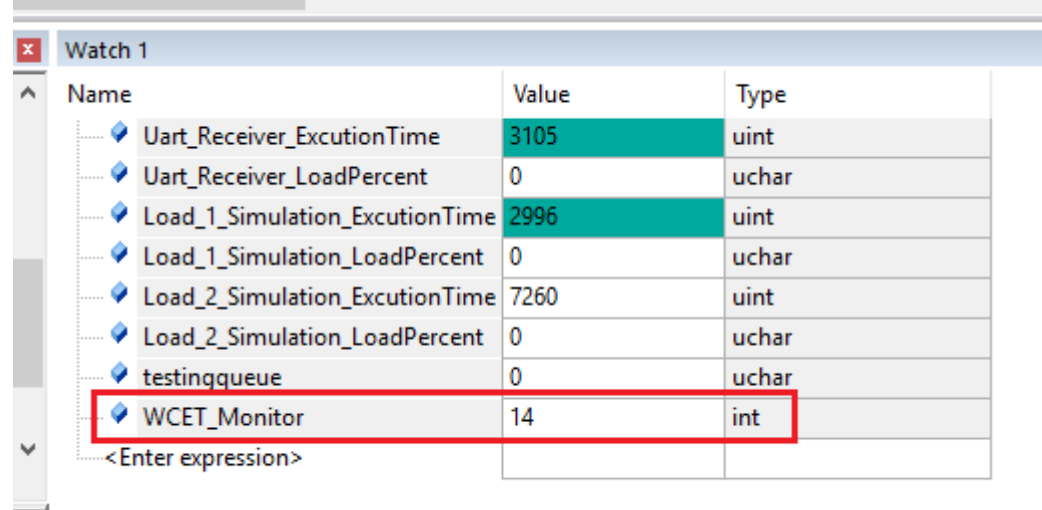
```

93 //////////////////////////////////////////////////
94 #define configTaskExecutionTimePeriodic 1
95
96 //////////////////////////////////////////////////
97 // uncomment only (one) definition to monitor the variable WCET_Monitor
98 // according to task number the code to get the worst case excution time will added
99 // to enable this variable you must define configTaskExecutionTimePeriodic = 1
100
101 #define Task1_WCET
102 //#define Task2_WCET
103 //#define Task3_WCET
104 //#define Task4_WCET
105 //#define Task5_WCET
106 //#define Task6_WCET
107 //#define Task7_WCET

```

Set configTaskExecutionTimePeriodic to 1 and one by one define Taskn_WCET

Then in Watch Variable window add the WCET_Monitor variable to read the required task WCET



Name	Value	Type
Uart_Receiver_ExcutionTime	3105	uint
Uart_Receiver_LoadPercent	0	uchar
Load_1_Simulation_ExcutionTime	2996	uint
Load_1_Simulation_LoadPercent	0	uchar
Load_2_Simulation_ExcutionTime	7260	uint
Load_2_Simulation_LoadPercent	0	uchar
testingqueue	0	uchar
WCET_Monitor	14	int
<Enter expression>		

In this case we displayed WCET OF Button1 task which is 14 and to convert it to ms we will divide it by 600 ($14/600 = 0.0233333 \gg 0.023$ ms or 23 us

Results

Tasks	WCET Read	Result
Button_1_Monitor	14	0.0233
Button_2_Monitor	14	0.0233
Periodic_Transmitter	96	0.16
Uart_Receiver	2953	4.92
Load_1_Simulation	3006	5.01
Load_2_Simulation	7246	12.075

Average results percentage

```

91
92
93 ////////////////////////////////////////////////////
94 //define configTaskExcutionTimePeriodic 1
95
96 ////////////////////////////////////////////////////

```

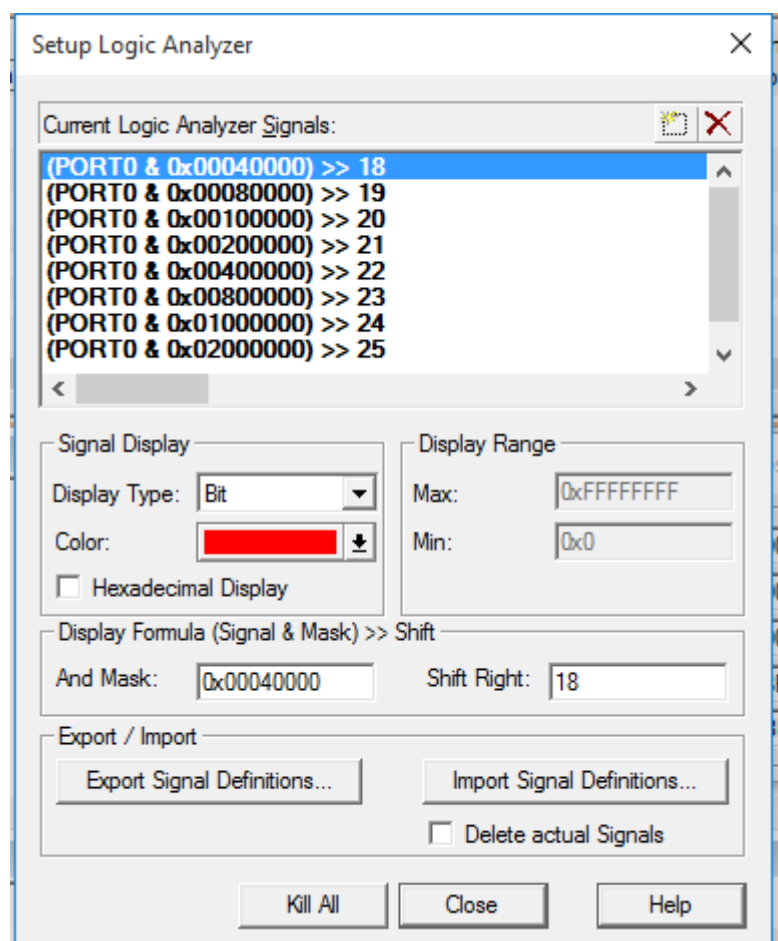
Undefine configTaskExcutionTimePeriodic then run simulation

Watch 1		
Name	Value	Type
CPU_Load	71 'G'	uchar average cpu load %
Button_1_Monitor_ExcutionTime	5507	uint
Button_1_Monitor_LoadPercent	0	uchar %
Button_2_Monitor_ExcutionTime	5549	uint
Button_2_Monitor_LoadPercent	0	uchar %
Periodic_Transmitter_ExcutionTi...	10945	uint
Periodic_Transmitter_LoadPerce...	0	uchar %
Uart_Receiver_ExcutionTime	373652	uint
Uart_Receiver_LoadPercent	7	uchar % average UART Receiver func its much less the WCET WAS 25%
Load_1_Simulation_ExcutionTime	2469710	uint
Load_1_Simulation_LoadPercent	50 '2'	uchar 50%
Load_2_Simulation_ExcutionTime	597322	uint
Load_2_Simulation_LoadPercent	12	uchar 12%
testingqueue	0	uchar
WCET_Monitor	0	uchar

opped Simulation t1: 8.24193187 sec L:518 C:1

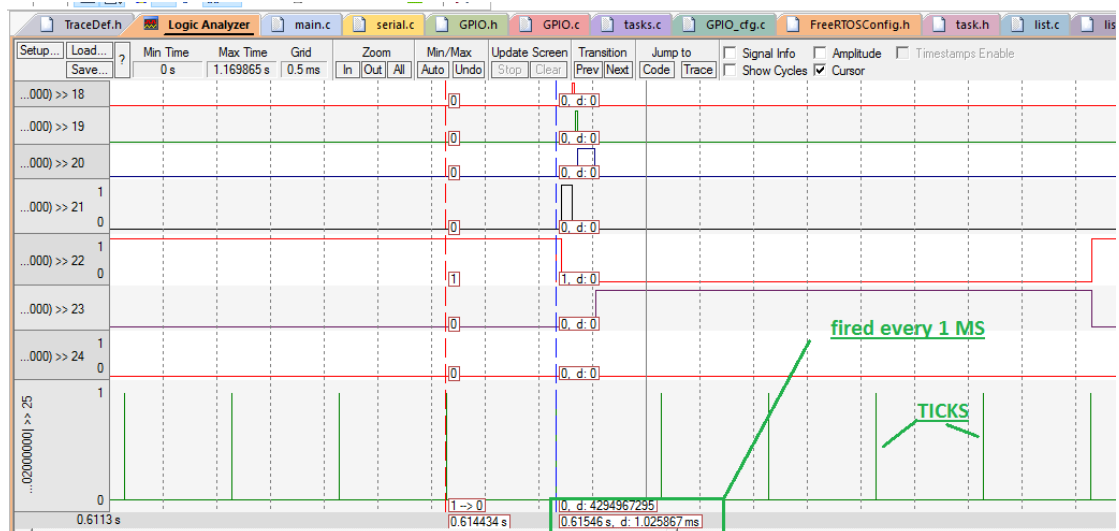
These are average percentage of tasks utilization time from trace macros

2- Logic analyzer interpretation



Button_1_Monitor	pin >> 18
Button_2_Monitor	pin >> 19
Periodic_Transmitter	pin >> 20
Uart_Receiver	pin >> 21
Load_1_Simulation	pin >> 22
Load_2_Simulation	pin >> 23
Idle	pin >> 24
Tick	pin >> 25

*Tick

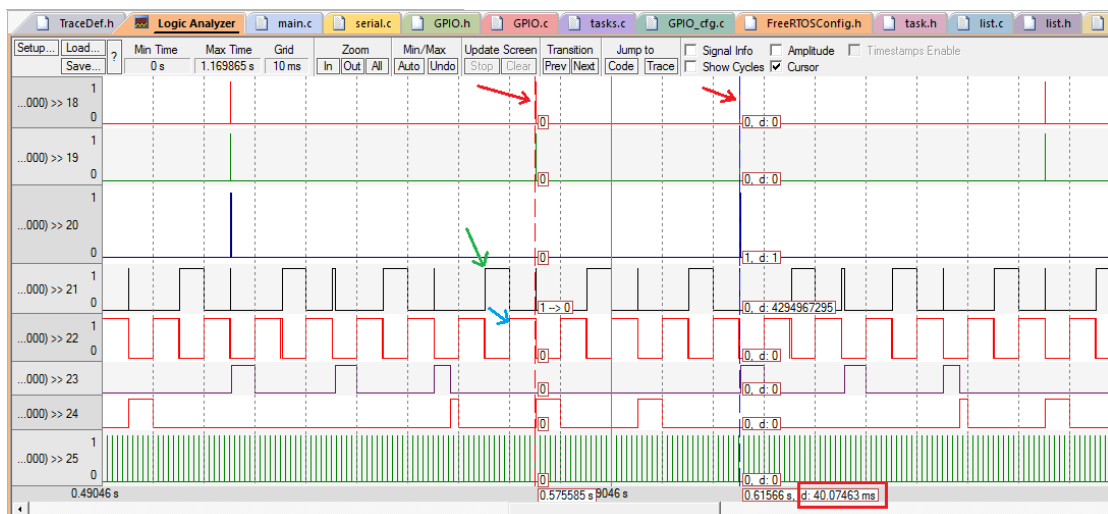


By logic analyzer the ticks will be fired every 1 ms

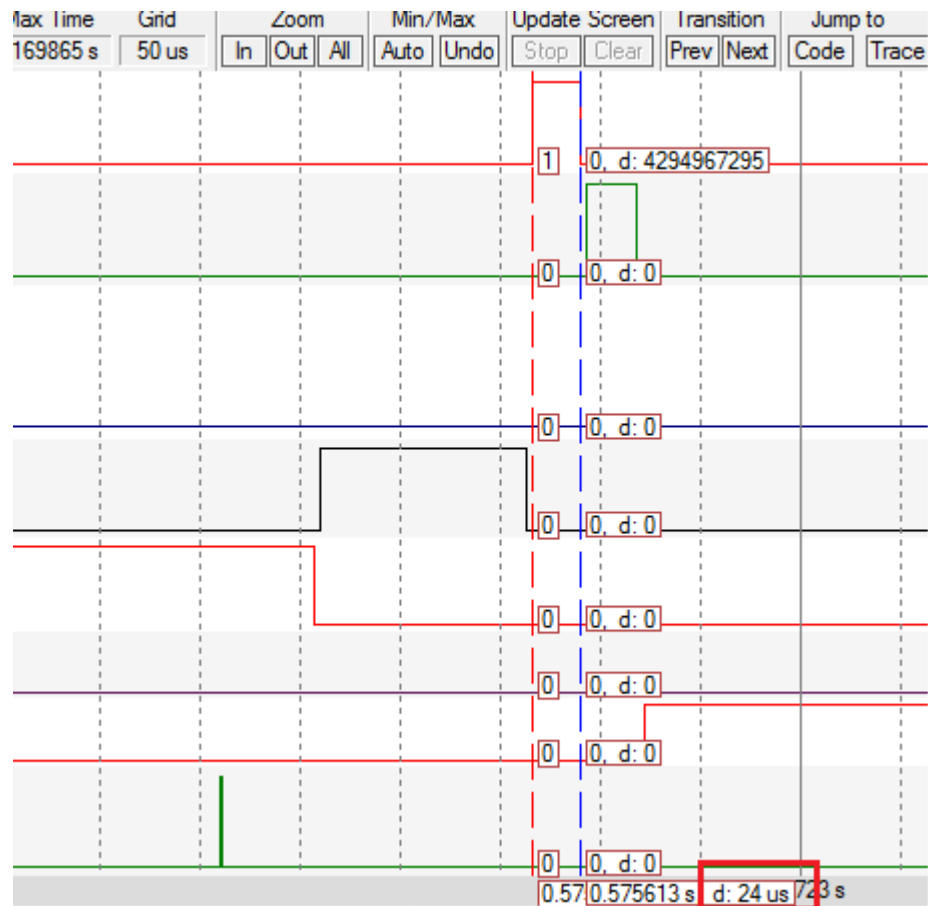
Button_1_Monitor & Button_2_Monitor



Button_1_Monitor & Button_2_Monitor should come every 50 ms . you can see the are delayed to 60 ms why??

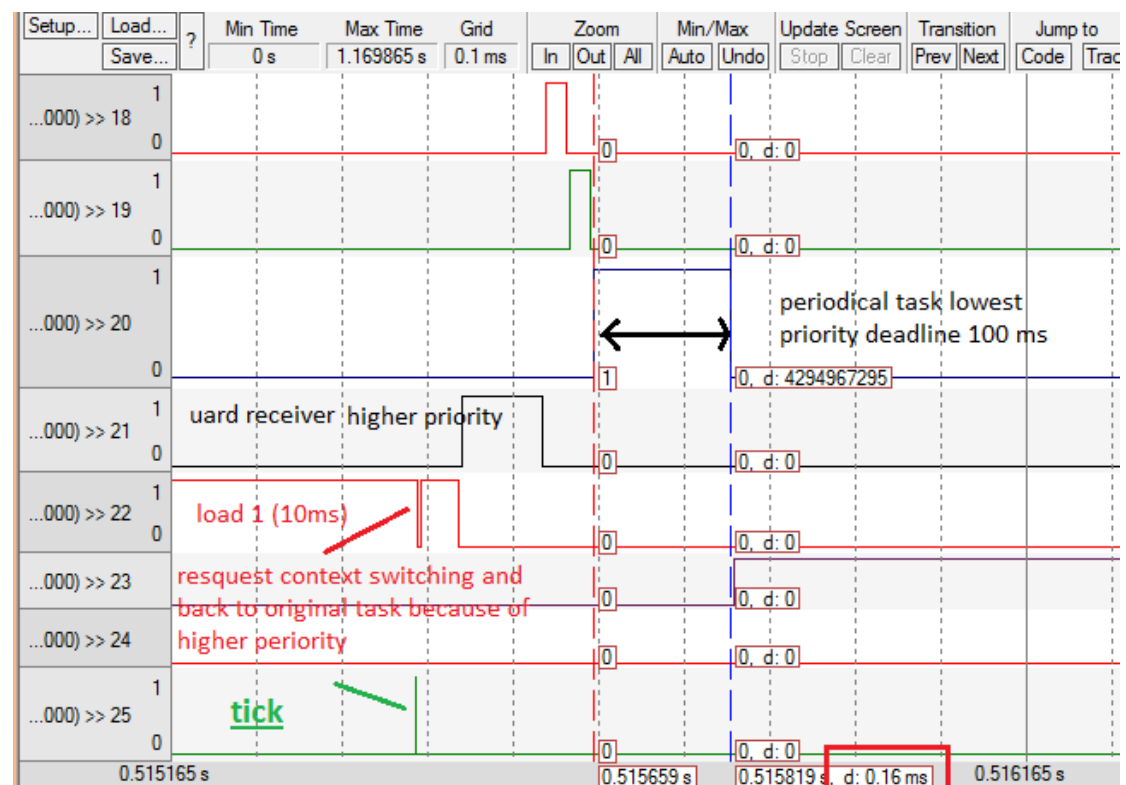


Because load1 simulation task (10 ms deadline) **blue arrow** and UART receiver task (20 ms deadline) **Green arrow** executed before it because their priorities are higher delaying it by 10 ms

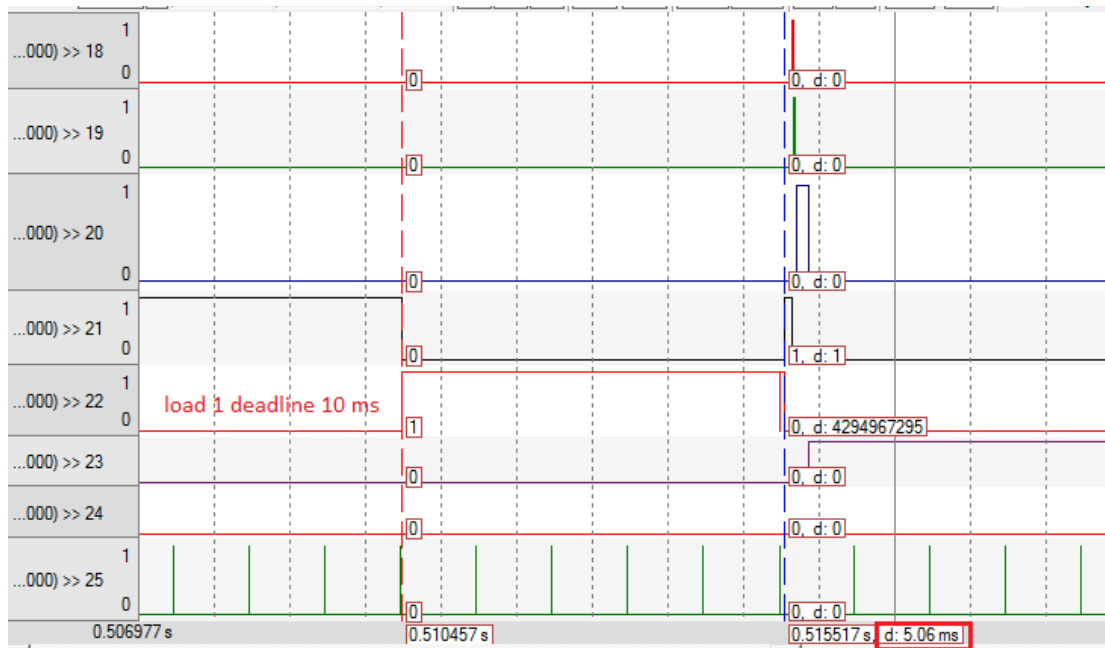


The execution time of Button 1 and 2 functions are 0.025 ms or 25 us

Periodic transmitter function

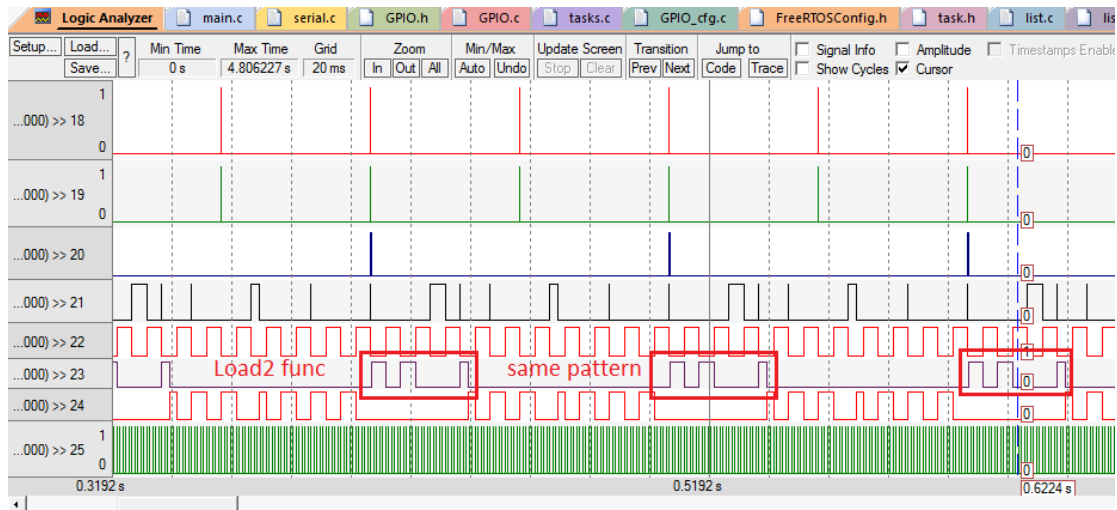


Load_1_simulation



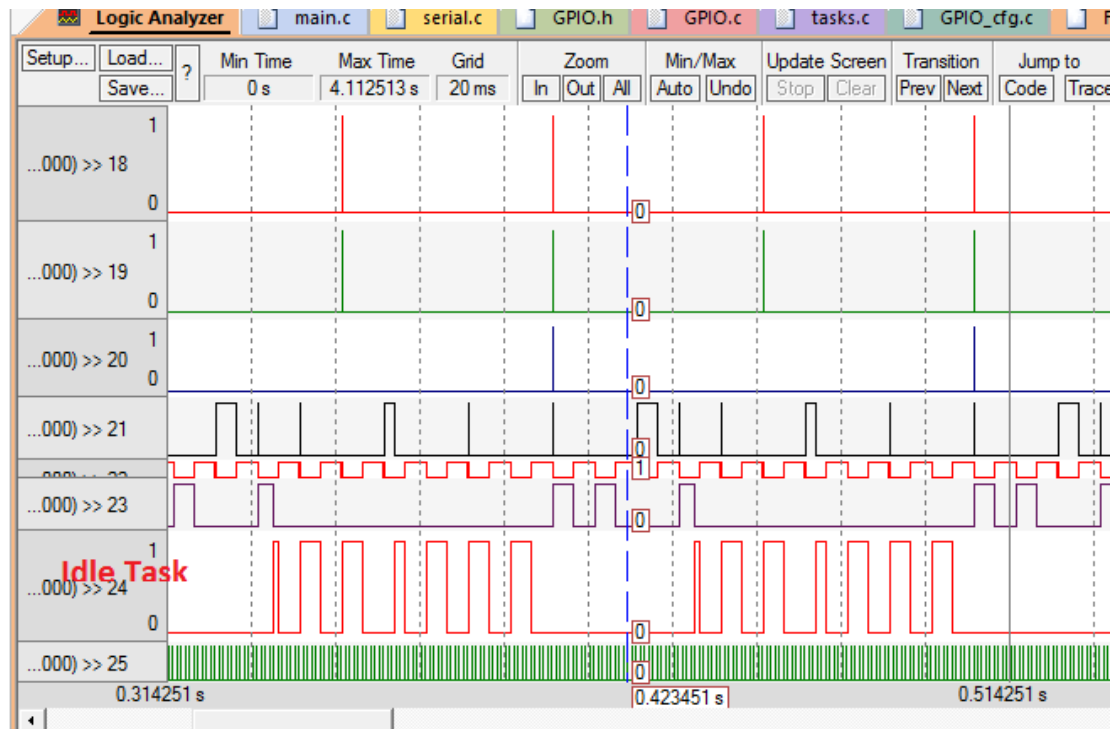
execution time of load 1 is 5 ms

Load_2_simulation



It is preempted by all other tasks has the same pattern every 100 ms = hyper period it is about 12 ms

IDLE task



It is preempted by any other tasks

3- Using FreeRTOS Statistics

Implementation

In FreeRTOS.h

```
56 #define configMAX_TASK_NAME_LEN ( 8 )
57 #define configUSE_TRACE_FACILITY 1
58 #define configUSE_16_BIT_TICKS 0
59 #define configIDLE_SHOULD_YIELD 0
```

Set configUSE_TRACE_FACILITY to 1

```
63 ///////////////////////////////////////////////////
64 // Definition
65 #define configGENERATE_RUN_TIME_STATS 1 ///////////////////////////////////////////////////
66 #define configUSE_STATS_FORMATTING_FUNCTIONS 1
67 #define portCONFIGURE_TIMER_FOR_RUN_TIME_STATS()
68 #define portGET_RUN_TIME_COUNTER_VALUE() T1TC // this macro will return
69
```

Add those macros

In Tasks.c

```
2720
2721 #if ( configUSE_TRACE_FACILITY == 1 )
2722
2723 UBaseType_t uxTaskGetSystemState( TaskStatus_t * const pxTaskStatusArray, const UBaseType_t uxArraySize )
2724 {
2725     UBaseType_t uxTask = 0, uxQueue = configMAX_PRIORITIES;
2726
2727     vTaskSuspendAll();
2728     {
2729         /* Is there a space in the array for each task in the system? */
2730         if( uxArraySize >= uxCurrentNumberOfTasks )
2731         {
2732             /* Fill in an TaskStatus_t structure with information on each task in the Ready state */
2733
```

Inside uxTaskGetSystemState function

```
2736 ///////////////////////////////////////////////////
2737 #if (configUSE_EDF_SCHEDULER == 1)
2738 {
2739     uxTask += prvListTasksWithinSingleList( &(amp; pxTaskStatusArray[ uxTask ] ), (List_t *) &( xReadyTasksListEDF ), eReady );
2740 }
2741 #else
2742
```

We will change to this by adding
(List_t *) &(xReadyTasksListEDF) instead of
&(pxReadyTasksLists[uxQueue])

Results

UART #2					I
DLE	57063	10%	Idle 10% so cpu load 90%		
Load2	68543	12%	load2 12 ms = 12%		
UartR	138964	25%	WCET by using for loop load 5ms		
Button1	316	<1%			
Button2	313	<1%			
Periodi	969	<1%			
Load1	277762	51%	load1 5 ms every 10 ms = 5%		

You can see that the UART load in WCET is 25%

UART #2					
Button1 is HIGH		Button2 is HIGH		Message from Periodic Func	
Button1 is HIGH		Button2 is HIGH		I	
DLE	235281	28%			
Load2	108967	13%			
UartR	62432	7%			
Button1	475	<1%			
Button2	478	<1%			
Periodi	1451	<1%			
Load1	425894	50%	UART Receiver printing its values		
Button1 is HIGH		Button2 is HIGH		Message from Periodic Func	
Button1 is HIGH		Button2 is HIGH		I	
DLE	252083	28%			
Load2	116751	13%			
UartR	66893	7%	this is the average UART Receiver utilization		
Button1	508	<1%			
Button2	511	<1%			
Periodi	1548	<1%			
Load1	456092	50%			
Button1 is HIGH		Button2 is HIGH			

When we just make the UART Receiver func do its job the average utilization will be 7% those results are consistent with our implementation

In conclusion the EDF implementation meets the requirements of no task will break its deadline and the system overall is schedulable with low margin as in WCET the cpu load will be 90% and we have only 10% margine to play around. All results from our implementation are consistent with FreeRTOS statistics function even more reliable than it.