

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80053001 AAKASH SHINDE MAMTA , 71100709J , , PICT , T80053001

01. CONTROL SYSTEMS	PP	100	40	51	P
02. DIGITAL COMMUNICATION	PP	100	40	44	P
03. DIGITAL COMMUNICATION	PR	50	20	07	F
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	55	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	37	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	43	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	10	F
08. DIGITAL SIGNAL PROCESSING	PP	100	40	40	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	24	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	25	P

FIRST TERM TOTAL = 336/750.

ORDN. 1 MARKS :

T80053002 AASHIQ HANEEF AHMED RASHEEDA , 71106382G , , PICT , T80053002

01. CONTROL SYSTEMS	PP	100	40	81	P
02. DIGITAL COMMUNICATION	PP	100	40	67	P
03. DIGITAL COMMUNICATION	PR	50	20	37	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	66	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	43	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	56	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	40	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	72	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	42	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	26	P

FIRST TERM TOTAL = 530/750.

ORDN. 1 MARKS :

T80053003 ABHISHEK KUMAR KIRAN DEVI , 71100714E , , PICT , T80053003

01. CONTROL SYSTEMS	PP	100	40	62	P
02. DIGITAL COMMUNICATION	PP	100	40	65	P
03. DIGITAL COMMUNICATION	PR	50	20	28	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	71	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	32	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	59	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	30	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	59	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	35	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	24	P

FIRST TERM TOTAL = 465/750.

ORDN. 1 MARKS :

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OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80053004 ABHISHEK SHARMA KANTA , 71100715C , , PICT , T80053004

01. CONTROL SYSTEMS	PP	100	40	61	P
02. DIGITAL COMMUNICATION	PP	100	40	66	P
03. DIGITAL COMMUNICATION	PR	50	20	30	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	64	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	38	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	61	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	38	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	55	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	36	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	25	P

FIRST TERM TOTAL = 474/750.

ORDN. 1 MARKS :

T80053005 ABIZAR HUSSAIN NASEEM , 71100716M , , PICT , T80053005

01. CONTROL SYSTEMS	PP	100	40	55	P
02. DIGITAL COMMUNICATION	PP	100	40	43	P
03. DIGITAL COMMUNICATION	PR	50	20	30	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	59	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	38	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	54	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	30	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	53	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	32	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	26	P

FIRST TERM TOTAL = 420/750.

ORDN. 1 MARKS :

T80053006 ADITYA KALIA NEELAM , 71100718H , , PICT , T80053006

01. CONTROL SYSTEMS	PP	100	40	52	P
02. DIGITAL COMMUNICATION	PP	100	40	40	P
03. DIGITAL COMMUNICATION	PR	50	20	20	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	44	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	34	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	53	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	25	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	32	F
09. DIGITAL SIGNAL PROCESSING	OR	50	20	11	F
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	22	P

FIRST TERM TOTAL = 333/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80053007 AGRAWAL PRANAT ADITYA ASHA , 71100721H , , PICT , T80053007

01. CONTROL SYSTEMS	PP	100	40	47	P
02. DIGITAL COMMUNICATION	PP	100	40	40	P
03. DIGITAL COMMUNICATION	PR	50	20	08	F
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	47	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	36	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	47	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	21	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	42	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	11	F
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	24	P

FIRST TERM TOTAL = 323/750.

ORDN. 1 MARKS :

T80053008 AIKAL SANKET KABIRNATH SANGEETHA , 71100723D , , PICT , T80053008

01. CONTROL SYSTEMS	PP	100	40	46	P
02. DIGITAL COMMUNICATION	PP	100	40	56	P
03. DIGITAL COMMUNICATION	PR	50	20	27	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	50	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	41	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	43	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	30	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	53	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	31	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	26	P

FIRST TERM TOTAL = 403/750.

ORDN. 1 MARKS :

T80053009 ALI ASGER MODI ZEENAT , 71100724B , , PICT , T80053009

01. CONTROL SYSTEMS	PP	100	40	40	P
02. DIGITAL COMMUNICATION	PP	100	40	40	P
03. DIGITAL COMMUNICATION	PR	50	20	30	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	48	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	34	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	56	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	27	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	32	F
09. DIGITAL SIGNAL PROCESSING	OR	50	20	23	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	22	P

FIRST TERM TOTAL = 352/750.

ORDN. 1 MARKS :

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OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80053010 AMBURE GANESH VITHALRAO ANITA , 71100726J , , PICT , T80053010

01. CONTROL SYSTEMS	PP	100	40	61	P
02. DIGITAL COMMUNICATION	PP	100	40	53	P
03. DIGITAL COMMUNICATION	PR	50	20	09	F
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	61	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	32	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	47	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	29	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	51	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	30	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	23	P

FIRST TERM TOTAL = 396/750.

ORDN. 1 MARKS :

T80053011 ANUPAM BHATTACHARJEE MOUSUMI , 71100731E , , PICT , T80053011

01. CONTROL SYSTEMS	PP	100	40	69	P
02. DIGITAL COMMUNICATION	PP	100	40	63	P
03. DIGITAL COMMUNICATION	PR	50	20	33	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	68	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	27	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	61	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	28	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	54	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	44	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	37	P

FIRST TERM TOTAL = 484/750.

ORDN. 1 MARKS :

T80053012 BAGALE PRASHANT MUTYAPPA MAHADEVI , 71236306J , , PICT , T80053012

01. CONTROL SYSTEMS	PP	100	40	52	P
02. DIGITAL COMMUNICATION	PP	100	40	45	P
03. DIGITAL COMMUNICATION	PR	50	20	06	F
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	40	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	42	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	62	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	36	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	51	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	38	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	26	P

FIRST TERM TOTAL = 398/750.

ORDN. 1 MARKS :

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T80053013 BAGALE PRITESH NAMDEV JIJABAI , 71100737D , , PICT , T80053013

01. CONTROL SYSTEMS	PP	100	40	61	P
02. DIGITAL COMMUNICATION	PP	100	40	AA	F
03. DIGITAL COMMUNICATION	PR	50	20	32	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	59	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	43	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	29	F
07. MICROCONTROLLERS & APPLICATION	PR	50	20	32	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	53	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	41	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	21	P

FIRST TERM TOTAL = 371/750.

ORDN. 1 MARKS :

T80053014 BAGUL KAUSTUBH HITENDRA JAYSHRI , 71100738B , , PICT , T80053014

01. CONTROL SYSTEMS	PP	100	40	60	P
02. DIGITAL COMMUNICATION	PP	100	40	51	P
03. DIGITAL COMMUNICATION	PR	50	20	42	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	63	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	42	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	58	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	29	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	44	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	36	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	34	P

FIRST TERM TOTAL = 459/750.

ORDN. 1 MARKS :

T80053015 BHOSALE ABHISHEK BHAUSAHEB MIRA , 71100750M , , PICT , T80053015

01. CONTROL SYSTEMS	PP	100	40	66	P
02. DIGITAL COMMUNICATION	PP	100	40	40	P
03. DIGITAL COMMUNICATION	PR	50	20	37	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	53	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	36	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	54	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	38	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	40	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	24	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	37	P

FIRST TERM TOTAL = 425/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

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T80053016 BHUTADA GOPAL MADHUSUDAN BASANTI , 71100752H , , PICT , T80053016

01. CONTROL SYSTEMS	PP	100	40	67	P
02. DIGITAL COMMUNICATION	PP	100	40	61	P
03. DIGITAL COMMUNICATION	PR	50	20	39	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	72	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	44	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	58	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	42	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	60	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	36	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	38	P

FIRST TERM TOTAL = 517/750.

ORDN. 1 MARKS :

T80053017 BUDHWANT UMA SHYAMRAO NANDINI , 71100757J , , PICT , T80053017

01. CONTROL SYSTEMS	PP	100	40	56	P
02. DIGITAL COMMUNICATION	PP	100	40	45	P
03. DIGITAL COMMUNICATION	PR	50	20	32	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	44	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	40	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	55	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	32	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	47	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	28	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	32	P

FIRST TERM TOTAL = 411/750.

ORDN. 1 MARKS :

T80053018 BUTALA KARAN SANDESH NANDINI , 71100759E , , PICT , T80053018

01. CONTROL SYSTEMS	PP	100	40	65	P
02. DIGITAL COMMUNICATION	PP	100	40	63	P
03. DIGITAL COMMUNICATION	PR	50	20	36	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	57	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	36	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	60	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	38	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	54	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	34	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	39	P

FIRST TERM TOTAL = 482/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80053019 CHANDAK RASIKA RAMGOPAL ALAKNANDA , 71236307G , , PICT , T80053019

01. CONTROL SYSTEMS	PP	100	40	46	P
02. DIGITAL COMMUNICATION	PP	100	40	63	P
03. DIGITAL COMMUNICATION	PR	50	20	36	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	58	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	45	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	62	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	30	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	70	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	35	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	23	P

FIRST TERM TOTAL = 468/750.

ORDN. 1 MARKS :

T80053020 CHANDAN PANCHLORIA SUMITRADEVI , 71100761G , , PICT , T80053020

01. CONTROL SYSTEMS	PP	100	40	45	P
02. DIGITAL COMMUNICATION	PP	100	40	43	P
03. DIGITAL COMMUNICATION	PR	50	20	32	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	58	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	40	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	40	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	33	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	44	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	21	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	28	P

FIRST TERM TOTAL = 384/750.

ORDN. 1 MARKS :

T80053021 CHAVARE PRANAV NITIN NITISHA , 71100765K , , PICT , T80053021

01. CONTROL SYSTEMS	PP	100	40	61	P
02. DIGITAL COMMUNICATION	PP	100	40	58	P
03. DIGITAL COMMUNICATION	PR	50	20	42	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	60	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	45	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	54	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	41	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	66	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	45	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	35	P

FIRST TERM TOTAL = 507/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80053022 CHAVHAN YOGESH LAXMAN ASHA , 71100766H , , PICT , T80053022

01. CONTROL SYSTEMS	PP	100	40	81	P
02. DIGITAL COMMUNICATION	PP	100	40	65	P
03. DIGITAL COMMUNICATION	PR	50	20	36	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	75	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	41	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	65	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	35	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	56	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	42	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	27	P

FIRST TERM TOTAL = 523/750.

ORDN. 1 MARKS :

T80053023 CHHATRE JAI VILAS HEMA , 71100767F , , PICT , T80053023

01. CONTROL SYSTEMS	PP	100	40	65	P
02. DIGITAL COMMUNICATION	PP	100	40	40	P
03. DIGITAL COMMUNICATION	PR	50	20	43	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	66	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	44	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	54	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	42	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	49	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	43	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	35	P

FIRST TERM TOTAL = 481/750.

ORDN. 1 MARKS :

T80053024 CHIDRAWAR DNYANESHWARI GIRISH GEETA , 71100768D , , PICT , T80053024

01. CONTROL SYSTEMS	PP	100	40	77	P
02. DIGITAL COMMUNICATION	PP	100	40	60	P
03. DIGITAL COMMUNICATION	PR	50	20	38	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	70	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	36	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	66	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	38	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	55	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	43	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	24	P

FIRST TERM TOTAL = 507/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80053025 CHITALE JANHAVI GOVIND VARSHA , 71100769B , , PICT , T80053025

01. CONTROL SYSTEMS	PP	100	40	56	P
02. DIGITAL COMMUNICATION	PP	100	40	48	P
03. DIGITAL COMMUNICATION	PR	50	20	41	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	55	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	42	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	57	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	38	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	47	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	42	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	25	P

FIRST TERM TOTAL = 451/750.

ORDN. 1 MARKS :

T80053026 DESHMUKH AMRUTA VILAS ANJALI , 71100779K , , PICT , T80053026

01. CONTROL SYSTEMS	PP	100	40	71	P
02. DIGITAL COMMUNICATION	PP	100	40	55	P
03. DIGITAL COMMUNICATION	PR	50	20	44	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	71	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	41	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	61	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	36	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	58	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	42	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	28	P

FIRST TERM TOTAL = 507/750.

ORDN. 1 MARKS :

T80053028 DESHPANDE ADITI VIDHYADHAR ALKA , 71100781M , , PICT , T80053028

01. CONTROL SYSTEMS	PP	100	40	58	P
02. DIGITAL COMMUNICATION	PP	100	40	42	P
03. DIGITAL COMMUNICATION	PR	50	20	27	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	46	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	38	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	51	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	30	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	51	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	26	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	34	P

FIRST TERM TOTAL = 403/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80053029 DHANDAR RUCHITA PANDURANG VIJAYA , 71236308E , , PICT , T80053029

01. CONTROL SYSTEMS	PP	100	40	64	P
02. DIGITAL COMMUNICATION	PP	100	40	65	P
03. DIGITAL COMMUNICATION	PR	50	20	38	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	62	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	46	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	70	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	34	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	57	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	31	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	31	P

FIRST TERM TOTAL = 498/750.

ORDN. 1 MARKS :

T80053030 DHANE AKSHAY MUKUNDRAO SHARADA , 71236309C , , PICT , T80053030

01. CONTROL SYSTEMS	PP	100	40	64	P
02. DIGITAL COMMUNICATION	PP	100	40	60	P
03. DIGITAL COMMUNICATION	PR	50	20	32	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	66	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	42	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	59	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	38	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	62	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	34	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	28	P

FIRST TERM TOTAL = 485/750.

ORDN. 1 MARKS :

T80053031 DHOLE NILAKSHI AJAY CHHAYA , 71100786B , , PICT , T80053031

01. CONTROL SYSTEMS	PP	100	40	57	P
02. DIGITAL COMMUNICATION	PP	100	40	48	P
03. DIGITAL COMMUNICATION	PR	50	20	37	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	54	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	41	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	49	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	25	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	50	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	39	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	27	P

FIRST TERM TOTAL = 427/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80053032 DONGARE MANOJ JAYSING SHOBHA , 71236311E , , PICT , T80053032

01. CONTROL SYSTEMS	PP	100	40	40	P
02. DIGITAL COMMUNICATION	PP	100	40	43	P
03. DIGITAL COMMUNICATION	PR	50	20	27	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	43	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	43	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	52	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	44	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	41	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	20	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	31	P

FIRST TERM TOTAL = 384/750.

ORDN. 1 MARKS :

T80053033 GAIKWAD PRATIKSHA JAGANNATH ANITA , 71100794C , , PICT , T80053033

01. CONTROL SYSTEMS	PP	100	40	56	P
02. DIGITAL COMMUNICATION	PP	100	40	52	P
03. DIGITAL COMMUNICATION	PR	50	20	42	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	62	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	42	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	52	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	42	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	50	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	39	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	30	P

FIRST TERM TOTAL = 467/750.

ORDN. 1 MARKS :

T80053034 GAVADE SACHIN SHIVAJI SNEHAL , 71100798F , , PICT , T80053034

01. CONTROL SYSTEMS	PP	100	40	49	P
02. DIGITAL COMMUNICATION	PP	100	40	40	P
03. DIGITAL COMMUNICATION	PR	50	20	30	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	50	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	39	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	48	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	27	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	32	F
09. DIGITAL SIGNAL PROCESSING	OR	50	20	34	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	27	P

FIRST TERM TOTAL = 376/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80053035 GHARDE SAGAR BHIMRAO SHARDA , 71236313M , , PICT , T80053035

01. CONTROL SYSTEMS	PP	100	40	40	P
02. DIGITAL COMMUNICATION	PP	100	40	48	P
03. DIGITAL COMMUNICATION	PR	50	20	28	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	41	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	39	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	57	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	35	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	40	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	22	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	29	P

FIRST TERM TOTAL = 379/750.

ORDN. 1 MARKS :

T80053036 GIRI POOJA RAVINDRA LATA , 71100805B , , PICT , T80053036

01. CONTROL SYSTEMS	PP	100	40	71	P
02. DIGITAL COMMUNICATION	PP	100	40	49	P
03. DIGITAL COMMUNICATION	PR	50	20	36	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	67	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	46	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	65	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	38	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	58	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	40	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	30	P

FIRST TERM TOTAL = 500/750.

ORDN. 1 MARKS :

T80053037 GOKHALE SIDDHARTH ADESH SUJATA , 71100808G , , PICT , T80053037

01. CONTROL SYSTEMS	PP	100	40	69	P
02. DIGITAL COMMUNICATION	PP	100	40	50	P
03. DIGITAL COMMUNICATION	PR	50	20	43	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	66	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	46	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	66	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	44	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	56	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	45	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	39	P

FIRST TERM TOTAL = 524/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80053038 HARSHIKA THUSU ANITA , 71100817F , , PICT , T80053038

01. CONTROL SYSTEMS	PP	100	40	74	P
02. DIGITAL COMMUNICATION	PP	100	40	51	P
03. DIGITAL COMMUNICATION	PR	50	20	37	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	55	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	46	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	69	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	42	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	69	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	48	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	38	P

FIRST TERM TOTAL = 529/750.

ORDN. 1 MARKS :

T80053039 HIMANSHU JAIN RACHANA , 71100821D , , PICT , T80053039

01. CONTROL SYSTEMS	PP	100	40	53	P
02. DIGITAL COMMUNICATION	PP	100	40	44	P
03. DIGITAL COMMUNICATION	PR	50	20	04	F
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	42	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	25	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	28	F
07. MICROCONTROLLERS & APPLICATION	PR	50	20	10	F
08. DIGITAL SIGNAL PROCESSING	PP	100	40	40	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	11	F
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	15	F

FIRST TERM TOTAL = 272/750.

ORDN. 1 MARKS :

T80053040 ISHTE PRAKASH BHAGVAN KAMAL , 70925442D , , PICT , T80053040

01. CONTROL SYSTEMS	PP	100	40	40	P
02. DIGITAL COMMUNICATION	PP	100	40	40	P
03. DIGITAL COMMUNICATION	PR	50	20	20	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	52	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	33	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	40	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	11	F
08. DIGITAL SIGNAL PROCESSING	PP	100	40	40	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	10	F
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	22	P

FIRST TERM TOTAL = 308/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80053041 JAGTAP MANJIRI ARVIND SHUBHANGI , 71100825G , , PICT , T80053041

01. CONTROL SYSTEMS	PP	100	40	43	P
02. DIGITAL COMMUNICATION	PP	100	40	AA	F
03. DIGITAL COMMUNICATION	PR	50	20	21	P
04. NETWORK SYNTHESIS & FILTER DESIGN	NPP	100	40	27	F
05. NETWORK SYNTHESIS & FILTER DESIGN	NTW	50	20	36	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	27	F
07. MICROCONTROLLERS & APPLICATION	PR	50	20	09	F
08. DIGITAL SIGNAL PROCESSING	PP	100	40	43	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	12	F
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	22	P

FIRST TERM TOTAL = 240/750.

ORDN. 1 MARKS :

T80053042 JAIN SAJAL ABHAYKUMAR SUNITA , 71100827C , , PICT , T80053042

01. CONTROL SYSTEMS	PP	100	40	45	P
02. DIGITAL COMMUNICATION	PP	100	40	42	P
03. DIGITAL COMMUNICATION	PR	50	20	13	F
04. NETWORK SYNTHESIS & FILTER DESIGN	NPP	100	40	40	P
05. NETWORK SYNTHESIS & FILTER DESIGN	NTW	50	20	36	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	55	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	22	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	40	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	30	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	26	P

FIRST TERM TOTAL = 349/750.

ORDN. 1 MARKS :

T80053043 JEDGULE UMASHANKAR RADHAKISAN ANITA , 71100831M , , PICT , T80053043

01. CONTROL SYSTEMS	PP	100	40	69	P
02. DIGITAL COMMUNICATION	PP	100	40	48	P
03. DIGITAL COMMUNICATION	PR	50	20	24	P
04. NETWORK SYNTHESIS & FILTER DESIGN	NPP	100	40	67	P
05. NETWORK SYNTHESIS & FILTER DESIGN	NTW	50	20	39	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	58	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	40	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	45	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	35	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	24	P

FIRST TERM TOTAL = 449/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80053044 JOSHI APOORV VIJAY ANAGHA , 71100835D , , PICT , T80053044

01. CONTROL SYSTEMS	PP	100	40	74	P
02. DIGITAL COMMUNICATION	PP	100	40	45	P
03. DIGITAL COMMUNICATION	PR	50	20	22	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	52	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	45	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	59	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	38	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	43	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	39	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	25	P

FIRST TERM TOTAL = 442/750.

ORDN. 1 MARKS :

T80053045 JOSHI CHAITRALI SANJAY VARSHA , 71100836B , , PICT , T80053045

01. CONTROL SYSTEMS	PP	100	40	75	P
02. DIGITAL COMMUNICATION	PP	100	40	50	P
03. DIGITAL COMMUNICATION	PR	50	20	37	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	68	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	46	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	57	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	45	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	69	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	46	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	36	P

FIRST TERM TOTAL = 529/750.

ORDN. 1 MARKS :

T80053046 KADAM SHRIKANT DEVIDASRAO SANJIVANI , 71100840L , , PICT , T80053046

01. CONTROL SYSTEMS	PP	100	40	52	P
02. DIGITAL COMMUNICATION	PP	100	40	43	P
03. DIGITAL COMMUNICATION	PR	50	20	09	F
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	53	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	36	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	51	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	35	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	48	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	36	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	35	P

FIRST TERM TOTAL = 398/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80053047 KAMBLE AMRUTA GORAKH NANDA , 71100844C , , PICT , T80053047

01. CONTROL SYSTEMS	PP	100	40	74	P
02. DIGITAL COMMUNICATION	PP	100	40	55	P
03. DIGITAL COMMUNICATION	PR	50	20	38	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	71	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	47	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	55	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	40	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	46	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	41	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	26	P

FIRST TERM TOTAL = 493/750.

ORDN. 1 MARKS :

T80053048 KAMBLE DASHARATH SATAPPA KAMAL , 71236314K , , PICT , T80053048

01. CONTROL SYSTEMS	PP	100	40	50	P
02. DIGITAL COMMUNICATION	PP	100	40	40	P
03. DIGITAL COMMUNICATION	PR	50	20	28	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	31	F
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	42	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	62	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	25	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	45	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	25	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	25	P

FIRST TERM TOTAL = 373/750.

ORDN. 1 MARKS :

T80053049 KAMBLE PRATIK RAVINDRA SUMAN , 71100845M , , PICT , T80053049

01. CONTROL SYSTEMS	PP	100	40	54	P
02. DIGITAL COMMUNICATION	PP	100	40	45	P
03. DIGITAL COMMUNICATION	PR	50	20	30	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	58	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	26	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	48	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	27	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	44	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	17	F
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	22	P

FIRST TERM TOTAL = 371/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80053050 KAMTHE SNEHAL SUNIL ALKA , 71236315H , , PICT , T80053050

01. CONTROL SYSTEMS	PP	100	40	67	P
02. DIGITAL COMMUNICATION	PP	100	40	57	P
03. DIGITAL COMMUNICATION	PR	50	20	33	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	58	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	47	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	75	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	45	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	69	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	25	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	23	P

FIRST TERM TOTAL = 499/750.

ORDN. 1 MARKS :

T80053051 KANSARA KRUTIKA SANJAY JIGISHA , 71100847H , , PICT , T80053051

01. CONTROL SYSTEMS	PP	100	40	64	P
02. DIGITAL COMMUNICATION	PP	100	40	52	P
03. DIGITAL COMMUNICATION	PR	50	20	45	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	56	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	48	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	71	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	45	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	48	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	44	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	29	P

FIRST TERM TOTAL = 502/750.

ORDN. 1 MARKS :

T80053052 KANUNGO KAPIL RAMESH KRISHNA , 71100849D , , PICT , T80053052

01. CONTROL SYSTEMS	PP	100	40	68	P
02. DIGITAL COMMUNICATION	PP	100	40	61	P
03. DIGITAL COMMUNICATION	PR	50	20	30	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	47	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	34	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	60	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	30	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	57	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	40	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	33	P

FIRST TERM TOTAL = 460/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80053053 KASHID SONALI BHARAT KALPANA , 71236316F , , PICT , T80053053

01. CONTROL SYSTEMS	PP	100	40	68	P
02. DIGITAL COMMUNICATION	PP	100	40	49	P
03. DIGITAL COMMUNICATION	PR	50	20	36	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	53	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	42	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	66	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	35	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	53	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	29	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	25	P

FIRST TERM TOTAL = 456/750.

ORDN. 1 MARKS :

T80053054 KESARKAR ONKAR PANDURANG ANITA , 71236317D , , PICT , T80053054

01. CONTROL SYSTEMS	PP	100	40	63	P
02. DIGITAL COMMUNICATION	PP	100	40	52	P
03. DIGITAL COMMUNICATION	PR	50	20	38	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	52	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	43	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	64	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	43	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	56	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	40	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	34	P

FIRST TERM TOTAL = 485/750.

ORDN. 1 MARKS :

T80053055 KHADATKAR NIRAJ MANOHAR TARA , 71100855J , , PICT , T80053055

01. CONTROL SYSTEMS	PP	100	40	68	P
02. DIGITAL COMMUNICATION	PP	100	40	52	P
03. DIGITAL COMMUNICATION	PR	50	20	33	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	57	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	40	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	64	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	32	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	60	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	33	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	23	P

FIRST TERM TOTAL = 462/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80053056 KHAMBE DIGVIJAY RAJARAM MANJUSHA , 71100859M , , PICT , T80053056

01. CONTROL SYSTEMS	PP	100	40	45	P
02. DIGITAL COMMUNICATION	PP	100	40	53	P
03. DIGITAL COMMUNICATION	PR	50	20	38	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	59	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	36	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	70	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	32	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	53	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	45	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	27	P

FIRST TERM TOTAL = 458/750.

ORDN. 1 MARKS :

T80053057 KOCHAR ANKITA VINOD SUREKHA , 71100865F , , PICT , T80053057

01. CONTROL SYSTEMS	PP	100	40	50	P
02. DIGITAL COMMUNICATION	PP	100	40	48	P
03. DIGITAL COMMUNICATION	PR	50	20	36	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	50	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	37	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	53	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	25	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	44	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	17	F
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	21	P

FIRST TERM TOTAL = 381/750.

ORDN. 1 MARKS :

T80053058 KRISHNAN SANTOSH KUMAR RADHIKA , 71100867B , , PICT , T80053058

01. CONTROL SYSTEMS	PP	100	40	63	P
02. DIGITAL COMMUNICATION	PP	100	40	52	P
03. DIGITAL COMMUNICATION	PR	50	20	41	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	55	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	43	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	59	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	35	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	52	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	48	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	28	P

FIRST TERM TOTAL = 476/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80053059 KULKARNI KRANTI MADHAV MADHURI , 71100871L , , PICT , T80053059

01. CONTROL SYSTEMS	PP	100	40	55	P
02. DIGITAL COMMUNICATION	PP	100	40	47	P
03. DIGITAL COMMUNICATION	PR	50	20	40	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	53	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	44	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	65	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	35	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	60	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	39	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	27	P

FIRST TERM TOTAL = 465/750.

ORDN. 1 MARKS :

T80053060 KUNAL AGARWAL POONAM DEVI , 71100875C , , PICT , T80053060

01. CONTROL SYSTEMS	PP	100	40	56	P
02. DIGITAL COMMUNICATION	PP	100	40	54	P
03. DIGITAL COMMUNICATION	PR	50	20	42	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	45	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	38	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	54	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	40	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	51	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	32	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	35	P

FIRST TERM TOTAL = 447/750.

ORDN. 1 MARKS :

T80053061 KURHADE RUSHIKESH SAMBHAJI SANGITA , 71100876M , , PICT , T80053061

01. CONTROL SYSTEMS	PP	100	40	68	P
02. DIGITAL COMMUNICATION	PP	100	40	53	P
03. DIGITAL COMMUNICATION	PR	50	20	40	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	49	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	47	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	65	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	46	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	56	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	42	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	34	P

FIRST TERM TOTAL = 500/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80053062 KUSH VARMA PARVEEN , 71100877K , , PICT , T80053062

01. CONTROL SYSTEMS	PP	100	40	70	P
02. DIGITAL COMMUNICATION	PP	100	40	50	P
03. DIGITAL COMMUNICATION	PR	50	20	30	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	57	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	44	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	69	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	45	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	51	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	47	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	28	P

FIRST TERM TOTAL = 491/750.

ORDN. 1 MARKS :

T80053063 M SUBRAMANIAN M KOHILA , 71100885L , , PICT , T80053063

01. CONTROL SYSTEMS	PP	100	40	40	P
02. DIGITAL COMMUNICATION	PP	100	40	28	F
03. DIGITAL COMMUNICATION	PR	50	20	36	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	45	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	33	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	51	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	08	F
08. DIGITAL SIGNAL PROCESSING	PP	100	40	55	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	33	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	35	P

FIRST TERM TOTAL = 364/750.

ORDN. 1 MARKS :

T80053064 MAHAJAN PRATEEK PARAG PREETI , 71100888E , , PICT , T80053064

01. CONTROL SYSTEMS	PP	100	40	51	P
02. DIGITAL COMMUNICATION	PP	100	40	50	P
03. DIGITAL COMMUNICATION	PR	50	20	42	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	58	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	46	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	62	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	40	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	63	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	40	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	39	P

FIRST TERM TOTAL = 491/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80053065 MAHANGARE BHAGYASHRI BABASAHEB SUREKHA , 71236318B , , PICT , T80053065

01. CONTROL SYSTEMS	PP	100	40	44	P
02. DIGITAL COMMUNICATION	PP	100	40	44	P
03. DIGITAL COMMUNICATION	PR	50	20	34	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	40	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	41	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	55	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	30	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	40	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	36	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	27	P

FIRST TERM TOTAL = 391/750.

ORDN. 1 MARKS :

T80053066 MALI RAKESH SAKHARAM REKHA , 71100889C , , PICT , T80053066

01. CONTROL SYSTEMS	PP	100	40	53	P
02. DIGITAL COMMUNICATION	PP	100	40	54	P
03. DIGITAL COMMUNICATION	PR	50	20	37	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	51	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	39	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	65	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	29	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	60	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	25	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	38	P

FIRST TERM TOTAL = 451/750.

ORDN. 1 MARKS :

T80053067 METHIKAR PRIYANKA JITENDRA SUSHMA , 71100895H , , PICT , T80053067

01. CONTROL SYSTEMS	PP	100	40	75	P
02. DIGITAL COMMUNICATION	PP	100	40	54	P
03. DIGITAL COMMUNICATION	PR	50	20	45	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	60	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	43	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	63	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	42	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	51	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	35	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	39	P

FIRST TERM TOTAL = 507/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80053068 MOHITE SHEETAL CHANDRAKANT SAVITA , 71236319L , , PICT , T80053068

01. CONTROL SYSTEMS	PP	100	40	64	P
02. DIGITAL COMMUNICATION	PP	100	40	46	P
03. DIGITAL COMMUNICATION	PR	50	20	43	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	62	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	40	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	55	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	36	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	58	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	43	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	41	P

FIRST TERM TOTAL = 488/750.

ORDN. 1 MARKS :

T80053069 MORE VIKAS CHANDRABHAN SUMAN , 71045527F , , PICT , T80053069

01. CONTROL SYSTEMS	PP	100	40	50	P
02. DIGITAL COMMUNICATION	PP	100	40	32	F
03. DIGITAL COMMUNICATION	PR	50	20	06	F
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	40	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	24	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	40	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	25	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	40	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	10	F
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	28	P

FIRST TERM TOTAL = 295/750.

ORDN. 1 MARKS :

T80053070 MUSALE VIVEK KISANRAO INDUBAI , 71236320D , , PICT , T80053070

01. CONTROL SYSTEMS	PP	100	40	44	P
02. DIGITAL COMMUNICATION	PP	100	40	40	P
03. DIGITAL COMMUNICATION	PR	50	20	07	F
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	45	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	38	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	53	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	27	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	27	F
09. DIGITAL SIGNAL PROCESSING	OR	50	20	30	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	27	P

FIRST TERM TOTAL = 338/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80053071 NADEKAR SANJIV CHANDRAKANT ALKA , 71236321B , , PICT , T80053071

01. CONTROL SYSTEMS	PP	100	40	60	P
02. DIGITAL COMMUNICATION	PP	100	40	44	P
03. DIGITAL COMMUNICATION	PR	50	20	22	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	50	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	40	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	44	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	34	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	55	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	35	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	27	P

FIRST TERM TOTAL = 411/750.

ORDN. 1 MARKS :

T80053072 NAIK DIGJA ANKUSH VASANTI , 71100906G , , PICT , T80053072

01. CONTROL SYSTEMS	PP	100	40	68	P
02. DIGITAL COMMUNICATION	PP	100	40	51	P
03. DIGITAL COMMUNICATION	PR	50	20	42	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	71	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	47	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	63	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	35	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	47	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	36	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	28	P

FIRST TERM TOTAL = 488/750.

ORDN. 1 MARKS :

T80053073 NAKHATE ANIKET BHAGWANRAO ANITA , 71100909M , , PICT , T80053073

01. CONTROL SYSTEMS	PP	100	40	43	P
02. DIGITAL COMMUNICATION	PP	100	40	40	P
03. DIGITAL COMMUNICATION	PR	50	20	27	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	51	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	44	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	40	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	40	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	40	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	40	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	32	P

FIRST TERM TOTAL = 397/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80053074 NARKAR PRAJAKTA SUHAS SUPRIYA , 71100914H , , PICT , T80053074

01. CONTROL SYSTEMS	PP	100	40	72	P
02. DIGITAL COMMUNICATION	PP	100	40	50	P
03. DIGITAL COMMUNICATION	PR	50	20	44	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	65	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	47	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	64	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	42	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	68	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	40	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	38	P

FIRST TERM TOTAL = 530/750.

ORDN. 1 MARKS :

T80053075 NERLEKAR SHWETA PRAKASH SUGANDHA , 71045546B , , PICT , T80053075

01. CONTROL SYSTEMS	PP	100	40	56	P
02. DIGITAL COMMUNICATION	PP	100	40	40	P
03. DIGITAL COMMUNICATION	PR	50	20	36	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	49	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	40	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	47	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	39	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	40	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	41	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	37	P

FIRST TERM TOTAL = 425/750.

ORDN. 1 MARKS :

T80053076 NIKAM JEEVAN SHANKAR SANGITA , 71045547L , , PICT , T80053076

01. CONTROL SYSTEMS	PP	100	40	50	P
02. DIGITAL COMMUNICATION	PP	100	40	40	P
03. DIGITAL COMMUNICATION	PR	50	20	08	F
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	30	F
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	37	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	49	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	11	F
08. DIGITAL SIGNAL PROCESSING	PP	100	40	46	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	27	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	39	P

FIRST TERM TOTAL = 337/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80053077 NIKITA AVINASH ANGRE CHITRA , 71100917B , , PICT , T80053077

01. CONTROL SYSTEMS	PP	100	40	43	P
02. DIGITAL COMMUNICATION	PP	100	40	40	P
03. DIGITAL COMMUNICATION	PR	50	20	35	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	47	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	38	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	40	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	38	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	42	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	25	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	29	P

FIRST TERM TOTAL = 377/750.

ORDN. 1 MARKS :

T80053078 PADGILWAR GAURAV VISHWAMBHAR SAVITA , 71100920B , , PICT , T80053078

01. CONTROL SYSTEMS	PP	100	40	59	P
02. DIGITAL COMMUNICATION	PP	100	40	52	P
03. DIGITAL COMMUNICATION	PR	50	20	28	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	60	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	40	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	62	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	42	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	45	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	38	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	33	P

FIRST TERM TOTAL = 459/750.

ORDN. 1 MARKS :

T80053079 PADHYE ABHINAV ANANT ANURADHA , 71236322L , , PICT , T80053079

01. CONTROL SYSTEMS	PP	100	40	76	P
02. DIGITAL COMMUNICATION	PP	100	40	54	P
03. DIGITAL COMMUNICATION	PR	50	20	34	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	53	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	46	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	61	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	42	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	60	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	41	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	36	P

FIRST TERM TOTAL = 503/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80053080 PADOLE NEHA SUNIL NIRMALA , 71100921L , , PICT , T80053080

01. CONTROL SYSTEMS	PP	100	40	74	P
02. DIGITAL COMMUNICATION	PP	100	40	59	P
03. DIGITAL COMMUNICATION	PR	50	20	35	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	62	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	37	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	72	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	42	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	59	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	38	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	40	P

FIRST TERM TOTAL = 518/750.

ORDN. 1 MARKS :

T80053081 PAGARIYA BHUSHAN RAMESHCHAND MANJUSHRI , 71100923G , , PICT , T80053081

01. CONTROL SYSTEMS	PP	100	40	73	P
02. DIGITAL COMMUNICATION	PP	100	40	44	P
03. DIGITAL COMMUNICATION	PR	50	20	43	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	63	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	39	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	66	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	43	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	53	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	43	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	39	P

FIRST TERM TOTAL = 506/750.

ORDN. 1 MARKS :

T80053082 PANDEY JUHI SANJAY SUNITA , 71100927K , , PICT , T80053082

01. CONTROL SYSTEMS	PP	100	40	70	P
02. DIGITAL COMMUNICATION	PP	100	40	56	P
03. DIGITAL COMMUNICATION	PR	50	20	46	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	74	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	49	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	66	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	45	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	76	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	45	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	41	P

FIRST TERM TOTAL = 568/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80053083 PANDILWAR RAMESHWAR KISHORE SUJATA , 71100928H , , PICT , T80053083

01. CONTROL SYSTEMS	PP	100	40	61	P
02. DIGITAL COMMUNICATION	PP	100	40	40	P
03. DIGITAL COMMUNICATION	PR	50	20	07	F
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	51	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	21	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	41	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	12	F
08. DIGITAL SIGNAL PROCESSING	PP	100	40	43	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	41	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	22	P

FIRST TERM TOTAL = 339/750.

ORDN. 1 MARKS :

T80053084 PARDESHI SAGAR RAJKUMAR USHA , 71100931H , , PICT , T80053084

01. CONTROL SYSTEMS	PP	100	40	65	P
02. DIGITAL COMMUNICATION	PP	100	40	48	P
03. DIGITAL COMMUNICATION	PR	50	20	44	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	57	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	43	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	63	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	40	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	61	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	39	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	36	P

FIRST TERM TOTAL = 496/750.

ORDN. 1 MARKS :

T80053085 PATHAK SWANAND SUDHIR SONALI , 71100936J , , PICT , T80053085

01. CONTROL SYSTEMS	PP	100	40	63	P
02. DIGITAL COMMUNICATION	PP	100	40	50	P
03. DIGITAL COMMUNICATION	PR	50	20	43	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	55	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	42	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	58	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	37	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	40	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	40	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	36	P

FIRST TERM TOTAL = 464/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80053086 PATIL AMOL HIRAMAN LATA , 71100937G , , PICT , T80053086

01. CONTROL SYSTEMS	PP	100	40	58	P
02. DIGITAL COMMUNICATION	PP	100	40	24	F
03. DIGITAL COMMUNICATION	PR	50	20	27	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	45	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	41	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	54	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	32	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	45	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	35	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	29	P

FIRST TERM TOTAL = 390/750.

ORDN. 1 MARKS :

T80053087 PATIL ASHISH ISHWAR KUSUM , 71100939C , , PICT , T80053087

01. CONTROL SYSTEMS	PP	100	40	71	P
02. DIGITAL COMMUNICATION	PP	100	40	53	P
03. DIGITAL COMMUNICATION	PR	50	20	34	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	55	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	41	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	56	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	32	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	54	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	37	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	34	P

FIRST TERM TOTAL = 467/750.

ORDN. 1 MARKS :

T80053088 PATIL PRADNYA RAJENDRA SNEHAL , 71236323J , , PICT , T80053088

01. CONTROL SYSTEMS	PP	100	40	62	P
02. DIGITAL COMMUNICATION	PP	100	40	47	P
03. DIGITAL COMMUNICATION	PR	50	20	38	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	58	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	40	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	67	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	36	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	53	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	38	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	34	P

FIRST TERM TOTAL = 473/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80053089 PATIL PUJA VIJAY MIRA , 71100942C , , PICT , T80053089

01. CONTROL SYSTEMS	PP	100	40	58	P
02. DIGITAL COMMUNICATION	PP	100	40	52	P
03. DIGITAL COMMUNICATION	PR	50	20	34	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	48	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	37	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	53	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	36	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	45	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	25	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	38	P

FIRST TERM TOTAL = 426/750.

ORDN. 1 MARKS :

T80053090 PATIL SHIVRATNA BALASAHEB SHEELA , 71236324G , , PICT , T80053090

01. CONTROL SYSTEMS	PP	100	40	63	P
02. DIGITAL COMMUNICATION	PP	100	40	41	P
03. DIGITAL COMMUNICATION	PR	50	20	37	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	55	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	35	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	56	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	39	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	48	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	20	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	37	P

FIRST TERM TOTAL = 431/750.

ORDN. 1 MARKS :

T80053091 PAVAN TENKALE SHIVKANTA , 71100946F , , PICT , T80053091

01. CONTROL SYSTEMS	PP	100	40	56	P
02. DIGITAL COMMUNICATION	PP	100	40	44	P
03. DIGITAL COMMUNICATION	PR	50	20	AA	F
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	51	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	25	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	40	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	02	F
08. DIGITAL SIGNAL PROCESSING	PP	100	40	32	F
09. DIGITAL SIGNAL PROCESSING	OR	50	20	AA	F
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	16	F

FIRST TERM TOTAL = 266/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80053092 PAWAR AJAY BALNATH SUNITA , 71100948B , , PICT , T80053092

01. CONTROL SYSTEMS	PP	100	40	48	P
02. DIGITAL COMMUNICATION	PP	100	40	50	P
03. DIGITAL COMMUNICATION	PR	50	20	40	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	46	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	45	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	56	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	38	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	49	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	41	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	35	P

FIRST TERM TOTAL = 448/750.

ORDN. 1 MARKS :

T80053093 PAWAR ASHWINI SUDHAKAR VANDANA , 71236325E , , PICT , T80053093

01. CONTROL SYSTEMS	PP	100	40	58	P
02. DIGITAL COMMUNICATION	PP	100	40	48	P
03. DIGITAL COMMUNICATION	PR	50	20	39	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	64	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	43	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	64	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	28	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	56	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	30	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	32	P

FIRST TERM TOTAL = 462/750.

ORDN. 1 MARKS :

T80053094 PHAPALE MONIKA DILIP ALKA , 71100955E , , PICT , T80053094

01. CONTROL SYSTEMS	PP	100	40	44	P
02. DIGITAL COMMUNICATION	PP	100	40	40	P
03. DIGITAL COMMUNICATION	PR	50	20	32	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	40	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	43	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	60	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	26	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	40	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	28	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	30	P

FIRST TERM TOTAL = 383/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80053095 PRAGATI VASANT SHAH VARSHA , 71100958K , , PICT , T80053095

01. CONTROL SYSTEMS	PP	100	40	55	P
02. DIGITAL COMMUNICATION	PP	100	40	55	P
03. DIGITAL COMMUNICATION	PR	50	20	35	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	49	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	43	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	61	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	32	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	58	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	40	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	38	P

FIRST TERM TOTAL = 466/750.

ORDN. 1 MARKS :

T80053096 PRAMOD KUMAR VERMA SUMAN DEVI , 71100959H , , PICT , T80053096

01. CONTROL SYSTEMS	PP	100	40	69	P
02. DIGITAL COMMUNICATION	PP	100	40	51	P
03. DIGITAL COMMUNICATION	PR	50	20	40	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	54	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	40	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	65	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	41	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	51	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	41	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	39	P

FIRST TERM TOTAL = 491/750.

ORDN. 1 MARKS :

T80053097 PRAYAG SHREYAS NAGNATH JAYASHRI , 71100960M , , PICT , T80053097

01. CONTROL SYSTEMS	PP	100	40	59	P
02. DIGITAL COMMUNICATION	PP	100	40	42	P
03. DIGITAL COMMUNICATION	PR	50	20	38	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	55	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	34	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	50	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	31	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	40	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	25	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	31	P

FIRST TERM TOTAL = 405/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80053098 PURANDARE ADWAIT PRASHANT MONIKA , 71100961K , , PICT , T80053098

01. CONTROL SYSTEMS	PP	100	40	73	P
02. DIGITAL COMMUNICATION	PP	100	40	59	P
03. DIGITAL COMMUNICATION	PR	50	20	45	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	64	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	47	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	70	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	40	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	70	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	45	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	37	P

FIRST TERM TOTAL = 550/750.

ORDN. 1 MARKS :

T80053099 RATHOD AKASH NURSING NALINI , 71100970J , , PICT , T80053099

01. CONTROL SYSTEMS	PP	100	40	54	P
02. DIGITAL COMMUNICATION	PP	100	40	40	P
03. DIGITAL COMMUNICATION	PR	50	20	32	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	40	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	42	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	51	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	30	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	40	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	30	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	34	P

FIRST TERM TOTAL = 393/750.

ORDN. 1 MARKS :

T80053100 RATHOD VIJAY HIRAMAN KANTA , 71236326C , , PICT , T80053100

01. CONTROL SYSTEMS	PP	100	40	60	P
02. DIGITAL COMMUNICATION	PP	100	40	57	P
03. DIGITAL COMMUNICATION	PR	50	20	28	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	52	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	39	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	61	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	32	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	42	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	34	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	36	P

FIRST TERM TOTAL = 441/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80053101 RAUT CHAITANYA SADASHIV CHHAYA , 71100972E , , PICT , T80053101

01. CONTROL SYSTEMS	PP	100	40	53	P
02. DIGITAL COMMUNICATION	PP	100	40	47	P
03. DIGITAL COMMUNICATION	PR	50	20	06	F
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	56	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	32	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	59	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	33	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	40	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	47	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	32	P

FIRST TERM TOTAL = 405/750.

ORDN. 1 MARKS :

T80053102 ROKADE VINITA SATISHKUMAR VIBHAVARI , 71100977F , , PICT , T80053102

01. CONTROL SYSTEMS	PP	100	40	41	P
02. DIGITAL COMMUNICATION	PP	100	40	40	P
03. DIGITAL COMMUNICATION	PR	50	20	30	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	40	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	33	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	41	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	26	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	48	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	08	F
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	29	P

FIRST TERM TOTAL = 336/750.

ORDN. 1 MARKS :

T80053103 RUDRAWAR SOURABH ASHWIN SHOBHA , 71100979B , , PICT , T80053103

01. CONTROL SYSTEMS	PP	100	40	56	P
02. DIGITAL COMMUNICATION	PP	100	40	52	P
03. DIGITAL COMMUNICATION	PR	50	20	33	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	58	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	35	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	43	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	28	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	49	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	30	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	32	P

FIRST TERM TOTAL = 416/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80053104 SALUNKE SAYALI RAJENDRA SANGEETA , 71100984J , , PICT , T80053104

01. CONTROL SYSTEMS	PP	100	40	60	P
02. DIGITAL COMMUNICATION	PP	100	40	51	P
03. DIGITAL COMMUNICATION	PR	50	20	40	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	60	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	43	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	64	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	35	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	64	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	38	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	23	P

FIRST TERM TOTAL = 478/750.

ORDN. 1 MARKS :

T80053105 SALUNKHE MITALI KISHOR BHARATI , 71236327M , , PICT , T80053105

01. CONTROL SYSTEMS	PP	100	40	63	P
02. DIGITAL COMMUNICATION	PP	100	40	40	P
03. DIGITAL COMMUNICATION	PR	50	20	26	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	55	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	40	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	63	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	40	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	46	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	34	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	22	P

FIRST TERM TOTAL = 429/750.

ORDN. 1 MARKS :

T80053106 SARODE BHUSHAN CHANDRAKANT ALKA , 71236328K , , PICT , T80053106

01. CONTROL SYSTEMS	PP	100	40	63	P
02. DIGITAL COMMUNICATION	PP	100	40	40	P
03. DIGITAL COMMUNICATION	PR	50	20	36	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	49	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	44	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	51	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	41	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	56	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	35	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	23	P

FIRST TERM TOTAL = 438/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80053107 SARWAR ABHIJIT ASHOK MEERA , 71100988M , , PICT , T80053107

01. CONTROL SYSTEMS	PP	100	40	51	P
02. DIGITAL COMMUNICATION	PP	100	40	40	P
03. DIGITAL COMMUNICATION	PR	50	20	30	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	40	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	36	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	42	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	30	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	42	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	36	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	28	P

FIRST TERM TOTAL = 375/750.

ORDN. 1 MARKS :

T80053108 SHAIKH MAAZ FAYYAZ SHAHEEN , 71100998J , , PICT , T80053108

01. CONTROL SYSTEMS	PP	100	40	73	P
02. DIGITAL COMMUNICATION	PP	100	40	53	P
03. DIGITAL COMMUNICATION	PR	50	20	35	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	62	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	42	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	58	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	35	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	44	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	37	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	29	P

FIRST TERM TOTAL = 468/750.

ORDN. 1 MARKS :

T80053109 SHELKE ISHWAR ANKUSH MUKTA , 71101001D , , PICT , T80053109

01. CONTROL SYSTEMS	PP	100	40	60	P
02. DIGITAL COMMUNICATION	PP	100	40	49	P
03. DIGITAL COMMUNICATION	PR	50	20	40	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	62	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	36	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	57	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	35	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	55	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	36	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	37	P

FIRST TERM TOTAL = 467/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80053110 SHIKHA SUMAN SINHA SAVITA , 71101004J , , PICT , T80053110

01. CONTROL SYSTEMS	PP	100	40	55	P
02. DIGITAL COMMUNICATION	PP	100	40	48	P
03. DIGITAL COMMUNICATION	PR	50	20	36	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	60	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	38	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	59	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	24	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	40	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	34	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	13	F

FIRST TERM TOTAL = 407/750.

ORDN. 1 MARKS :

T80053111 SHITRE SOURABH DNYANESHWAR SNEHA , 71101011M , , PICT , T80053111

01. CONTROL SYSTEMS	PP	100	40	47	P
02. DIGITAL COMMUNICATION	PP	100	40	40	P
03. DIGITAL COMMUNICATION	PR	50	20	42	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	46	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	43	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	48	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	40	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	46	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	35	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	38	P

FIRST TERM TOTAL = 425/750.

ORDN. 1 MARKS :

T80053112 SHRADDHA AGRAWAL REKHA , 71101013H , , PICT , T80053112

01. CONTROL SYSTEMS	PP	100	40	76	P
02. DIGITAL COMMUNICATION	PP	100	40	40	P
03. DIGITAL COMMUNICATION	PR	50	20	36	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	51	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	41	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	58	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	27	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	43	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	36	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	23	P

FIRST TERM TOTAL = 431/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80053113 SIDDAMAL GANESH UDDANAPPA PREMA , 71101014F , , PICT , T80053113

01. CONTROL SYSTEMS	PP	100	40	48	P
02. DIGITAL COMMUNICATION	PP	100	40	43	P
03. DIGITAL COMMUNICATION	PR	50	20	38	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	57	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	40	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	41	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	30	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	49	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	37	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	34	P

FIRST TERM TOTAL = 417/750.

ORDN. 1 MARKS :

T80053114 SIDDHANT KHURANA SAROJ , 71101016B , , PICT , T80053114

01. CONTROL SYSTEMS	PP	100	40	40	P
02. DIGITAL COMMUNICATION	PP	100	40	13	F
03. DIGITAL COMMUNICATION	PR	50	20	20	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	42	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	31	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	32	F
07. MICROCONTROLLERS & APPLICATION	PR	50	20	13	F
08. DIGITAL SIGNAL PROCESSING	PP	100	40	24	F
09. DIGITAL SIGNAL PROCESSING	OR	50	20	24	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	32	P

FIRST TERM TOTAL = 271/750.

ORDN. 1 MARKS :

T80053115 SONAWANE ANUJA GIRIDHAR SUNITA , 71101019G , , PICT , T80053115

01. CONTROL SYSTEMS	PP	100	40	47	P
02. DIGITAL COMMUNICATION	PP	100	40	28	F
03. DIGITAL COMMUNICATION	PR	50	20	25	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	47	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	41	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	55	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	29	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	40	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	26	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	23	P

FIRST TERM TOTAL = 361/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80053116 SONOO KUMAR GAURADEVI , 71101020L , , PICT , T80053116

01. CONTROL SYSTEMS	PP	100	40	64	P
02. DIGITAL COMMUNICATION	PP	100	40	42	P
03. DIGITAL COMMUNICATION	PR	50	20	10	F
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	63	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	29	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	48	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	09	F
08. DIGITAL SIGNAL PROCESSING	PP	100	40	47	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	35	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	28	P

FIRST TERM TOTAL = 375/750.

ORDN. 1 MARKS :

T80053117 SONTAKKE BASVESHVAR RAOSAHEB SEETABAI , 71101021J , , PICT , T80053117

01. CONTROL SYSTEMS	PP	100	40	65	P
02. DIGITAL COMMUNICATION	PP	100	40	40	P
03. DIGITAL COMMUNICATION	PR	50	20	30	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	50	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	35	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	56	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	31	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	40	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	35	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	31	P

FIRST TERM TOTAL = 413/750.

ORDN. 1 MARKS :

T80053118 SUMIT SINGH USHA , 71101023E , , PICT , T80053118

01. CONTROL SYSTEMS	PP	100	40	91	P
02. DIGITAL COMMUNICATION	PP	100	40	60	P
03. DIGITAL COMMUNICATION	PR	50	20	44	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	68	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	43	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	68	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	33	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	53	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	39	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	32	P

FIRST TERM TOTAL = 531/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80053119 TANK HIMANSHU SHARAD GEETA , 71101025M , , PICT , T80053119

01. CONTROL SYSTEMS	PP	100	40	50	P
02. DIGITAL COMMUNICATION	PP	100	40	40	P
03. DIGITAL COMMUNICATION	PR	50	20	35	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	53	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	32	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	41	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	28	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	40	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	35	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	28	P

FIRST TERM TOTAL = 382/750.

ORDN. 1 MARKS :

T80053120 TANNA VATSAL AJAY VAISHALI , 71236329H , , PICT , T80053120

01. CONTROL SYSTEMS	PP	100	40	62	P
02. DIGITAL COMMUNICATION	PP	100	40	28	F
03. DIGITAL COMMUNICATION	PR	50	20	12	F
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	43	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	34	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	43	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	23	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	40	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	30	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	29	P

FIRST TERM TOTAL = 344/750.

ORDN. 1 MARKS :

T80053121 TANYA SINGHAL SUSHMA , 71101026K , , PICT , T80053121

01. CONTROL SYSTEMS	PP	100	40	58	P
02. DIGITAL COMMUNICATION	PP	100	40	40	P
03. DIGITAL COMMUNICATION	PR	50	20	34	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	40	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	37	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	51	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	30	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	51	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	34	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	33	P

FIRST TERM TOTAL = 408/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80053122 TAWATE PRIYA GIRISH SNEHAPRABHA , 71236330M , , PICT , T80053122

01. CONTROL SYSTEMS	PP	100	40	65	P
02. DIGITAL COMMUNICATION	PP	100	40	59	P
03. DIGITAL COMMUNICATION	PR	50	20	38	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	67	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	44	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	71	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	45	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	60	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	42	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	39	P

FIRST TERM TOTAL = 530/750.

ORDN. 1 MARKS :

T80053123 TILAK AMEY CHANDRASHEKHAR APARNA , 71101036G , , PICT , T80053123

01. CONTROL SYSTEMS	PP	100	40	65	P
02. DIGITAL COMMUNICATION	PP	100	40	41	P
03. DIGITAL COMMUNICATION	PR	50	20	45	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	58	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	45	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	55	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	41	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	43	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	39	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	25	P

FIRST TERM TOTAL = 457/750.

ORDN. 1 MARKS :

T80053124 TIWARI BHAVYA YOGESH NEHA , 71101037E , , PICT , T80053124

01. CONTROL SYSTEMS	PP	100	40	62	P
02. DIGITAL COMMUNICATION	PP	100	40	40	P
03. DIGITAL COMMUNICATION	PR	50	20	36	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	53	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	34	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	40	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	26	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	42	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	38	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	21	P

FIRST TERM TOTAL = 392/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80053125 TIWARI VIPUL TARUN PADMA , 71101038C , , PICT , T80053125

01. CONTROL SYSTEMS	PP	100	40	61	P
02. DIGITAL COMMUNICATION	PP	100	40	29	F
03. DIGITAL COMMUNICATION	PR	50	20	07	F
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	40	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	31	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	55	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	03	F
08. DIGITAL SIGNAL PROCESSING	PP	100	40	29	F
09. DIGITAL SIGNAL PROCESSING	OR	50	20	20	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	12	F

FIRST TERM TOTAL = 287/750.

ORDN. 1 MARKS :

T80053126 VAIBHAV RAMDAS BORUDE MANDAKINI , 71101043K , , PICT , T80053126

01. CONTROL SYSTEMS	PP	100	40	78	P
02. DIGITAL COMMUNICATION	PP	100	40	42	P
03. DIGITAL COMMUNICATION	PR	50	20	42	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	69	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	37	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	64	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	34	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	58	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	47	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	37	P

FIRST TERM TOTAL = 508/750.

ORDN. 1 MARKS :

T80053127 VAIDYA SANDIP MILIND SANGEETA , 71101044H , , PICT , T80053127

01. CONTROL SYSTEMS	PP	100	40	65	P
02. DIGITAL COMMUNICATION	PP	100	40	42	P
03. DIGITAL COMMUNICATION	PR	50	20	35	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	56	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	42	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	53	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	41	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	40	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	35	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	37	P

FIRST TERM TOTAL = 446/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80053128 VARADE RUSHIKESH DILIP CHHAYA , 71101045F , , PICT , T80053128

01. CONTROL SYSTEMS	PP	100	40	46	P
02. DIGITAL COMMUNICATION	PP	100	40	AA	F
03. DIGITAL COMMUNICATION	PR	50	20	23	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	40	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	27	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	40	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	05	F
08. DIGITAL SIGNAL PROCESSING	PP	100	40	26	F
09. DIGITAL SIGNAL PROCESSING	OR	50	20	40	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	29	P

FIRST TERM TOTAL = 276/750.

ORDN. 1 MARKS :

T80053129 VASAGADEKAR SWATI PRADEEP SUPRIYA , 71236331K , , PICT , T80053129

01. CONTROL SYSTEMS	PP	100	40	75	P
02. DIGITAL COMMUNICATION	PP	100	40	60	P
03. DIGITAL COMMUNICATION	PR	50	20	36	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	74	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	43	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	58	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	41	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	51	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	39	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	30	P

FIRST TERM TOTAL = 507/750.

ORDN. 1 MARKS :

T80053130 VIDHATE TRUPTI SOPAN VARSHA , 71101048L , , PICT , T80053130

01. CONTROL SYSTEMS	PP	100	40	52	P
02. DIGITAL COMMUNICATION	PP	100	40	27	F
03. DIGITAL COMMUNICATION	PR	50	20	36	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	40	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	38	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	41	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	27	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	45	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	30	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	27	P

FIRST TERM TOTAL = 363/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80053131 WANI ANIKET ARUN BHARTI , 71101055C , , PICT , T80053131

01. CONTROL SYSTEMS	PP	100	40	75	P
02. DIGITAL COMMUNICATION	PP	100	40	52	P
03. DIGITAL COMMUNICATION	PR	50	20	42	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	72	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	40	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	60	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	28	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	65	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	33	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	24	P

FIRST TERM TOTAL = 491/750.

ORDN. 1 MARKS :

T80053132 WANKHEDE ROHIT NITIN UMA , 71101057K , , PICT , T80053132

01. CONTROL SYSTEMS	PP	100	40	61	P
02. DIGITAL COMMUNICATION	PP	100	40	53	P
03. DIGITAL COMMUNICATION	PR	50	20	41	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	71	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	40	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	67	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	46	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	46	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	43	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	25	P

FIRST TERM TOTAL = 493/750.

ORDN. 1 MARKS :

T80053133 YAWALKAR SIDDHESH SHRIPAD SANDHYA , 71101061H , , PICT , T80053133

01. CONTROL SYSTEMS	PP	100	40	79	P
02. DIGITAL COMMUNICATION	PP	100	40	40	P
03. DIGITAL COMMUNICATION	PR	50	20	44	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	51	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	43	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	67	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	44	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	58	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	46	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	44	P

FIRST TERM TOTAL = 516/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80053135	ABDULE MONALI MUKUND				SANDHYA				, 71045354L				T8053001		PICT			T80053135
01. CONTROL SYSTEMS	PP	100	40	48	P C	11. SIGNAL CODING & ESTIMATION THEORY	PP	100	40	49	P C							
02. DIGITAL COMMUNICATION	PP	100	40	49	P C	12. SIGNAL CODING & ESTIMATION THEORY	PR	50	20	37	P							
03. DIGITAL COMMUNICATION	PR	50	20	28	P C	13. SYSTEM PROGRA.& OPERATING SYS.	PP	100	40	57	P C							
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	53	P C	14. SYSTEM PROGRA.& OPERATING SYS.	TW	50	20	39	P C							
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	41	P C	15. COMPUTER ORGANIZATION & ARCHITEC	PP	100	40	54	P C							
06. MICROCONTROLLERS & APPLICATION	PP	100	40	47	P C	16. INDUSTRIAL MANAGEMENT	PP	100	40	51	P C							
07. MICROCONTROLLERS & APPLICATION	PR	50	20	32	P C	17. WAVE THEORY & ANTENNA	PP	100	40	47	P C							
08. DIGITAL SIGNAL PROCESSING	PP	100	40	42	P C	18. WAVE THEORY & ANTENNA	PR	50	20	25	P C							
09. DIGITAL SIGNAL PROCESSING	OR	50	20	30	P C	19. MINI PROJECT & SEMINAR	OR	50	20	38	P C							
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	25	P C	20. TEST & MEASUREMENT TECHNIQUES	OR	50	20	33	P C							

GRAND TOTAL = 825/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

T80053136	ABHISHEK ANAND				INDRANI				, 71045356G				T8053003		PICT			T80053136
01. CONTROL SYSTEMS	PP	100	40	63	P C	11. SIGNAL CODING & ESTIMATION THEORY	PP	100	40	55	P C							
02. DIGITAL COMMUNICATION	PP	100	40	40	P C	12. SIGNAL CODING & ESTIMATION THEORY	PR	50	20	28	P C							
03. DIGITAL COMMUNICATION	PR	50	20	33	P C	13. SYSTEM PROGRA.& OPERATING SYS.	PP	100	40	43	P C							
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	51	P C	14. SYSTEM PROGRA.& OPERATING SYS.	TW	50	20	39	P C							
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	36	P C	15. COMPUTER ORGANIZATION & ARCHITEC	PP	100	40	59	P C							
06. MICROCONTROLLERS & APPLICATION	PP	100	40	40	P C	16. INDUSTRIAL MANAGEMENT	PP	100	40	44	P C							
07. MICROCONTROLLERS & APPLICATION	PR	50	20	25	P C	17. WAVE THEORY & ANTENNA	PP	100	40	40	P C							
08. DIGITAL SIGNAL PROCESSING	PP	100	40	52	P C	18. WAVE THEORY & ANTENNA	PR	50	20	38	P							
09. DIGITAL SIGNAL PROCESSING	OR	50	20	37	P C	19. MINI PROJECT & SEMINAR	OR	50	20	38	P C							
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	25	P C	20. TEST & MEASUREMENT TECHNIQUES	OR	50	20	35	P C							

GRAND TOTAL = 821+04/1500, RESULT: HIGHER SECOND CLASS [0.2]

ORDN. 1 MARKS :

T80053137	BHAGAT ABHILASHA RAMDAS				MADHURI				, 71045386J				T8053013		PICT			T80053137
01. CONTROL SYSTEMS	PP	100	40	AA	F	11. SIGNAL CODING & ESTIMATION THEORY	PP	100	40	51	P C							
02. DIGITAL COMMUNICATION	PP	100	40	50	P C	12. SIGNAL CODING & ESTIMATION THEORY	PR	50	20	28	P C							
03. DIGITAL COMMUNICATION	PR	50	20	20	P C	13. SYSTEM PROGRA.& OPERATING SYS.	PP	100	40	58	P C							
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	48	P C	14. SYSTEM PROGRA.& OPERATING SYS.	TW	50	20	41	P C							
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	39	P C	15. COMPUTER ORGANIZATION & ARCHITEC	PP	100	40	51	P C							
06. MICROCONTROLLERS & APPLICATION	PP	100	40	41	P C	16. INDUSTRIAL MANAGEMENT	PP	100	40	61	P C							
07. MICROCONTROLLERS & APPLICATION	PR	50	20	30	P C	17. WAVE THEORY & ANTENNA	PP	100	40	49	P C							
08. DIGITAL SIGNAL PROCESSING	PP	100	40	56	P C	18. WAVE THEORY & ANTENNA	PR	50	20	30	P C							
09. DIGITAL SIGNAL PROCESSING	OR	50	20	32	P C	19. MINI PROJECT & SEMINAR	OR	50	20	32	P							
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	32	P C	20. TEST & MEASUREMENT TECHNIQUES	OR	50	20	38	P C							

GRAND TOTAL = 787/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80053138	BHATIA DHAWAL RAMESHCHAND				REENA				, 70801366L			T8053114		PICT		T80053138
01. CONTROL SYSTEMS	PP	100	40	49	P C	11. SIGNAL CODING & ESTIMATION THEORY	PP	100	40	32	F					
02. DIGITAL COMMUNICATION	PP	100	40	07	F	12. SIGNAL CODING & ESTIMATION THEORY	PR	50	20	22	P C					
03. DIGITAL COMMUNICATION	PR	50	20	40	P C	13. SYSTEM PROGRA.& OPERATING SYS.	PP	100	40	49	P C					
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	40	P C	14. SYSTEM PROGRA.& OPERATING SYS.	TW	50	20	38	P C					
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	39	P C	15. COMPUTER ORGANIZATION & ARCHITECTURE	PP	100	40	40	P C					
06. MICROCONTROLLERS & APPLICATION	PP	100	40	40	P C	16. INDUSTRIAL MANAGEMENT	PP	100	40	44	P C					
07. MICROCONTROLLERS & APPLICATION	PR	50	20	35	P C	17. WAVE THEORY & ANTENNA	PP	100	40	AA	F					
08. DIGITAL SIGNAL PROCESSING	PP	100	40	44	P C	18. WAVE THEORY & ANTENNA	PR	50	20	38	P C					
09. DIGITAL SIGNAL PROCESSING	OR	50	20	22	P C	19. MINI PROJECT & SEMINAR	OR	50	20	39	P C					
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	25	P C	20. TEST & MEASUREMENT TECHNIQUES	OR	50	20	39	P C					

GRAND TOTAL = 682/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

T80053139	BHOIR PRASAD SHIVAJI				USHA				, 70925368M			T8053115		PICT		T80053139
01. CONTROL SYSTEMS	PP	100	40	40	P C	11. SIGNAL CODING & ESTIMATION THEORY	PP	100	40	40	P C					
02. DIGITAL COMMUNICATION	PP	100	40	40	P C	12. SIGNAL CODING & ESTIMATION THEORY	PR	50	20	30	P C					
03. DIGITAL COMMUNICATION	PR	50	20	23	P C	13. SYSTEM PROGRA.& OPERATING SYS.	PP	100	40	40	P C					
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	40	P C	14. SYSTEM PROGRA.& OPERATING SYS.	TW	50	20	34	P C					
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	38	P C	15. COMPUTER ORGANIZATION & ARCHITECTURE	PP	100	40	21	F					
06. MICROCONTROLLERS & APPLICATION	PP	100	40	43	P C	16. INDUSTRIAL MANAGEMENT	PP	100	40	40	P C					
07. MICROCONTROLLERS & APPLICATION	PR	50	20	20	P C	17. WAVE THEORY & ANTENNA	PP	100	40	14	F					
08. DIGITAL SIGNAL PROCESSING	PP	100	40	40	P C	18. WAVE THEORY & ANTENNA	PR	50	20	35	P C					
09. DIGITAL SIGNAL PROCESSING	OR	50	20	26	P C	19. MINI PROJECT & SEMINAR	OR	50	20	32	P C					
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	21	P C	20. TEST & MEASUREMENT TECHNIQUES	OR	50	20	28	P C					

GRAND TOTAL = 645/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

T80053140	BHOSALE PRASHANT DNYANADEO				RANJANA				, 71045393M			T8053016		PICT		T80053140
01. CONTROL SYSTEMS	PP	100	40	59	P C	11. SIGNAL CODING & ESTIMATION THEORY	PP	100	40	66	P C					
02. DIGITAL COMMUNICATION	PP	100	40	45	P C	12. SIGNAL CODING & ESTIMATION THEORY	PR	50	20	30	P C					
03. DIGITAL COMMUNICATION	PR	50	20	32	P C	13. SYSTEM PROGRA.& OPERATING SYS.	PP	100	40	57	P C					
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	61	P C	14. SYSTEM PROGRA.& OPERATING SYS.	TW	50	20	37	P C					
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	32	P C	15. COMPUTER ORGANIZATION & ARCHITECTURE	PP	100	40	65	P C					
06. MICROCONTROLLERS & APPLICATION	PP	100	40	46	P C	16. INDUSTRIAL MANAGEMENT	PP	100	40	60	P C					
07. MICROCONTROLLERS & APPLICATION	PR	50	20	36	P C	17. WAVE THEORY & ANTENNA	PP	100	40	55	P C					
08. DIGITAL SIGNAL PROCESSING	PP	100	40	60	P C	18. WAVE THEORY & ANTENNA	PR	50	20	35	P					
09. DIGITAL SIGNAL PROCESSING	OR	50	20	22	P C	19. MINI PROJECT & SEMINAR	OR	50	20	22	P C					
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	26	P C	20. TEST & MEASUREMENT TECHNIQUES	OR	50	20	30	P C					

GRAND TOTAL = 876/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80053141	GADHE GANESH VITTHALRAO				KEVALBAI				, 71045427K				T8053025		PICT				T80053141
01. CONTROL SYSTEMS	PP	100	40	42	P C	11. SIGNAL CODING & ESTIMATION THEORY	PP	100	40	46	P C								
02. DIGITAL COMMUNICATION	PP	100	40	40	P C	12. SIGNAL CODING & ESTIMATION THEORY	PR	50	20	29	P C								
03. DIGITAL COMMUNICATION	PR	50	20	36	P C	13. SYSTEM PROGRA.& OPERATING SYS.	PP	100	40	51	P C								
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	47	P C	14. SYSTEM PROGRA.& OPERATING SYS.	TW	50	20	41	P C								
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	35	P C	15. COMPUTER ORGANIZATION & ARCHITECTURE	PP	100	40	55	P								
06. MICROCONTROLLERS & APPLICATION	PP	100	40	45	P C	16. INDUSTRIAL MANAGEMENT	PP	100	40	52	P C								
07. MICROCONTROLLERS & APPLICATION	PR	50	20	38	P C	17. WAVE THEORY & ANTENNA	PP	100	40	43	P								
08. DIGITAL SIGNAL PROCESSING	PP	100	40	57	P C	18. WAVE THEORY & ANTENNA	PR	50	20	29	P C								
09. DIGITAL SIGNAL PROCESSING	OR	50	20	35	P C	19. MINI PROJECT & SEMINAR	OR	50	20	34	P C								
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	33	P C	20. TEST & MEASUREMENT TECHNIQUES	OR	50	20	32	P C								

GRAND TOTAL = 820+05/1500, RESULT: HIGHER SECOND CLASS [0.2]

ORDN. 1 MARKS :

T80053142	GAVNEKAR SHREYAS SANJIV				NILIMA				, 71045435L				T8053026		PICT				T80053142
01. CONTROL SYSTEMS	PP	100	40	44	P C	11. SIGNAL CODING & ESTIMATION THEORY	PP	100	40	47	P C								
02. DIGITAL COMMUNICATION	PP	100	40	43	P C	12. SIGNAL CODING & ESTIMATION THEORY	PR	50	20	34	P								
03. DIGITAL COMMUNICATION	PR	50	20	31	P C	13. SYSTEM PROGRA.& OPERATING SYS.	PP	100	40	61	P C								
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	56	P C	14. SYSTEM PROGRA.& OPERATING SYS.	TW	50	20	40	P C								
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	31	P C	15. COMPUTER ORGANIZATION & ARCHITECTURE	PP	100	40	48	P C								
06. MICROCONTROLLERS & APPLICATION	PP	100	40	47	P C	16. INDUSTRIAL MANAGEMENT	PP	100	40	57	P C								
07. MICROCONTROLLERS & APPLICATION	PR	50	20	30	P C	17. WAVE THEORY & ANTENNA	PP	100	40	40	P								
08. DIGITAL SIGNAL PROCESSING	PP	100	40	57	P C	18. WAVE THEORY & ANTENNA	PR	50	20	23	P C								
09. DIGITAL SIGNAL PROCESSING	OR	50	20	30	P C	19. MINI PROJECT & SEMINAR	OR	50	20	30	P C								
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	27	P C	20. TEST & MEASUREMENT TECHNIQUES	OR	50	20	30	P C								

GRAND TOTAL = 806/1500, RESULT: SECOND CLASS

ORDN. 1 MARKS :

T80053143	GHONE PRASAD SHARAD				HEMLATA				, 71045437G				T8053028		PICT				T80053143
01. CONTROL SYSTEMS	PP	100	40	64	P C	11. SIGNAL CODING & ESTIMATION THEORY	PP	100	40	56	P C								
02. DIGITAL COMMUNICATION	PP	100	40	40	P C	12. SIGNAL CODING & ESTIMATION THEORY	PR	50	20	31	P								
03. DIGITAL COMMUNICATION	PR	50	20	29	P C	13. SYSTEM PROGRA.& OPERATING SYS.	PP	100	40	53	P C								
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	52	P C	14. SYSTEM PROGRA.& OPERATING SYS.	TW	50	20	42	P C								
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	37	P C	15. COMPUTER ORGANIZATION & ARCHITECTURE	PP	100	40	52	P C								
06. MICROCONTROLLERS & APPLICATION	PP	100	40	40	P C	16. INDUSTRIAL MANAGEMENT	PP	100	40	58	P C								
07. MICROCONTROLLERS & APPLICATION	PR	50	20	35	P C	17. WAVE THEORY & ANTENNA	PP	100	40	40	P C								
08. DIGITAL SIGNAL PROCESSING	PP	100	40	52	P C	18. WAVE THEORY & ANTENNA	PR	50	20	34	P C								
09. DIGITAL SIGNAL PROCESSING	OR	50	20	27	P C	19. MINI PROJECT & SEMINAR	OR	50	20	44	P C								
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	23	P C	20. TEST & MEASUREMENT TECHNIQUES	OR	50	20	28	P C								

GRAND TOTAL = 837/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80053144	GUJAR AKASH SANJAY				ROHINI				, 71045438E				T8053029		PICT			T80053144
01. CONTROL SYSTEMS	PP	100	40	45	P C	11. SIGNAL CODING & ESTIMATION THEORY	PP	100	40	64	P C							
02. DIGITAL COMMUNICATION	PP	100	40	41	P C	12. SIGNAL CODING & ESTIMATION THEORY	PR	50	20	34	P							
03. DIGITAL COMMUNICATION	PR	50	20	27	P C	13. SYSTEM PROGRA.& OPERATING SYS.	PP	100	40	53	P							
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	50	P C	14. SYSTEM PROGRA.& OPERATING SYS.	TW	50	20	39	P C							
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	32	P C	15. COMPUTER ORGANIZATION & ARCHITEC	PP	100	40	54	P C							
06. MICROCONTROLLERS & APPLICATION	PP	100	40	48	P C	16. INDUSTRIAL MANAGEMENT	PP	100	40	41	P C							
07. MICROCONTROLLERS & APPLICATION	PR	50	20	36	P C	17. WAVE THEORY & ANTENNA	PP	100	40	40	P C							
08. DIGITAL SIGNAL PROCESSING	PP	100	40	52	P C	18. WAVE THEORY & ANTENNA	PR	50	20	30	P C							
09. DIGITAL SIGNAL PROCESSING	OR	50	20	20	P C	19. MINI PROJECT & SEMINAR	OR	50	20	38	P C							
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	30	P C	20. TEST & MEASUREMENT TECHNIQUES	OR	50	20	31	P C							

GRAND TOTAL = 805/1500, RESULT: SECOND CLASS

ORDN. 1 MARKS :

T80053145	INGLE CHETAN VASUDEO				SHALINI				, 71129930H				T8053035		PICT			T80053145
01. CONTROL SYSTEMS	PP	100	40	64	P C	11. SIGNAL CODING & ESTIMATION THEORY	PP	100	40	48	P C							
02. DIGITAL COMMUNICATION	PP	100	40	48	P C	12. SIGNAL CODING & ESTIMATION THEORY	PR	50	20	28	P C							
03. DIGITAL COMMUNICATION	PR	50	20	37	P C	13. SYSTEM PROGRA.& OPERATING SYS.	PP	100	40	51	P C							
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	53	P C	14. SYSTEM PROGRA.& OPERATING SYS.	TW	50	20	40	P C							
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	42	P C	15. COMPUTER ORGANIZATION & ARCHITEC	PP	100	40	69	P C							
06. MICROCONTROLLERS & APPLICATION	PP	100	40	56	P C	16. INDUSTRIAL MANAGEMENT	PP	100	40	51	P C							
07. MICROCONTROLLERS & APPLICATION	PR	50	20	34	P C	17. WAVE THEORY & ANTENNA	PP	100	40	40	P C							
08. DIGITAL SIGNAL PROCESSING	PP	100	40	52	P C	18. WAVE THEORY & ANTENNA	PR	50	20	36	P							
09. DIGITAL SIGNAL PROCESSING	OR	50	20	37	P C	19. MINI PROJECT & SEMINAR	OR	50	20	41	P C							
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	24	P C	20. TEST & MEASUREMENT TECHNIQUES	OR	50	20	35	P C							

GRAND TOTAL = 886/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

T80053146	JADHAV OMKAR FULCHAND				JYOTSNA				, 70701463J				T8053037		PICT			T80053146
01. CONTROL SYSTEMS	PP	100	40	45	P C	11. SIGNAL CODING & ESTIMATION THEORY	PP	100	40	42	P C							
02. DIGITAL COMMUNICATION	PP	100	40	41	P C	12. SIGNAL CODING & ESTIMATION THEORY	PR	50	20	AA	F							
03. DIGITAL COMMUNICATION	PR	50	20	29	P C	13. SYSTEM PROGRA.& OPERATING SYS.	PP	100	40	40	P C							
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	53	P C	14. SYSTEM PROGRA.& OPERATING SYS.	TW	50	20	32	P C							
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	27	P C	15. COMPUTER ORGANIZATION & ARCHITEC	PP	100	40	49	P							
06. MICROCONTROLLERS & APPLICATION	PP	100	40	48	P C	16. INDUSTRIAL MANAGEMENT	PP	100	40	41	P C							
07. MICROCONTROLLERS & APPLICATION	PR	50	20	22	P C	17. WAVE THEORY & ANTENNA	PP	100	40	48	P C							
08. DIGITAL SIGNAL PROCESSING	PP	100	40	40	P C	18. WAVE THEORY & ANTENNA	PR	50	20	15	F							
09. DIGITAL SIGNAL PROCESSING	OR	50	20	36	P C	19. MINI PROJECT & SEMINAR	OR	50	20	30	P C							
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	25	P C	20. TEST & MEASUREMENT TECHNIQUES	OR	50	20	AA	F							

GRAND TOTAL = 663/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80053147	KULKARNI HARSHAD RAVINDRA				SADHANA					, 71045495D		T8053047		PICT		T80053147
01. CONTROL SYSTEMS	PP	100	40	47	P C	11. SIGNAL CODING & ESTIMATION THEORY	PP	100	40	69	P C					
02. DIGITAL COMMUNICATION	PP	100	40	47	P C	12. SIGNAL CODING & ESTIMATION THEORY	PR	50	20	40	P					
03. DIGITAL COMMUNICATION	PR	50	20	34	P C	13. SYSTEM PROGRA.& OPERATING SYS.	PP	100	40	66	P C					
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	54	P C	14. SYSTEM PROGRA.& OPERATING SYS.	TW	50	20	39	P C					
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	33	P C	15. COMPUTER ORGANIZATION & ARCHITECTURE	PP	100	40	61	P C					
06. MICROCONTROLLERS & APPLICATION	PP	100	40	59	P C	16. INDUSTRIAL MANAGEMENT	PP	100	40	58	P C					
07. MICROCONTROLLERS & APPLICATION	PR	50	20	30	P C	17. WAVE THEORY & ANTENNA	PP	100	40	52	P C					
08. DIGITAL SIGNAL PROCESSING	PP	100	40	70	P C	18. WAVE THEORY & ANTENNA	PR	50	20	37	P C					
09. DIGITAL SIGNAL PROCESSING	OR	50	20	35	P C	19. MINI PROJECT & SEMINAR	OR	50	20	46	P C					
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	32	P C	20. TEST & MEASUREMENT TECHNIQUES	OR	50	20	40	P C					

GRAND TOTAL = 949/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

T80053148	KULKARNI SAURABH CHANDRAKANT				SWARALI					, 71045497L		T8053048		PICT		T80053148
01. CONTROL SYSTEMS	PP	100	40	57	P C	11. SIGNAL CODING & ESTIMATION THEORY	PP	100	40	65	P C					
02. DIGITAL COMMUNICATION	PP	100	40	48	P C	12. SIGNAL CODING & ESTIMATION THEORY	PR	50	20	28	P C					
03. DIGITAL COMMUNICATION	PR	50	20	30	P C	13. SYSTEM PROGRA.& OPERATING SYS.	PP	100	40	71	P C					
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	65	P C	14. SYSTEM PROGRA.& OPERATING SYS.	TW	50	20	44	P C					
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	39	P C	15. COMPUTER ORGANIZATION & ARCHITECTURE	PP	100	40	62	P C					
06. MICROCONTROLLERS & APPLICATION	PP	100	40	56	P C	16. INDUSTRIAL MANAGEMENT	PP	100	40	56	P C					
07. MICROCONTROLLERS & APPLICATION	PR	50	20	32	P C	17. WAVE THEORY & ANTENNA	PP	100	40	61	P C					
08. DIGITAL SIGNAL PROCESSING	PP	100	40	65	P C	18. WAVE THEORY & ANTENNA	PR	50	20	33	P C					
09. DIGITAL SIGNAL PROCESSING	OR	50	20	35	P C	19. MINI PROJECT & SEMINAR	OR	50	20	40	P C					
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	20	P C	20. TEST & MEASUREMENT TECHNIQUES	OR	50	20	35	P					

GRAND TOTAL = 942/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

T80053149	MAHESHWARI SNEHAL RAVINDRA				AARTI					, 71045512H		T8053057		PICT		T80053149
01. CONTROL SYSTEMS	PP	100	40	40	P C	11. SIGNAL CODING & ESTIMATION THEORY	PP	100	40	46	P C					
02. DIGITAL COMMUNICATION	PP	100	40	48	P C	12. SIGNAL CODING & ESTIMATION THEORY	PR	50	20	26	P					
03. DIGITAL COMMUNICATION	PR	50	20	38	P C	13. SYSTEM PROGRA.& OPERATING SYS.	PP	100	40	40	P C					
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	45	P C	14. SYSTEM PROGRA.& OPERATING SYS.	TW	50	20	39	P C					
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	38	P C	15. COMPUTER ORGANIZATION & ARCHITECTURE	PP	100	40	40	P C					
06. MICROCONTROLLERS & APPLICATION	PP	100	40	49	P C	16. INDUSTRIAL MANAGEMENT	PP	100	40	30#	P					
07. MICROCONTROLLERS & APPLICATION	PR	50	20	34	P C	17. WAVE THEORY & ANTENNA	PP	100	40	40	P C					
08. DIGITAL SIGNAL PROCESSING	PP	100	40	41	P C	18. WAVE THEORY & ANTENNA	PR	50	20	33	P C					
09. DIGITAL SIGNAL PROCESSING	OR	50	20	32	P C	19. MINI PROJECT & SEMINAR	OR	50	20	39	P C					
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	28	P C	20. TEST & MEASUREMENT TECHNIQUES	OR	50	20	26	P C					

GRAND TOTAL = 752/1500, RESULT: SECOND CLASS # [0.4]

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80053150	MANE GAURANGI LALASAHEB				KALPANA				, 71045518G			T8053060		PICT		T80053150
01. CONTROL SYSTEMS	PP	100	40	40	P C	11. SIGNAL CODING & ESTIMATION THEORY	PP	100	40	52	P C					
02. DIGITAL COMMUNICATION	PP	100	40	54	P C	12. SIGNAL CODING & ESTIMATION THEORY	PR	50	20	37	P					
03. DIGITAL COMMUNICATION	PR	50	20	30	P C	13. SYSTEM PROGRA.& OPERATING SYS.	PP	100	40	44	P C					
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	44	P C	14. SYSTEM PROGRA.& OPERATING SYS.	TW	50	20	41	P C					
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	42	P C	15. COMPUTER ORGANIZATION & ARCHITEC	PP	100	40	43	P C					
06. MICROCONTROLLERS & APPLICATION	PP	100	40	57	P C	16. INDUSTRIAL MANAGEMENT	PP	100	40	47	P C					
07. MICROCONTROLLERS & APPLICATION	PR	50	20	39	P C	17. WAVE THEORY & ANTENNA	PP	100	40	41	P C					
08. DIGITAL SIGNAL PROCESSING	PP	100	40	52	P C	18. WAVE THEORY & ANTENNA	PR	50	20	29	P C					
09. DIGITAL SIGNAL PROCESSING	OR	50	20	32	P C	19. MINI PROJECT & SEMINAR	OR	50	20	38	P C					
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	30	P C	20. TEST & MEASUREMENT TECHNIQUES	OR	50	20	35	P C					

GRAND TOTAL = 827/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

T80053151	MORE NISHANT PRAVIN				SHARADA				, 70925509J			T8053065		PICT		T80053151
01. CONTROL SYSTEMS	PP	100	40	56	P	11. SIGNAL CODING & ESTIMATION THEORY	PP	100	40	48	P C					
02. DIGITAL COMMUNICATION	PP	100	40	29	F	12. SIGNAL CODING & ESTIMATION THEORY	PR	50	20	30	P C					
03. DIGITAL COMMUNICATION	PR	50	20	30	P C	13. SYSTEM PROGRA.& OPERATING SYS.	PP	100	40	49	P C					
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	43	P C	14. SYSTEM PROGRA.& OPERATING SYS.	TW	50	20	39	P C					
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	37	P C	15. COMPUTER ORGANIZATION & ARCHITEC	PP	100	40	32	F					
06. MICROCONTROLLERS & APPLICATION	PP	100	40	43	P C	16. INDUSTRIAL MANAGEMENT	PP	100	40	45	P					
07. MICROCONTROLLERS & APPLICATION	PR	50	20	37	P C	17. WAVE THEORY & ANTENNA	PP	100	40	40	P C					
08. DIGITAL SIGNAL PROCESSING	PP	100	40	50	P C	18. WAVE THEORY & ANTENNA	PR	50	20	30	P C					
09. DIGITAL SIGNAL PROCESSING	OR	50	20	20	P C	19. MINI PROJECT & SEMINAR	OR	50	20	22	P					
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	33	P C	20. TEST & MEASUREMENT TECHNIQUES	OR	50	20	28	P C					

GRAND TOTAL = 741/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

T80053152	MRIDUL NAGAR				MANJU				, 71045528D			T8053066		PICT		T80053152
01. CONTROL SYSTEMS	PP	100	40	40	P C	11. SIGNAL CODING & ESTIMATION THEORY	PP	100	40	57	P C					
02. DIGITAL COMMUNICATION	PP	100	40	51	P C	12. SIGNAL CODING & ESTIMATION THEORY	PR	50	20	35	P					
03. DIGITAL COMMUNICATION	PR	50	20	38	P C	13. SYSTEM PROGRA.& OPERATING SYS.	PP	100	40	66	P C					
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	54	P C	14. SYSTEM PROGRA.& OPERATING SYS.	TW	50	20	34	P C					
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	32	P C	15. COMPUTER ORGANIZATION & ARCHITEC	PP	100	40	63	P C					
06. MICROCONTROLLERS & APPLICATION	PP	100	40	63	P C	16. INDUSTRIAL MANAGEMENT	PP	100	40	64	P C					
07. MICROCONTROLLERS & APPLICATION	PR	50	20	30	P C	17. WAVE THEORY & ANTENNA	PP	100	40	56	P C					
08. DIGITAL SIGNAL PROCESSING	PP	100	40	59	P C	18. WAVE THEORY & ANTENNA	PR	50	20	32	P					
09. DIGITAL SIGNAL PROCESSING	OR	50	20	36	P C	19. MINI PROJECT & SEMINAR	OR	50	20	38	P C					
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	28	P C	20. TEST & MEASUREMENT TECHNIQUES	OR	50	20	30	P C					

GRAND TOTAL = 906/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80053153	NEVASE PRAJAKTA MARUTI				NANDA				, 71129934L				, T8053072		PICT			, T80053153
01. CONTROL SYSTEMS	PP	100	40	61	P C	11. SIGNAL CODING & ESTIMATION THEORY	PP	100	40	67	P C							
02. DIGITAL COMMUNICATION	PP	100	40	53	P C	12. SIGNAL CODING & ESTIMATION THEORY	PR	50	20	32	P C							
03. DIGITAL COMMUNICATION	PR	50	20	26	P C	13. SYSTEM PROGRA.& OPERATING SYS.	PP	100	40	68	P C							
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	40	P C	14. SYSTEM PROGRA.& OPERATING SYS.	TW	50	20	40	P C							
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	41	P C	15. COMPUTER ORGANIZATION & ARCHITEC	PP	100	40	67	P C							
06. MICROCONTROLLERS & APPLICATION	PP	100	40	50	P C	16. INDUSTRIAL MANAGEMENT	PP	100	40	66	P C							
07. MICROCONTROLLERS & APPLICATION	PR	50	20	32	P C	17. WAVE THEORY & ANTENNA	PP	100	40	64	P C							
08. DIGITAL SIGNAL PROCESSING	PP	100	40	60	P C	18. WAVE THEORY & ANTENNA	PR	50	20	33	P C							
09. DIGITAL SIGNAL PROCESSING	OR	50	20	30	P C	19. MINI PROJECT & SEMINAR	OR	50	20	41	P C							
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	35	P C	20. TEST & MEASUREMENT TECHNIQUES	OR	50	20	30	P							

GRAND TOTAL = 936/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

T80053154	NITESH KR SINGH				RITA				, 70801532J				, T8053125		PICT			, T80053154
01. CONTROL SYSTEMS	PP	100	40	50	P C	11. SIGNAL CODING & ESTIMATION THEORY	PP	100	40	32	F							
02. DIGITAL COMMUNICATION	PP	100	40	AA	F	12. SIGNAL CODING & ESTIMATION THEORY	PR	50	20	22	P							
03. DIGITAL COMMUNICATION	PR	50	20	23	P C	13. SYSTEM PROGRA.& OPERATING SYS.	PP	100	40	AA	F							
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	44	P C	14. SYSTEM PROGRA.& OPERATING SYS.	TW	50	20	26	P C							
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	22	P C	15. COMPUTER ORGANIZATION & ARCHITEC	PP	100	40	46	P C							
06. MICROCONTROLLERS & APPLICATION	PP	100	40	44	P C	16. INDUSTRIAL MANAGEMENT	PP	100	40	49	P C							
07. MICROCONTROLLERS & APPLICATION	PR	50	20	28	P C	17. WAVE THEORY & ANTENNA	PP	100	40	AA	F							
08. DIGITAL SIGNAL PROCESSING	PP	100	40	40	P C	18. WAVE THEORY & ANTENNA	PR	50	20	25	P C							
09. DIGITAL SIGNAL PROCESSING	OR	50	20	26	P C	19. MINI PROJECT & SEMINAR	OR	50	20	20	P							
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	28	P C	20. TEST & MEASUREMENT TECHNIQUES	OR	50	20	30	P C							

GRAND TOTAL = 555/1500, RESULT: FAILS

ORDN. 1 MARKS :

T80053155	PAWAR PRADIPKUMAR RAOSAHEB				SANGITA				, 71045572M				, T8053081		PICT			, T80053155
01. CONTROL SYSTEMS	PP	100	40	59	P C	11. SIGNAL CODING & ESTIMATION THEORY	PP	100	40	57	P C							
02. DIGITAL COMMUNICATION	PP	100	40	58	P C	12. SIGNAL CODING & ESTIMATION THEORY	PR	50	20	36	P C							
03. DIGITAL COMMUNICATION	PR	50	20	32	P C	13. SYSTEM PROGRA.& OPERATING SYS.	PP	100	40	59	P C							
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	65	P C	14. SYSTEM PROGRA.& OPERATING SYS.	TW	50	20	41	P C							
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	36	P C	15. COMPUTER ORGANIZATION & ARCHITEC	PP	100	40	58	P							
06. MICROCONTROLLERS & APPLICATION	PP	100	40	40	P C	16. INDUSTRIAL MANAGEMENT	PP	100	40	58	P C							
07. MICROCONTROLLERS & APPLICATION	PR	50	20	28	P C	17. WAVE THEORY & ANTENNA	PP	100	40	57	P C							
08. DIGITAL SIGNAL PROCESSING	PP	100	40	55	P C	18. WAVE THEORY & ANTENNA	PR	50	20	35	P C							
09. DIGITAL SIGNAL PROCESSING	OR	50	20	38	P C	19. MINI PROJECT & SEMINAR	OR	50	20	35	P C							
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	33	P C	20. TEST & MEASUREMENT TECHNIQUES	OR	50	20	40	P C							

GRAND TOTAL = 920/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80053156	PAWAR SURAJ SUBHASH				VIDYA				, 71045573K			T8053082		PICT		T80053156
01. CONTROL SYSTEMS	PP	100	40	46	P C	11. SIGNAL CODING & ESTIMATION THEORY	PP	100	40	44	P C					
02. DIGITAL COMMUNICATION	PP	100	40	40	P C	12. SIGNAL CODING & ESTIMATION THEORY	PR	50	20	35	P C					
03. DIGITAL COMMUNICATION	PR	50	20	20	P	13. SYSTEM PROGRA.& OPERATING SYS.	PP	100	40	40	P C					
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	43	P C	14. SYSTEM PROGRA.& OPERATING SYS.	TW	50	20	36	P C					
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	31	P C	15. COMPUTER ORGANIZATION & ARCHITECTURE	PP	100	40	40	P C					
06. MICROCONTROLLERS & APPLICATION	PP	100	40	40	P	16. INDUSTRIAL MANAGEMENT	PP	100	40	45	P C					
07. MICROCONTROLLERS & APPLICATION	PR	50	20	28	P C	17. WAVE THEORY & ANTENNA	PP	100	40	43	P					
08. DIGITAL SIGNAL PROCESSING	PP	100	40	46	P C	18. WAVE THEORY & ANTENNA	PR	50	20	34	P					
09. DIGITAL SIGNAL PROCESSING	OR	50	20	20	P C	19. MINI PROJECT & SEMINAR	OR	50	20	41	P C					
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	27	P C	20. TEST & MEASUREMENT TECHNIQUES	OR	50	20	33	P					

GRAND TOTAL = 732/1500, RESULT: PASS CLASS

ORDN. 1 MARKS :

T80053157	PAWAR VISHAL ALIAS DIGVIJAY BALASAHEB				VIJAYANTI				, 71070203F			T8053083		PICT		T80053157
01. CONTROL SYSTEMS	PP	100	40	55	P C	11. SIGNAL CODING & ESTIMATION THEORY	PP	100	40	AA	F					
02. DIGITAL COMMUNICATION	PP	100	40	AA	F	12. SIGNAL CODING & ESTIMATION THEORY	PR	50	20	AA	F					
03. DIGITAL COMMUNICATION	PR	50	20	44	P C	13. SYSTEM PROGRA.& OPERATING SYS.	PP	100	40	AA	F					
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	AA	F	14. SYSTEM PROGRA.& OPERATING SYS.	TW	50	20	AA	F					
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	27	P C	15. COMPUTER ORGANIZATION & ARCHITECTURE	PP	100	40	AA	F					
06. MICROCONTROLLERS & APPLICATION	PP	100	40	40	P C	16. INDUSTRIAL MANAGEMENT	PP	100	40	AA	F					
07. MICROCONTROLLERS & APPLICATION	PR	50	20	30	P C	17. WAVE THEORY & ANTENNA	PP	100	40	AA	F					
08. DIGITAL SIGNAL PROCESSING	PP	100	40	AA	F	18. WAVE THEORY & ANTENNA	PR	50	20	AA	F					
09. DIGITAL SIGNAL PROCESSING	OR	50	20	20	P C	19. MINI PROJECT & SEMINAR	OR	50	20	AA	F					
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	21	P C	20. TEST & MEASUREMENT TECHNIQUES	OR	50	20	AA	F					

GRAND TOTAL = 237/1500, RESULT: FAILS

RESULT RESERVED FOR BKL

ORDN. 1 MARKS :

T80053158	PRASHANT POPAT JAGTAP				MUKTA				, 70801568K			T8053128		PICT		T80053158
01. CONTROL SYSTEMS	PP	100	40	46	P C	11. SIGNAL CODING & ESTIMATION THEORY	PP	100	40	40	P C					
02. DIGITAL COMMUNICATION	PP	100	40	48	P C	12. SIGNAL CODING & ESTIMATION THEORY	PR	50	20	24	P C					
03. DIGITAL COMMUNICATION	PR	50	20	22	P C	13. SYSTEM PROGRA.& OPERATING SYS.	PP	100	40	40	P C					
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	47	P C	14. SYSTEM PROGRA.& OPERATING SYS.	TW	50	20	26	P C					
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	20	P C	15. COMPUTER ORGANIZATION & ARCHITECTURE	PP	100	40	40	P C					
06. MICROCONTROLLERS & APPLICATION	PP	100	40	41	P C	16. INDUSTRIAL MANAGEMENT	PP	100	40	51	P C					
07. MICROCONTROLLERS & APPLICATION	PR	50	20	25	P C	17. WAVE THEORY & ANTENNA	PP	100	40	40	P C					
08. DIGITAL SIGNAL PROCESSING	PP	100	40	17	F	18. WAVE THEORY & ANTENNA	PR	50	20	23	P C					
09. DIGITAL SIGNAL PROCESSING	OR	50	20	21	P C	19. MINI PROJECT & SEMINAR	OR	50	20	20	P					
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	21	P C	20. TEST & MEASUREMENT TECHNIQUES	OR	50	20	30	P C					

GRAND TOTAL = 642/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80053159	SHENDKAR SUSHMA HANUMANT				REKHA				, 71129939M			T8053093		PICT		T80053159
01. CONTROL SYSTEMS	PP	100	40	47	P C	11. SIGNAL CODING & ESTIMATION THEORY	PP	100	40	40	P C					
02. DIGITAL COMMUNICATION	PP	100	40	49	P C	12. SIGNAL CODING & ESTIMATION THEORY	PR	50	20	32	P C					
03. DIGITAL COMMUNICATION	PR	50	20	24	P C	13. SYSTEM PROGRA.& OPERATING SYS.	PP	100	40	44	P C					
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	40	P C	14. SYSTEM PROGRA.& OPERATING SYS.	TW	50	20	42	P C					
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	45	P C	15. COMPUTER ORGANIZATION & ARCHITEC	PP	100	40	43	P C					
06. MICROCONTROLLERS & APPLICATION	PP	100	40	43	P C	16. INDUSTRIAL MANAGEMENT	PP	100	40	42	P C					
07. MICROCONTROLLERS & APPLICATION	PR	50	20	30	P C	17. WAVE THEORY & ANTENNA	PP	100	40	29	F					
08. DIGITAL SIGNAL PROCESSING	PP	100	40	57	P C	18. WAVE THEORY & ANTENNA	PR	50	20	22	P C					
09. DIGITAL SIGNAL PROCESSING	OR	50	20	30	P C	19. MINI PROJECT & SEMINAR	OR	50	20	45	P C					
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	27	P C	20. TEST & MEASUREMENT TECHNIQUES	OR	50	20	31	P C					

GRAND TOTAL = 762/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

T80053160	SONU SINGH				USHA				, 71045633G			T8053097		PICT		T80053160
01. CONTROL SYSTEMS	PP	100	40	29	F											
02. DIGITAL COMMUNICATION	PP	100	40	21	F											
03. DIGITAL COMMUNICATION	PR	50	20	07	F											
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	40	P											
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	31	P C											
06. MICROCONTROLLERS & APPLICATION	PP	100	40	40	P											
07. MICROCONTROLLERS & APPLICATION	PR	50	20	11	F											
08. DIGITAL SIGNAL PROCESSING	PP	100	40	31	F											
09. DIGITAL SIGNAL PROCESSING	OR	50	20	35	P											
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	14	F											

FIRST TERM TOTAL = 259/750.

ORDN. 1 MARKS :

T80053161	SUBAGHYA MAHAJAN				VIJAY				, 71045635C			T8053098		PICT		T80053161
01. CONTROL SYSTEMS	PP	100	40	45	P C	11. SIGNAL CODING & ESTIMATION THEORY	PP	100	40	40	P C					
02. DIGITAL COMMUNICATION	PP	100	40	41	P	12. SIGNAL CODING & ESTIMATION THEORY	PR	50	20	34	P					
03. DIGITAL COMMUNICATION	PR	50	20	30	P C	13. SYSTEM PROGRA.& OPERATING SYS.	PP	100	40	46	P C					
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	40	P C	14. SYSTEM PROGRA.& OPERATING SYS.	TW	50	20	38	P C					
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	26	P C	15. COMPUTER ORGANIZATION & ARCHITEC	PP	100	40	44	P C					
06. MICROCONTROLLERS & APPLICATION	PP	100	40	55	P C	16. INDUSTRIAL MANAGEMENT	PP	100	40	40	P C					
07. MICROCONTROLLERS & APPLICATION	PR	50	20	32	P C	17. WAVE THEORY & ANTENNA	PP	100	40	42	P C					
08. DIGITAL SIGNAL PROCESSING	PP	100	40	49	P C	18. WAVE THEORY & ANTENNA	PR	50	20	21	P C					
09. DIGITAL SIGNAL PROCESSING	OR	50	20	33	P C	19. MINI PROJECT & SEMINAR	OR	50	20	42	P C					
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	22	P C	20. TEST & MEASUREMENT TECHNIQUES	OR	50	20	35	P C					

GRAND TOTAL = 755/1500, RESULT: SECOND CLASS

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80053162	THORAT TANVI TATYASAHEB				SULOCHANA			, 71045650G		T8053102		PICT		T80053162
01. CONTROL SYSTEMS	PP	100	40	40	P C	11. SIGNAL CODING & ESTIMATION THEORY	PP	100	40	56	P C			
02. DIGITAL COMMUNICATION	PP	100	40	45	P C	12. SIGNAL CODING & ESTIMATION THEORY	PR	50	20	22	P C			
03. DIGITAL COMMUNICATION	PR	50	20	39	P C	13. SYSTEM PROGRA.& OPERATING SYS.	PP	100	40	52	P C			
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	45	P C	14. SYSTEM PROGRA.& OPERATING SYS.	TW	50	20	43	P C			
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	37	P C	15. COMPUTER ORGANIZATION & ARCHITECTURE	PP	100	40	44	P C			
06. MICROCONTROLLERS & APPLICATION	PP	100	40	40	P C	16. INDUSTRIAL MANAGEMENT	PP	100	40	56	P C			
07. MICROCONTROLLERS & APPLICATION	PR	50	20	23	P C	17. WAVE THEORY & ANTENNA	PP	100	40	40	P C			
08. DIGITAL SIGNAL PROCESSING	PP	100	40	45	P C	18. WAVE THEORY & ANTENNA	PR	50	20	28	P			
09. DIGITAL SIGNAL PROCESSING	OR	50	20	38	P C	19. MINI PROJECT & SEMINAR	OR	50	20	40	P C			
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	32	P C	20. TEST & MEASUREMENT TECHNIQUES	OR	50	20	35	P C			

GRAND TOTAL = 800/1500, RESULT: SECOND CLASS

ORDN. 1 MARKS :

T80053163	WATHORE SANKET UTTAMRAO				KAVITA			, 71045665E		T8053106		PICT		T80053163
01. CONTROL SYSTEMS	PP	100	40	40	P C	11. SIGNAL CODING & ESTIMATION THEORY	PP	100	40	41	P C			
02. DIGITAL COMMUNICATION	PP	100	40	32#	P	12. SIGNAL CODING & ESTIMATION THEORY	PR	50	20	34	P C			
03. DIGITAL COMMUNICATION	PR	50	20	29	P C	13. SYSTEM PROGRA.& OPERATING SYS.	PP	100	40	52	P C			
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	47	P C	14. SYSTEM PROGRA.& OPERATING SYS.	TW	50	20	40	P C			
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	32	P C	15. COMPUTER ORGANIZATION & ARCHITECTURE	PP	100	40	40	P C			
06. MICROCONTROLLERS & APPLICATION	PP	100	40	41	P C	16. INDUSTRIAL MANAGEMENT	PP	100	40	47	P C			
07. MICROCONTROLLERS & APPLICATION	PR	50	20	26	P C	17. WAVE THEORY & ANTENNA	PP	100	40	40	P C			
08. DIGITAL SIGNAL PROCESSING	PP	100	40	43	P C	18. WAVE THEORY & ANTENNA	PR	50	20	37	P C			
09. DIGITAL SIGNAL PROCESSING	OR	50	20	26	P C	19. MINI PROJECT & SEMINAR	OR	50	20	22	P			
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	25	P C	20. TEST & MEASUREMENT TECHNIQUES	OR	50	20	33	P C			

GRAND TOTAL = 727/1500, RESULT: PASS CLASS # [0.4]

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80054201 AAMIR AHMED ABOOBAKAR SADIQUE YASMEEN , 71100710B , , PICT , T80054201

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	29	F
02. DATA COMMUNICATION	PP	100	40	61	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	48	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	42	P
05. THEORY OF COMPUTATION	PP	100	40	47	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	34	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	37	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	18	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	21	P
10. HARDWARE LABORATORY	TW	25	10	19	P
11. HARDWARE LABORATORY	PR	50	20	10	F

FIRST TERM TOTAL = 366/750.

ORDN. 1 MARKS :

T80054202 ABNAVE OMKAR RAJENDRA SHOBHA , 71045358C , , PICT , T80054202

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	51	P
02. DATA COMMUNICATION	PP	100	40	AA	F
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	AA	F
04. DIGITAL SIGNAL PROCESSING	PP	100	40	AA	F
05. THEORY OF COMPUTATION	PP	100	40	AA	F
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	21	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	AA	F
08. SIGNAL PROCESSING LABORATORY	TW	25	10	10	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	09	F
10. HARDWARE LABORATORY	TW	25	10	15	P
11. HARDWARE LABORATORY	PR	50	20	AA	F

FIRST TERM TOTAL = 106/750.

ORDN. 1 MARKS :

T80054203 AMRUTKAR SNEHAL RAJENDRA JYOTI , 71100728E , , PICT , T80054203

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	52	P
02. DATA COMMUNICATION	PP	100	40	74	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	64	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	65	P
05. THEORY OF COMPUTATION	PP	100	40	51	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	43	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	44	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	22	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	30	P
10. HARDWARE LABORATORY	TW	25	10	23	P
11. HARDWARE LABORATORY	PR	50	20	38	P

FIRST TERM TOTAL = 506/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80054204 AVHAD PRIYA SANTOSH MADHURI , 71100735H , , PICT , T80054204

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	60	P
02. DATA COMMUNICATION	PP	100	40	67	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	78	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	76	P
05. THEORY OF COMPUTATION	PP	100	40	56	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	34	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	39	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	16	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	37	P
10. HARDWARE LABORATORY	TW	25	10	18	P
11. HARDWARE LABORATORY	PR	50	20	32	P

FIRST TERM TOTAL = 513/750.

ORDN. 1 MARKS :

T80054205 BALI SONIYA NARAYAN SHOBHA , 71100740D , , PICT , T80054205

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	42	P
02. DATA COMMUNICATION	PP	100	40	62	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	54	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	40	P
05. THEORY OF COMPUTATION	PP	100	40	40	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	36	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	43	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	18	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	07	F
10. HARDWARE LABORATORY	TW	25	10	21	P
11. HARDWARE LABORATORY	PR	50	20	34	P

FIRST TERM TOTAL = 397/750.

ORDN. 1 MARKS :

T80054206 BALIWANT SOURABH SANJAY KALPANA , 71100741B , , PICT , T80054206

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	43	P
02. DATA COMMUNICATION	PP	100	40	61	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	45	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	44	P
05. THEORY OF COMPUTATION	PP	100	40	44	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	33	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	00	F
08. SIGNAL PROCESSING LABORATORY	TW	25	10	16	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	22	P
10. HARDWARE LABORATORY	TW	25	10	18	P
11. HARDWARE LABORATORY	PR	50	20	30	P

FIRST TERM TOTAL = 356/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80054207 BAPAT RUTUGANDHA SUNIL ANAGHA , 71239286G , , PICT , T80054207

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	55	P
02. DATA COMMUNICATION	PP	100	40	62	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	65	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	43	P
05. THEORY OF COMPUTATION	PP	100	40	57	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	37	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	30	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	18	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	22	P
10. HARDWARE LABORATORY	TW	25	10	21	P
11. HARDWARE LABORATORY	PR	50	20	26	P

FIRST TERM TOTAL = 436/750.

ORDN. 1 MARKS :

T80054208 BAPAT SWARNADEE SANDEEP RIJUTA , 71100742L , , PICT , T80054208

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	58	P
02. DATA COMMUNICATION	PP	100	40	73	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	70	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	61	P
05. THEORY OF COMPUTATION	PP	100	40	58	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	38	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	38	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	22	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	39	P
10. HARDWARE LABORATORY	TW	25	10	22	P
11. HARDWARE LABORATORY	PR	50	20	40	P

FIRST TERM TOTAL = 519/750.

ORDN. 1 MARKS :

T80054209 BARDE KAJAL NARAYAN SULOCHANA , 71239287E , , PICT , T80054209

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	57	P
02. DATA COMMUNICATION	PP	100	40	62	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	72	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	58	P
05. THEORY OF COMPUTATION	PP	100	40	51	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	42	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	45	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	22	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	38	P
10. HARDWARE LABORATORY	TW	25	10	22	P
11. HARDWARE LABORATORY	PR	50	20	42	P

FIRST TERM TOTAL = 511/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80054210 BARDE KOMAL NARAYAN SULOCHANA , 71239288C , , PICT , T80054210

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	56	P
02. DATA COMMUNICATION	PP	100	40	60	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	76	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	62	P
05. THEORY OF COMPUTATION	PP	100	40	42	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	42	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	43	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	21	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	40	P
10. HARDWARE LABORATORY	TW	25	10	22	P
11. HARDWARE LABORATORY	PR	50	20	38	P

FIRST TERM TOTAL = 502/750.

ORDN. 1 MARKS :

T80054211 BAWALE DEVEN ABHAY SANDHYA , 71100745E , , PICT , T80054211

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	40	P
02. DATA COMMUNICATION	PP	100	40	53	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	53	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	40	P
05. THEORY OF COMPUTATION	PP	100	40	23	F
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	40	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	44	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	21	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	42	P
10. HARDWARE LABORATORY	TW	25	10	23	P
11. HARDWARE LABORATORY	PR	50	20	36	P

FIRST TERM TOTAL = 415/750.

ORDN. 1 MARKS :

T80054212 BELOTE AMOL PRABHAKAR GANGUBAI , 71100746C , , PICT , T80054212

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	48	P
02. DATA COMMUNICATION	PP	100	40	67	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	57	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	45	P
05. THEORY OF COMPUTATION	PP	100	40	41	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	31	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	32	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	18	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	25	P
10. HARDWARE LABORATORY	TW	25	10	16	P
11. HARDWARE LABORATORY	PR	50	20	24	P

FIRST TERM TOTAL = 404/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80054213 BHADKE ASHUTOSH CHANDRAKANT SUNANDA , 71239289M , , PICT , T80054213

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	51	P
02. DATA COMMUNICATION	PP	100	40	79	P
03. MICROPROCESSORS & MICROCONTROLLERPP	PP	100	40	75	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	66	P
05. THEORY OF COMPUTATION	PP	100	40	55	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	33	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	37	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	17	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	37	P
10. HARDWARE LABORATORY	TW	25	10	19	P
11. HARDWARE LABORATORY	PR	50	20	22	P

FIRST TERM TOTAL = 491/750.

ORDN. 1 MARKS :

T80054214 BHANDARI RUHI BABU SMITA , 71239290E , , PICT , T80054214

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	51	P
02. DATA COMMUNICATION	PP	100	40	75	P
03. MICROPROCESSORS & MICROCONTROLLERPP	PP	100	40	70	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	40	P
05. THEORY OF COMPUTATION	PP	100	40	50	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	40	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	38	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	19	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	08	F
10. HARDWARE LABORATORY	TW	25	10	21	P
11. HARDWARE LABORATORY	PR	50	20	40	P

FIRST TERM TOTAL = 452/750.

ORDN. 1 MARKS :

T80054215 BHANDARI YASHASHRI VASANT RANJANA , 71239291C , , PICT , T80054215

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	55	P
02. DATA COMMUNICATION	PP	100	40	71	P
03. MICROPROCESSORS & MICROCONTROLLERPP	PP	100	40	69	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	42	P
05. THEORY OF COMPUTATION	PP	100	40	45	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	38	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	39	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	16	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	40	P
10. HARDWARE LABORATORY	TW	25	10	18	P
11. HARDWARE LABORATORY	PR	50	20	35	P

FIRST TERM TOTAL = 468/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80054216 BHOMBE AMOL SONAJI KOKILA , 70701380B , , PICT , T80054216

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	AA	F
02. DATA COMMUNICATION	PP	100	40	55	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	52	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	29	F
05. THEORY OF COMPUTATION	PP	100	40	42	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	21	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	AA	F
08. SIGNAL PROCESSING LABORATORY	TW	25	10	13	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	21	P
10. HARDWARE LABORATORY	TW	25	10	13	P
11. HARDWARE LABORATORY	PR	50	20	05	F

FIRST TERM TOTAL = 251/750.

ORDN. 1 MARKS :

T80054217 BHURKE SHWETA MILIND MADHURA , 71100751K , , PICT , T80054217

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	50	P
02. DATA COMMUNICATION	PP	100	40	66	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	62	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	40	P
05. THEORY OF COMPUTATION	PP	100	40	46	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	42	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	42	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	22	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	27	P
10. HARDWARE LABORATORY	TW	25	10	23	P
11. HARDWARE LABORATORY	PR	50	20	38	P

FIRST TERM TOTAL = 458/750.

ORDN. 1 MARKS :

T80054218 CHAUDHAR VISHAL SHESHRAO SAVITA , 71100762E , , PICT , T80054218

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	52	P
02. DATA COMMUNICATION	PP	100	40	72	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	55	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	42	P
05. THEORY OF COMPUTATION	PP	100	40	32	F
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	24	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	34	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	16	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	30	P
10. HARDWARE LABORATORY	TW	25	10	18	P
11. HARDWARE LABORATORY	PR	50	20	24	P

FIRST TERM TOTAL = 399/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80054219 CHAUDHARI POOJA DHANRAJ DNYNESHWARI , 71100763C , , PICT , T80054219

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	53	P
02. DATA COMMUNICATION	PP	100	40	75	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	63	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	51	P
05. THEORY OF COMPUTATION	PP	100	40	54	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	38	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	37	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	21	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	33	P
10. HARDWARE LABORATORY	TW	25	10	21	P
11. HARDWARE LABORATORY	PR	50	20	20	P

FIRST TERM TOTAL = 466/750.

ORDN. 1 MARKS :

T80054220 CHAVAN KARISHMA SANJAY SHARADA , 71239292M , , PICT , T80054220

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	60	P
02. DATA COMMUNICATION	PP	100	40	72	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	72	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	60	P
05. THEORY OF COMPUTATION	PP	100	40	46	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	36	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	38	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	19	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	39	P
10. HARDWARE LABORATORY	TW	25	10	20	P
11. HARDWARE LABORATORY	PR	50	20	36	P

FIRST TERM TOTAL = 498/750.

ORDN. 1 MARKS :

T80054221 CHIYA IFFA IMTIAZ FAMIDA , 71239293K , , PICT , T80054221

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	48	P
02. DATA COMMUNICATION	PP	100	40	53	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	43	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	45	P
05. THEORY OF COMPUTATION	PP	100	40	40	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	35	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	40	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	18	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	39	P
10. HARDWARE LABORATORY	TW	25	10	19	P
11. HARDWARE LABORATORY	PR	50	20	32	P

FIRST TERM TOTAL = 412/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80054222 DAMLE RUCHA JAYANT VANDANA , 71100771D , , PICT , T80054222

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	64	P
02. DATA COMMUNICATION	PP	100	40	76	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	69	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	54	P
05. THEORY OF COMPUTATION	PP	100	40	52	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	39	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	44	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	19	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	40	P
10. HARDWARE LABORATORY	TW	25	10	20	P
11. HARDWARE LABORATORY	PR	50	20	40	P

FIRST TERM TOTAL = 517/750.

ORDN. 1 MARKS :

T80054223 DAMLE VISHAKHA RAVINDRA VIJAYANTI , 71100772B , , PICT , T80054223

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	59	P
02. DATA COMMUNICATION	PP	100	40	76	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	78	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	69	P
05. THEORY OF COMPUTATION	PP	100	40	61	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	35	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	40	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	20	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	38	P
10. HARDWARE LABORATORY	TW	25	10	17	P
11. HARDWARE LABORATORY	PR	50	20	44	P

FIRST TERM TOTAL = 537/750.

ORDN. 1 MARKS :

T80054224 DAMLE VISHWAS MILIND MUGDHA , 71100773L , , PICT , T80054224

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	58	P
02. DATA COMMUNICATION	PP	100	40	67	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	72	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	62	P
05. THEORY OF COMPUTATION	PP	100	40	47	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	39	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	40	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	19	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	37	P
10. HARDWARE LABORATORY	TW	25	10	23	P
11. HARDWARE LABORATORY	PR	50	20	45	P

FIRST TERM TOTAL = 509/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80054225 DAMSE SAYALI LAXMAN LATA , 71100774J , , PICT , T80054225

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	40	P
02. DATA COMMUNICATION	PP	100	40	40	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	43	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	20	F
05. THEORY OF COMPUTATION	PP	100	40	40	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	31	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	15	F
08. SIGNAL PROCESSING LABORATORY	TW	25	10	18	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	28	P
10. HARDWARE LABORATORY	TW	25	10	17	P
11. HARDWARE LABORATORY	PR	50	20	08	F

FIRST TERM TOTAL = 300/750.

ORDN. 1 MARKS :

T80054226 DEBOJEET CHATTERJEE SHIKHA , 71100775G , , PICT , T80054226

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	61	P
02. DATA COMMUNICATION	PP	100	40	74	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	65	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	52	P
05. THEORY OF COMPUTATION	PP	100	40	50	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	36	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	42	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	18	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	37	P
10. HARDWARE LABORATORY	TW	25	10	20	P
11. HARDWARE LABORATORY	PR	50	20	43	P

FIRST TERM TOTAL = 498/750.

ORDN. 1 MARKS :

T80054227 DEEPANSHU JOSHI LALITA , 71100776E , , PICT , T80054227

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	45	P
02. DATA COMMUNICATION	PP	100	40	40	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	40	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	24	F
05. THEORY OF COMPUTATION	PP	100	40	30	F
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	27	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	29	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	12	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	26	P
10. HARDWARE LABORATORY	TW	25	10	13	P
11. HARDWARE LABORATORY	PR	50	20	10	F

FIRST TERM TOTAL = 296/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80054228 DESALE VRUSHALI SANJEEV MEENAKSHI , 71239294H , , PICT , T80054228

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	48	P
02. DATA COMMUNICATION	PP	100	40	73	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	82	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	66	P
05. THEORY OF COMPUTATION	PP	100	40	55	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	38	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	36	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	22	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	33	P
10. HARDWARE LABORATORY	TW	25	10	19	P
11. HARDWARE LABORATORY	PR	50	20	40	P

FIRST TERM TOTAL = 512/750.

ORDN. 1 MARKS :

T80054229 DESHMUKH AKSHAY ANIL JYOTI , 71100778M , , PICT , T80054229

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	57	P
02. DATA COMMUNICATION	PP	100	40	64	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	62	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	56	P
05. THEORY OF COMPUTATION	PP	100	40	40	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	35	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	38	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	17	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	37	P
10. HARDWARE LABORATORY	TW	25	10	18	P
11. HARDWARE LABORATORY	PR	50	20	24	P

FIRST TERM TOTAL = 448/750.

ORDN. 1 MARKS :

T80054230 DESHPANDE ANUJ ABHAY KALPANA , 71100782K , , PICT , T80054230

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	50	P
02. DATA COMMUNICATION	PP	100	40	69	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	66	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	46	P
05. THEORY OF COMPUTATION	PP	100	40	50	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	34	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	38	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	16	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	34	P
10. HARDWARE LABORATORY	TW	25	10	16	P
11. HARDWARE LABORATORY	PR	50	20	30	P

FIRST TERM TOTAL = 449/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80054231 DHAPTE OMKAR HANUMANT MINAXI , 71239295F , , PICT , T80054231

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	56	P
02. DATA COMMUNICATION	PP	100	40	62	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	61	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	53	P
05. THEORY OF COMPUTATION	PP	100	40	40	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	33	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	39	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	15	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	21	P
10. HARDWARE LABORATORY	TW	25	10	18	P
11. HARDWARE LABORATORY	PR	50	20	25	P

FIRST TERM TOTAL = 423/750.

ORDN. 1 MARKS :

T80054232 DIPESH SINGH REEN KOMAL KAUR , 71100787L , , PICT , T80054232

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	42	P
02. DATA COMMUNICATION	PP	100	40	64	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	57	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	48	P
05. THEORY OF COMPUTATION	PP	100	40	51	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	35	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	32	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	19	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	32	P
10. HARDWARE LABORATORY	TW	25	10	22	P
11. HARDWARE LABORATORY	PR	50	20	34	P

FIRST TERM TOTAL = 436/750.

ORDN. 1 MARKS :

T80054233 DIXIT PRATIKSHA SUBHASHCHANDRA POONAM , 71100788J , , PICT , T80054233

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	57	P
02. DATA COMMUNICATION	PP	100	40	69	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	71	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	43	P
05. THEORY OF COMPUTATION	PP	100	40	55	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	35	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	37	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	17	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	33	P
10. HARDWARE LABORATORY	TW	25	10	19	P
11. HARDWARE LABORATORY	PR	50	20	40	P

FIRST TERM TOTAL = 476/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80054234 DOIPHODE NISHIGANDHA SANJAY MADHUBALA , 71100789G , , PICT , T80054234

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	49	P
02. DATA COMMUNICATION	PP	100	40	44	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	45	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	41	P
05. THEORY OF COMPUTATION	PP	100	40	32	F
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	36	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	40	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	17	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	32	P
10. HARDWARE LABORATORY	TW	25	10	19	P
11. HARDWARE LABORATORY	PR	50	20	30	P

FIRST TERM TOTAL = 385/750.

ORDN. 1 MARKS :

T80054235 DOSHI ARCHIT RAVINDRA ARCHANA , 71100790L , , PICT , T80054235

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	45	P
02. DATA COMMUNICATION	PP	100	40	60	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	56	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	47	P
05. THEORY OF COMPUTATION	PP	100	40	40	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	34	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	38	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	17	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	26	P
10. HARDWARE LABORATORY	TW	25	10	20	P
11. HARDWARE LABORATORY	PR	50	20	28	P

FIRST TERM TOTAL = 411/750.

ORDN. 1 MARKS :

T80054236 DURUGKAR SURAJ ANIL NIRMALA , 71045424E , , PICT , T80054236

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	AA	F
02. DATA COMMUNICATION	PP	100	40	AA	F
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	AA	F
04. DIGITAL SIGNAL PROCESSING	PP	100	40	AA	F
05. THEORY OF COMPUTATION	PP	100	40	AA	F
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	22	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	AA	F
08. SIGNAL PROCESSING LABORATORY	TW	25	10	12	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	21	P
10. HARDWARE LABORATORY	TW	25	10	12	P
11. HARDWARE LABORATORY	PR	50	20	05	F

FIRST TERM TOTAL = 72/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80054237 EKHANDI ATUL ANIL TRISHALA , 71100791J , , PICT , T80054237

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	57	P
02. DATA COMMUNICATION	PP	100	40	49	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	40	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	40	P
05. THEORY OF COMPUTATION	PP	100	40	40	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	34	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	36	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	16	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	30	P
10. HARDWARE LABORATORY	TW	25	10	19	P
11. HARDWARE LABORATORY	PR	50	20	36	P

FIRST TERM TOTAL = 397/750.

ORDN. 1 MARKS :

T80054238 GABHALE VARSHA LIMBAJI JANABAI , 71045426M , , PICT , T80054238

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	42	P
02. DATA COMMUNICATION	PP	100	40	32	F
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	40	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	41	P
05. THEORY OF COMPUTATION	PP	100	40	28	F
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	33	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	22	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	18	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	32	P
10. HARDWARE LABORATORY	TW	25	10	20	P
11. HARDWARE LABORATORY	PR	50	20	10	F

FIRST TERM TOTAL = 318/750.

ORDN. 1 MARKS :

T80054239 GALGALI VAISHNAVI VITHAL VAISHALI , 71100795M , , PICT , T80054239

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	70	P
02. DATA COMMUNICATION	PP	100	40	82	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	66	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	57	P
05. THEORY OF COMPUTATION	PP	100	40	55	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	37	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	29	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	19	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	34	P
10. HARDWARE LABORATORY	TW	25	10	22	P
11. HARDWARE LABORATORY	PR	50	20	40	P

FIRST TERM TOTAL = 511/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80054240 GANVIR AJAY SUDHAKAR SUREKHA , 71100797H , , PICT , T80054240

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	67	P
02. DATA COMMUNICATION	PP	100	40	57	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	56	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	59	P
05. THEORY OF COMPUTATION	PP	100	40	56	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	28	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	31	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	11	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	22	P
10. HARDWARE LABORATORY	TW	25	10	12	P
11. HARDWARE LABORATORY	PR	50	20	20	P

FIRST TERM TOTAL = 419/750.

ORDN. 1 MARKS :

T80054241 GAVALI TANUJA KASHINATH PARVATA , 71100799D , , PICT , T80054241

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	52	P
02. DATA COMMUNICATION	PP	100	40	44	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	47	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	40	P
05. THEORY OF COMPUTATION	PP	100	40	40	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	32	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	25	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	16	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	21	P
10. HARDWARE LABORATORY	TW	25	10	15	P
11. HARDWARE LABORATORY	PR	50	20	22	P

FIRST TERM TOTAL = 354/750.

ORDN. 1 MARKS :

T80054242 GEET KAUR GIRVAR SINGH SUKHMANI PAVANJEET , 71100801K , , PICT , T80054242

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	58	P
02. DATA COMMUNICATION	PP	100	40	56	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	41	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	43	P
05. THEORY OF COMPUTATION	PP	100	40	40	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	32	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	35	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	16	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	23	P
10. HARDWARE LABORATORY	TW	25	10	15	P
11. HARDWARE LABORATORY	PR	50	20	08	F

FIRST TERM TOTAL = 367/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80054243 GHULE RUTUJA POPAT SANGEETA , 71100803F , , PICT , T80054243

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	61	P
02. DATA COMMUNICATION	PP	100	40	61	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	45	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	47	P
05. THEORY OF COMPUTATION	PP	100	40	51	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	33	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	36	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	17	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	30	P
10. HARDWARE LABORATORY	TW	25	10	20	P
11. HARDWARE LABORATORY	PR	50	20	36	P

FIRST TERM TOTAL = 437/750.

ORDN. 1 MARKS :

T80054244 GINA RIGZIN CHETEN ARCHANA , 71100804D , , PICT , T80054244

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	59	P
02. DATA COMMUNICATION	PP	100	40	76	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	65	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	63	P
05. THEORY OF COMPUTATION	PP	100	40	50	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	43	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	45	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	23	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	40	P
10. HARDWARE LABORATORY	TW	25	10	21	P
11. HARDWARE LABORATORY	PR	50	20	42	P

FIRST TERM TOTAL = 527/750.

ORDN. 1 MARKS :

T80054245 GOLE MADHURI JAYANT SANJIVANI , 71100809E , , PICT , T80054245

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	68	P
02. DATA COMMUNICATION	PP	100	40	78	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	56	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	56	P
05. THEORY OF COMPUTATION	PP	100	40	49	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	31	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	40	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	15	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	31	P
10. HARDWARE LABORATORY	TW	25	10	18	P
11. HARDWARE LABORATORY	PR	50	20	28	P

FIRST TERM TOTAL = 470/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80054246 GUJAR MANDAR SHRINIVAS LEELA , 71239296D , , PICT , T80054246

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	57	P
02. DATA COMMUNICATION	PP	100	40	67	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	71	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	43	P
05. THEORY OF COMPUTATION	PP	100	40	49	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	43	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	44	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	22	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	36	P
10. HARDWARE LABORATORY	TW	25	10	22	P
11. HARDWARE LABORATORY	PR	50	20	41	P

FIRST TERM TOTAL = 495/750.

ORDN. 1 MARKS :

T80054247 GUPTA TARUN SHITALPRASAD USHA , 71100813C , , PICT , T80054247

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	67	P
02. DATA COMMUNICATION	PP	100	40	80	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	65	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	59	P
05. THEORY OF COMPUTATION	PP	100	40	46	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	34	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	43	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	18	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	43	P
10. HARDWARE LABORATORY	TW	25	10	21	P
11. HARDWARE LABORATORY	PR	50	20	42	P

FIRST TERM TOTAL = 518/750.

ORDN. 1 MARKS :

T80054248 GURJAR ADITYA SHRIKANT BHARATI , 71100814M , , PICT , T80054248

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	56	P
02. DATA COMMUNICATION	PP	100	40	81	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	64	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	66	P
05. THEORY OF COMPUTATION	PP	100	40	49	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	35	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	27	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	22	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	43	P
10. HARDWARE LABORATORY	TW	25	10	20	P
11. HARDWARE LABORATORY	PR	50	20	42	P

FIRST TERM TOTAL = 505/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80054249 HARSHITA GANGRADE NEETA , 71100818D , , PICT , T80054249

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	79	P
02. DATA COMMUNICATION	PP	100	40	84	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	71	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	69	P
05. THEORY OF COMPUTATION	PP	100	40	55	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	32	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	43	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	16	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	40	P
10. HARDWARE LABORATORY	TW	25	10	20	P
11. HARDWARE LABORATORY	PR	50	20	43	P

FIRST TERM TOTAL = 552/750.

ORDN. 1 MARKS :

T80054250 IBUSE AMANIYA MUZIR YASMEEN , 71239297B , , PICT , T80054250

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	65	P
02. DATA COMMUNICATION	PP	100	40	78	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	50	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	57	P
05. THEORY OF COMPUTATION	PP	100	40	40	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	39	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	39	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	17	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	32	P
10. HARDWARE LABORATORY	TW	25	10	20	P
11. HARDWARE LABORATORY	PR	50	20	30	P

FIRST TERM TOTAL = 467/750.

ORDN. 1 MARKS :

T80054251 INGOLE SHAIKESH GANESH ARTI , 71100822B , , PICT , T80054251

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	72	P
02. DATA COMMUNICATION	PP	100	40	76	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	61	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	71	P
05. THEORY OF COMPUTATION	PP	100	40	48	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	37	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	42	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	22	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	37	P
10. HARDWARE LABORATORY	TW	25	10	20	P
11. HARDWARE LABORATORY	PR	50	20	26	P

FIRST TERM TOTAL = 512/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80054252 JADHAV AMOL JETA LATA , 71045453J , , PICT , T80054252

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	60	P
02. DATA COMMUNICATION	PP	100	40	60	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	46	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	11	F
05. THEORY OF COMPUTATION	PP	100	40	26	F
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	21	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	AA	F
08. SIGNAL PROCESSING LABORATORY	TW	25	10	10	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	AA	F
10. HARDWARE LABORATORY	TW	25	10	10	P
11. HARDWARE LABORATORY	PR	50	20	AA	F

FIRST TERM TOTAL = 244/750.

ORDN. 1 MARKS :

T80054253 JADHAV APURWA MANIK SANDHYA , 71100823L , , PICT , T80054253

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	68	P
02. DATA COMMUNICATION	PP	100	40	71	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	48	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	58	P
05. THEORY OF COMPUTATION	PP	100	40	55	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	35	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	39	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	20	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	33	P
10. HARDWARE LABORATORY	TW	25	10	20	P
11. HARDWARE LABORATORY	PR	50	20	40	P

FIRST TERM TOTAL = 487/750.

ORDN. 1 MARKS :

T80054254 JADHAV ROHINI DAGADU RANJANA , 71239298L , , PICT , T80054254

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	72	P
02. DATA COMMUNICATION	PP	100	40	73	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	67	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	50	P
05. THEORY OF COMPUTATION	PP	100	40	53	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	40	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	41	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	18	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	30	P
10. HARDWARE LABORATORY	TW	25	10	21	P
11. HARDWARE LABORATORY	PR	50	20	30	P

FIRST TERM TOTAL = 495/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80054255 JANTRE NEHA ASHOK ARUNA , 71239299J , , PICT , T80054255

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	70	P
02. DATA COMMUNICATION	PP	100	40	68	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	74	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	46	P
05. THEORY OF COMPUTATION	PP	100	40	43	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	36	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	42	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	16	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	34	P
10. HARDWARE LABORATORY	TW	25	10	20	P
11. HARDWARE LABORATORY	PR	50	20	34	P

FIRST TERM TOTAL = 483/750.

ORDN. 1 MARKS :

T80054256 JASIM MAIMUNA , 71100830C , , PICT , T80054256

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	62	P
02. DATA COMMUNICATION	PP	100	40	68	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	62	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	66	P
05. THEORY OF COMPUTATION	PP	100	40	55	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	41	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	41	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	23	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	35	P
10. HARDWARE LABORATORY	TW	25	10	20	P
11. HARDWARE LABORATORY	PR	50	20	36	P

FIRST TERM TOTAL = 509/750.

ORDN. 1 MARKS :

T80054257 JOGLEKAR SPHOORTI CHANDRASHEKHAR POOJA , 71100832K , , PICT , T80054257

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	73	P
02. DATA COMMUNICATION	PP	100	40	78	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	67	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	72	P
05. THEORY OF COMPUTATION	PP	100	40	61	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	33	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	42	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	20	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	34	P
10. HARDWARE LABORATORY	TW	25	10	20	P
11. HARDWARE LABORATORY	PR	50	20	45	P

FIRST TERM TOTAL = 545/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80054258 JOSHI ANUJA RAMKRUSHNA VEENA , 71100834F , , PICT , T80054258

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	65	P
02. DATA COMMUNICATION	PP	100	40	76	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	63	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	56	P
05. THEORY OF COMPUTATION	PP	100	40	62	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	40	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	42	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	20	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	40	P
10. HARDWARE LABORATORY	TW	25	10	19	P
11. HARDWARE LABORATORY	PR	50	20	35	P

FIRST TERM TOTAL = 518/750.

ORDN. 1 MARKS :

T80054259 JOSHI ESHAN CHANDRASHEKHAR MEDHAVINI , 71100837L , , PICT , T80054259

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	58	P
02. DATA COMMUNICATION	PP	100	40	73	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	55	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	61	P
05. THEORY OF COMPUTATION	PP	100	40	42	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	33	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	32	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	17	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	32	P
10. HARDWARE LABORATORY	TW	25	10	22	P
11. HARDWARE LABORATORY	PR	50	20	40	P

FIRST TERM TOTAL = 465/750.

ORDN. 1 MARKS :

T80054260 KALE APARNA SUNIL MADHURI , 71100843E , , PICT , T80054260

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	72	P
02. DATA COMMUNICATION	PP	100	40	79	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	64	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	71	P
05. THEORY OF COMPUTATION	PP	100	40	62	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	48	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	46	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	24	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	45	P
10. HARDWARE LABORATORY	TW	25	10	24	P
11. HARDWARE LABORATORY	PR	50	20	42	P

FIRST TERM TOTAL = 577/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80054261 KAMBLE VIDYA ARUN SINDHU , 71100846K , , PICT , T80054261

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	52	P
02. DATA COMMUNICATION	PP	100	40	63	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	46	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	40	P
05. THEORY OF COMPUTATION	PP	100	40	44	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	33	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	37	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	20	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	21	P
10. HARDWARE LABORATORY	TW	25	10	20	P
11. HARDWARE LABORATORY	PR	50	20	38	P

FIRST TERM TOTAL = 414/750.

ORDN. 1 MARKS :

T80054262 KANTALE SACHIN WAMANRAO PRAMILA , 71100848F , , PICT , T80054262

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	19	F
02. DATA COMMUNICATION	PP	100	40	16	F
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	03	F
04. DIGITAL SIGNAL PROCESSING	PP	100	40	02	F
05. THEORY OF COMPUTATION	PP	100	40	05	F
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	24	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	23	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	11	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	07	F
10. HARDWARE LABORATORY	TW	25	10	13	P
11. HARDWARE LABORATORY	PR	50	20	05	F

FIRST TERM TOTAL = 128/750.

ORDN. 1 MARKS :

T80054263 KARANDE PRADNYA VASANT CHHAYA , 71100850H , , PICT , T80054263

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	66	P
02. DATA COMMUNICATION	PP	100	40	71	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	68	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	63	P
05. THEORY OF COMPUTATION	PP	100	40	43	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	36	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	36	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	19	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	39	P
10. HARDWARE LABORATORY	TW	25	10	22	P
11. HARDWARE LABORATORY	PR	50	20	40	P

FIRST TERM TOTAL = 503/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80054264 KARANDE RAHUL MADAN SUNITA , 71045472E , , PICT , T80054264

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	40	P
02. DATA COMMUNICATION	PP	100	40	49	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	25	F
04. DIGITAL SIGNAL PROCESSING	PP	100	40	20	F
05. THEORY OF COMPUTATION	PP	100	40	18	F
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	23	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	25	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	11	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	22	P
10. HARDWARE LABORATORY	TW	25	10	10	P
11. HARDWARE LABORATORY	PR	50	20	26	P

FIRST TERM TOTAL = 269/750.

ORDN. 1 MARKS :

T80054265 KARANDE SHWETA RAMESH SANGEETA , 71100851F , , PICT , T80054265

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	49	P
02. DATA COMMUNICATION	PP	100	40	56	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	41	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	40	P
05. THEORY OF COMPUTATION	PP	100	40	30	F
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	33	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	25	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	18	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	29	P
10. HARDWARE LABORATORY	TW	25	10	20	P
11. HARDWARE LABORATORY	PR	50	20	35	P

FIRST TERM TOTAL = 376/750.

ORDN. 1 MARKS :

T80054266 KATRE RAHUL SURESH USHA , 71100852D , , PICT , T80054266

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	46	P
02. DATA COMMUNICATION	PP	100	40	46	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	40	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	42	P
05. THEORY OF COMPUTATION	PP	100	40	32	F
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	31	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	36	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	14	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	31	P
10. HARDWARE LABORATORY	TW	25	10	17	P
11. HARDWARE LABORATORY	PR	50	20	35	P

FIRST TERM TOTAL = 370/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80054267 KEDIYA RIDDHI PRAVIN MANJUSHA , 71100854L , , PICT , T80054267

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	45	P
02. DATA COMMUNICATION	PP	100	40	50	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	22	F
04. DIGITAL SIGNAL PROCESSING	PP	100	40	30	F
05. THEORY OF COMPUTATION	PP	100	40	29	F
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	32	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	32	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	17	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	34	P
10. HARDWARE LABORATORY	TW	25	10	19	P
11. HARDWARE LABORATORY	PR	50	20	10	F

FIRST TERM TOTAL = 320/750.

ORDN. 1 MARKS :

T80054268 KHAIRNAR APOORV DATTATRAYA PRAGATI , 71100858C , , PICT , T80054268

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	65	P
02. DATA COMMUNICATION	PP	100	40	63	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	46	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	64	P
05. THEORY OF COMPUTATION	PP	100	40	47	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	29	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	26	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	16	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	36	P
10. HARDWARE LABORATORY	TW	25	10	18	P
11. HARDWARE LABORATORY	PR	50	20	41	P

FIRST TERM TOTAL = 451/750.

ORDN. 1 MARKS :

T80054270 KHANDAGALE RAVEE SANJEEV ASHWINI , 71100861C , , PICT , T80054270

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	56	P
02. DATA COMMUNICATION	PP	100	40	51	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	45	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	21	F
05. THEORY OF COMPUTATION	PP	100	40	40	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	30	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	25	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	15	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	21	P
10. HARDWARE LABORATORY	TW	25	10	15	P
11. HARDWARE LABORATORY	PR	50	20	08	F

FIRST TERM TOTAL = 327/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80054271 KHANDEKAR GAUTAMI MILIND VAISHALI , 71100862M , , PICT , T80054271

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	76	P
02. DATA COMMUNICATION	PP	100	40	71	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	75	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	64	P
05. THEORY OF COMPUTATION	PP	100	40	67	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	37	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	42	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	21	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	40	P
10. HARDWARE LABORATORY	TW	25	10	23	P
11. HARDWARE LABORATORY	PR	50	20	40	P

FIRST TERM TOTAL = 556/750.

ORDN. 1 MARKS :

T80054272 KHARADE TUSHAR DILIP PURNIMA , 71100864H , , PICT , T80054272

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	56	P
02. DATA COMMUNICATION	PP	100	40	69	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	48	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	50	P
05. THEORY OF COMPUTATION	PP	100	40	44	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	36	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	30	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	20	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	38	P
10. HARDWARE LABORATORY	TW	25	10	19	P
11. HARDWARE LABORATORY	PR	50	20	30	P

FIRST TERM TOTAL = 440/750.

ORDN. 1 MARKS :

T80054273 KSHIRSAGAR NIRANJAN VALMIK JAYASHRI , 71100868L , , PICT , T80054273

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	59	P
02. DATA COMMUNICATION	PP	100	40	59	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	51	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	44	P
05. THEORY OF COMPUTATION	PP	100	40	40	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	34	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	26	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	19	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	28	P
10. HARDWARE LABORATORY	TW	25	10	19	P
11. HARDWARE LABORATORY	PR	50	20	31	P

FIRST TERM TOTAL = 410/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80054274 KUBER SHRUTI KRISHNADATTA SUNITA , 71100869J , , PICT , T80054274

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	69	P
02. DATA COMMUNICATION	PP	100	40	75	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	70	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	57	P
05. THEORY OF COMPUTATION	PP	100	40	62	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	35	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	41	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	20	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	36	P
10. HARDWARE LABORATORY	TW	25	10	18	P
11. HARDWARE LABORATORY	PR	50	20	40	P

FIRST TERM TOTAL = 523/750.

ORDN. 1 MARKS :

T80054275 KULKARNI APARNA HARISHCHANDRA PRADNYA , 71239300F , , PICT , T80054275

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	64	P
02. DATA COMMUNICATION	PP	100	40	80	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	73	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	62	P
05. THEORY OF COMPUTATION	PP	100	40	55	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	36	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	30	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	20	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	40	P
10. HARDWARE LABORATORY	TW	25	10	18	P
11. HARDWARE LABORATORY	PR	50	20	43	P

FIRST TERM TOTAL = 521/750.

ORDN. 1 MARKS :

T80054276 KULKARNI CHAITANYA AVINASH AMRUTA , 71100870B , , PICT , T80054276

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	68	P
02. DATA COMMUNICATION	PP	100	40	73	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	52	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	64	P
05. THEORY OF COMPUTATION	PP	100	40	49	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	38	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	32	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	20	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	40	P
10. HARDWARE LABORATORY	TW	25	10	20	P
11. HARDWARE LABORATORY	PR	50	20	35	P

FIRST TERM TOTAL = 491/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80054277 KULKARNI SARANG SANTOSH SNEHA , 71100873G , , PICT , T80054277

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	54	P
02. DATA COMMUNICATION	PP	100	40	57	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	59	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	40	P
05. THEORY OF COMPUTATION	PP	100	40	25	F
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	25	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	24	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	15	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	21	P
10. HARDWARE LABORATORY	TW	25	10	14	P
11. HARDWARE LABORATORY	PR	50	20	22	P

FIRST TERM TOTAL = 356/750.

ORDN. 1 MARKS :

T80054278 KULKARNI SAURABH NITIN REVATI , 71100874E , , PICT , T80054278

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	51	P
02. DATA COMMUNICATION	PP	100	40	57	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	49	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	44	P
05. THEORY OF COMPUTATION	PP	100	40	40	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	32	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	39	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	19	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	39	P
10. HARDWARE LABORATORY	TW	25	10	20	P
11. HARDWARE LABORATORY	PR	50	20	40	P

FIRST TERM TOTAL = 430/750.

ORDN. 1 MARKS :

T80054280 LAJPAT BISHNOI SAROJ , 71100879F , , PICT , T80054280

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	56	P
02. DATA COMMUNICATION	PP	100	40	70	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	68	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	72	P
05. THEORY OF COMPUTATION	PP	100	40	61	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	44	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	42	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	24	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	45	P
10. HARDWARE LABORATORY	TW	25	10	24	P
11. HARDWARE LABORATORY	PR	50	20	44	P

FIRST TERM TOTAL = 550/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80054281 LALANI SANIA SALIM NASHINA , 71100880K , , PICT , T80054281

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	75	P
02. DATA COMMUNICATION	PP	100	40	73	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	66	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	54	P
05. THEORY OF COMPUTATION	PP	100	40	59	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	37	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	43	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	19	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	44	P
10. HARDWARE LABORATORY	TW	25	10	22	P
11. HARDWARE LABORATORY	PR	50	20	40	P

FIRST TERM TOTAL = 532/750.

ORDN. 1 MARKS :

T80054282 LIMAYE ARNAV ABHAY VEENA , 71100882F , , PICT , T80054282

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	63	P
02. DATA COMMUNICATION	PP	100	40	58	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	41	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	53	P
05. THEORY OF COMPUTATION	PP	100	40	40	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	34	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	36	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	16	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	35	P
10. HARDWARE LABORATORY	TW	25	10	19	P
11. HARDWARE LABORATORY	PR	50	20	45	P

FIRST TERM TOTAL = 440/750.

ORDN. 1 MARKS :

T80054283 LOYA SHRUTI SHAMSUNDER PREMLATA , 71100883D , , PICT , T80054283

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	73	P
02. DATA COMMUNICATION	PP	100	40	65	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	63	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	76	P
05. THEORY OF COMPUTATION	PP	100	40	60	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	41	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	40	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	18	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	40	P
10. HARDWARE LABORATORY	TW	25	10	20	P
11. HARDWARE LABORATORY	PR	50	20	32	P

FIRST TERM TOTAL = 528/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80054284 MAHAJAN ABHIJEET SHIVDAS PRABHAVATI , 71100887G , , PICT , T80054284

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	63	P
02. DATA COMMUNICATION	PP	100	40	65	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	48	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	51	P
05. THEORY OF COMPUTATION	PP	100	40	45	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	27	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	42	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	15	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	35	P
10. HARDWARE LABORATORY	TW	25	10	16	P
11. HARDWARE LABORATORY	PR	50	20	30	P

FIRST TERM TOTAL = 437/750.

ORDN. 1 MARKS :

T80054285 MARATHE SONALI ASHOK ARUNA , 71239301D , , PICT , T80054285

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	71	P
02. DATA COMMUNICATION	PP	100	40	71	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	60	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	52	P
05. THEORY OF COMPUTATION	PP	100	40	48	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	43	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	42	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	22	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	38	P
10. HARDWARE LABORATORY	TW	25	10	23	P
11. HARDWARE LABORATORY	PR	50	20	40	P

FIRST TERM TOTAL = 510/750.

ORDN. 1 MARKS :

T80054286 MARUDA PRAVIN POPATLAL NANDA , 71239302B , , PICT , T80054286

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	72	P
02. DATA COMMUNICATION	PP	100	40	61	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	74	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	46	P
05. THEORY OF COMPUTATION	PP	100	40	50	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	35	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	36	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	19	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	38	P
10. HARDWARE LABORATORY	TW	25	10	19	P
11. HARDWARE LABORATORY	PR	50	20	30	P

FIRST TERM TOTAL = 480/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80054287 MEHTA MIHIR ARPIT FALGUNI , 71100894K , , PICT , T80054287

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	59	P
02. DATA COMMUNICATION	PP	100	40	64	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	51	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	77	P
05. THEORY OF COMPUTATION	PP	100	40	43	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	35	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	43	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	19	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	35	P
10. HARDWARE LABORATORY	TW	25	10	20	P
11. HARDWARE LABORATORY	PR	50	20	40	P

FIRST TERM TOTAL = 486/750.

ORDN. 1 MARKS :

T80054288 MISTRY HASIT NIPUN SONAL , 71100896F , , PICT , T80054288

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	67	P
02. DATA COMMUNICATION	PP	100	40	65	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	48	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	40	P
05. THEORY OF COMPUTATION	PP	100	40	44	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	33	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	40	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	15	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	28	P
10. HARDWARE LABORATORY	TW	25	10	16	P
11. HARDWARE LABORATORY	PR	50	20	10	F

FIRST TERM TOTAL = 406/750.

ORDN. 1 MARKS :

T80054289 MOHIT RAJVARDHAN MEENA KUMARI , 71045671K , , PICT , T80054289

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	44	P
02. DATA COMMUNICATION	PP	100	40	40	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	49	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	16	F
05. THEORY OF COMPUTATION	PP	100	40	41	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	32	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	38	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	16	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	20	P
10. HARDWARE LABORATORY	TW	25	10	15	P
11. HARDWARE LABORATORY	PR	50	20	36	P

FIRST TERM TOTAL = 347/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80054290 MORANKAR SNEHAL RAMESH JAYASHREE , 71100899L , , PICT , T80054290

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	68	P
02. DATA COMMUNICATION	PP	100	40	55	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	52	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	51	P
05. THEORY OF COMPUTATION	PP	100	40	55	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	39	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	39	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	18	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	38	P
10. HARDWARE LABORATORY	TW	25	10	20	P
11. HARDWARE LABORATORY	PR	50	20	40	P

FIRST TERM TOTAL = 475/750.

ORDN. 1 MARKS :

T80054291 MUNMOON GHOSH REKHA , 71100902D , , PICT , T80054291

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	59	P
02. DATA COMMUNICATION	PP	100	40	53	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	62	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	30	F
05. THEORY OF COMPUTATION	PP	100	40	41	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	29	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	37	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	15	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	34	P
10. HARDWARE LABORATORY	TW	25	10	14	P
11. HARDWARE LABORATORY	PR	50	20	25	P

FIRST TERM TOTAL = 399/750.

ORDN. 1 MARKS :

T80054292 NAGRE KARTIK ATUL MANISHA , 71100905J , , PICT , T80054292

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	67	P
02. DATA COMMUNICATION	PP	100	40	63	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	60	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	60	P
05. THEORY OF COMPUTATION	PP	100	40	59	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	35	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	39	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	18	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	38	P
10. HARDWARE LABORATORY	TW	25	10	19	P
11. HARDWARE LABORATORY	PR	50	20	28	P

FIRST TERM TOTAL = 486/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80054293 NAIK SANDESH TARSING KAMAL , 71100908C , PICT , T80054293

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	63	P
02. DATA COMMUNICATION	PP	100	40	66	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	48	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	48	P
05. THEORY OF COMPUTATION	PP	100	40	10	F
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	24	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	25	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	10	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	32	P
10. HARDWARE LABORATORY	TW	25	10	13	P
11. HARDWARE LABORATORY	PR	50	20	26	P

FIRST TERM TOTAL = 365/750.

ORDN. 1 MARKS :

T80054294 NARGUNDE TUSHAR VYANKATESH ANJALI , 71100913K , PICT , T80054294

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	72	P
02. DATA COMMUNICATION	PP	100	40	70	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	61	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	69	P
05. THEORY OF COMPUTATION	PP	100	40	65	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	40	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	41	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	21	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	33	P
10. HARDWARE LABORATORY	TW	25	10	20	P
11. HARDWARE LABORATORY	PR	50	20	35	P

FIRST TERM TOTAL = 527/750.

ORDN. 1 MARKS :

T80054295 NAVEEN KUMAR GUPTA ANITA , 71100915F , PICT , T80054295

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	75	P
02. DATA COMMUNICATION	PP	100	40	63	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	57	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	62	P
05. THEORY OF COMPUTATION	PP	100	40	56	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	25	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	38	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	14	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	35	P
10. HARDWARE LABORATORY	TW	25	10	15	P
11. HARDWARE LABORATORY	PR	50	20	30	P

FIRST TERM TOTAL = 470/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80054296 PALNITKAR AMOGH NARENDRA JAYASHREE , 71100924E , , PICT , T80054296

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	65	P
02. DATA COMMUNICATION	PP	100	40	69	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	54	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	54	P
05. THEORY OF COMPUTATION	PP	100	40	49	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	33	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	42	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	15	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	41	P
10. HARDWARE LABORATORY	TW	25	10	16	P
11. HARDWARE LABORATORY	PR	50	20	38	P

FIRST TERM TOTAL = 476/750.

ORDN. 1 MARKS :

T80054297 PANDE SWAPNIL SURESH SADHANA , 71100926M , , PICT , T80054297

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	64	P
02. DATA COMMUNICATION	PP	100	40	69	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	56	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	62	P
05. THEORY OF COMPUTATION	PP	100	40	48	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	38	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	28	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	16	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	32	P
10. HARDWARE LABORATORY	TW	25	10	19	P
11. HARDWARE LABORATORY	PR	50	20	28	P

FIRST TERM TOTAL = 460/750.

ORDN. 1 MARKS :

T80054298 PANHALKAR SHREYAS SHRIKANT NAYANA , 71100929F , , PICT , T80054298

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	60	P
02. DATA COMMUNICATION	PP	100	40	64	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	56	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	59	P
05. THEORY OF COMPUTATION	PP	100	40	53	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	40	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	41	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	19	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	12	F
10. HARDWARE LABORATORY	TW	25	10	20	P
11. HARDWARE LABORATORY	PR	50	20	38	P

FIRST TERM TOTAL = 462/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80054299 PAPAT VAIBHAV VINAYAK VANDANA , 71100930K , , PICT , T80054299

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	62	P
02. DATA COMMUNICATION	PP	100	40	61	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	46	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	51	P
05. THEORY OF COMPUTATION	PP	100	40	50	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	40	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	20	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	21	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	25	P
10. HARDWARE LABORATORY	TW	25	10	22	P
11. HARDWARE LABORATORY	PR	50	20	42	P

FIRST TERM TOTAL = 440/750.

ORDN. 1 MARKS :

T80054300 PATHAK ROHAN ABHAY ARCHANA , 71100935L , , PICT , T80054300

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	65	P
02. DATA COMMUNICATION	PP	100	40	48	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	45	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	44	P
05. THEORY OF COMPUTATION	PP	100	40	45	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	34	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	32	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	17	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	12	F
10. HARDWARE LABORATORY	TW	25	10	20	P
11. HARDWARE LABORATORY	PR	50	20	40	P

FIRST TERM TOTAL = 402/750.

ORDN. 1 MARKS :

T80054301 PATIL MADHUSHRI VINOD VRUSHALI , 71239303L , , PICT , T80054301

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	68	P
02. DATA COMMUNICATION	PP	100	40	59	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	59	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	53	P
05. THEORY OF COMPUTATION	PP	100	40	49	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	42	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	40	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	19	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	23	P
10. HARDWARE LABORATORY	TW	25	10	20	P
11. HARDWARE LABORATORY	PR	50	20	40	P

FIRST TERM TOTAL = 472/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80054302 PATIL MANDAR RAMRAO MUKTA , 71100940G , , PICT , T80054302

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	43	P
02. DATA COMMUNICATION	PP	100	40	46	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	44	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	40	P
05. THEORY OF COMPUTATION	PP	100	40	31	F
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	30	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	36	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	18	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	25	P
10. HARDWARE LABORATORY	TW	25	10	18	P
11. HARDWARE LABORATORY	PR	50	20	33	P

FIRST TERM TOTAL = 364/750.

ORDN. 1 MARKS :

T80054303 PATIL YOJANA VILAS SARALA , 71100945H , , PICT , T80054303

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	70	P
02. DATA COMMUNICATION	PP	100	40	72	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	65	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	63	P
05. THEORY OF COMPUTATION	PP	100	40	54	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	37	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	32	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	19	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	34	P
10. HARDWARE LABORATORY	TW	25	10	22	P
11. HARDWARE LABORATORY	PR	50	20	35	P

FIRST TERM TOTAL = 503/750.

ORDN. 1 MARKS :

T80054304 PAWAR ABHIJEET SADANAND RENUKA , 71100947D , , PICT , T80054304

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	64	P
02. DATA COMMUNICATION	PP	100	40	63	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	56	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	61	P
05. THEORY OF COMPUTATION	PP	100	40	49	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	37	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	38	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	20	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	41	P
10. HARDWARE LABORATORY	TW	25	10	22	P
11. HARDWARE LABORATORY	PR	50	20	36	P

FIRST TERM TOTAL = 487/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80054305 PAWAR NAYAN SUNIL NALINI , 71239304J , , PICT , T80054305

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	66	P
02. DATA COMMUNICATION	PP	100	40	62	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	65	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	56	P
05. THEORY OF COMPUTATION	PP	100	40	57	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	37	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	33	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	19	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	20	P
10. HARDWARE LABORATORY	TW	25	10	21	P
11. HARDWARE LABORATORY	PR	50	20	34	P

FIRST TERM TOTAL = 470/750.

ORDN. 1 MARKS :

T80054306 PAWAR OM DILIP MADHURI , 71100950D , , PICT , T80054306

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	40	P
02. DATA COMMUNICATION	PP	100	40	51	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	55	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	45	P
05. THEORY OF COMPUTATION	PP	100	40	48	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	36	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	15	F
08. SIGNAL PROCESSING LABORATORY	TW	25	10	16	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	21	P
10. HARDWARE LABORATORY	TW	25	10	19	P
11. HARDWARE LABORATORY	PR	50	20	05	F

FIRST TERM TOTAL = 351/750.

ORDN. 1 MARKS :

T80054307 PAYAL KALRA SIMMI , 71100953J , , PICT , T80054307

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	50	P
02. DATA COMMUNICATION	PP	100	40	64	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	61	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	49	P
05. THEORY OF COMPUTATION	PP	100	40	46	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	30	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	38	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	17	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	30	P
10. HARDWARE LABORATORY	TW	25	10	20	P
11. HARDWARE LABORATORY	PR	50	20	32	P

FIRST TERM TOTAL = 437/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80054308 POL ANIKET BHIMRAO ARUNA , 71100956C , , PICT , T80054308

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	63	P
02. DATA COMMUNICATION	PP	100	40	76	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	64	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	77	P
05. THEORY OF COMPUTATION	PP	100	40	62	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	39	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	40	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	18	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	36	P
10. HARDWARE LABORATORY	TW	25	10	18	P
11. HARDWARE LABORATORY	PR	50	20	26	P

FIRST TERM TOTAL = 519/750.

ORDN. 1 MARKS :

T80054309 RAMTIRTH IRA NITIN ANJALI , 71100965B , , PICT , T80054309

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	55	P
02. DATA COMMUNICATION	PP	100	40	60	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	64	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	45	P
05. THEORY OF COMPUTATION	PP	100	40	53	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	34	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	34	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	18	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	32	P
10. HARDWARE LABORATORY	TW	25	10	18	P
11. HARDWARE LABORATORY	PR	50	20	30	P

FIRST TERM TOTAL = 443/750.

ORDN. 1 MARKS :

T80054310 RANGNEKAR SARVESH DATTATRAYA MANJIRI , 71100967J , , PICT , T80054310

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	57	P
02. DATA COMMUNICATION	PP	100	40	81	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	55	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	72	P
05. THEORY OF COMPUTATION	PP	100	40	70	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	42	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	38	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	16	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	38	P
10. HARDWARE LABORATORY	TW	25	10	21	P
11. HARDWARE LABORATORY	PR	50	20	29	P

FIRST TERM TOTAL = 519/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80054311 RATHI ROSHANI DIPAK JYOTI , 71100969E , , PICT , T80054311

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	40	P
02. DATA COMMUNICATION	PP	100	40	55	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	50	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	45	P
05. THEORY OF COMPUTATION	PP	100	40	67	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	29	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	38	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	18	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	28	P
10. HARDWARE LABORATORY	TW	25	10	19	P
11. HARDWARE LABORATORY	PR	50	20	28	P

FIRST TERM TOTAL = 417/750.

ORDN. 1 MARKS :

T80054312 RAUT VAIBHAV SHARAD RUKMINI , 71100973C , , PICT , T80054312

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	54	P
02. DATA COMMUNICATION	PP	100	40	66	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	54	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	73	P
05. THEORY OF COMPUTATION	PP	100	40	65	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	23	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	39	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	10	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	35	P
10. HARDWARE LABORATORY	TW	25	10	13	P
11. HARDWARE LABORATORY	PR	50	20	33	P

FIRST TERM TOTAL = 465/750.

ORDN. 1 MARKS :

T80054313 RENUKE JAGDISH SHIVAJIRAO VANAMALA , 71100974M , , PICT , T80054313

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	40	P
02. DATA COMMUNICATION	PP	100	40	48	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	50	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	43	P
05. THEORY OF COMPUTATION	PP	100	40	50	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	33	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	22	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	16	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	20	P
10. HARDWARE LABORATORY	TW	25	10	19	P
11. HARDWARE LABORATORY	PR	50	20	06	F

FIRST TERM TOTAL = 347/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80054314 RUPNER GEETA MADHAV GOKULA , 71239305G , , PICT , T80054314

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	60	P
02. DATA COMMUNICATION	PP	100	40	67	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	65	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	46	P
05. THEORY OF COMPUTATION	PP	100	40	59	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	40	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	08	F
08. SIGNAL PROCESSING LABORATORY	TW	25	10	23	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	29	P
10. HARDWARE LABORATORY	TW	25	10	23	P
11. HARDWARE LABORATORY	PR	50	20	35	P

FIRST TERM TOTAL = 455/750.

ORDN. 1 MARKS :

T80054315 SALECHA DIVYA SANJAY NIRMALA , 71100983L , , PICT , T80054315

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	31	F
02. DATA COMMUNICATION	PP	100	40	53	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	43	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	55	P
05. THEORY OF COMPUTATION	PP	100	40	62	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	32	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	37	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	18	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	36	P
10. HARDWARE LABORATORY	TW	25	10	18	P
11. HARDWARE LABORATORY	PR	50	20	32	P

FIRST TERM TOTAL = 417/750.

ORDN. 1 MARKS :

T80054316 SAMRIDDH HADA MAHESH RUBY , 71100985G , , PICT , T80054316

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	40	P
02. DATA COMMUNICATION	PP	100	40	52	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	41	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	42	P
05. THEORY OF COMPUTATION	PP	100	40	40	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	30	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	04	F
08. SIGNAL PROCESSING LABORATORY	TW	25	10	16	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	20	P
10. HARDWARE LABORATORY	TW	25	10	16	P
11. HARDWARE LABORATORY	PR	50	20	25	P

FIRST TERM TOTAL = 326/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80054317 SARAF HRISHIKESH SHRIRAM VRUSHALI , 71100987C , , PICT , T80054317

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	48	P
02. DATA COMMUNICATION	PP	100	40	75	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	48	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	70	P
05. THEORY OF COMPUTATION	PP	100	40	69	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	35	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	40	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	16	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	42	P
10. HARDWARE LABORATORY	TW	25	10	16	P
11. HARDWARE LABORATORY	PR	50	20	35	P

FIRST TERM TOTAL = 494/750.

ORDN. 1 MARKS :

T80054318 SATHE DEEPASHREE CHANDRAKANT CHITRA , 71239306E , , PICT , T80054318

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	56	P
02. DATA COMMUNICATION	PP	100	40	66	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	61	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	69	P
05. THEORY OF COMPUTATION	PP	100	40	67	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	44	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	42	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	21	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	41	P
10. HARDWARE LABORATORY	TW	25	10	21	P
11. HARDWARE LABORATORY	PR	50	20	38	P

FIRST TERM TOTAL = 526/750.

ORDN. 1 MARKS :

T80054319 SATYANARAYAN RAJAGOPALAN VIJAYASHANTHI , 71100990C , , PICT , T80054319

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	40	P
02. DATA COMMUNICATION	PP	100	40	47	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	43	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	50	P
05. THEORY OF COMPUTATION	PP	100	40	40	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	36	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	43	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	16	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	29	P
10. HARDWARE LABORATORY	TW	25	10	20	P
11. HARDWARE LABORATORY	PR	50	20	30	P

FIRST TERM TOTAL = 394/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80054320 SAVAJI ADITYA JAYANT SHRUTI , 71100991M , , PICT , T80054320

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	47	P
02. DATA COMMUNICATION	PP	100	40	72	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	44	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	54	P
05. THEORY OF COMPUTATION	PP	100	40	40	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	35	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	39	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	20	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	38	P
10. HARDWARE LABORATORY	TW	25	10	18	P
11. HARDWARE LABORATORY	PR	50	20	26	P

FIRST TERM TOTAL = 433/750.

ORDN. 1 MARKS :

T80054321 SAWANT TEJASWINI NANASAHEB SUNITA , 71100992K , , PICT , T80054321

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	48	P
02. DATA COMMUNICATION	PP	100	40	58	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	47	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	29	F
05. THEORY OF COMPUTATION	PP	100	40	47	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	32	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	30	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	16	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	34	P
10. HARDWARE LABORATORY	TW	25	10	20	P
11. HARDWARE LABORATORY	PR	50	20	07	F

FIRST TERM TOTAL = 368/750.

ORDN. 1 MARKS :

T80054322 SAYALI ANUP LUNKAD SMITA , 71100993H , , PICT , T80054322

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	40	P
02. DATA COMMUNICATION	PP	100	40	68	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	55	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	54	P
05. THEORY OF COMPUTATION	PP	100	40	54	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	31	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	32	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	15	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	32	P
10. HARDWARE LABORATORY	TW	25	10	16	P
11. HARDWARE LABORATORY	PR	50	20	10	F

FIRST TERM TOTAL = 407/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80054323 SAYKAR SAURABH VIJAY ARCHANA , 71100994F , , PICT , T80054323

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	49	P
02. DATA COMMUNICATION	PP	100	40	65	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	54	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	56	P
05. THEORY OF COMPUTATION	PP	100	40	51	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	35	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	40	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	19	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	34	P
10. HARDWARE LABORATORY	TW	25	10	21	P
11. HARDWARE LABORATORY	PR	50	20	11	F

FIRST TERM TOTAL = 435/750.

ORDN. 1 MARKS :

T80054324 SHAH ROUNAK BHARAT SHILPA , 71100995D , , PICT , T80054324

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	60	P
02. DATA COMMUNICATION	PP	100	40	80	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	69	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	75	P
05. THEORY OF COMPUTATION	PP	100	40	66	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	46	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	42	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	24	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	45	P
10. HARDWARE LABORATORY	TW	25	10	24	P
11. HARDWARE LABORATORY	PR	50	20	44	P

FIRST TERM TOTAL = 575/750.

ORDN. 1 MARKS :

T80054325 SHAH SAMKIT UPESHKUMAR JAGRUTI , 71100996B , , PICT , T80054325

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	49	P
02. DATA COMMUNICATION	PP	100	40	60	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	56	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	70	P
05. THEORY OF COMPUTATION	PP	100	40	65	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	43	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	42	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	23	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	43	P
10. HARDWARE LABORATORY	TW	25	10	23	P
11. HARDWARE LABORATORY	PR	50	20	38	P

FIRST TERM TOTAL = 512/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80054326 SHENOY ROHIT VEDVYAS VEENA , 71101002B , , PICT , T80054326

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	40	P
02. DATA COMMUNICATION	PP	100	40	61	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	40	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	50	P
05. THEORY OF COMPUTATION	PP	100	40	52	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	37	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	40	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	18	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	30	P
10. HARDWARE LABORATORY	TW	25	10	22	P
11. HARDWARE LABORATORY	PR	50	20	44	P

FIRST TERM TOTAL = 434/750.

ORDN. 1 MARKS :

T80054327 SHINDE SHWETA SANJAY SNEHA , 71101007C , , PICT , T80054327

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	57	P
02. DATA COMMUNICATION	PP	100	40	76	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	60	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	68	P
05. THEORY OF COMPUTATION	PP	100	40	70	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	38	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	42	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	20	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	25	P
10. HARDWARE LABORATORY	TW	25	10	23	P
11. HARDWARE LABORATORY	PR	50	20	39	P

FIRST TERM TOTAL = 518/750.

ORDN. 1 MARKS :

T80054328 SHINDE VIRENDRA BIJWANT SEEMA , 71101008M , , PICT , T80054328

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	57	P
02. DATA COMMUNICATION	PP	100	40	71	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	51	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	64	P
05. THEORY OF COMPUTATION	PP	100	40	59	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	43	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	40	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	20	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	39	P
10. HARDWARE LABORATORY	TW	25	10	22	P
11. HARDWARE LABORATORY	PR	50	20	33	P

FIRST TERM TOTAL = 499/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80054329 SHIVANGI BOHRA MONA , 71101012K , , PICT , T80054329

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	52	P
02. DATA COMMUNICATION	PP	100	40	61	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	55	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	57	P
05. THEORY OF COMPUTATION	PP	100	40	56	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	34	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	37	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	18	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	35	P
10. HARDWARE LABORATORY	TW	25	10	20	P
11. HARDWARE LABORATORY	PR	50	20	12	F

FIRST TERM TOTAL = 437/750.

ORDN. 1 MARKS :

T80054330 SIDDHARTH BATRA SANGEETA , 71045624H , , PICT , T80054330

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	40	P
02. DATA COMMUNICATION	PP	100	40	AA	F
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	46	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	40	P
05. THEORY OF COMPUTATION	PP	100	40	40	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	21	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	22	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	13	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	28	P
10. HARDWARE LABORATORY	TW	25	10	15	P
11. HARDWARE LABORATORY	PR	50	20	24	P

FIRST TERM TOTAL = 289/750.

ORDN. 1 MARKS :

T80054331 SOLANKI GAURAV NANAJI SHOBHA , 71101018J , , PICT , T80054331

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	40	P
02. DATA COMMUNICATION	PP	100	40	51	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	49	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	48	P
05. THEORY OF COMPUTATION	PP	100	40	31	F
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	30	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	22	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	15	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	35	P
10. HARDWARE LABORATORY	TW	25	10	15	P
11. HARDWARE LABORATORY	PR	50	20	20	P

FIRST TERM TOTAL = 356/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80054332 TAKALKAR SIYA HEMANT VAISHALI , 71239308M , , PICT , T80054332

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	59	P
02. DATA COMMUNICATION	PP	100	40	73	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	62	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	51	P
05. THEORY OF COMPUTATION	PP	100	40	47	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	38	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	40	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	20	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	38	P
10. HARDWARE LABORATORY	TW	25	10	22	P
11. HARDWARE LABORATORY	PR	50	20	38	P

FIRST TERM TOTAL = 488/750.

ORDN. 1 MARKS :

T80054333 TARANUM BI BI RASHEED AHMED SIDDIQUI NAZEMA BEGUM , 71101027H , , PICT , T80054333

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	49	P
02. DATA COMMUNICATION	PP	100	40	61	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	59	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	53	P
05. THEORY OF COMPUTATION	PP	100	40	56	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	37	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	41	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	19	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	38	P
10. HARDWARE LABORATORY	TW	25	10	23	P
11. HARDWARE LABORATORY	PR	50	20	43	P

FIRST TERM TOTAL = 479/750.

ORDN. 1 MARKS :

T80054334 TELHARKAR ARVIND DATTATREYA MEENAKSHI , 71101029D , , PICT , T80054334

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	46	P
02. DATA COMMUNICATION	PP	100	40	59	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	46	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	66	P
05. THEORY OF COMPUTATION	PP	100	40	57	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	40	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	41	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	21	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	37	P
10. HARDWARE LABORATORY	TW	25	10	20	P
11. HARDWARE LABORATORY	PR	50	20	38	P

FIRST TERM TOTAL = 471/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80054335 THAKUR DHIRAJ SING UDAYSING SAROJADEVI , 71045648E , , PICT , T80054335

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	AA	F
02. DATA COMMUNICATION	PP	100	40	AA	F
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	40	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	AA	F
05. THEORY OF COMPUTATION	PP	100	40	AA	F
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	20	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	27	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	10	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	22	P
10. HARDWARE LABORATORY	TW	25	10	10	P
11. HARDWARE LABORATORY	PR	50	20	05	F

FIRST TERM TOTAL = 134/750.

ORDN. 1 MARKS :

T80054336 THIGALE ANIKET SUNIL MADHURI , 71101033B , , PICT , T80054336

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	58	P
02. DATA COMMUNICATION	PP	100	40	62	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	59	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	57	P
05. THEORY OF COMPUTATION	PP	100	40	61	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	40	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	35	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	21	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	39	P
10. HARDWARE LABORATORY	TW	25	10	23	P
11. HARDWARE LABORATORY	PR	50	20	33	P

FIRST TERM TOTAL = 488/750.

ORDN. 1 MARKS :

T80054337 UPPAL SAHIL RAJIV JYOTI , 71101041C , , PICT , T80054337

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	53	P
02. DATA COMMUNICATION	PP	100	40	61	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	58	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	54	P
05. THEORY OF COMPUTATION	PP	100	40	69	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	37	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	35	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	16	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	21	P
10. HARDWARE LABORATORY	TW	25	10	16	P
11. HARDWARE LABORATORY	PR	50	20	40	P

FIRST TERM TOTAL = 460/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80054338 VARMA NITESH PREMSING REKHA , 71239309K , , PICT , T80054338

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	59	P
02. DATA COMMUNICATION	PP	100	40	73	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	61	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	45	P
05. THEORY OF COMPUTATION	PP	100	40	63	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	33	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	08	F
08. SIGNAL PROCESSING LABORATORY	TW	25	10	15	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	26	P
10. HARDWARE LABORATORY	TW	25	10	16	P
11. HARDWARE LABORATORY	PR	50	20	07	F

FIRST TERM TOTAL = 406/750.

ORDN. 1 MARKS :

T80054339 VIKRAM DATTU SHOBHA , 71101049J , , PICT , T80054339

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	53	P
02. DATA COMMUNICATION	PP	100	40	58	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	54	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	65	P
05. THEORY OF COMPUTATION	PP	100	40	80	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	42	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	42	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	20	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	43	P
10. HARDWARE LABORATORY	TW	25	10	22	P
11. HARDWARE LABORATORY	PR	50	20	36	P

FIRST TERM TOTAL = 515/750.

ORDN. 1 MARKS :

T80054340 YARDI JUHI PRAKASH PRIYA , 71101060K , , PICT , T80054340

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	52	P
02. DATA COMMUNICATION	PP	100	40	73	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	63	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	71	P
05. THEORY OF COMPUTATION	PP	100	40	74	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	45	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	43	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	23	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	38	P
10. HARDWARE LABORATORY	TW	25	10	23	P
11. HARDWARE LABORATORY	PR	50	20	42	P

FIRST TERM TOTAL = 547/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80054341 YEMUL SANDEEP PRAKASH BHULAXMI , 71101062F , , PICT , T80054341

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	44	P
02. DATA COMMUNICATION	PP	100	40	62	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	53	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	48	P
05. THEORY OF COMPUTATION	PP	100	40	51	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	26	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	39	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	15	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	27	P
10. HARDWARE LABORATORY	TW	25	10	15	P
11. HARDWARE LABORATORY	PR	50	20	20	P

FIRST TERM TOTAL = 400/750.

ORDN. 1 MARKS :

T80054342 YEREKAR KARISHMA TUKARAM RANJANA , 71101064B , , PICT , T80054342

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	44	P
02. DATA COMMUNICATION	PP	100	40	67	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	58	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	53	P
05. THEORY OF COMPUTATION	PP	100	40	52	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	34	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	32	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	18	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	30	P
10. HARDWARE LABORATORY	TW	25	10	21	P
11. HARDWARE LABORATORY	PR	50	20	30	P

FIRST TERM TOTAL = 439/750.

ORDN. 1 MARKS :

T80054343 ANIRUDH JODHA ANJU , 71045370B , T8054209 , PICT , T80054343

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	56	P C	12. PRINCIPLES OF PROGRAMMING LANG.	PP	100	40	45	P C
02. DATA COMMUNICATION	PP	100	40	42	P C	13. COMPUTER NETWORKS	PP	100	40	59	P C
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	47	P C	14. FINANCE & MANAGEMENT INFORMA.SYS.	PP	100	40	58	P C
04. DIGITAL SIGNAL PROCESSING	PP	100	40	46	P C	15. SYSTEMS PROGRAMMING & OPERA.SYS.	PP	100	40	40	P C
05. THEORY OF COMPUTATION	PP	100	40	55	P C	16. SOFTWARE ENGINEERING	PP	100	40	40	P C
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	35	P C	17. SOFTWARE LABORATORY	TW	25	10	16	P C
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	38	P C	18. SOFTWARE LABORATORY	PR	50	20	24	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	16	P C	19. COMPUTER NETWORK	TW	25	10	17	P C
09. SIGNAL PROCESSING LABORATORY	OR	50	20	22	P C	20. COMPUTER NETWORK	OR	50	20	30	P C
10. HARDWARE LABORATORY	TW	25	10	19	P C	21. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	29	P C
11. HARDWARE LABORATORY	PR	50	20	36	P C	22. SEMINAR AND TECHNICAL COMMUNI.	TW	50	20	33	P C

GRAND TOTAL = 803/1500, RESULT: SECOND CLASS

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80054344	ARASHDEEP SINGH HEIR				RUPINDER				, 70925338K				T8054352		PICT			T80054344
01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	40	P	C		12.	PRINCIPLES OF PROGRAMMING LANG.	PP	100	40	19	F			
02.	DATA COMMUNICATION	PP	100	40	51	P	C		13.	COMPUTER NETWORKS	PP	100	40	40	P	C		
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	40	P	C		14.	FINANCE & MANAGEMENT INFORMA.SYS.	PP	100	40	47	P	C		
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	40	P	C		15.	SYSTEMS PROGRAMMING & OPERA.SYS.	PP	100	40	18	F			
05.	THEORY OF COMPUTATION	PP	100	40	31	F			16.	SOFTWARE ENGINEERING	PP	100	40	28	F			
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	20	P	C		17.	SOFTWARE LABORATORY	TW	25	10	10	P	C		
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	20	P	C		18.	SOFTWARE LABORATORY	PR	50	20	10	F			
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	10	P	C		19.	COMPUTER NETWORK	TW	25	10	12	P	C		
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	22	P	C		20.	COMPUTER NETWORK	OR	50	20	24	P	C		
10.	HARDWARE LABORATORY	TW	25	10	10	P	C		21.	SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	25	P	C		
11.	HARDWARE LABORATORY	PR	50	20	26	P	C		22.	SEMINAR AND TECHNICAL COMMUNI.	TW	50	20	20	P	C		

GRAND TOTAL = 563/1500, RESULT: FAILS

ORDN. 1 MARKS :

T80054345	AVHAD PRATIK GULAB				MAMTA				, 71045377K				T8054210		PICT			T80054345
01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	52	P	C		12.	PRINCIPLES OF PROGRAMMING LANG.	PP	100	40	48	P	C		
02.	DATA COMMUNICATION	PP	100	40	40	P	C		13.	COMPUTER NETWORKS	PP	100	40	46	P	C		
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	40	P	C		14.	FINANCE & MANAGEMENT INFORMA.SYS.	PP	100	40	52	P	C		
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	42	P	C		15.	SYSTEMS PROGRAMMING & OPERA.SYS.	PP	100	40	42	P			
05.	THEORY OF COMPUTATION	PP	100	40	40	P	C		16.	SOFTWARE ENGINEERING	PP	100	40	48	P	C		
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	38	P	C		17.	SOFTWARE LABORATORY	TW	25	10	19	P	C		
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	30	P	C		18.	SOFTWARE LABORATORY	PR	50	20	25	P			
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	17	P	C		19.	COMPUTER NETWORK	TW	25	10	19	P	C		
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	23	P	C		20.	COMPUTER NETWORK	OR	50	20	21	P	C		
10.	HARDWARE LABORATORY	TW	25	10	20	P	C		21.	SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	34	P	C		
11.	HARDWARE LABORATORY	PR	50	20	25	P	C		22.	SEMINAR AND TECHNICAL COMMUNI.	TW	50	20	37	P	C		

GRAND TOTAL = 758/1500, RESULT: SECOND CLASS

ORDN. 1 MARKS :

T80054346	AVLEEN UPPAL				INDERDEEP				, 70801350D				T8054355		PICT			T80054346
01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	46	P	C		12.	PRINCIPLES OF PROGRAMMING LANG.	PP	100	40	46	P	C		
02.	DATA COMMUNICATION	PP	100	40	AA	F			13.	COMPUTER NETWORKS	PP	100	40	52	P	C		
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	40	P	C		14.	FINANCE & MANAGEMENT INFORMA.SYS.	PP	100	40	53	P	C		
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	AA	F			15.	SYSTEMS PROGRAMMING & OPERA.SYS.	PP	100	40	40	P	C		
05.	THEORY OF COMPUTATION	PP	100	40	40	P	C		16.	SOFTWARE ENGINEERING	PP	100	40	45	P	C		
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	22	P	C		17.	SOFTWARE LABORATORY	TW	25	10	13	P	C		
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	AA	F			18.	SOFTWARE LABORATORY	PR	50	20	22	P			
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	15	P	C		19.	COMPUTER NETWORK	TW	25	10	12	P	C		
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	27	P	C		20.	COMPUTER NETWORK	OR	50	20	39	P			
10.	HARDWARE LABORATORY	TW	25	10	16	P	C		21.	SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	27	P	C		
11.	HARDWARE LABORATORY	PR	50	20	23	P	C		22.	SEMINAR AND TECHNICAL COMMUNI.	TW	50	20	38	P	C		

GRAND TOTAL = 616/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80054347	BHOSALE SHITAL MANOHAR			JAYA				, 71132426D		T8054220		PICT		T80054347
01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	45	P	C		12. PRINCIPLES OF PROGRAMMING LANG.	PP	100	40	40	P	C
02. DATA COMMUNICATION	PP	100	40	40	P	C		13. COMPUTER NETWORKS	PP	100	40	47	P	
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	40	P	C		14. FINANCE & MANAGEMENT INFORMA.SYS.	PP	100	40	46	P	C
04. DIGITAL SIGNAL PROCESSING	PP	100	40	26	F			15. SYSTEMS PROGRAMMING & OPERA.SYS.	PP	100	40	40	P	
05. THEORY OF COMPUTATION	PP	100	40	40	P	C		16. SOFTWARE ENGINEERING	PP	100	40	40	P	C
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	34	P	C		17. SOFTWARE LABORATORY	TW	25	10	15	P	C
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	22	P			18. SOFTWARE LABORATORY	PR	50	20	25	P	
08. SIGNAL PROCESSING LABORATORY	TW	25	10	15	P	C		19. COMPUTER NETWORK	TW	25	10	18	P	C
09. SIGNAL PROCESSING LABORATORY	OR	50	20	22	P	C		20. COMPUTER NETWORK	OR	50	20	21	P	C
10. HARDWARE LABORATORY	TW	25	10	18	P	C		21. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	27	P	C
11. HARDWARE LABORATORY	PR	50	20	23	P			22. SEMINAR AND TECHNICAL COMMUNI.	TW	50	20	34	P	C
GRAND TOTAL = 678/1500, RESULT: FAILS A.T.K.T.														RESULT RESERVED FOR BKLG

ORDN. 1 MARKS :

T80054349	DURGESH CHAPORKAR			SUMEDHA				, 71045423G		T8054234		PICT		T80054349
01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	58	P	C		12. PRINCIPLES OF PROGRAMMING LANG.	PP	100	40	43	P	C
02. DATA COMMUNICATION	PP	100	40	46	P	C		13. COMPUTER NETWORKS	PP	100	40	59	P	C
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	56	P	C		14. FINANCE & MANAGEMENT INFORMA.SYS.	PP	100	40	59	P	C
04. DIGITAL SIGNAL PROCESSING	PP	100	40	43	P	C		15. SYSTEMS PROGRAMMING & OPERA.SYS.	PP	100	40	44	P	C
05. THEORY OF COMPUTATION	PP	100	40	40	P	C		16. SOFTWARE ENGINEERING	PP	100	40	50	P	C
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	28	P	C		17. SOFTWARE LABORATORY	TW	25	10	10	P	C
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	30	P	C		18. SOFTWARE LABORATORY	PR	50	20	38	P	
08. SIGNAL PROCESSING LABORATORY	TW	25	10	10	P	C		19. COMPUTER NETWORK	TW	25	10	15	P	C
09. SIGNAL PROCESSING LABORATORY	OR	50	20	34	P	C		20. COMPUTER NETWORK	OR	50	20	22	P	C
10. HARDWARE LABORATORY	TW	25	10	10	P	C		21. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	20	P	C
11. HARDWARE LABORATORY	PR	50	20	32	P	C		22. SEMINAR AND TECHNICAL COMMUNI.	TW	50	20	38	P	C

GRAND TOTAL = 785/1500, RESULT: SECOND CLASS

ORDN. 1 MARKS :

T80054350	GAIKWAD MAHESH EKNATH			LATA				, 71045431H		T8054239		PICT		T80054350
01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	40	P	C		12. PRINCIPLES OF PROGRAMMING LANG.	PP	100	40	40	P	C
02. DATA COMMUNICATION	PP	100	40	42	P	C		13. COMPUTER NETWORKS	PP	100	40	40	P	C
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	42	P	C		14. FINANCE & MANAGEMENT INFORMA.SYS.	PP	100	40	46	P	C
04. DIGITAL SIGNAL PROCESSING	PP	100	40	45	P	C		15. SYSTEMS PROGRAMMING & OPERA.SYS.	PP	100	40	40	P	C
05. THEORY OF COMPUTATION	PP	100	40	41	P	C		16. SOFTWARE ENGINEERING	PP	100	40	44	P	C
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	34	P	C		17. SOFTWARE LABORATORY	TW	25	10	10	P	C
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	25	P			18. SOFTWARE LABORATORY	PR	50	20	10	F	
08. SIGNAL PROCESSING LABORATORY	TW	25	10	14	P	C		19. COMPUTER NETWORK	TW	25	10	15	P	C
09. SIGNAL PROCESSING LABORATORY	OR	50	20	20	P	C		20. COMPUTER NETWORK	OR	50	20	35	P	
10. HARDWARE LABORATORY	TW	25	10	13	P	C		21. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	20	P	C
11. HARDWARE LABORATORY	PR	50	20	AA	F			22. SEMINAR AND TECHNICAL COMMUNI.	TW	50	20	27	P	C

GRAND TOTAL = 643/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80054351	GAIKWAD SHIVRAJ VITTHALRAO				SINDHU				, 70925417C				T8054363		PICT			T80054351
01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	43	P	C		12.	PRINCIPLES OF PROGRAMMING LANG.	PP	100	40	40	P	C		
02.	DATA COMMUNICATION	PP	100	40	47	P	C		13.	COMPUTER NETWORKS	PP	100	40	50	P	C		
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	45	P	C		14.	FINANCE & MANAGEMENT INFORMA.SYS.	PP	100	40	50	P	C		
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	54	P	C		15.	SYSTEMS PROGRAMMING & OPERA.SYS.	PP	100	40	43	P	C		
05.	THEORY OF COMPUTATION	PP	100	40	46	P	C		16.	SOFTWARE ENGINEERING	PP	100	40	40	P			
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	20	P	C		17.	SOFTWARE LABORATORY	TW	25	10	10	P	C		
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	21	P	C		18.	SOFTWARE LABORATORY	PR	50	20	27	P	C		
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	16	P	C		19.	COMPUTER NETWORK	TW	25	10	14	P	C		
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	40	P	C		20.	COMPUTER NETWORK	OR	50	20	42	P	C		
10.	HARDWARE LABORATORY	TW	25	10	11	P	C		21.	SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	27	P	C		
11.	HARDWARE LABORATORY	PR	50	20	28	P	C		22.	SEMINAR AND TECHNICAL COMMUNI.	TW	50	20	28	P	C		

GRAND TOTAL = 742+08/1500, RESULT: SECOND CLASS [0.2]

ORDN. 1 MARKS :

T80054352	GOKHALE GAURAV SUHAS				SWATI				, 71132428L				T8054243		PICT			T80054352
01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	63	P	C		12.	PRINCIPLES OF PROGRAMMING LANG.	PP	100	40	56	P	C		
02.	DATA COMMUNICATION	PP	100	40	50	P	C		13.	COMPUTER NETWORKS	PP	100	40	61	P	C		
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	66	P	C		14.	FINANCE & MANAGEMENT INFORMA.SYS.	PP	100	40	66	P	C		
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	45	P	C		15.	SYSTEMS PROGRAMMING & OPERA.SYS.	PP	100	40	56	P	C		
05.	THEORY OF COMPUTATION	PP	100	40	46	P	C		16.	SOFTWARE ENGINEERING	PP	100	40	84	P	C		
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	28	P	C		17.	SOFTWARE LABORATORY	TW	25	10	10	P	C		
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	21	P	C		18.	SOFTWARE LABORATORY	PR	50	20	AA	F			
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	14	P	C		19.	COMPUTER NETWORK	TW	25	10	13	P	C		
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	AA	F			20.	COMPUTER NETWORK	OR	50	20	30	P	C		
10.	HARDWARE LABORATORY	TW	25	10	13	P	C		21.	SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	20	P	C		
11.	HARDWARE LABORATORY	PR	50	20	AA	F			22.	SEMINAR AND TECHNICAL COMMUNI.	TW	50	20	30	P	C		

GRAND TOTAL = 772/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

T80054353	JADHAV PRASHANT UTTAM				ANITA				, 71045454G				T8054246		PICT			T80054353
01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	28	F			12.	PRINCIPLES OF PROGRAMMING LANG.	PP	100	40	40	P	C		
02.	DATA COMMUNICATION	PP	100	40	40	P	C		13.	COMPUTER NETWORKS	PP	100	40	40	P	C		
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	28	F			14.	FINANCE & MANAGEMENT INFORMA.SYS.	PP	100	40	40	P			
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	15	F			15.	SYSTEMS PROGRAMMING & OPERA.SYS.	PP	100	40	23	F			
05.	THEORY OF COMPUTATION	PP	100	40	19	F			16.	SOFTWARE ENGINEERING	PP	100	40	19	F			
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	24	P	C		17.	SOFTWARE LABORATORY	TW	25	10	10	P	C		
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	32	P			18.	SOFTWARE LABORATORY	PR	50	20	20	P			
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	11	P	C		19.	COMPUTER NETWORK	TW	25	10	11	P	C		
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	20	P	C		20.	COMPUTER NETWORK	OR	50	20	28	P			
10.	HARDWARE LABORATORY	TW	25	10	13	P	C		21.	SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	20	P	C		
11.	HARDWARE LABORATORY	PR	50	20	22	P	C		22.	SEMINAR AND TECHNICAL COMMUNI.	TW	50	20	25	P	C		

GRAND TOTAL = 528/1500, RESULT: FAILS

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80054354	JAMES DADO				ANIA				, 71045460M				T8054250		PICT			T80054354
01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	40	P	C		12.	PRINCIPLES OF PROGRAMMING LANG.	PP	100	40	54	P	C		
02.	DATA COMMUNICATION	PP	100	40	48	P	C		13.	COMPUTER NETWORKS	PP	100	40	59	P	C		
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	47	P	C		14.	FINANCE & MANAGEMENT INFORMA.SYS.	PP	100	40	51	P	C		
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	40	P	C		15.	SYSTEMS PROGRAMMING & OPERA.SYS.	PP	100	40	54	P	C		
05.	THEORY OF COMPUTATION	PP	100	40	42	P	C		16.	SOFTWARE ENGINEERING	PP	100	40	56	P	C		
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	28	P	C		17.	SOFTWARE LABORATORY	TW	25	10	17	P	C		
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	20	P	C		18.	SOFTWARE LABORATORY	PR	50	20	12	F			
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	11	P	C		19.	COMPUTER NETWORK	TW	25	10	14	P	C		
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	34	P	C		20.	COMPUTER NETWORK	OR	50	20	28	P			
10.	HARDWARE LABORATORY	TW	25	10	13	P	C		21.	SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	25	P	C		
11.	HARDWARE LABORATORY	PR	50	20	25	P	C		22.	SEMINAR AND TECHNICAL COMMUNI.	TW	50	20	40	P	C		

GRAND TOTAL = 758/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

T80054355	KOTE UDAY ARUN				SUNITA				, 70925488B				T8054267		PICT			T80054355
01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	40	P	C		12.	PRINCIPLES OF PROGRAMMING LANG.	PP	100	40	46	P	C		
02.	DATA COMMUNICATION	PP	100	40	49	P	C		13.	COMPUTER NETWORKS	PP	100	40	48	P	C		
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	47	P	C		14.	FINANCE & MANAGEMENT INFORMA.SYS.	PP	100	40	55	P	C		
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	44	P	C		15.	SYSTEMS PROGRAMMING & OPERA.SYS.	PP	100	40	42	P			
05.	THEORY OF COMPUTATION	PP	100	40	40	P	C		16.	SOFTWARE ENGINEERING	PP	100	40	63	P	C		
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	34	P	C		17.	SOFTWARE LABORATORY	TW	25	10	12	P	C		
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	22	P	C		18.	SOFTWARE LABORATORY	PR	50	20	21	P	C		
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	15	P	C		19.	COMPUTER NETWORK	TW	25	10	11	P	C		
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	20	P	C		20.	COMPUTER NETWORK	OR	50	20	24	P			
10.	HARDWARE LABORATORY	TW	25	10	17	P	C		21.	SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	20	P	C		
11.	HARDWARE LABORATORY	PR	50	20	36	P	C		22.	SEMINAR AND TECHNICAL COMMUNI.	TW	50	20	34	P	C		

GRAND TOTAL = 740+10/1500, RESULT: SECOND CLASS [0.2]

RESULT RESERVED FOR BKLG

ORDN. 1 MARKS :

T80054356	KULKARNI MANASI MILIND				JYOTI				, 71045496B				T8054273		PICT			T80054356
01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	61	P	C		12.	PRINCIPLES OF PROGRAMMING LANG.	PP	100	40	40	P	C		
02.	DATA COMMUNICATION	PP	100	40	45	P	C		13.	COMPUTER NETWORKS	PP	100	40	40	P	C		
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	44	P	C		14.	FINANCE & MANAGEMENT INFORMA.SYS.	PP	100	40	57	P	C		
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	40	P	C		15.	SYSTEMS PROGRAMMING & OPERA.SYS.	PP	100	40	44	P	C		
05.	THEORY OF COMPUTATION	PP	100	40	51	P	C		16.	SOFTWARE ENGINEERING	PP	100	40	55	P	C		
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	30	P	C		17.	SOFTWARE LABORATORY	TW	25	10	20	P	C		
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	34	P	C		18.	SOFTWARE LABORATORY	PR	50	20	35	P	C		
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	17	P	C		19.	COMPUTER NETWORK	TW	25	10	18	P	C		
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	30	P	C		20.	COMPUTER NETWORK	OR	50	20	35	P			
10.	HARDWARE LABORATORY	TW	25	10	20	P	C		21.	SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	30	P	C		
11.	HARDWARE LABORATORY	PR	50	20	30	P	C		22.	SEMINAR AND TECHNICAL COMMUNI.	TW	50	20	35	P	C		

GRAND TOTAL = 811/1500, RESULT: SECOND CLASS

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80054357	KULKARNI PRATHAMESH DEEPAK				VIDYA				, 71132433G				T8054274		PICT		T80054357
01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	66	P	C		12.	PRINCIPLES OF PROGRAMMING LANG.	PP	100	40	46	P	C	
02.	DATA COMMUNICATION	PP	100	40	48	P	C		13.	COMPUTER NETWORKS	PP	100	40	60	P	C	
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	52	P	C		14.	FINANCE & MANAGEMENT INFORMA.SYS.	PP	100	40	60	P	C	
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	50	P	C		15.	SYSTEMS PROGRAMMING & OPERA.SYS.	PP	100	40	67	P	C	
05.	THEORY OF COMPUTATION	PP	100	40	59	P	C		16.	SOFTWARE ENGINEERING	PP	100	40	63	P	C	
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	41	P	C		17.	SOFTWARE LABORATORY	TW	25	10	20	P	C	
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	42	P	C		18.	SOFTWARE LABORATORY	PR	50	20	42	P	C	
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	19	P	C		19.	COMPUTER NETWORK	TW	25	10	18	P	C	
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	31	P	C		20.	COMPUTER NETWORK	OR	50	20	36	P		
10.	HARDWARE LABORATORY	TW	25	10	20	P	C		21.	SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	33	P	C	
11.	HARDWARE LABORATORY	PR	50	20	43	P	C		22.	SEMINAR AND TECHNICAL COMMUNI.	TW	50	20	35	P	C	

GRAND TOTAL = 951/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

T80054358	KUNTALWAD GNYANESH GAJJARAM				SARASWATHI				, 71045501B				T8054276		PICT		T80054358
01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	40	P	C		12.	PRINCIPLES OF PROGRAMMING LANG.	PP	100	40	40	P	C	
02.	DATA COMMUNICATION	PP	100	40	40	P	C		13.	COMPUTER NETWORKS	PP	100	40	40	P	C	
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	40	P	C		14.	FINANCE & MANAGEMENT INFORMA.SYS.	PP	100	40	41	P	C	
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	44	P	C		15.	SYSTEMS PROGRAMMING & OPERA.SYS.	PP	100	40	50	P	C	
05.	THEORY OF COMPUTATION	PP	100	40	44	P	C		16.	SOFTWARE ENGINEERING	PP	100	40	46	P	C	
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	30	P	C		17.	SOFTWARE LABORATORY	TW	25	10	14	P	C	
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	22	P	C		18.	SOFTWARE LABORATORY	PR	50	20	00	F		
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	14	P	C		19.	COMPUTER NETWORK	TW	25	10	13	P	C	
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	36	P	C		20.	COMPUTER NETWORK	OR	50	20	20	P	C	
10.	HARDWARE LABORATORY	TW	25	10	15	P	C		21.	SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	22	P	C	
11.	HARDWARE LABORATORY	PR	50	20	21	P	C		22.	SEMINAR AND TECHNICAL COMMUNI.	TW	50	20	36	P	C	

GRAND TOTAL = 668/1500, RESULT: FAILS A.T.K.T.

RESULT RESERVED FOR BKLG

ORDN. 1 MARKS :

T80054359	LATIFI AAMASH IMTIYAZ AHMED				SHAHERA				, 70925492L				T8054277		PICT		T80054359
01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	50	P	C		12.	PRINCIPLES OF PROGRAMMING LANG.	PP	100	40	40	P	C	
02.	DATA COMMUNICATION	PP	100	40	40	P	C		13.	COMPUTER NETWORKS	PP	100	40	43	P	C	
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	40	P	C		14.	FINANCE & MANAGEMENT INFORMA.SYS.	PP	100	40	46	P	C	
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	21	F			15.	SYSTEMS PROGRAMMING & OPERA.SYS.	PP	100	40	41	P	C	
05.	THEORY OF COMPUTATION	PP	100	40	48	P	C		16.	SOFTWARE ENGINEERING	PP	100	40	52	P	C	
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	26	P	C		17.	SOFTWARE LABORATORY	TW	25	10	10	P	C	
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	22	P	C		18.	SOFTWARE LABORATORY	PR	50	20	22	P		
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	11	P	C		19.	COMPUTER NETWORK	TW	25	10	11	P	C	
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	25	P	C		20.	COMPUTER NETWORK	OR	50	20	22	P		
10.	HARDWARE LABORATORY	TW	25	10	13	P	C		21.	SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	20	P	C	
11.	HARDWARE LABORATORY	PR	50	20	20	P	C		22.	SEMINAR AND TECHNICAL COMMUNI.	TW	50	20	24	P	C	

GRAND TOTAL = 647/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80054360	MAHARANWAR NAMITA ANILRAO				RENUKA				, 71045510M			T8054279		PICT		T80054360
01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	57	P	C		12.	PRINCIPLES OF PROGRAMMING LANG.	PP	100	40	51	P	C
02.	DATA COMMUNICATION	PP	100	40	47	P	C		13.	COMPUTER NETWORKS	PP	100	40	47	P	C
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	57	P	C		14.	FINANCE & MANAGEMENT INFORMA.SYS.	PP	100	40	56	P	C
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	46	P	C		15.	SYSTEMS PROGRAMMING & OPERA.SYS.	PP	100	40	69	P	C
05.	THEORY OF COMPUTATION	PP	100	40	61	P	C		16.	SOFTWARE ENGINEERING	PP	100	40	62	P	C
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	40	P	C		17.	SOFTWARE LABORATORY	TW	25	10	18	P	C
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	20	P	C		18.	SOFTWARE LABORATORY	PR	50	20	31	P	C
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	19	P	C		19.	COMPUTER NETWORK	TW	25	10	18	P	C
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	22	P	C		20.	COMPUTER NETWORK	OR	50	20	35	P	
10.	HARDWARE LABORATORY	TW	25	10	20	P	C		21.	SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	32	P	C
11.	HARDWARE LABORATORY	PR	50	20	26	P	C		22.	SEMINAR AND TECHNICAL COMMUNI.	TW	50	20	41	P	C

GRAND TOTAL = 875/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

T80054361	MANE OM ARUN				RAJANI				, 70925499H			T8054283		PICT		T80054361
01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	40	P	C		12.	PRINCIPLES OF PROGRAMMING LANG.	PP	100	40	32#	P	
02.	DATA COMMUNICATION	PP	100	40	40	P	C		13.	COMPUTER NETWORKS	PP	100	40	48	P	
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	40	P	C		14.	FINANCE & MANAGEMENT INFORMA.SYS.	PP	100	40	48	P	C
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	40	P	C		15.	SYSTEMS PROGRAMMING & OPERA.SYS.	PP	100	40	40	P	C
05.	THEORY OF COMPUTATION	PP	100	40	45	P	C		16.	SOFTWARE ENGINEERING	PP	100	40	45	P	C
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	24	P	C		17.	SOFTWARE LABORATORY	TW	25	10	10	P	C
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	22	P	C		18.	SOFTWARE LABORATORY	PR	50	20	28	P	C
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	11	P	C		19.	COMPUTER NETWORK	TW	25	10	11	P	C
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	21	P	C		20.	COMPUTER NETWORK	OR	50	20	26	P	
10.	HARDWARE LABORATORY	TW	25	10	14	P	C		21.	SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	20	P	C
11.	HARDWARE LABORATORY	PR	50	20	28	P	C		22.	SEMINAR AND TECHNICAL COMMUNI.	TW	50	20	28	P	C

GRAND TOTAL = 661/1500, RESULT: PASS CLASS # [0.4]

ORDN. 1 MARKS :

T80054362	MASKE SHRUTI HIRALAL				SANGEETA				, 71045523C			T8054286		PICT		T80054362
01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	40	P	C		12.	PRINCIPLES OF PROGRAMMING LANG.	PP	100	40	45	P	C
02.	DATA COMMUNICATION	PP	100	40	44	P	C		13.	COMPUTER NETWORKS	PP	100	40	53	P	
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	47	P	C		14.	FINANCE & MANAGEMENT INFORMA.SYS.	PP	100	40	42	P	C
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	42	P	C		15.	SYSTEMS PROGRAMMING & OPERA.SYS.	PP	100	40	49	P	C
05.	THEORY OF COMPUTATION	PP	100	40	48	P	C		16.	SOFTWARE ENGINEERING	PP	100	40	46	P	C
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	40	P	C		17.	SOFTWARE LABORATORY	TW	25	10	19	P	C
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	35	P	C		18.	SOFTWARE LABORATORY	PR	50	20	28	P	C
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	19	P	C		19.	COMPUTER NETWORK	TW	25	10	16	P	C
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	36	P	C		20.	COMPUTER NETWORK	OR	50	20	32	P	
10.	HARDWARE LABORATORY	TW	25	10	19	P	C		21.	SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	32	P	C
11.	HARDWARE LABORATORY	PR	50	20	27	P	C		22.	SEMINAR AND TECHNICAL COMMUNI.	TW	50	20	42	P	C

GRAND TOTAL = 801/1500, RESULT: SECOND CLASS

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80054363	MUSALE SAGAR RAJENDRA				KALPANA				, 71045534J				T8054288		PICT		T80054363
01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	52	P	C		12.	PRINCIPLES OF PROGRAMMING LANG.	PP	100	40	55	P	C	
02.	DATA COMMUNICATION	PP	100	40	41	P	C		13.	COMPUTER NETWORKS	PP	100	40	40	P	C	
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	52	P	C		14.	FINANCE & MANAGEMENT INFORMA.SYS.	PP	100	40	46	P	C	
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	46	P	C		15.	SYSTEMS PROGRAMMING & OPERA.SYS.	PP	100	40	52	P	C	
05.	THEORY OF COMPUTATION	PP	100	40	40	P	C		16.	SOFTWARE ENGINEERING	PP	100	40	41	P	C	
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	40	P	C		17.	SOFTWARE LABORATORY	TW	25	10	19	P	C	
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	39	P	C		18.	SOFTWARE LABORATORY	PR	50	20	42	P	C	
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	17	P	C		19.	COMPUTER NETWORK	TW	25	10	21	P	C	
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	34	P	C		20.	COMPUTER NETWORK	OR	50	20	30	P		
10.	HARDWARE LABORATORY	TW	25	10	19	P	C		21.	SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	34	P	C	
11.	HARDWARE LABORATORY	PR	50	20	38	P	C		22.	SEMINAR AND TECHNICAL COMMUNI.	TW	50	20	42	P	C	

GRAND TOTAL = 840/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

T80054364	NAJAN KAUSTUBH GORAKSHANATH				MEERA				, 71045540C				T8054292		PICT		T80054364
01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	40	P	C		12.	PRINCIPLES OF PROGRAMMING LANG.	PP	100	40	40	P	C	
02.	DATA COMMUNICATION	PP	100	40	42	P	C		13.	COMPUTER NETWORKS	PP	100	40	41	P	C	
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	40	P	C		14.	FINANCE & MANAGEMENT INFORMA.SYS.	PP	100	40	40	P	C	
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	40	P	C		15.	SYSTEMS PROGRAMMING & OPERA.SYS.	PP	100	40	45	P	C	
05.	THEORY OF COMPUTATION	PP	100	40	41	P	C		16.	SOFTWARE ENGINEERING	PP	100	40	49	P	C	
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	45	P	C		17.	SOFTWARE LABORATORY	TW	25	10	20	P	C	
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	34	P	C		18.	SOFTWARE LABORATORY	PR	50	20	40	P	C	
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	17	P	C		19.	COMPUTER NETWORK	TW	25	10	21	P	C	
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	20	P	C		20.	COMPUTER NETWORK	OR	50	20	28	P		
10.	HARDWARE LABORATORY	TW	25	10	20	P	C		21.	SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	35	P	C	
11.	HARDWARE LABORATORY	PR	50	20	35	P	C		22.	SEMINAR AND TECHNICAL COMMUNI.	TW	50	20	36	P	C	

GRAND TOTAL = 769/1500, RESULT: SECOND CLASS

ORDN. 1 MARKS :

T80054365	NANWANI SHRUTI RAMESH				KAMLESH				, 71045541M				T8054294		PICT		T80054365
01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	67	P	C		12.	PRINCIPLES OF PROGRAMMING LANG.	PP	100	40	60	P	C	
02.	DATA COMMUNICATION	PP	100	40	45	P	C		13.	COMPUTER NETWORKS	PP	100	40	58	P	C	
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	62	P	C		14.	FINANCE & MANAGEMENT INFORMA.SYS.	PP	100	40	59	P	C	
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	61	P	C		15.	SYSTEMS PROGRAMMING & OPERA.SYS.	PP	100	40	60	P	C	
05.	THEORY OF COMPUTATION	PP	100	40	62	P	C		16.	SOFTWARE ENGINEERING	PP	100	40	66	P	C	
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	40	P	C		17.	SOFTWARE LABORATORY	TW	25	10	17	P	C	
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	39	P	C		18.	SOFTWARE LABORATORY	PR	50	20	36	P	C	
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	18	P	C		19.	COMPUTER NETWORK	TW	25	10	19	P	C	
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	38	P	C		20.	COMPUTER NETWORK	OR	50	20	33	P		
10.	HARDWARE LABORATORY	TW	25	10	20	P	C		21.	SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	25	P	C	
11.	HARDWARE LABORATORY	PR	50	20	31	P	C		22.	SEMINAR AND TECHNICAL COMMUNI.	TW	50	20	39	P	C	

GRAND TOTAL = 955/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80054366 NITYA RAJ SARASWATHY , 71045550L , T8054299 , PICT , T80054366

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	59	P C	12. PRINCIPLES OF PROGRAMMING LANG.	PP	100	40	41	P C
02. DATA COMMUNICATION	PP	100	40	40	P C	13. COMPUTER NETWORKS	PP	100	40	49	P C
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	44	P C	14. FINANCE & MANAGEMENT INFORMA.SYS.	PP	100	40	51	P C
04. DIGITAL SIGNAL PROCESSING	PP	100	40	60	P C	15. SYSTEMS PROGRAMMING & OPERA.SYS.	PP	100	40	64	P C
05. THEORY OF COMPUTATION	PP	100	40	51	P C	16. SOFTWARE ENGINEERING	PP	100	40	51	P C
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	38	P C	17. SOFTWARE LABORATORY	TW	25	10	20	P C
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	43	P C	18. SOFTWARE LABORATORY	PR	50	20	36	P C
08. SIGNAL PROCESSING LABORATORY	TW	25	10	20	P C	19. COMPUTER NETWORK	TW	25	10	23	P C
09. SIGNAL PROCESSING LABORATORY	OR	50	20	26	P C	20. COMPUTER NETWORK	OR	50	20	34	P
10. HARDWARE LABORATORY	TW	25	10	20	P C	21. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	40	P C
11. HARDWARE LABORATORY	PR	50	20	35	P C	22. SEMINAR AND TECHNICAL COMMUNI.	TW	50	20	43	P C

GRAND TOTAL = 888/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

T80054367 PANDYA PARTH VIPUL ASHA , 71045553E , T8054300 , PICT , T80054367

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	50	P C	12. PRINCIPLES OF PROGRAMMING LANG.	PP	100	40	48	P C
02. DATA COMMUNICATION	PP	100	40	41	P C	13. COMPUTER NETWORKS	PP	100	40	50	P C
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	48	P C	14. FINANCE & MANAGEMENT INFORMA.SYS.	PP	100	40	43	P C
04. DIGITAL SIGNAL PROCESSING	PP	100	40	40	P C	15. SYSTEMS PROGRAMMING & OPERA.SYS.	PP	100	40	70	P C
05. THEORY OF COMPUTATION	PP	100	40	60	P C	16. SOFTWARE ENGINEERING	PP	100	40	47	P C
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	28	P C	17. SOFTWARE LABORATORY	TW	25	10	16	P C
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	33	P C	18. SOFTWARE LABORATORY	PR	50	20	28	P C
08. SIGNAL PROCESSING LABORATORY	TW	25	10	14	P C	19. COMPUTER NETWORK	TW	25	10	18	P C
09. SIGNAL PROCESSING LABORATORY	OR	50	20	33	P C	20. COMPUTER NETWORK	OR	50	20	40	P
10. HARDWARE LABORATORY	TW	25	10	17	P C	21. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	27	P C
11. HARDWARE LABORATORY	PR	50	20	25	P C	22. SEMINAR AND TECHNICAL COMMUNI.	TW	50	20	31	P C

GRAND TOTAL = 807/1500, RESULT: SECOND CLASS

ORDN. 1 MARKS :

T80054368 PATIL SWAPNIL PRABHAKAR URMILA , 70601435K , T8054380 , PICT , T80054368

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	40	P C	12. PRINCIPLES OF PROGRAMMING LANG.	PP	100	40	40	P C
02. DATA COMMUNICATION	PP	100	40	40	P C	13. COMPUTER NETWORKS	PP	100	40	16	F
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	01	F	14. FINANCE & MANAGEMENT INFORMA.SYS.	PP	100	40	46	P C
04. DIGITAL SIGNAL PROCESSING	PP	100	40	AA	F	15. SYSTEMS PROGRAMMING & OPERA.SYS.	PP	100	40	42	P C
05. THEORY OF COMPUTATION	PP	100	40	AA	F	16. SOFTWARE ENGINEERING	PP	100	40	40	P C
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	21	P C	17. SOFTWARE LABORATORY	TW	25	10	10	P C
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	25	P C	18. SOFTWARE LABORATORY	PR	50	20	24	P C
08. SIGNAL PROCESSING LABORATORY	TW	25	10	13	P C	19. COMPUTER NETWORK	TW	25	10	14	P C
09. SIGNAL PROCESSING LABORATORY	OR	50	20	21	P C	20. COMPUTER NETWORK	OR	50	20	22	P C
10. HARDWARE LABORATORY	TW	25	10	14	P C	21. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	32	P C
11. HARDWARE LABORATORY	PR	50	20	24	P C	22. SEMINAR AND TECHNICAL COMMUNI.	TW	50	20	37	P C

GRAND TOTAL = 522/1500, RESULT: FAILS

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80054369	PATIL VINEET JAYANT				PRAJAKTA				, 70601438D				T8054381		PICT			T80054369
01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	46	P C	12. PRINCIPLES OF PROGRAMMING LANG.	PP	100	40	45	P C							
02. DATA COMMUNICATION	PP	100	40	AA	F	13. COMPUTER NETWORKS	PP	100	40	44	P C							
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	AA	F	14. FINANCE & MANAGEMENT INFORMA.SYS.	PP	100	40	46	P C							
04. DIGITAL SIGNAL PROCESSING	PP	100	40	41	P C	15. SYSTEMS PROGRAMMING & OPERA.SYS.	PP	100	40	62	P C							
05. THEORY OF COMPUTATION	PP	100	40	44	P C	16. SOFTWARE ENGINEERING	PP	100	40	40	P C							
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	20	P C	17. SOFTWARE LABORATORY	TW	25	10	10	P C							
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	AA	F	18. SOFTWARE LABORATORY	PR	50	20	AA	F							
08. SIGNAL PROCESSING LABORATORY	TW	25	10	11	P C	19. COMPUTER NETWORK	TW	25	10	12	P C							
09. SIGNAL PROCESSING LABORATORY	OR	50	20	22	P C	20. COMPUTER NETWORK	OR	50	20	21	P C							
10. HARDWARE LABORATORY	TW	25	10	10	P C	21. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	21	P C							
11. HARDWARE LABORATORY	PR	50	20	27	P C	22. SEMINAR AND TECHNICAL COMMUNI.	TW	50	20	21	P C							

GRAND TOTAL = 543/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

T80054370	PAWAR AJINKYA NANDU				VIDYA				, 71045570E				T8054307		PICT			T80054370
01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	45	P C	12. PRINCIPLES OF PROGRAMMING LANG.	PP	100	40	AA	F							
02. DATA COMMUNICATION	PP	100	40	51	P C	13. COMPUTER NETWORKS	PP	100	40	AA	F							
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	41	P C	14. FINANCE & MANAGEMENT INFORMA.SYS.	PP	100	40	50	P C							
04. DIGITAL SIGNAL PROCESSING	PP	100	40	AA	F	15. SYSTEMS PROGRAMMING & OPERA.SYS.	PP	100	40	46	P C							
05. THEORY OF COMPUTATION	PP	100	40	40	P C	16. SOFTWARE ENGINEERING	PP	100	40	42	P C							
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	24	P C	17. SOFTWARE LABORATORY	TW	25	10	10	P C							
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	28	P C	18. SOFTWARE LABORATORY	PR	50	20	05	F							
08. SIGNAL PROCESSING LABORATORY	TW	25	10	11	P C	19. COMPUTER NETWORK	TW	25	10	11	P C							
09. SIGNAL PROCESSING LABORATORY	OR	50	20	20	P C	20. COMPUTER NETWORK	OR	50	20	29	P							
10. HARDWARE LABORATORY	TW	25	10	11	P C	21. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	20	P C							
11. HARDWARE LABORATORY	PR	50	20	22	P C	22. SEMINAR AND TECHNICAL COMMUNI.	TW	50	20	29	P C							

GRAND TOTAL = 535/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

T80054371	PAWAR HARISH MANGESH				RENUKA				, 71132438H				T8054308		PICT			T80054371
01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	50	P C	12. PRINCIPLES OF PROGRAMMING LANG.	PP	100	40	60	P C							
02. DATA COMMUNICATION	PP	100	40	49	P C	13. COMPUTER NETWORKS	PP	100	40	59	P C							
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	52	P C	14. FINANCE & MANAGEMENT INFORMA.SYS.	PP	100	40	64	P C							
04. DIGITAL SIGNAL PROCESSING	PP	100	40	53	P C	15. SYSTEMS PROGRAMMING & OPERA.SYS.	PP	100	40	67	P C							
05. THEORY OF COMPUTATION	PP	100	40	40	P C	16. SOFTWARE ENGINEERING	PP	100	40	72	P C							
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	35	P C	17. SOFTWARE LABORATORY	TW	25	10	14	P C							
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	38	P C	18. SOFTWARE LABORATORY	PR	50	20	25	P C							
08. SIGNAL PROCESSING LABORATORY	TW	25	10	14	P C	19. COMPUTER NETWORK	TW	25	10	14	P C							
09. SIGNAL PROCESSING LABORATORY	OR	50	20	25	P C	20. COMPUTER NETWORK	OR	50	20	29	P							
10. HARDWARE LABORATORY	TW	25	10	16	P C	21. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	29	P C							
11. HARDWARE LABORATORY	PR	50	20	33	P C	22. SEMINAR AND TECHNICAL COMMUNI.	TW	50	20	28	P C							

GRAND TOTAL = 866/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80054372	POORVI ARVIND DHARWAD				ROHINI				, 70925566H			T8054310		PICT		T80054372
01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	45	P			12.	PRINCIPLES OF PROGRAMMING LANG.	PP	100	40	40	P	
02.	DATA COMMUNICATION	PP	100	40	40	P C			13.	COMPUTER NETWORKS	PP	100	40	40	P	
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	40	P C			14.	FINANCE & MANAGEMENT INFORMA.SYS.	PP	100	40	40	P C	
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	40	P			15.	SYSTEMS PROGRAMMING & OPERA.SYS.	PP	100	40	31#	P	
05.	THEORY OF COMPUTATION	PP	100	40	40	P C			16.	SOFTWARE ENGINEERING	PP	100	40	40	P C	
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	32	P C			17.	SOFTWARE LABORATORY	TW	25	10	18	P C	
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	26	P C			18.	SOFTWARE LABORATORY	PR	50	20	21	P	
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	20	P C			19.	COMPUTER NETWORK	TW	25	10	17	P C	
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	20	P C			20.	COMPUTER NETWORK	OR	50	20	32	P	
10.	HARDWARE LABORATORY	TW	25	10	21	P C			21.	SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	29	P C	
11.	HARDWARE LABORATORY	PR	50	20	21	P			22.	SEMINAR AND TECHNICAL COMMUNI.	TW	50	20	41	P C	
GRAND TOTAL = 694/1500, RESULT: PASS CLASS # [0.4]																RESULT RESERVED FOR BKLG

ORDN. 1 MARKS :

T80054373	RAWALE SUHAS SUDAMRAO				RATNMALA				, 71045588H			T8054317		PICT		T80054373
01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	40	P C			12.	PRINCIPLES OF PROGRAMMING LANG.	PP	100	40	43	P C	
02.	DATA COMMUNICATION	PP	100	40	43	P C			13.	COMPUTER NETWORKS	PP	100	40	46	P C	
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	40	P C			14.	FINANCE & MANAGEMENT INFORMA.SYS.	PP	100	40	42	P C	
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	54	P C			15.	SYSTEMS PROGRAMMING & OPERA.SYS.	PP	100	40	47	P C	
05.	THEORY OF COMPUTATION	PP	100	40	40	P C			16.	SOFTWARE ENGINEERING	PP	100	40	59	P C	
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	35	P C			17.	SOFTWARE LABORATORY	TW	25	10	14	P C	
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	32	P			18.	SOFTWARE LABORATORY	PR	50	20	25	P	
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	14	P C			19.	COMPUTER NETWORK	TW	25	10	14	P C	
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	25	P C			20.	COMPUTER NETWORK	OR	50	20	28	P	
10.	HARDWARE LABORATORY	TW	25	10	13	P C			21.	SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	24	P C	
11.	HARDWARE LABORATORY	PR	50	20	23	P C			22.	SEMINAR AND TECHNICAL COMMUNI.	TW	50	20	32	P C	

GRAND TOTAL = 733/1500, RESULT: PASS CLASS

ORDN. 1 MARKS :

T80054374	SAKORE MITHILA RAMNATH				MADHURI				, 71045593D			T8054318		PICT		T80054374
01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	71	P C			12.	PRINCIPLES OF PROGRAMMING LANG.	PP	100	40	56	P C	
02.	DATA COMMUNICATION	PP	100	40	63	P C			13.	COMPUTER NETWORKS	PP	100	40	58	P C	
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	49	P C			14.	FINANCE & MANAGEMENT INFORMA.SYS.	PP	100	40	65	P C	
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	40	P C			15.	SYSTEMS PROGRAMMING & OPERA.SYS.	PP	100	40	61	P C	
05.	THEORY OF COMPUTATION	PP	100	40	65	P C			16.	SOFTWARE ENGINEERING	PP	100	40	76	P C	
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	39	P C			17.	SOFTWARE LABORATORY	TW	25	10	14	P C	
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	28	P C			18.	SOFTWARE LABORATORY	PR	50	20	29	P C	
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	18	P C			19.	COMPUTER NETWORK	TW	25	10	15	P C	
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	24	P C			20.	COMPUTER NETWORK	OR	50	20	32	P	
10.	HARDWARE LABORATORY	TW	25	10	19	P C			21.	SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	29	P C	
11.	HARDWARE LABORATORY	PR	50	20	21	P C			22.	SEMINAR AND TECHNICAL COMMUNI.	TW	50	20	40	P C	

GRAND TOTAL = 912/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80054375	SANDEEP AGARWAL				RITA				, 70925587L			T8054319		PICT		T80054375
01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	47	P C	12. PRINCIPLES OF PROGRAMMING LANG.	PP	100	40	19	F					
02. DATA COMMUNICATION	PP	100	40	40	P C	13. COMPUTER NETWORKS	PP	100	40	41	P C					
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	AA	F	14. FINANCE & MANAGEMENT INFORMA.SYS.	PP	100	40	40	P C					
04. DIGITAL SIGNAL PROCESSING	PP	100	40	13	F	15. SYSTEMS PROGRAMMING & OPERA.SYS.	PP	100	40	11	F					
05. THEORY OF COMPUTATION	PP	100	40	50	P C	16. SOFTWARE ENGINEERING	PP	100	40	44	P C					
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	34	P C	17. SOFTWARE LABORATORY	TW	25	10	10	P C					
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	20	P C	18. SOFTWARE LABORATORY	PR	50	20	22	P C					
08. SIGNAL PROCESSING LABORATORY	TW	25	10	15	P C	19. COMPUTER NETWORK	TW	25	10	11	P C					
09. SIGNAL PROCESSING LABORATORY	OR	50	20	22	P C	20. COMPUTER NETWORK	OR	50	20	21	P C					
10. HARDWARE LABORATORY	TW	25	10	17	P C	21. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	20	P C					
11. HARDWARE LABORATORY	PR	50	20	30	P C	22. SEMINAR AND TECHNICAL COMMUNI.	TW	50	20	24	P C					
GRAND TOTAL = 551/1500, RESULT: FAILS											RESULT RESERVED FOR BKLG					

ORDN. 1 MARKS :

T80054376	SHASHANK KUMAR GUPTA				ANJANI				, 70925598F			T8054326		PICT		T80054376
01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	44	P C	12. PRINCIPLES OF PROGRAMMING LANG.	PP	100	40	40	P C					
02. DATA COMMUNICATION	PP	100	40	44	P C	13. COMPUTER NETWORKS	PP	100	40	43	P C					
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	40	P C	14. FINANCE & MANAGEMENT INFORMA.SYS.	PP	100	40	48	P C					
04. DIGITAL SIGNAL PROCESSING	PP	100	40	44	P C	15. SYSTEMS PROGRAMMING & OPERA.SYS.	PP	100	40	42	P C					
05. THEORY OF COMPUTATION	PP	100	40	45	P C	16. SOFTWARE ENGINEERING	PP	100	40	57	P C					
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	25	P C	17. SOFTWARE LABORATORY	TW	25	10	10	P C					
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	33	P C	18. SOFTWARE LABORATORY	PR	50	20	22	P C					
08. SIGNAL PROCESSING LABORATORY	TW	25	10	12	P C	19. COMPUTER NETWORK	TW	25	10	11	P C					
09. SIGNAL PROCESSING LABORATORY	OR	50	20	20	P C	20. COMPUTER NETWORK	OR	50	20	28	P					
10. HARDWARE LABORATORY	TW	25	10	16	P C	21. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	20	P C					
11. HARDWARE LABORATORY	PR	50	20	20	P C	22. SEMINAR AND TECHNICAL COMMUNI.	TW	50	20	23	P C					

GRAND TOTAL = 687/1500, RESULT: PASS CLASS

ORDN. 1 MARKS :

T80054377	SHERKAR ANIRUDH UMAKANT				SMITA				, 70925601K			T8054328		PICT		T80054377
01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	40	P C	12. PRINCIPLES OF PROGRAMMING LANG.	PP	100	40	AA	F					
02. DATA COMMUNICATION	PP	100	40	40	P C	13. COMPUTER NETWORKS	PP	100	40	40	P C					
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	40	P C	14. FINANCE & MANAGEMENT INFORMA.SYS.	PP	100	40	40	P C					
04. DIGITAL SIGNAL PROCESSING	PP	100	40	23	F	15. SYSTEMS PROGRAMMING & OPERA.SYS.	PP	100	40	43	P C					
05. THEORY OF COMPUTATION	PP	100	40	42	P C	16. SOFTWARE ENGINEERING	PP	100	40	44	P C					
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	40	P C	17. SOFTWARE LABORATORY	TW	25	10	19	P C					
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	42	P C	18. SOFTWARE LABORATORY	PR	50	20	34	P C					
08. SIGNAL PROCESSING LABORATORY	TW	25	10	19	P C	19. COMPUTER NETWORK	TW	25	10	15	P C					
09. SIGNAL PROCESSING LABORATORY	OR	50	20	32	P C	20. COMPUTER NETWORK	OR	50	20	28	P					
10. HARDWARE LABORATORY	TW	25	10	21	P C	21. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	29	P C					
11. HARDWARE LABORATORY	PR	50	20	33	P C	22. SEMINAR AND TECHNICAL COMMUNI.	TW	50	20	38	P C					

GRAND TOTAL = 702/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80054378 SHINDE AMIT KEDARNATH SANGEETA , 71045614L , T8054329 , PICT , T80054378

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	52	P C	12. PRINCIPLES OF PROGRAMMING LANG.	PP	100	40	40	P C
02. DATA COMMUNICATION	PP	100	40	42	P C	13. COMPUTER NETWORKS	PP	100	40	40	P C
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	56	P	14. FINANCE & MANAGEMENT INFORMA.SYS.	PP	100	40	42	P C
04. DIGITAL SIGNAL PROCESSING	PP	100	40	26	F	15. SYSTEMS PROGRAMMING & OPERA.SYS.	PP	100	40	43	P C
05. THEORY OF COMPUTATION	PP	100	40	45	P C	16. SOFTWARE ENGINEERING	PP	100	40	49	P C
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	38	P C	17. SOFTWARE LABORATORY	TW	25	10	21	P C
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	28	P C	18. SOFTWARE LABORATORY	PR	50	20	40	P C
08. SIGNAL PROCESSING LABORATORY	TW	25	10	15	P C	19. COMPUTER NETWORK	TW	25	10	17	P C
09. SIGNAL PROCESSING LABORATORY	OR	50	20	20	P C	20. COMPUTER NETWORK	OR	50	20	41	P C
10. HARDWARE LABORATORY	TW	25	10	17	P C	21. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	37	P C
11. HARDWARE LABORATORY	PR	50	20	36	P C	22. SEMINAR AND TECHNICAL COMMUNI.	TW	50	20	33	P C

GRAND TOTAL = 778/1500, RESULT: FAILS A.T.K.T. RESULT RESERVED FOR BKLG

ORDN. 1 MARKS :

T80054379 SHUBHAM SINGH JAISHREE , 70601498H , T8054387 , PICT , T80054379

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	AA	F
02. DATA COMMUNICATION	PP	100	40	AA	F
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	AA	F
04. DIGITAL SIGNAL PROCESSING	PP	100	40	AA	F
05. THEORY OF COMPUTATION	PP	100	40	AA	F
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	20	P C
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	AA	F
08. SIGNAL PROCESSING LABORATORY	TW	25	10	11	P C
09. SIGNAL PROCESSING LABORATORY	OR	50	20	20	P C
10. HARDWARE LABORATORY	TW	25	10	10	P C
11. HARDWARE LABORATORY	PR	50	20	20	P C

FIRST TERM TOTAL = 81/750.

ORDN. 1 MARKS :

T80054380 SOUNDANKAR KOMAL RAJENDRA KALPANA , 71045634E , T8054336 , PICT , T80054380

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	40	P C	12. PRINCIPLES OF PROGRAMMING LANG.	PP	100	40	45	P C
02. DATA COMMUNICATION	PP	100	40	42	P C	13. COMPUTER NETWORKS	PP	100	40	48	P C
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	42	P C	14. FINANCE & MANAGEMENT INFORMA.SYS.	PP	100	40	53	P C
04. DIGITAL SIGNAL PROCESSING	PP	100	40	40	P C	15. SYSTEMS PROGRAMMING & OPERA.SYS.	PP	100	40	54	P C
05. THEORY OF COMPUTATION	PP	100	40	40	P C	16. SOFTWARE ENGINEERING	PP	100	40	60	P C
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	34	P C	17. SOFTWARE LABORATORY	TW	25	10	14	P C
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	24	P C	18. SOFTWARE LABORATORY	PR	50	20	32	P C
08. SIGNAL PROCESSING LABORATORY	TW	25	10	15	P C	19. COMPUTER NETWORK	TW	25	10	15	P C
09. SIGNAL PROCESSING LABORATORY	OR	50	20	21	P C	20. COMPUTER NETWORK	OR	50	20	27	P
10. HARDWARE LABORATORY	TW	25	10	19	P C	21. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	25	P C
11. HARDWARE LABORATORY	PR	50	20	25	P C	22. SEMINAR AND TECHNICAL COMMUNI.	TW	50	20	41	P C

GRAND TOTAL = 756/1500, RESULT: SECOND CLASS

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80054381 THAKARE SAGAR SATISH JAYASHREE , 70925632K , T8054343 , PICT , T80054381

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	42	P C	12. PRINCIPLES OF PROGRAMMING LANG.	PP	100	40	45	P C
02. DATA COMMUNICATION	PP	100	40	47	P C	13. COMPUTER NETWORKS	PP	100	40	40	P C
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	40	P	14. FINANCE & MANAGEMENT INFORMA.SYS.	PP	100	40	48	P C
04. DIGITAL SIGNAL PROCESSING	PP	100	40	23	F	15. SYSTEMS PROGRAMMING & OPERA.SYS.	PP	100	40	59	P C
05. THEORY OF COMPUTATION	PP	100	40	40	P C	16. SOFTWARE ENGINEERING	PP	100	40	47	P C
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	30	P C	17. SOFTWARE LABORATORY	TW	25	10	10	P C
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	AA	F	18. SOFTWARE LABORATORY	PR	50	20	20\$	P C
08. SIGNAL PROCESSING LABORATORY	TW	25	10	12	P C	19. COMPUTER NETWORK	TW	25	10	11	P C
09. SIGNAL PROCESSING LABORATORY	OR	50	20	25	P C	20. COMPUTER NETWORK	OR	50	20	21	P C
10. HARDWARE LABORATORY	TW	25	10	15	P C	21. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	20	P C
11. HARDWARE LABORATORY	PR	50	20	21	P	22. SEMINAR AND TECHNICAL COMMUNI.	TW	50	20	33	P C

GRAND TOTAL = 649/1500, RESULT: FAILS A.T.K.T. [\$ 0.1] RESULT RESERVED FOR BKL

ORDN. 1 MARKS : (18)2,

T80054382 UTKARSH MISHRA MAMTA , 71045654K , T8054346 , PICT , T80054382

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	56	P C	12. PRINCIPLES OF PROGRAMMING LANG.	PP	100	40	51	P C
02. DATA COMMUNICATION	PP	100	40	62	P C	13. COMPUTER NETWORKS	PP	100	40	42	P C
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	40	P C	14. FINANCE & MANAGEMENT INFORMA.SYS.	PP	100	40	58	P C
04. DIGITAL SIGNAL PROCESSING	PP	100	40	50	P C	15. SYSTEMS PROGRAMMING & OPERA.SYS.	PP	100	40	67	P C
05. THEORY OF COMPUTATION	PP	100	40	46	P C	16. SOFTWARE ENGINEERING	PP	100	40	51	P C
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	20	P C	17. SOFTWARE LABORATORY	TW	25	10	10	P C
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	26	P C	18. SOFTWARE LABORATORY	PR	50	20	26	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	14	P C	19. COMPUTER NETWORK	TW	25	10	11	P C
09. SIGNAL PROCESSING LABORATORY	OR	50	20	20	P C	20. COMPUTER NETWORK	OR	50	20	21	P C
10. HARDWARE LABORATORY	TW	25	10	16	P C	21. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	20	P C
11. HARDWARE LABORATORY	PR	50	20	22	P	22. SEMINAR AND TECHNICAL COMMUNI.	TW	50	20	21	P C

GRAND TOTAL = 750/1500, RESULT: SECOND CLASS

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80058501 AAYUSH FUSKELAY SHASHI , 71100711L , , PICT , T80058501

01. OPERATING SYSTEM	PP	100	40	72	P
02. THEORY OF COMPUTATION	PP	100	40	54	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	57	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	71	P
05. SOFTWARE ENGINEERING	PP	100	40	53	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	21	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	35	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	18	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	38	P
10. NETWORK LABORATORY	TW	25	10	18	P
11. NETWORK LABORATORY	OR	50	20	39	P
12. SOFT SKILLS LABORATORY	TW	25	10	18	P

FIRST TERM TOTAL = 494/750.

ORDN. 1 MARKS :

T80058502 ABHISHEK BHATTACHARJEE MOUSUMI , 71100713G , , PICT , T80058502

01. OPERATING SYSTEM	PP	100	40	71	P
02. THEORY OF COMPUTATION	PP	100	40	74	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	68	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	65	P
05. SOFTWARE ENGINEERING	PP	100	40	53	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	22	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	40	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	20	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	40	P
10. NETWORK LABORATORY	TW	25	10	20	P
11. NETWORK LABORATORY	OR	50	20	40	P
12. SOFT SKILLS LABORATORY	TW	25	10	20	P

FIRST TERM TOTAL = 533/750.

ORDN. 1 MARKS :

T80058503 ADEP AKASH RAJENDRA KALPANA , 71100717K , , PICT , T80058503

01. OPERATING SYSTEM	PP	100	40	30	F
02. THEORY OF COMPUTATION	PP	100	40	44	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	51	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	48	P
05. SOFTWARE ENGINEERING	PP	100	40	47	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	19	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	31	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	19	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	36	P
10. NETWORK LABORATORY	TW	25	10	13	P
11. NETWORK LABORATORY	OR	50	20	31	P
12. SOFT SKILLS LABORATORY	TW	25	10	16	P

FIRST TERM TOTAL = 385/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80058504 ADITYA SINGH SOLANKI DIMPLE , 71100719F , , PICT , T80058504

01. OPERATING SYSTEM	PP	100	40	40	P
02. THEORY OF COMPUTATION	PP	100	40	25	F
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	46	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	40	P
05. SOFTWARE ENGINEERING	PP	100	40	43	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	15	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	10	F
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	13	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	05	F
10. NETWORK LABORATORY	TW	25	10	15	P
11. NETWORK LABORATORY	OR	50	20	10	F
12. SOFT SKILLS LABORATORY	TW	25	10	14	P

FIRST TERM TOTAL = 276/750.

ORDN. 1 MARKS :

T80058505 AGARWAL RUHI DINESH RADHA , 71100720K , , PICT , T80058505

01. OPERATING SYSTEM	PP	100	40	81	P
02. THEORY OF COMPUTATION	PP	100	40	58	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	63	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	70	P
05. SOFTWARE ENGINEERING	PP	100	40	62	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	24	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	42	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	24	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	45	P
10. NETWORK LABORATORY	TW	25	10	24	P
11. NETWORK LABORATORY	OR	50	20	45	P
12. SOFT SKILLS LABORATORY	TW	25	10	24	P

FIRST TERM TOTAL = 562/750.

ORDN. 1 MARKS :

T80058506 AHERKAR SHRIJEET CHANDRASHEKHAR MANGALA , 71100722F , , PICT , T80058506

01. OPERATING SYSTEM	PP	100	40	49	P
02. THEORY OF COMPUTATION	PP	100	40	42	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	71	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	62	P
05. SOFTWARE ENGINEERING	PP	100	40	55	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	21	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	38	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	20	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	38	P
10. NETWORK LABORATORY	TW	25	10	18	P
11. NETWORK LABORATORY	OR	50	20	36	P
12. SOFT SKILLS LABORATORY	TW	25	10	20	P

FIRST TERM TOTAL = 470/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80058507 ALAMWALE SAMEER GAMA BUDDHO , 71241751G , , PICT , T80058507

01. OPERATING SYSTEM	PP	100	40	48	P
02. THEORY OF COMPUTATION	PP	100	40	40	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	54	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	61	P
05. SOFTWARE ENGINEERING	PP	100	40	45	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	24	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	07	F
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	20	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	25	P
10. NETWORK LABORATORY	TW	25	10	21	P
11. NETWORK LABORATORY	OR	50	20	28	P
12. SOFT SKILLS LABORATORY	TW	25	10	21	P

FIRST TERM TOTAL = 394/750.

ORDN. 1 MARKS :

T80058508 ALHAT ANIKETH KANILAL MANGAL , 71241752E , , PICT , T80058508

01. OPERATING SYSTEM	PP	100	40	84	P
02. THEORY OF COMPUTATION	PP	100	40	73	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	72	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	69	P
05. SOFTWARE ENGINEERING	PP	100	40	56	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	24	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	45	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	24	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	48	P
10. NETWORK LABORATORY	TW	25	10	24	P
11. NETWORK LABORATORY	OR	50	20	45	P
12. SOFT SKILLS LABORATORY	TW	25	10	24	P

FIRST TERM TOTAL = 588/750.

ORDN. 1 MARKS :

T80058509 ANIL BAJAJ KAMAL , 71100729C , , PICT , T80058509

01. OPERATING SYSTEM	PP	100	40	41	P
02. THEORY OF COMPUTATION	PP	100	40	30	F
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	56	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	55	P
05. SOFTWARE ENGINEERING	PP	100	40	50	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	19	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	35	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	19	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	39	P
10. NETWORK LABORATORY	TW	25	10	17	P
11. NETWORK LABORATORY	OR	50	20	32	P
12. SOFT SKILLS LABORATORY	TW	25	10	20	P

FIRST TERM TOTAL = 413/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80058510 ANIRUDH SHISHODIA VIMLESH , 71100730G , PICT , T80058510

01. OPERATING SYSTEM	PP	100	40	62	P
02. THEORY OF COMPUTATION	PP	100	40	63	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	57	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	66	P
05. SOFTWARE ENGINEERING	PP	100	40	49	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	24	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	38	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	21	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	39	P
10. NETWORK LABORATORY	TW	25	10	22	P
11. NETWORK LABORATORY	OR	50	20	38	P
12. SOFT SKILLS LABORATORY	TW	25	10	21	P

FIRST TERM TOTAL = 500/750.

ORDN. 1 MARKS :

T80058511 ANUPAMA TRIVEDI SANGITA , 71100732C , PICT , T80058511

01. OPERATING SYSTEM	PP	100	40	79	P
02. THEORY OF COMPUTATION	PP	100	40	61	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	80	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	71	P
05. SOFTWARE ENGINEERING	PP	100	40	65	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	22	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	35	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	21	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	40	P
10. NETWORK LABORATORY	TW	25	10	22	P
11. NETWORK LABORATORY	OR	50	20	43	P
12. SOFT SKILLS LABORATORY	TW	25	10	22	P

FIRST TERM TOTAL = 561/750.

ORDN. 1 MARKS :

T80058513 ASHUTOSH PANDEY MAMTA , 71100733M , PICT , T80058513

01. OPERATING SYSTEM	PP	100	40	42	P
02. THEORY OF COMPUTATION	PP	100	40	26	F
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	64	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	51	P
05. SOFTWARE ENGINEERING	PP	100	40	40	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	18	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	27	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	18	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	14	F
10. NETWORK LABORATORY	TW	25	10	16	P
11. NETWORK LABORATORY	OR	50	20	20	P
12. SOFT SKILLS LABORATORY	TW	25	10	19	P

FIRST TERM TOTAL = 355/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80058514 ASHWIN AJAY HABBU AARTI , 71100734K , , PICT , T80058514

01. OPERATING SYSTEM	PP	100	40	59	P
02. THEORY OF COMPUTATION	PP	100	40	69	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	68	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	68	P
05. SOFTWARE ENGINEERING	PP	100	40	55	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	20	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	33	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	16	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	31	P
10. NETWORK LABORATORY	TW	25	10	14	P
11. NETWORK LABORATORY	OR	50	20	32	P
12. SOFT SKILLS LABORATORY	TW	25	10	16	P

FIRST TERM TOTAL = 481/750.

ORDN. 1 MARKS :

T80058515 AUTADE SAGAR ASHOK INDIRA , 71045376M , , PICT , T80058515

01. OPERATING SYSTEM	PP	100	40	45	P
02. THEORY OF COMPUTATION	PP	100	40	40	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	46	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	40	P
05. SOFTWARE ENGINEERING	PP	100	40	42	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	21	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	39	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	22	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	41	P
10. NETWORK LABORATORY	TW	25	10	21	P
11. NETWORK LABORATORY	OR	50	20	17	F
12. SOFT SKILLS LABORATORY	TW	25	10	22	P

FIRST TERM TOTAL = 396/750.

ORDN. 1 MARKS :

T80058516 AYMAN MUSHTAQ AHMAD VIKAR UN NISA , 71100736F , , PICT , T80058516

01. OPERATING SYSTEM	PP	100	40	59	P
02. THEORY OF COMPUTATION	PP	100	40	48	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	62	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	58	P
05. SOFTWARE ENGINEERING	PP	100	40	45	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	18	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	29	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	19	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	36	P
10. NETWORK LABORATORY	TW	25	10	18	P
11. NETWORK LABORATORY	OR	50	20	28	P
12. SOFT SKILLS LABORATORY	TW	25	10	20	P

FIRST TERM TOTAL = 440/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80058517 BAHETI MAHESH RAJENDRA CHANDA , 71100739L , , PICT , T80058517

01. OPERATING SYSTEM	PP	100	40	62	P
02. THEORY OF COMPUTATION	PP	100	40	30	F
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	56	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	45	P
05. SOFTWARE ENGINEERING	PP	100	40	52	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	22	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	34	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	23	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	42	P
10. NETWORK LABORATORY	TW	25	10	20	P
11. NETWORK LABORATORY	OR	50	20	35	P
12. SOFT SKILLS LABORATORY	TW	25	10	20	P

FIRST TERM TOTAL = 441/750.

ORDN. 1 MARKS :

T80058518 BHAKKAD MOHIT KISHANLALJI POOJA , 71100747M , , PICT , T80058518

01. OPERATING SYSTEM	PP	100	40	69	P
02. THEORY OF COMPUTATION	PP	100	40	55	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	71	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	59	P
05. SOFTWARE ENGINEERING	PP	100	40	52	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	23	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	35	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	22	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	40	P
10. NETWORK LABORATORY	TW	25	10	23	P
11. NETWORK LABORATORY	OR	50	20	38	P
12. SOFT SKILLS LABORATORY	TW	25	10	23	P

FIRST TERM TOTAL = 510/750.

ORDN. 1 MARKS :

T80058519 BHAND PRITI PARAJI RUKHMINI , 71100748K , , PICT , T80058519

01. OPERATING SYSTEM	PP	100	40	47	P
02. THEORY OF COMPUTATION	PP	100	40	55	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	58	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	50	P
05. SOFTWARE ENGINEERING	PP	100	40	47	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	23	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	07	F
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	20	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	36	P
10. NETWORK LABORATORY	TW	25	10	20	P
11. NETWORK LABORATORY	OR	50	20	22	P
12. SOFT SKILLS LABORATORY	TW	25	10	20	P

FIRST TERM TOTAL = 405/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80058520 BHANDWALKAR ONKAR MADHUKAR PRABHAVATI , 71100749H , , PICT , T80058520

01. OPERATING SYSTEM	PP	100	40	75	P
02. THEORY OF COMPUTATION	PP	100	40	69	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	66	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	57	P
05. SOFTWARE ENGINEERING	PP	100	40	57	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	22	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	31	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	23	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	40	P
10. NETWORK LABORATORY	TW	25	10	22	P
11. NETWORK LABORATORY	OR	50	20	44	P
12. SOFT SKILLS LABORATORY	TW	25	10	23	P

FIRST TERM TOTAL = 529/750.

ORDN. 1 MARKS :

T80058521 BHOIR KIRTI RAMCHANDRA ROHINI , 71241753C , , PICT , T80058521

01. OPERATING SYSTEM	PP	100	40	70	P
02. THEORY OF COMPUTATION	PP	100	40	51	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	66	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	58	P
05. SOFTWARE ENGINEERING	PP	100	40	65	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	24	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	34	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	23	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	39	P
10. NETWORK LABORATORY	TW	25	10	24	P
11. NETWORK LABORATORY	OR	50	20	40	P
12. SOFT SKILLS LABORATORY	TW	25	10	23	P

FIRST TERM TOTAL = 517/750.

ORDN. 1 MARKS :

T80058522 BORHADE BHUSHAN MALHARI SUREKHA , 71241754M , , PICT , T80058522

01. OPERATING SYSTEM	PP	100	40	75	P
02. THEORY OF COMPUTATION	PP	100	40	53	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	60	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	63	P
05. SOFTWARE ENGINEERING	PP	100	40	63	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	24	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	06	F
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	23	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	44	P
10. NETWORK LABORATORY	TW	25	10	21	P
11. NETWORK LABORATORY	OR	50	20	38	P
12. SOFT SKILLS LABORATORY	TW	25	10	22	P

FIRST TERM TOTAL = 492/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80058523 BOTHARA PRIYANKA RAMESH VIJAYA , 71100755B , , PICT , T80058523

01. OPERATING SYSTEM	PP	100	40	66	P
02. THEORY OF COMPUTATION	PP	100	40	61	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	80	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	69	P
05. SOFTWARE ENGINEERING	PP	100	40	66	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	24	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	36	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	24	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	41	P
10. NETWORK LABORATORY	TW	25	10	21	P
11. NETWORK LABORATORY	OR	50	20	40	P
12. SOFT SKILLS LABORATORY	TW	25	10	22	P

FIRST TERM TOTAL = 550/750.

ORDN. 1 MARKS :

T80058524 BOTHRA PAYAL SUMATILAL SANDHYA , 71100756L , , PICT , T80058524

01. OPERATING SYSTEM	PP	100	40	67	P
02. THEORY OF COMPUTATION	PP	100	40	66	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	72	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	68	P
05. SOFTWARE ENGINEERING	PP	100	40	48	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	21	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	34	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	23	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	39	P
10. NETWORK LABORATORY	TW	25	10	20	P
11. NETWORK LABORATORY	OR	50	20	29	P
12. SOFT SKILLS LABORATORY	TW	25	10	21	P

FIRST TERM TOTAL = 508/750.

ORDN. 1 MARKS :

T80058525 BURSE RASHMI KIRAN MADHURI , 71100758G , , PICT , T80058525

01. OPERATING SYSTEM	PP	100	40	71	P
02. THEORY OF COMPUTATION	PP	100	40	60	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	72	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	64	P
05. SOFTWARE ENGINEERING	PP	100	40	59	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	21	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	12	F
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	23	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	42	P
10. NETWORK LABORATORY	TW	25	10	22	P
11. NETWORK LABORATORY	OR	50	20	35	P
12. SOFT SKILLS LABORATORY	TW	25	10	23	P

FIRST TERM TOTAL = 504/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80058526 CHANDAN PRITI DILIP MALATI , 71241755K , , PICT , T80058526

01. OPERATING SYSTEM	PP	100	40	70	P
02. THEORY OF COMPUTATION	PP	100	40	52	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	56	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	60	P
05. SOFTWARE ENGINEERING	PP	100	40	56	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	23	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	27	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	24	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	46	P
10. NETWORK LABORATORY	TW	25	10	24	P
11. NETWORK LABORATORY	OR	50	20	41	P
12. SOFT SKILLS LABORATORY	TW	25	10	23	P

FIRST TERM TOTAL = 502/750.

ORDN. 1 MARKS :

T80058527 CHAUDHARI PRANITA RAVISHANKAR MEENA , 71100764M , , PICT , T80058527

01. OPERATING SYSTEM	PP	100	40	40	P
02. THEORY OF COMPUTATION	PP	100	40	23	F
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	40	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	22	F
05. SOFTWARE ENGINEERING	PP	100	40	24	F
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	21	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	30	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	18	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	33	P
10. NETWORK LABORATORY	TW	25	10	18	P
11. NETWORK LABORATORY	OR	50	20	28	P
12. SOFT SKILLS LABORATORY	TW	25	10	19	P

FIRST TERM TOTAL = 316/750.

ORDN. 1 MARKS :

T80058528 CHAVAN SHRIKANT SHIVAJI MALAN , 71241756H , , PICT , T80058528

01. OPERATING SYSTEM	PP	100	40	77	P
02. THEORY OF COMPUTATION	PP	100	40	55	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	73	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	66	P
05. SOFTWARE ENGINEERING	PP	100	40	59	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	24	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	44	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	22	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	43	P
10. NETWORK LABORATORY	TW	25	10	22	P
11. NETWORK LABORATORY	OR	50	20	31	P
12. SOFT SKILLS LABORATORY	TW	25	10	22	P

FIRST TERM TOTAL = 538/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80058529 DAHAWAD SUNIL NAMDEO VANITA , 71241757F , , PICT , T80058529

01. OPERATING SYSTEM	PP	100	40	43	P
02. THEORY OF COMPUTATION	PP	100	40	31	F
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	57	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	31	F
05. SOFTWARE ENGINEERING	PP	100	40	40	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	18	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	10	F
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	18	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	12	F
10. NETWORK LABORATORY	TW	25	10	17	P
11. NETWORK LABORATORY	OR	50	20	14	F
12. SOFT SKILLS LABORATORY	TW	25	10	19	P

FIRST TERM TOTAL = 310/750.

ORDN. 1 MARKS :

T80058530 DARDA MAYOOR MONISH KALPANA , 71045410E , , PICT , T80058530

01. OPERATING SYSTEM	PP	100	40	52	P
02. THEORY OF COMPUTATION	PP	100	40	25	F
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	60	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	40	P
05. SOFTWARE ENGINEERING	PP	100	40	53	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	10	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	AA	F
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	11	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	AA	F
10. NETWORK LABORATORY	TW	25	10	13	P
11. NETWORK LABORATORY	OR	50	20	AA	F
12. SOFT SKILLS LABORATORY	TW	25	10	13	P

FIRST TERM TOTAL = 277/750.

ORDN. 1 MARKS :

T80058531 DHANAVE PRATIK BALASAHEB UJJWALLA , 71100783H , , PICT , T80058531

01. OPERATING SYSTEM	PP	100	40	42	P
02. THEORY OF COMPUTATION	PP	100	40	17	F
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	51	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	43	P
05. SOFTWARE ENGINEERING	PP	100	40	48	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	12	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	AA	F
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	16	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	35	P
10. NETWORK LABORATORY	TW	25	10	13	P
11. NETWORK LABORATORY	OR	50	20	AA	F
12. SOFT SKILLS LABORATORY	TW	25	10	13	P

FIRST TERM TOTAL = 290/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80058532 DHAWALE PRACHI PRAMOD MEENA , 71100784F , , PICT , T80058532

01. OPERATING SYSTEM	PP	100	40	69	P
02. THEORY OF COMPUTATION	PP	100	40	66	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	79	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	67	P
05. SOFTWARE ENGINEERING	PP	100	40	55	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	21	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	12	F
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	22	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	40	P
10. NETWORK LABORATORY	TW	25	10	18	P
11. NETWORK LABORATORY	OR	50	20	31	P
12. SOFT SKILLS LABORATORY	TW	25	10	19	P

FIRST TERM TOTAL = 499/750.

ORDN. 1 MARKS :

T80058533 DHEPLE ARCHANA HARIBHAU MEENA , 71100785D , , PICT , T80058533

01. OPERATING SYSTEM	PP	100	40	48	P
02. THEORY OF COMPUTATION	PP	100	40	57	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	64	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	49	P
05. SOFTWARE ENGINEERING	PP	100	40	46	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	18	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	34	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	20	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	30	P
10. NETWORK LABORATORY	TW	25	10	18	P
11. NETWORK LABORATORY	OR	50	20	25	P
12. SOFT SKILLS LABORATORY	TW	25	10	20	P

FIRST TERM TOTAL = 429/750.

ORDN. 1 MARKS :

T80058534 GAIKWAD NETRANJALI SHAMRAO MOHINI , 71241758D , , PICT , T80058534

01. OPERATING SYSTEM	PP	100	40	75	P
02. THEORY OF COMPUTATION	PP	100	40	55	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	60	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	63	P
05. SOFTWARE ENGINEERING	PP	100	40	60	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	23	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	38	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	24	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	44	P
10. NETWORK LABORATORY	TW	25	10	24	P
11. NETWORK LABORATORY	OR	50	20	45	P
12. SOFT SKILLS LABORATORY	TW	25	10	24	P

FIRST TERM TOTAL = 535/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80058535 GAIKWAD PIYUSH GHANASHYAM VASUDHA , 71100793E , , PICT , T80058535

01. OPERATING SYSTEM	PP	100	40	40	P
02. THEORY OF COMPUTATION	PP	100	40	25	F
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	41	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	40	P
05. SOFTWARE ENGINEERING	PP	100	40	48	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	10	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	31	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	15	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	06	F
10. NETWORK LABORATORY	TW	25	10	14	P
11. NETWORK LABORATORY	OR	50	20	17	F
12. SOFT SKILLS LABORATORY	TW	25	10	16	P

FIRST TERM TOTAL = 303/750.

ORDN. 1 MARKS :

T80058536 GANGARDE MAYURI MUKUND PRAMILA , 71100796K , , PICT , T80058536

01. OPERATING SYSTEM	PP	100	40	62	P
02. THEORY OF COMPUTATION	PP	100	40	46	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	61	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	40	P
05. SOFTWARE ENGINEERING	PP	100	40	55	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	22	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	25	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	23	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	34	P
10. NETWORK LABORATORY	TW	25	10	24	P
11. NETWORK LABORATORY	OR	50	20	23	P
12. SOFT SKILLS LABORATORY	TW	25	10	24	P

FIRST TERM TOTAL = 439/750.

ORDN. 1 MARKS :

T80058537 GAWANDE ANAGHA ARVIND MAYA , 71100800M , , PICT , T80058537

01. OPERATING SYSTEM	PP	100	40	52	P
02. THEORY OF COMPUTATION	PP	100	40	45	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	50	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	67	P
05. SOFTWARE ENGINEERING	PP	100	40	52	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	23	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	12	F
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	19	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	39	P
10. NETWORK LABORATORY	TW	25	10	18	P
11. NETWORK LABORATORY	OR	50	20	25	P
12. SOFT SKILLS LABORATORY	TW	25	10	18	P

FIRST TERM TOTAL = 420/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80058538 GHODAKE PRAJAKTA DHANANJAY SWATI , 71100802H , , PICT , T80058538

01. OPERATING SYSTEM	PP	100	40	51	P
02. THEORY OF COMPUTATION	PP	100	40	24	F
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	40	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	40	P
05. SOFTWARE ENGINEERING	PP	100	40	42	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	18	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	05	F
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	18	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	24	P
10. NETWORK LABORATORY	TW	25	10	16	P
11. NETWORK LABORATORY	OR	50	20	20	P
12. SOFT SKILLS LABORATORY	TW	25	10	18	P

FIRST TERM TOTAL = 316/750.

ORDN. 1 MARKS :

T80058539 GHODE AMAR PRADEEPKUMAR SINDHU , 71045436J , , PICT , T80058539

01. OPERATING SYSTEM	PP	100	40	53	P
02. THEORY OF COMPUTATION	PP	100	40	18	F
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	48	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	40	P
05. SOFTWARE ENGINEERING	PP	100	40	52	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	17	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	AA	F
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	17	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	04	F
10. NETWORK LABORATORY	TW	25	10	12	P
11. NETWORK LABORATORY	OR	50	20	AA	F
12. SOFT SKILLS LABORATORY	TW	25	10	15	P

FIRST TERM TOTAL = 276/750.

ORDN. 1 MARKS :

T80058540 GHODESWAR MAYUR DEEPAK RATNAPRABHA , 71241759B , , PICT , T80058540

01. OPERATING SYSTEM	PP	100	40	51	P
02. THEORY OF COMPUTATION	PP	100	40	46	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	66	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	65	P
05. SOFTWARE ENGINEERING	PP	100	40	67	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	21	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	37	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	20	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	12	F
10. NETWORK LABORATORY	TW	25	10	19	P
11. NETWORK LABORATORY	OR	50	20	10	F
12. SOFT SKILLS LABORATORY	TW	25	10	20	P

FIRST TERM TOTAL = 434/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80058541 GHODKE AMRUTA PRAKASH JYOTI , 71241760F , , PICT , T80058541

01. OPERATING SYSTEM	PP	100	40	79	P
02. THEORY OF COMPUTATION	PP	100	40	66	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	68	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	62	P
05. SOFTWARE ENGINEERING	PP	100	40	58	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	23	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	40	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	24	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	45	P
10. NETWORK LABORATORY	TW	25	10	22	P
11. NETWORK LABORATORY	OR	50	20	45	P
12. SOFT SKILLS LABORATORY	TW	25	10	24	P

FIRST TERM TOTAL = 556/750.

ORDN. 1 MARKS :

T80058542 GOENKA PALAK DILIP MANJU , 71100807J , , PICT , T80058542

01. OPERATING SYSTEM	PP	100	40	52	P
02. THEORY OF COMPUTATION	PP	100	40	44	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	61	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	64	P
05. SOFTWARE ENGINEERING	PP	100	40	57	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	17	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	38	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	17	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	41	P
10. NETWORK LABORATORY	TW	25	10	17	P
11. NETWORK LABORATORY	OR	50	20	32	P
12. SOFT SKILLS LABORATORY	TW	25	10	18	P

FIRST TERM TOTAL = 458/750.

ORDN. 1 MARKS :

T80058543 GUJRATHI DHANESH NARESH JYOTI , 71100811G , , PICT , T80058543

01. OPERATING SYSTEM	PP	100	40	73	P
02. THEORY OF COMPUTATION	PP	100	40	62	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	68	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	61	P
05. SOFTWARE ENGINEERING	PP	100	40	55	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	22	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	40	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	21	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	40	P
10. NETWORK LABORATORY	TW	25	10	22	P
11. NETWORK LABORATORY	OR	50	20	41	P
12. SOFT SKILLS LABORATORY	TW	25	10	22	P

FIRST TERM TOTAL = 527/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80058544 GUTTE ASHWINI ASHOKRAO SUNANDA , 71100815K , , PICT , T80058544

01. OPERATING SYSTEM	PP	100	40	49	P
02. THEORY OF COMPUTATION	PP	100	40	51	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	61	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	54	P
05. SOFTWARE ENGINEERING	PP	100	40	54	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	22	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	10	F
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	19	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	10	F
10. NETWORK LABORATORY	TW	25	10	20	P
11. NETWORK LABORATORY	OR	50	20	26	P
12. SOFT SKILLS LABORATORY	TW	25	10	21	P

FIRST TERM TOTAL = 397/750.

ORDN. 1 MARKS :

T80058545 HARAL SMITA DATTATRAYA SARITA , 71100816H , , PICT , T80058545

01. OPERATING SYSTEM	PP	100	40	71	P
02. THEORY OF COMPUTATION	PP	100	40	67	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	67	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	57	P
05. SOFTWARE ENGINEERING	PP	100	40	55	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	22	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	30	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	21	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	39	P
10. NETWORK LABORATORY	TW	25	10	22	P
11. NETWORK LABORATORY	OR	50	20	37	P
12. SOFT SKILLS LABORATORY	TW	25	10	23	P

FIRST TERM TOTAL = 511/750.

ORDN. 1 MARKS :

T80058546 JADHWANI KANCHAN OMPRAKASH NIRMALAL , 71100824J , , PICT , T80058546

01. OPERATING SYSTEM	PP	100	40	75	P
02. THEORY OF COMPUTATION	PP	100	40	51	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	66	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	65	P
05. SOFTWARE ENGINEERING	PP	100	40	56	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	22	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	32	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	21	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	42	P
10. NETWORK LABORATORY	TW	25	10	21	P
11. NETWORK LABORATORY	OR	50	20	38	P
12. SOFT SKILLS LABORATORY	TW	25	10	21	P

FIRST TERM TOTAL = 510/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80058547 JAIN BHUSHAN SUMATILAL MANISHA , 71100826E , , PICT , T80058547

01. OPERATING SYSTEM	PP	100	40	76	P
02. THEORY OF COMPUTATION	PP	100	40	72	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	69	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	70	P
05. SOFTWARE ENGINEERING	PP	100	40	64	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	20	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	35	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	22	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	43	P
10. NETWORK LABORATORY	TW	25	10	20	P
11. NETWORK LABORATORY	OR	50	20	36	P
12. SOFT SKILLS LABORATORY	TW	25	10	20	P

FIRST TERM TOTAL = 547/750.

ORDN. 1 MARKS :

T80058548 JAIPURIA ROHIT SHARADKUMAR AMITA , 71100828M , , PICT , T80058548

01. OPERATING SYSTEM	PP	100	40	40	P
02. THEORY OF COMPUTATION	PP	100	40	29	F
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	45	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	43	P
05. SOFTWARE ENGINEERING	PP	100	40	42	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	19	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	AA	F
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	18	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	12	F
10. NETWORK LABORATORY	TW	25	10	19	P
11. NETWORK LABORATORY	OR	50	20	28	P
12. SOFT SKILLS LABORATORY	TW	25	10	20	P

FIRST TERM TOTAL = 315/750.

ORDN. 1 MARKS :

T80058549 JAMBHULKAR SAYALI SUNIL SHUBHAANGI , 71100829K , , PICT , T80058549

01. OPERATING SYSTEM	PP	100	40	66	P
02. THEORY OF COMPUTATION	PP	100	40	49	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	73	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	54	P
05. SOFTWARE ENGINEERING	PP	100	40	56	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	20	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	42	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	19	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	40	P
10. NETWORK LABORATORY	TW	25	10	19	P
11. NETWORK LABORATORY	OR	50	20	39	P
12. SOFT SKILLS LABORATORY	TW	25	10	20	P

FIRST TERM TOTAL = 497/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80058550 JOSHI AKHIL NIRANJAN NEHA , 71100833H , , PICT , T80058550

01. OPERATING SYSTEM	PP	100	40	74	P
02. THEORY OF COMPUTATION	PP	100	40	55	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	69	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	68	P
05. SOFTWARE ENGINEERING	PP	100	40	54	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	20	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	44	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	18	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	40	P
10. NETWORK LABORATORY	TW	25	10	17	P
11. NETWORK LABORATORY	OR	50	20	39	P
12. SOFT SKILLS LABORATORY	TW	25	10	18	P

FIRST TERM TOTAL = 516/750.

ORDN. 1 MARKS :

T80058551 KABRE AMEYA ATUL MANGALA , 71100838J , , PICT , T80058551

01. OPERATING SYSTEM	PP	100	40	72	P
02. THEORY OF COMPUTATION	PP	100	40	43	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	74	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	57	P
05. SOFTWARE ENGINEERING	PP	100	40	59	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	24	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	37	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	23	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	43	P
10. NETWORK LABORATORY	TW	25	10	24	P
11. NETWORK LABORATORY	OR	50	20	45	P
12. SOFT SKILLS LABORATORY	TW	25	10	23	P

FIRST TERM TOTAL = 524/750.

ORDN. 1 MARKS :

T80058552 KADAM CHIRAG RAM APSARA , 71100839G , , PICT , T80058552

01. OPERATING SYSTEM	PP	100	40	76	P
02. THEORY OF COMPUTATION	PP	100	40	61	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	68	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	53	P
05. SOFTWARE ENGINEERING	PP	100	40	65	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	24	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	42	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	22	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	38	P
10. NETWORK LABORATORY	TW	25	10	19	P
11. NETWORK LABORATORY	OR	50	20	43	P
12. SOFT SKILLS LABORATORY	TW	25	10	23	P

FIRST TERM TOTAL = 534/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80058553 KAHATE SAUMITRA SHYAMKANT JYOTI , 71100841J , , PICT , T80058553

01. OPERATING SYSTEM	PP	100	40	74	P
02. THEORY OF COMPUTATION	PP	100	40	64	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	69	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	61	P
05. SOFTWARE ENGINEERING	PP	100	40	60	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	21	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	40	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	22	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	39	P
10. NETWORK LABORATORY	TW	25	10	23	P
11. NETWORK LABORATORY	OR	50	20	40	P
12. SOFT SKILLS LABORATORY	TW	25	10	22	P

FIRST TERM TOTAL = 535/750.

ORDN. 1 MARKS :

T80058554 KAUTHALE TRUPTI SAMPAT JAYASHRI , 71100853B , , PICT , T80058554

01. OPERATING SYSTEM	PP	100	40	56	P
02. THEORY OF COMPUTATION	PP	100	40	53	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	63	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	62	P
05. SOFTWARE ENGINEERING	PP	100	40	62	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	20	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	35	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	21	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	42	P
10. NETWORK LABORATORY	TW	25	10	21	P
11. NETWORK LABORATORY	OR	50	20	20	P
12. SOFT SKILLS LABORATORY	TW	25	10	21	P

FIRST TERM TOTAL = 476/750.

ORDN. 1 MARKS :

T80058555 KHADKIWALA HATIM JAINUDDIN MASUMA , 71100856G , , PICT , T80058555

01. OPERATING SYSTEM	PP	100	40	67	P
02. THEORY OF COMPUTATION	PP	100	40	68	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	70	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	55	P
05. SOFTWARE ENGINEERING	PP	100	40	59	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	24	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	46	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	23	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	45	P
10. NETWORK LABORATORY	TW	25	10	24	P
11. NETWORK LABORATORY	OR	50	20	46	P
12. SOFT SKILLS LABORATORY	TW	25	10	23	P

FIRST TERM TOTAL = 550/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80058556 KHADSE MAYUR DILIP PRAMILA , 71100857E , , PICT , T80058556

01. OPERATING SYSTEM	PP	100	40	48	P
02. THEORY OF COMPUTATION	PP	100	40	42	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	53	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	41	P
05. SOFTWARE ENGINEERING	PP	100	40	40	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	17	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	40	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	19	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	41	P
10. NETWORK LABORATORY	TW	25	10	16	P
11. NETWORK LABORATORY	OR	50	20	25	P
12. SOFT SKILLS LABORATORY	TW	25	10	17	P

FIRST TERM TOTAL = 399/750.

ORDN. 1 MARKS :

T80058557 KHAN RUQAIYA IQBAL SHAHEEN , 71100860E , , PICT , T80058557

01. OPERATING SYSTEM	PP	100	40	74	P
02. THEORY OF COMPUTATION	PP	100	40	59	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	68	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	65	P
05. SOFTWARE ENGINEERING	PP	100	40	52	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	24	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	42	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	24	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	46	P
10. NETWORK LABORATORY	TW	25	10	24	P
11. NETWORK LABORATORY	OR	50	20	38	P
12. SOFT SKILLS LABORATORY	TW	25	10	24	P

FIRST TERM TOTAL = 540/750.

ORDN. 1 MARKS :

T80058558 KODITKAR POOJA NAMDEO JYOTI , 71241761D , , PICT , T80058558

01. OPERATING SYSTEM	PP	100	40	68	P
02. THEORY OF COMPUTATION	PP	100	40	51	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	67	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	59	P
05. SOFTWARE ENGINEERING	PP	100	40	67	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	23	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	25	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	23	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	34	P
10. NETWORK LABORATORY	TW	25	10	24	P
11. NETWORK LABORATORY	OR	50	20	37	P
12. SOFT SKILLS LABORATORY	TW	25	10	23	P

FIRST TERM TOTAL = 501/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80058559 KOSHTI ANUJA RAMESH MANGAL , 71045485G , , PICT , T80058559

01. OPERATING SYSTEM	PP	100	40	46	P
02. THEORY OF COMPUTATION	PP	100	40	40	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	45	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	43	P
05. SOFTWARE ENGINEERING	PP	100	40	52	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	20	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	AA	F
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	18	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	10	F
10. NETWORK LABORATORY	TW	25	10	20	P
11. NETWORK LABORATORY	OR	50	20	30	P
12. SOFT SKILLS LABORATORY	TW	25	10	20	P

FIRST TERM TOTAL = 344/750.

ORDN. 1 MARKS :

T80058560 KULKARNI MITALI SUHAS RUPA , 71100872J , , PICT , T80058560

01. OPERATING SYSTEM	PP	100	40	66	P
02. THEORY OF COMPUTATION	PP	100	40	54	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	63	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	58	P
05. SOFTWARE ENGINEERING	PP	100	40	48	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	22	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	27	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	23	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	42	P
10. NETWORK LABORATORY	TW	25	10	22	P
11. NETWORK LABORATORY	OR	50	20	38	P
12. SOFT SKILLS LABORATORY	TW	25	10	22	P

FIRST TERM TOTAL = 485/750.

ORDN. 1 MARKS :

T80058561 LAGWANKAR NIKHIL KISHOR RASHMI , 71100878H , , PICT , T80058561

01. OPERATING SYSTEM	PP	100	40	73	P
02. THEORY OF COMPUTATION	PP	100	40	51	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	67	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	48	P
05. SOFTWARE ENGINEERING	PP	100	40	54	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	24	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	46	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	24	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	46	P
10. NETWORK LABORATORY	TW	25	10	24	P
11. NETWORK LABORATORY	OR	50	20	44	P
12. SOFT SKILLS LABORATORY	TW	25	10	24	P

FIRST TERM TOTAL = 525/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80058562 LUV VARMA PARVEEN , 71100884B , , PICT , T80058562

01. OPERATING SYSTEM	PP	100	40	71	P
02. THEORY OF COMPUTATION	PP	100	40	59	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	79	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	71	P
05. SOFTWARE ENGINEERING	PP	100	40	60	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	23	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	35	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	23	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	43	P
10. NETWORK LABORATORY	TW	25	10	21	P
11. NETWORK LABORATORY	OR	50	20	36	P
12. SOFT SKILLS LABORATORY	TW	25	10	22	P

FIRST TERM TOTAL = 543/750.

ORDN. 1 MARKS :

T80058563 MADGULWAR AMIT ASHOK VIJAYA , 71100886J , , PICT , T80058563

01. OPERATING SYSTEM	PP	100	40	81	P
02. THEORY OF COMPUTATION	PP	100	40	67	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	72	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	69	P
05. SOFTWARE ENGINEERING	PP	100	40	63	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	23	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	40	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	23	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	44	P
10. NETWORK LABORATORY	TW	25	10	23	P
11. NETWORK LABORATORY	OR	50	20	34	P
12. SOFT SKILLS LABORATORY	TW	25	10	23	P

FIRST TERM TOTAL = 562/750.

ORDN. 1 MARKS :

T80058564 MAHAJAN DEEPIKA UDDHAV MALATI , 71241762B , , PICT , T80058564

01. OPERATING SYSTEM	PP	100	40	65	P
02. THEORY OF COMPUTATION	PP	100	40	47	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	69	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	53	P
05. SOFTWARE ENGINEERING	PP	100	40	59	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	22	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	45	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	22	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	37	P
10. NETWORK LABORATORY	TW	25	10	22	P
11. NETWORK LABORATORY	OR	50	20	31	P
12. SOFT SKILLS LABORATORY	TW	25	10	22	P

FIRST TERM TOTAL = 494/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80058565 MAHARSHI PAYAL VIJAY NIRMALA , 71241763L , , PICT , T80058565

01. OPERATING SYSTEM	PP	100	40	53	P
02. THEORY OF COMPUTATION	PP	100	40	52	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	62	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	56	P
05. SOFTWARE ENGINEERING	PP	100	40	51	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	20	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	35	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	23	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	40	P
10. NETWORK LABORATORY	TW	25	10	20	P
11. NETWORK LABORATORY	OR	50	20	41	P
12. SOFT SKILLS LABORATORY	TW	25	10	20	P

FIRST TERM TOTAL = 473/750.

ORDN. 1 MARKS :

T80058566 MANSI MATHUR PREETI , 71100890G , , PICT , T80058566

01. OPERATING SYSTEM	PP	100	40	54	P
02. THEORY OF COMPUTATION	PP	100	40	40	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	60	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	50	P
05. SOFTWARE ENGINEERING	PP	100	40	57	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	21	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	12	F
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	17	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	24	P
10. NETWORK LABORATORY	TW	25	10	17	P
11. NETWORK LABORATORY	OR	50	20	22	P
12. SOFT SKILLS LABORATORY	TW	25	10	18	P

FIRST TERM TOTAL = 392/750.

ORDN. 1 MARKS :

T80058567 MANTRI ANKIT JAGDISH SANGITA , 71100891E , , PICT , T80058567

01. OPERATING SYSTEM	PP	100	40	60	P
02. THEORY OF COMPUTATION	PP	100	40	48	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	64	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	57	P
05. SOFTWARE ENGINEERING	PP	100	40	49	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	22	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	44	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	23	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	42	P
10. NETWORK LABORATORY	TW	25	10	24	P
11. NETWORK LABORATORY	OR	50	20	45	P
12. SOFT SKILLS LABORATORY	TW	25	10	22	P

FIRST TERM TOTAL = 500/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80058568 MATHANGI KRISHNAMURTHI K JEYANTHY , 71100892C , , PICT , T80058568

01. OPERATING SYSTEM	PP	100	40	79	P
02. THEORY OF COMPUTATION	PP	100	40	48	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	71	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	66	P
05. SOFTWARE ENGINEERING	PP	100	40	65	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	24	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	46	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	24	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	47	P
10. NETWORK LABORATORY	TW	25	10	24	P
11. NETWORK LABORATORY	OR	50	20	44	P
12. SOFT SKILLS LABORATORY	TW	25	10	24	P

FIRST TERM TOTAL = 562/750.

ORDN. 1 MARKS :

T80058569 MATNANI PRIYA SHIVADAS UPASANA , 71100893M , , PICT , T80058569

01. OPERATING SYSTEM	PP	100	40	73	P
02. THEORY OF COMPUTATION	PP	100	40	73	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	77	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	68	P
05. SOFTWARE ENGINEERING	PP	100	40	68	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	23	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	38	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	24	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	42	P
10. NETWORK LABORATORY	TW	25	10	21	P
11. NETWORK LABORATORY	OR	50	20	44	P
12. SOFT SKILLS LABORATORY	TW	25	10	22	P

FIRST TERM TOTAL = 573/750.

ORDN. 1 MARKS :

T80058570 MONGA MANPREET KAUR DARSHAN SINGH GURPREET KAUR , 71100898B , , PICT , T80058570

01. OPERATING SYSTEM	PP	100	40	57	P
02. THEORY OF COMPUTATION	PP	100	40	41	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	65	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	65	P
05. SOFTWARE ENGINEERING	PP	100	40	55	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	20	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	39	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	21	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	41	P
10. NETWORK LABORATORY	TW	25	10	19	P
11. NETWORK LABORATORY	OR	50	20	35	P
12. SOFT SKILLS LABORATORY	TW	25	10	20	P

FIRST TERM TOTAL = 478/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80058571 MORE SWATI ANNARAO ANITA , 71241764J , , PICT , T80058571

01. OPERATING SYSTEM	PP	100	40	68	P
02. THEORY OF COMPUTATION	PP	100	40	71	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	70	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	69	P
05. SOFTWARE ENGINEERING	PP	100	40	67	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	24	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	40	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	24	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	44	P
10. NETWORK LABORATORY	TW	25	10	24	P
11. NETWORK LABORATORY	OR	50	20	40	P
12. SOFT SKILLS LABORATORY	TW	25	10	24	P

FIRST TERM TOTAL = 565/750.

ORDN. 1 MARKS :

T80058572 MULCHANDANI POOJA OM PRITI , 71100901F , , PICT , T80058572

01. OPERATING SYSTEM	PP	100	40	56	P
02. THEORY OF COMPUTATION	PP	100	40	59	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	51	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	44	P
05. SOFTWARE ENGINEERING	PP	100	40	47	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	23	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	38	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	21	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	37	P
10. NETWORK LABORATORY	TW	25	10	21	P
11. NETWORK LABORATORY	OR	50	20	23	P
12. SOFT SKILLS LABORATORY	TW	25	10	21	P

FIRST TERM TOTAL = 441/750.

ORDN. 1 MARKS :

T80058573 MULLA ASMA MUSA RABIYA , 71134970D , , PICT , T80058573

01. OPERATING SYSTEM	PP	100	40	54	P
02. THEORY OF COMPUTATION	PP	100	40	40	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	60	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	45	P
05. SOFTWARE ENGINEERING	PP	100	40	59	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	18	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	38	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	18	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	37	P
10. NETWORK LABORATORY	TW	25	10	17	P
11. NETWORK LABORATORY	OR	50	20	24	P
12. SOFT SKILLS LABORATORY	TW	25	10	18	P

FIRST TERM TOTAL = 428/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80058574 MUPADE PRIYA SHIVAJI KANTABAI , 71241765G , , PICT , T80058574

01. OPERATING SYSTEM	PP	100	40	52	P
02. THEORY OF COMPUTATION	PP	100	40	49	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	47	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	51	P
05. SOFTWARE ENGINEERING	PP	100	40	57	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	20	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	10	F
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	16	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	10	F
10. NETWORK LABORATORY	TW	25	10	19	P
11. NETWORK LABORATORY	OR	50	20	12	F
12. SOFT SKILLS LABORATORY	TW	25	10	19	P

FIRST TERM TOTAL = 362/750.

ORDN. 1 MARKS :

T80058575 MUTHA VINITA VINOD SUREKHA , 71100904L , , PICT , T80058575

01. OPERATING SYSTEM	PP	100	40	49	P
02. THEORY OF COMPUTATION	PP	100	40	42	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	49	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	47	P
05. SOFTWARE ENGINEERING	PP	100	40	50	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	16	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	38	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	20	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	40	P
10. NETWORK LABORATORY	TW	25	10	15	P
11. NETWORK LABORATORY	OR	50	20	34	P
12. SOFT SKILLS LABORATORY	TW	25	10	16	P

FIRST TERM TOTAL = 416/750.

ORDN. 1 MARKS :

T80058576 NAIK RASHMI BHARAT MEENA , 71100907E , , PICT , T80058576

01. OPERATING SYSTEM	PP	100	40	40	P
02. THEORY OF COMPUTATION	PP	100	40	43	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	51	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	40	P
05. SOFTWARE ENGINEERING	PP	100	40	56	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	18	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	27	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	21	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	39	P
10. NETWORK LABORATORY	TW	25	10	21	P
11. NETWORK LABORATORY	OR	50	20	25	P
12. SOFT SKILLS LABORATORY	TW	25	10	21	P

FIRST TERM TOTAL = 402/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80058577 NARAD POONAM DEEPAK VIDYA , 71100911C , , PICT , T80058577

01. OPERATING SYSTEM	PP	100	40	54	P
02. THEORY OF COMPUTATION	PP	100	40	40	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	55	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	40	P
05. SOFTWARE ENGINEERING	PP	100	40	50	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	20	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	27	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	17	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	22	P
10. NETWORK LABORATORY	TW	25	10	16	P
11. NETWORK LABORATORY	OR	50	20	30	P
12. SOFT SKILLS LABORATORY	TW	25	10	20	P

FIRST TERM TOTAL = 391/750.

ORDN. 1 MARKS :

T80058578 NARANG KARISHMA KISHOR SUNITA , 71100912M , , PICT , T80058578

01. OPERATING SYSTEM	PP	100	40	74	P
02. THEORY OF COMPUTATION	PP	100	40	62	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	62	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	62	P
05. SOFTWARE ENGINEERING	PP	100	40	64	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	18	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	31	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	19	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	40	P
10. NETWORK LABORATORY	TW	25	10	18	P
11. NETWORK LABORATORY	OR	50	20	40	P
12. SOFT SKILLS LABORATORY	TW	25	10	18	P

FIRST TERM TOTAL = 508/750.

ORDN. 1 MARKS :

T80058579 NEVATIA SAKSHI SUNIL ANJU , 71100916D , , PICT , T80058579

01. OPERATING SYSTEM	PP	100	40	59	P
02. THEORY OF COMPUTATION	PP	100	40	56	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	61	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	63	P
05. SOFTWARE ENGINEERING	PP	100	40	65	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	21	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	32	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	21	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	37	P
10. NETWORK LABORATORY	TW	25	10	21	P
11. NETWORK LABORATORY	OR	50	20	41	P
12. SOFT SKILLS LABORATORY	TW	25	10	21	P

FIRST TERM TOTAL = 498/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80058580 OKA TANVI VISHWAS MEDHA , 71100918L , , PICT , T80058580

01. OPERATING SYSTEM	PP	100	40	76	P
02. THEORY OF COMPUTATION	PP	100	40	73	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	71	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	64	P
05. SOFTWARE ENGINEERING	PP	100	40	65	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	23	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	29	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	19	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	40	P
10. NETWORK LABORATORY	TW	25	10	17	P
11. NETWORK LABORATORY	OR	50	20	41	P
12. SOFT SKILLS LABORATORY	TW	25	10	17	P

FIRST TERM TOTAL = 535/750.

ORDN. 1 MARKS :

T80058581 OSWAL DARSHAN MANISH NEETA , 71100919J , , PICT , T80058581

01. OPERATING SYSTEM	PP	100	40	66	P
02. THEORY OF COMPUTATION	PP	100	40	69	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	62	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	55	P
05. SOFTWARE ENGINEERING	PP	100	40	55	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	23	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	33	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	22	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	43	P
10. NETWORK LABORATORY	TW	25	10	22	P
11. NETWORK LABORATORY	OR	50	20	31	P
12. SOFT SKILLS LABORATORY	TW	25	10	22	P

FIRST TERM TOTAL = 503/750.

ORDN. 1 MARKS :

T80058582 PAGAR NILESH DADAJI YOGITA , 71100922J , , PICT , T80058582

01. OPERATING SYSTEM	PP	100	40	61	P
02. THEORY OF COMPUTATION	PP	100	40	51	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	72	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	63	P
05. SOFTWARE ENGINEERING	PP	100	40	67	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	23	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	36	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	23	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	38	P
10. NETWORK LABORATORY	TW	25	10	22	P
11. NETWORK LABORATORY	OR	50	20	40	P
12. SOFT SKILLS LABORATORY	TW	25	10	22	P

FIRST TERM TOTAL = 518/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80058583 PANDAY PRASHANT RAMDHARASH ASHA , 71100925C , , PICT , T80058583

01. OPERATING SYSTEM	PP	100	40	66	P
02. THEORY OF COMPUTATION	PP	100	40	63	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	67	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	62	P
05. SOFTWARE ENGINEERING	PP	100	40	61	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	20	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	29	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	18	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	43	P
10. NETWORK LABORATORY	TW	25	10	18	P
11. NETWORK LABORATORY	OR	50	20	38	P
12. SOFT SKILLS LABORATORY	TW	25	10	18	P

FIRST TERM TOTAL = 503/750.

ORDN. 1 MARKS :

T80058584 PANDIT SNEHALATA GANAPATI SHOBHA , 71241766E , , PICT , T80058584

01. OPERATING SYSTEM	PP	100	40	70	P
02. THEORY OF COMPUTATION	PP	100	40	73	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	72	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	56	P
05. SOFTWARE ENGINEERING	PP	100	40	72	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	24	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	41	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	24	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	44	P
10. NETWORK LABORATORY	TW	25	10	24	P
11. NETWORK LABORATORY	OR	50	20	33	P
12. SOFT SKILLS LABORATORY	TW	25	10	23	P

FIRST TERM TOTAL = 556/750.

ORDN. 1 MARKS :

T80058585 PARSE JYOTI SHRIKRISHNA AMBIKA , 71241767C , , PICT , T80058585

01. OPERATING SYSTEM	PP	100	40	77	P
02. THEORY OF COMPUTATION	PP	100	40	65	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	69	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	56	P
05. SOFTWARE ENGINEERING	PP	100	40	63	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	23	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	35	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	22	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	43	P
10. NETWORK LABORATORY	TW	25	10	19	P
11. NETWORK LABORATORY	OR	50	20	23	P
12. SOFT SKILLS LABORATORY	TW	25	10	20	P

FIRST TERM TOTAL = 515/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80058586 PATEL MANISH UTTAM KAILAS , 71100933D , , PICT , T80058586

01. OPERATING SYSTEM	PP	100	40	80	P
02. THEORY OF COMPUTATION	PP	100	40	64	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	66	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	69	P
05. SOFTWARE ENGINEERING	PP	100	40	70	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	24	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	31	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	24	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	40	P
10. NETWORK LABORATORY	TW	25	10	23	P
11. NETWORK LABORATORY	OR	50	20	38	P
12. SOFT SKILLS LABORATORY	TW	25	10	23	P

FIRST TERM TOTAL = 552/750.

ORDN. 1 MARKS :

T80058587 PATHAK ANUJA PRAVIN UJJWALA , 71100934B , , PICT , T80058587

01. OPERATING SYSTEM	PP	100	40	49	P
02. THEORY OF COMPUTATION	PP	100	40	57	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	53	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	41	P
05. SOFTWARE ENGINEERING	PP	100	40	47	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	19	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	29	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	18	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	36	P
10. NETWORK LABORATORY	TW	25	10	17	P
11. NETWORK LABORATORY	OR	50	20	29	P
12. SOFT SKILLS LABORATORY	TW	25	10	17	P

FIRST TERM TOTAL = 412/750.

ORDN. 1 MARKS :

T80058588 PATIL AMOL MADHUKAR JYOTI , 71100938E , , PICT , T80058588

01. OPERATING SYSTEM	PP	100	40	54	P
02. THEORY OF COMPUTATION	PP	100	40	44	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	45	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	45	P
05. SOFTWARE ENGINEERING	PP	100	40	52	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	14	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	32	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	18	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	38	P
10. NETWORK LABORATORY	TW	25	10	12	P
11. NETWORK LABORATORY	OR	50	20	08	F
12. SOFT SKILLS LABORATORY	TW	25	10	12	P

FIRST TERM TOTAL = 374/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80058589 PATIL PRIYA KISHOR PUSHPA , 71100941E , , PICT , T80058589

01. OPERATING SYSTEM	PP	100	40	60	P
02. THEORY OF COMPUTATION	PP	100	40	55	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	57	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	65	P
05. SOFTWARE ENGINEERING	PP	100	40	54	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	20	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	30	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	19	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	35	P
10. NETWORK LABORATORY	TW	25	10	19	P
11. NETWORK LABORATORY	OR	50	20	25	P
12. SOFT SKILLS LABORATORY	TW	25	10	19	P

FIRST TERM TOTAL = 458/750.

ORDN. 1 MARKS :

T80058590 PATIL RUPESHSING RAMESH KANCHAN , 71100943M , , PICT , T80058590

01. OPERATING SYSTEM	PP	100	40	56	P
02. THEORY OF COMPUTATION	PP	100	40	61	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	59	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	64	P
05. SOFTWARE ENGINEERING	PP	100	40	58	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	14	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	25	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	18	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	35	P
10. NETWORK LABORATORY	TW	25	10	15	P
11. NETWORK LABORATORY	OR	50	20	10	F
12. SOFT SKILLS LABORATORY	TW	25	10	17	P

FIRST TERM TOTAL = 432/750.

ORDN. 1 MARKS :

T80058591 PATIL RUSHIKESH VASANTRAO SHOBHANA , 71100944K , , PICT , T80058591

01. OPERATING SYSTEM	PP	100	40	66	P
02. THEORY OF COMPUTATION	PP	100	40	70	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	63	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	64	P
05. SOFTWARE ENGINEERING	PP	100	40	53	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	20	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	32	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	19	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	43	P
10. NETWORK LABORATORY	TW	25	10	19	P
11. NETWORK LABORATORY	OR	50	20	39	P
12. SOFT SKILLS LABORATORY	TW	25	10	19	P

FIRST TERM TOTAL = 507/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80058592 PAWAR CHANDAN DILIP REKHA , 70925555B , , PICT , T80058592

01. OPERATING SYSTEM	PP	100	40	72	P
02. THEORY OF COMPUTATION	PP	100	40	43	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	61	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	52	P
05. SOFTWARE ENGINEERING	PP	100	40	57	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	16	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	AA	F
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	17	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	04	F
10. NETWORK LABORATORY	TW	25	10	14	P
11. NETWORK LABORATORY	OR	50	20	37	P
12. SOFT SKILLS LABORATORY	TW	25	10	13	P

FIRST TERM TOTAL = 386/750.

ORDN. 1 MARKS :

T80058593 PAWAR PRASAD GORAKSH MEENAKSHI , 71100951B , , PICT , T80058593

01. OPERATING SYSTEM	PP	100	40	42	P
02. THEORY OF COMPUTATION	PP	100	40	21	F
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	40	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	40	P
05. SOFTWARE ENGINEERING	PP	100	40	40	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	18	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	33	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	15	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	37	P
10. NETWORK LABORATORY	TW	25	10	12	P
11. NETWORK LABORATORY	OR	50	20	05	F
12. SOFT SKILLS LABORATORY	TW	25	10	12	P

FIRST TERM TOTAL = 315/750.

ORDN. 1 MARKS :

T80058594 PAWAR SNEHA SADASHIV JYOTI , 71241768M , , PICT , T80058594

01. OPERATING SYSTEM	PP	100	40	88	P
02. THEORY OF COMPUTATION	PP	100	40	59	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	73	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	75	P
05. SOFTWARE ENGINEERING	PP	100	40	75	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	23	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	41	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	23	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	44	P
10. NETWORK LABORATORY	TW	25	10	22	P
11. NETWORK LABORATORY	OR	50	20	22	P
12. SOFT SKILLS LABORATORY	TW	25	10	23	P

FIRST TERM TOTAL = 568/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80058595 PAYGUDE GAUTAMI SANJAY SUMAN , 71241769K , , PICT , T80058595

01. OPERATING SYSTEM	PP	100	40	73	P
02. THEORY OF COMPUTATION	PP	100	40	45	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	67	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	54	P
05. SOFTWARE ENGINEERING	PP	100	40	66	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	24	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	39	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	24	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	38	P
10. NETWORK LABORATORY	TW	25	10	24	P
11. NETWORK LABORATORY	OR	50	20	40	P
12. SOFT SKILLS LABORATORY	TW	25	10	24	P

FIRST TERM TOTAL = 518/750.

ORDN. 1 MARKS :

T80058596 PHADKE MUGDHA RAVINDRA PRADNYA , 71100954G , , PICT , T80058596

01. OPERATING SYSTEM	PP	100	40	57	P
02. THEORY OF COMPUTATION	PP	100	40	44	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	56	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	55	P
05. SOFTWARE ENGINEERING	PP	100	40	49	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	22	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	35	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	21	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	37	P
10. NETWORK LABORATORY	TW	25	10	21	P
11. NETWORK LABORATORY	OR	50	20	06	F
12. SOFT SKILLS LABORATORY	TW	25	10	22	P

FIRST TERM TOTAL = 425/750.

ORDN. 1 MARKS :

T80058597 POTE ISHWAR VITTHAL SUNITA , 71100957M , , PICT , T80058597

01. OPERATING SYSTEM	PP	100	40	66	P
02. THEORY OF COMPUTATION	PP	100	40	40	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	44	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	44	P
05. SOFTWARE ENGINEERING	PP	100	40	44	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	19	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	33	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	15	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	37	P
10. NETWORK LABORATORY	TW	25	10	16	P
11. NETWORK LABORATORY	OR	50	20	38	P
12. SOFT SKILLS LABORATORY	TW	25	10	16	P

FIRST TERM TOTAL = 412/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80058598 RAHUL BAIJAL SADHNA , 71100962H , , PICT , T80058598

01. OPERATING SYSTEM	PP	100	40	80	P
02. THEORY OF COMPUTATION	PP	100	40	67	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	70	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	69	P
05. SOFTWARE ENGINEERING	PP	100	40	64	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	21	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	40	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	22	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	44	P
10. NETWORK LABORATORY	TW	25	10	18	P
11. NETWORK LABORATORY	OR	50	20	33	P
12. SOFT SKILLS LABORATORY	TW	25	10	21	P

FIRST TERM TOTAL = 549/750.

ORDN. 1 MARKS :

T80058599 RAHUL SHARMA PROMILA , 71100963F , , PICT , T80058599

01. OPERATING SYSTEM	PP	100	40	59	P
02. THEORY OF COMPUTATION	PP	100	40	66	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	46	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	51	P
05. SOFTWARE ENGINEERING	PP	100	40	43	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	12	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	AA	F
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	11	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	AA	F
10. NETWORK LABORATORY	TW	25	10	13	P
11. NETWORK LABORATORY	OR	50	20	AA	F
12. SOFT SKILLS LABORATORY	TW	25	10	12	P

FIRST TERM TOTAL = 313/750.

ORDN. 1 MARKS :

T80058600 RAMPURAWALA MARIYA FIROZ FARIDA , 71100964D , , PICT , T80058600

01. OPERATING SYSTEM	PP	100	40	70	P
02. THEORY OF COMPUTATION	PP	100	40	73	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	48	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	56	P
05. SOFTWARE ENGINEERING	PP	100	40	57	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	20	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	32	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	22	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	39	P
10. NETWORK LABORATORY	TW	25	10	19	P
11. NETWORK LABORATORY	OR	50	20	07	F
12. SOFT SKILLS LABORATORY	TW	25	10	20	P

FIRST TERM TOTAL = 463/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80058601 RANADIVE ABHIJEET JANARDAN KANCHAN , 71100966L , , PICT , T80058601

01. OPERATING SYSTEM	PP	100	40	56	P
02. THEORY OF COMPUTATION	PP	100	40	58	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	53	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	56	P
05. SOFTWARE ENGINEERING	PP	100	40	55	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	23	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	31	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	21	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	40	P
10. NETWORK LABORATORY	TW	25	10	19	P
11. NETWORK LABORATORY	OR	50	20	43	P
12. SOFT SKILLS LABORATORY	TW	25	10	19	P

FIRST TERM TOTAL = 474/750.

ORDN. 1 MARKS :

T80058602 RATHOD ASHWINKUMAR AMARSING RANJANA , 71241770C , , PICT , T80058602

01. OPERATING SYSTEM	PP	100	40	71	P
02. THEORY OF COMPUTATION	PP	100	40	62	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	67	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	74	P
05. SOFTWARE ENGINEERING	PP	100	40	72	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	21	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	36	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	20	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	41	P
10. NETWORK LABORATORY	TW	25	10	20	P
11. NETWORK LABORATORY	OR	50	20	41	P
12. SOFT SKILLS LABORATORY	TW	25	10	20	P

FIRST TERM TOTAL = 545/750.

ORDN. 1 MARKS :

T80058603 RATTAN NAKUL VISHWAS BIMLA , 71100971G , , PICT , T80058603

01. OPERATING SYSTEM	PP	100	40	66	P
02. THEORY OF COMPUTATION	PP	100	40	53	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	49	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	42	P
05. SOFTWARE ENGINEERING	PP	100	40	44	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	22	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	36	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	22	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	34	P
10. NETWORK LABORATORY	TW	25	10	19	P
11. NETWORK LABORATORY	OR	50	20	40	P
12. SOFT SKILLS LABORATORY	TW	25	10	19	P

FIRST TERM TOTAL = 446/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80058604 RIA NARAYAN MANJU , 71100975K , , PICT , T80058604

01. OPERATING SYSTEM	PP	100	40	73	P
02. THEORY OF COMPUTATION	PP	100	40	73	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	69	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	66	P
05. SOFTWARE ENGINEERING	PP	100	40	65	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	18	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	41	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	19	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	39	P
10. NETWORK LABORATORY	TW	25	10	18	P
11. NETWORK LABORATORY	OR	50	20	38	P
12. SOFT SKILLS LABORATORY	TW	25	10	19	P

FIRST TERM TOTAL = 538/750.

ORDN. 1 MARKS :

T80058605 ROMIL MEHTA SHEELA , 71100978D , , PICT , T80058605

01. OPERATING SYSTEM	PP	100	40	69	P
02. THEORY OF COMPUTATION	PP	100	40	74	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	66	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	68	P
05. SOFTWARE ENGINEERING	PP	100	40	63	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	23	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	29	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	22	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	36	P
10. NETWORK LABORATORY	TW	25	10	21	P
11. NETWORK LABORATORY	OR	50	20	41	P
12. SOFT SKILLS LABORATORY	TW	25	10	21	P

FIRST TERM TOTAL = 533/750.

ORDN. 1 MARKS :

T80058606 SABALE BHUSHAN BALASAHEB SUMAN , 71100980F , , PICT , T80058606

01. OPERATING SYSTEM	PP	100	40	70	P
02. THEORY OF COMPUTATION	PP	100	40	57	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	65	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	56	P
05. SOFTWARE ENGINEERING	PP	100	40	62	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	21	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	36	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	21	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	44	P
10. NETWORK LABORATORY	TW	25	10	21	P
11. NETWORK LABORATORY	OR	50	20	42	P
12. SOFT SKILLS LABORATORY	TW	25	10	21	P

FIRST TERM TOTAL = 516/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80058607 SAGAR SABIR DAMANI SHAHIN , 71100981D , , PICT , T80058607

01. OPERATING SYSTEM	PP	100	40	79	P
02. THEORY OF COMPUTATION	PP	100	40	64	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	74	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	64	P
05. SOFTWARE ENGINEERING	PP	100	40	65	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	24	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	39	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	24	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	46	P
10. NETWORK LABORATORY	TW	25	10	23	P
11. NETWORK LABORATORY	OR	50	20	29	P
12. SOFT SKILLS LABORATORY	TW	25	10	24	P

FIRST TERM TOTAL = 555/750.

ORDN. 1 MARKS :

T80058608 SAHARE NUPUR ARVIND MAMTA , 71100982B , , PICT , T80058608

01. OPERATING SYSTEM	PP	100	40	74	P
02. THEORY OF COMPUTATION	PP	100	40	56	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	65	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	53	P
05. SOFTWARE ENGINEERING	PP	100	40	64	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	19	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	15	F
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	20	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	32	P
10. NETWORK LABORATORY	TW	25	10	19	P
11. NETWORK LABORATORY	OR	50	20	32	P
12. SOFT SKILLS LABORATORY	TW	25	10	19	P

FIRST TERM TOTAL = 468/750.

ORDN. 1 MARKS :

T80058609 SARAF DARPAN RAJENDRA AMITA , 71100986E , , PICT , T80058609

01. OPERATING SYSTEM	PP	100	40	62	P
02. THEORY OF COMPUTATION	PP	100	40	62	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	73	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	60	P
05. SOFTWARE ENGINEERING	PP	100	40	62	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	19	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	33	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	19	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	44	P
10. NETWORK LABORATORY	TW	25	10	19	P
11. NETWORK LABORATORY	OR	50	20	25	P
12. SOFT SKILLS LABORATORY	TW	25	10	19	P

FIRST TERM TOTAL = 497/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80058610 SARWAR PRIYANKA ASHOK MEERA , 71045599C , , PICT , T80058610

01. OPERATING SYSTEM	PP	100	40	60	P
02. THEORY OF COMPUTATION	PP	100	40	44	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	73	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	49	P
05. SOFTWARE ENGINEERING	PP	100	40	57	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	18	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	15	F
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	19	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	37	P
10. NETWORK LABORATORY	TW	25	10	15	P
11. NETWORK LABORATORY	OR	50	20	28	P
12. SOFT SKILLS LABORATORY	TW	25	10	18	P

FIRST TERM TOTAL = 433/750.

ORDN. 1 MARKS :

T80058611 SASANE PRANITA RAMESH ALKA , 71100989K , , PICT , T80058611

01. OPERATING SYSTEM	PP	100	40	55	P
02. THEORY OF COMPUTATION	PP	100	40	50	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	42	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	40	P
05. SOFTWARE ENGINEERING	PP	100	40	40	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	21	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	30	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	21	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	34	P
10. NETWORK LABORATORY	TW	25	10	21	P
11. NETWORK LABORATORY	OR	50	20	21	P
12. SOFT SKILLS LABORATORY	TW	25	10	21	P

FIRST TERM TOTAL = 396/750.

ORDN. 1 MARKS :

T80058612 SAYYED FAKHRUDDIN KAMALUDDIN RAZIYA KHANAM , 71241771M , , PICT , T80058612

01. OPERATING SYSTEM	PP	100	40	75	P
02. THEORY OF COMPUTATION	PP	100	40	56	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	63	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	55	P
05. SOFTWARE ENGINEERING	PP	100	40	61	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	24	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	43	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	22	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	42	P
10. NETWORK LABORATORY	TW	25	10	21	P
11. NETWORK LABORATORY	OR	50	20	47	P
12. SOFT SKILLS LABORATORY	TW	25	10	23	P

FIRST TERM TOTAL = 532/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80058613 SHARMA LAKSHMI KANAK , 71100999G , , PICT , T80058613

01. OPERATING SYSTEM	PP	100	40	60	P
02. THEORY OF COMPUTATION	PP	100	40	67	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	73	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	57	P
05. SOFTWARE ENGINEERING	PP	100	40	67	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	24	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	39	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	23	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	41	P
10. NETWORK LABORATORY	TW	25	10	19	P
11. NETWORK LABORATORY	OR	50	20	28	P
12. SOFT SKILLS LABORATORY	TW	25	10	23	P

FIRST TERM TOTAL = 521/750.

ORDN. 1 MARKS :

T80058614 SHINDE NUTAN BALASAHEB MANISHA , 71101006E , , PICT , T80058614

01. OPERATING SYSTEM	PP	100	40	79	P
02. THEORY OF COMPUTATION	PP	100	40	55	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	67	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	57	P
05. SOFTWARE ENGINEERING	PP	100	40	69	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	24	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	38	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	23	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	40	P
10. NETWORK LABORATORY	TW	25	10	23	P
11. NETWORK LABORATORY	OR	50	20	33	P
12. SOFT SKILLS LABORATORY	TW	25	10	22	P

FIRST TERM TOTAL = 530/750.

ORDN. 1 MARKS :

T80058615 SHINDE SUSHANT GAUTAM SANGITA , 71241772K , , PICT , T80058615

01. OPERATING SYSTEM	PP	100	40	60	P
02. THEORY OF COMPUTATION	PP	100	40	46	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	67	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	59	P
05. SOFTWARE ENGINEERING	PP	100	40	65	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	24	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	35	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	24	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	46	P
10. NETWORK LABORATORY	TW	25	10	23	P
11. NETWORK LABORATORY	OR	50	20	26	P
12. SOFT SKILLS LABORATORY	TW	25	10	24	P

FIRST TERM TOTAL = 499/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80058616 SHIRA VISHAL BHAGWANSING MALTA , 71101009K , , PICT , T80058616

01. OPERATING SYSTEM	PP	100	40	59	P
02. THEORY OF COMPUTATION	PP	100	40	51	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	61	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	40	P
05. SOFTWARE ENGINEERING	PP	100	40	59	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	18	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	32	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	19	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	42	P
10. NETWORK LABORATORY	TW	25	10	16	P
11. NETWORK LABORATORY	OR	50	20	08	F
12. SOFT SKILLS LABORATORY	TW	25	10	20	P

FIRST TERM TOTAL = 425/750.

ORDN. 1 MARKS :

T80058617 SHIRBHATE ABHILASH DNYANESHWAR RAJSHRI , 71101010C , , PICT , T80058617

01. OPERATING SYSTEM	PP	100	40	46	P
02. THEORY OF COMPUTATION	PP	100	40	43	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	62	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	45	P
05. SOFTWARE ENGINEERING	PP	100	40	49	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	17	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	21	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	15	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	39	P
10. NETWORK LABORATORY	TW	25	10	12	P
11. NETWORK LABORATORY	OR	50	20	32	P
12. SOFT SKILLS LABORATORY	TW	25	10	14	P

FIRST TERM TOTAL = 395/750.

ORDN. 1 MARKS :

T80058618 SHUKLA MANISH NITYANAND MADHURI , 71045622M , , PICT , T80058618

01. OPERATING SYSTEM	PP	100	40	63	P
02. THEORY OF COMPUTATION	PP	100	40	50	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	63	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	51	P
05. SOFTWARE ENGINEERING	PP	100	40	55	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	17	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	29	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	16	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	41	P
10. NETWORK LABORATORY	TW	25	10	14	P
11. NETWORK LABORATORY	OR	50	20	29	P
12. SOFT SKILLS LABORATORY	TW	25	10	17	P

FIRST TERM TOTAL = 445/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80058619 SIDDHANT BANSAL SUJATA , 71101015D , , PICT , T80058619

01. OPERATING SYSTEM	PP	100	40	69	P
02. THEORY OF COMPUTATION	PP	100	40	60	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	67	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	71	P
05. SOFTWARE ENGINEERING	PP	100	40	57	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	18	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	35	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	19	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	40	P
10. NETWORK LABORATORY	TW	25	10	16	P
11. NETWORK LABORATORY	OR	50	20	10	F
12. SOFT SKILLS LABORATORY	TW	25	10	18	P

FIRST TERM TOTAL = 480/750.

ORDN. 1 MARKS :

T80058620 SOLANKE BHUSHAN NARENDRA REKHA , 71101017L , , PICT , T80058620

01. OPERATING SYSTEM	PP	100	40	42	P
02. THEORY OF COMPUTATION	PP	100	40	48	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	42	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	40	P
05. SOFTWARE ENGINEERING	PP	100	40	56	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	22	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	27	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	16	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	27	P
10. NETWORK LABORATORY	TW	25	10	15	P
11. NETWORK LABORATORY	OR	50	20	11	F
12. SOFT SKILLS LABORATORY	TW	25	10	16	P

FIRST TERM TOTAL = 362/750.

ORDN. 1 MARKS :

T80058621 SONAWANE JAGDISH RAJENDRA HIRABAI , 71241773H , , PICT , T80058621

01. OPERATING SYSTEM	PP	100	40	64	P
02. THEORY OF COMPUTATION	PP	100	40	50	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	72	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	65	P
05. SOFTWARE ENGINEERING	PP	100	40	65	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	19	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	30	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	17	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	43	P
10. NETWORK LABORATORY	TW	25	10	18	P
11. NETWORK LABORATORY	OR	50	20	43	P
12. SOFT SKILLS LABORATORY	TW	25	10	19	P

FIRST TERM TOTAL = 505/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80058622 STUTI MITTAL BHAVNA , 71101022G , , PICT , T80058622

01. OPERATING SYSTEM	PP	100	40	61	P
02. THEORY OF COMPUTATION	PP	100	40	40	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	54	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	49	P
05. SOFTWARE ENGINEERING	PP	100	40	55	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	19	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	31	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	22	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	25	P
10. NETWORK LABORATORY	TW	25	10	18	P
11. NETWORK LABORATORY	OR	50	20	42	P
12. SOFT SKILLS LABORATORY	TW	25	10	18	P

FIRST TERM TOTAL = 434/750.

ORDN. 1 MARKS :

T80058623 SWETAMBARI ANJU , 71101024C , , PICT , T80058623

01. OPERATING SYSTEM	PP	100	40	78	P
02. THEORY OF COMPUTATION	PP	100	40	57	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	77	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	67	P
05. SOFTWARE ENGINEERING	PP	100	40	59	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	23	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	32	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	24	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	40	P
10. NETWORK LABORATORY	TW	25	10	24	P
11. NETWORK LABORATORY	OR	50	20	43	P
12. SOFT SKILLS LABORATORY	TW	25	10	24	P

FIRST TERM TOTAL = 548/750.

ORDN. 1 MARKS :

T80058624 TEKE AMRUTA PRAKASH SHALINEE , 71101028F , , PICT , T80058624

01. OPERATING SYSTEM	PP	100	40	46	P
02. THEORY OF COMPUTATION	PP	100	40	40	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	66	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	60	P
05. SOFTWARE ENGINEERING	PP	100	40	61	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	19	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	21	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	19	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	38	P
10. NETWORK LABORATORY	TW	25	10	16	P
11. NETWORK LABORATORY	OR	50	20	12	F
12. SOFT SKILLS LABORATORY	TW	25	10	19	P

FIRST TERM TOTAL = 417/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80058625 THORAT AKSHAY RAMAKANT SUNANDA , 71101034L , , PICT , T80058625

01. OPERATING SYSTEM	PP	100	40	56	P
02. THEORY OF COMPUTATION	PP	100	40	40	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	62	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	46	P
05. SOFTWARE ENGINEERING	PP	100	40	46	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	21	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	30	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	20	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	43	P
10. NETWORK LABORATORY	TW	25	10	18	P
11. NETWORK LABORATORY	OR	50	20	44	P
12. SOFT SKILLS LABORATORY	TW	25	10	20	P

FIRST TERM TOTAL = 446/750.

ORDN. 1 MARKS :

T80058626 THOSARE YOGESH YASHWANT USHADEVI , 71101035J , , PICT , T80058626

01. OPERATING SYSTEM	PP	100	40	58	P
02. THEORY OF COMPUTATION	PP	100	40	44	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	56	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	54	P
05. SOFTWARE ENGINEERING	PP	100	40	70	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	21	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	43	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	19	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	37	P
10. NETWORK LABORATORY	TW	25	10	20	P
11. NETWORK LABORATORY	OR	50	20	25	P
12. SOFT SKILLS LABORATORY	TW	25	10	20	P

FIRST TERM TOTAL = 467/750.

ORDN. 1 MARKS :

T80058627 TOSHNIWAL VIVEK ANILKUMAR SANJAYA , 71101039M , , PICT , T80058627

01. OPERATING SYSTEM	PP	100	40	59	P
02. THEORY OF COMPUTATION	PP	100	40	55	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	62	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	40	P
05. SOFTWARE ENGINEERING	PP	100	40	46	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	19	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	23	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	20	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	43	P
10. NETWORK LABORATORY	TW	25	10	18	P
11. NETWORK LABORATORY	OR	50	20	29	P
12. SOFT SKILLS LABORATORY	TW	25	10	18	P

FIRST TERM TOTAL = 432/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80058628 TRIPATHI SHREYA BHARAT REENA , 71101040E , , PICT , T80058628

01. OPERATING SYSTEM	PP	100	40	66	P
02. THEORY OF COMPUTATION	PP	100	40	47	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	63	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	52	P
05. SOFTWARE ENGINEERING	PP	100	40	50	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	18	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	22	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	21	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	40	P
10. NETWORK LABORATORY	TW	25	10	16	P
11. NETWORK LABORATORY	OR	50	20	28	P
12. SOFT SKILLS LABORATORY	TW	25	10	18	P

FIRST TERM TOTAL = 441/750.

ORDN. 1 MARKS :

T80058629 UTSAV DUSAD LAXMI , 71101042M , , PICT , T80058629

01. OPERATING SYSTEM	PP	100	40	65	P
02. THEORY OF COMPUTATION	PP	100	40	59	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	70	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	57	P
05. SOFTWARE ENGINEERING	PP	100	40	51	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	24	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	33	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	24	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	44	P
10. NETWORK LABORATORY	TW	25	10	19	P
11. NETWORK LABORATORY	OR	50	20	08	F
12. SOFT SKILLS LABORATORY	TW	25	10	23	P

FIRST TERM TOTAL = 477/750.

ORDN. 1 MARKS :

T80058630 VARAT KAUSTUBH SOMKANT TILOTTAMA , 71101046D , , PICT , T80058630

01. OPERATING SYSTEM	PP	100	40	27	F
02. THEORY OF COMPUTATION	PP	100	40	45	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	40	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	50	P
05. SOFTWARE ENGINEERING	PP	100	40	45	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	10	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	AA	F
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	12	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	AA	F
10. NETWORK LABORATORY	TW	25	10	10	P
11. NETWORK LABORATORY	OR	50	20	AA	F
12. SOFT SKILLS LABORATORY	TW	25	10	12	P

FIRST TERM TOTAL = 251/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80058631 VICHARE GAURAV GIRISH VINA , 71101047B , , PICT , T80058631

01. OPERATING SYSTEM	PP	100	40	43	P
02. THEORY OF COMPUTATION	PP	100	40	44	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	52	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	40	P
05. SOFTWARE ENGINEERING	PP	100	40	40	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	21	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	23	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	24	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	41	P
10. NETWORK LABORATORY	TW	25	10	20	P
11. NETWORK LABORATORY	OR	50	20	31	P
12. SOFT SKILLS LABORATORY	TW	25	10	21	P

FIRST TERM TOTAL = 400/750.

ORDN. 1 MARKS :

T80058632 VIMAL BHAT KIRAN , 71101050B , , PICT , T80058632

01. OPERATING SYSTEM	PP	100	40	72	P
02. THEORY OF COMPUTATION	PP	100	40	61	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	70	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	61	P
05. SOFTWARE ENGINEERING	PP	100	40	71	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	22	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	33	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	20	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	42	P
10. NETWORK LABORATORY	TW	25	10	20	P
11. NETWORK LABORATORY	OR	50	20	31	P
12. SOFT SKILLS LABORATORY	TW	25	10	21	P

FIRST TERM TOTAL = 524/750.

ORDN. 1 MARKS :

T80058634 WADILE NITIN ADHAR SUMAN , 71101052J , , PICT , T80058634

01. OPERATING SYSTEM	PP	100	40	40	P
02. THEORY OF COMPUTATION	PP	100	40	43	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	48	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	40	P
05. SOFTWARE ENGINEERING	PP	100	40	51	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	10	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	AA	F
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	12	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	AA	F
10. NETWORK LABORATORY	TW	25	10	10	P
11. NETWORK LABORATORY	OR	50	20	AA	F
12. SOFT SKILLS LABORATORY	TW	25	10	13	P

FIRST TERM TOTAL = 267/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80058635 WALKE AMRUTA GHANSHYAM ASHA , 71101054E , , PICT , T80058635

01. OPERATING SYSTEM	PP	100	40	67	P
02. THEORY OF COMPUTATION	PP	100	40	58	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	59	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	57	P
05. SOFTWARE ENGINEERING	PP	100	40	63	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	24	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	21	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	23	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	38	P
10. NETWORK LABORATORY	TW	25	10	23	P
11. NETWORK LABORATORY	OR	50	20	25	P
12. SOFT SKILLS LABORATORY	TW	25	10	22	P

FIRST TERM TOTAL = 480/750.

ORDN. 1 MARKS :

T80058636 WANKHADE POOJA SUNIL VIDYA , 71101056M , , PICT , T80058636

01. OPERATING SYSTEM	PP	100	40	68	P
02. THEORY OF COMPUTATION	PP	100	40	72	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	69	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	69	P
05. SOFTWARE ENGINEERING	PP	100	40	62	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	23	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	32	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	23	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	40	P
10. NETWORK LABORATORY	TW	25	10	20	P
11. NETWORK LABORATORY	OR	50	20	41	P
12. SOFT SKILLS LABORATORY	TW	25	10	23	P

FIRST TERM TOTAL = 542/750.

ORDN. 1 MARKS :

T80058637 WAVAL BHAGYASHREE RAMESH MEGHA , 71241774F , , PICT , T80058637

01. OPERATING SYSTEM	PP	100	40	78	P
02. THEORY OF COMPUTATION	PP	100	40	51	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	76	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	58	P
05. SOFTWARE ENGINEERING	PP	100	40	68	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	23	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	44	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	24	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	40	P
10. NETWORK LABORATORY	TW	25	10	24	P
11. NETWORK LABORATORY	OR	50	20	37	P
12. SOFT SKILLS LABORATORY	TW	25	10	23	P

FIRST TERM TOTAL = 546/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80058638 WHABI JAIDEEP MAHESH CHANDRIKA , 71101058H , , PICT , T80058638

01. OPERATING SYSTEM	PP	100	40	69	P
02. THEORY OF COMPUTATION	PP	100	40	40	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	56	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	55	P
05. SOFTWARE ENGINEERING	PP	100	40	52	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	23	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	32	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	20	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	42	P
10. NETWORK LABORATORY	TW	25	10	21	P
11. NETWORK LABORATORY	OR	50	20	06	F
12. SOFT SKILLS LABORATORY	TW	25	10	21	P

FIRST TERM TOTAL = 437/750.

ORDN. 1 MARKS :

T80058639 YEOLE SHRADDHA CHANDRASHEKHAR HEMLATA , 71101063D , , PICT , T80058639

01. OPERATING SYSTEM	PP	100	40	75	P
02. THEORY OF COMPUTATION	PP	100	40	55	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	64	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	61	P
05. SOFTWARE ENGINEERING	PP	100	40	62	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	21	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	38	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	22	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	39	P
10. NETWORK LABORATORY	TW	25	10	17	P
11. NETWORK LABORATORY	OR	50	20	30	P
12. SOFT SKILLS LABORATORY	TW	25	10	21	P

FIRST TERM TOTAL = 505/750.

ORDN. 1 MARKS :

T80058640 YUSRA AIJAZ SAHAF SAMINA FARHAT , 71101066J , , PICT , T80058640

01. OPERATING SYSTEM	PP	100	40	60	P
02. THEORY OF COMPUTATION	PP	100	40	44	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	57	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	40	P
05. SOFTWARE ENGINEERING	PP	100	40	47	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	17	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	28	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	22	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	10	F
10. NETWORK LABORATORY	TW	25	10	18	P
11. NETWORK LABORATORY	OR	50	20	09	F
12. SOFT SKILLS LABORATORY	TW	25	10	18	P

FIRST TERM TOTAL = 370/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80058641	ABHINAV CHATURVEDI				ABHA					, 70801315F		T8058650		PICT		T80058641
01. OPERATING SYSTEM	PP	100	40	40	P C	13. SYSTEM SOFTWARE PROGRAMMING	PP	100	40	40	P C					
02. THEORY OF COMPUTATION	PP	100	40	41	P C	14. MANAGEMENT INFORMATION SYSTEMS	PP	100	40	48	P C					
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	41	P C	15. PROGRAMMING PARADIGMS	PP	100	40	41	P C					
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	AA	F	16. DESIGN & ANALYSIS OF ALGORITHMS	PP	100	40	45	P C					
05. SOFTWARE ENGINEERING	PP	100	40	40	P C	17. HUMAN COMPU.INTERACTION & USABI.	PP	100	40	47	P C					
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	10	P C	18. SOFTWARE DESIGN LABORATORY	TW	50	20	20	P C					
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	AA	F	19. SOFTWARE DESIGN LABORATORY	PR	50	20	24	P C					
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	10	P C	20. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	20	P C					
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	27	P C	21. SOFTWARE DEVELOPMENT TOOLS LAB.	OR	50	20	32	P C					
10. NETWORK LABORATORY	TW	25	10	10	P C	22. SEMINAR AND TECHNICAL COMMUN.	TW	50	20	20	P C					
11. NETWORK LABORATORY	OR	50	20	26	P C											
12. SOFT SKILLS LABORATORY	TW	25	10	10	P C											

GRAND TOTAL = 592/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

T80058642	BHALERAO MANGESH GANESH				NANDA					, 70925361D		T8058510		PICT		T80058642
01. OPERATING SYSTEM	PP	100	40	40	P C	13. SYSTEM SOFTWARE PROGRAMMING	PP	100	40	53	P C					
02. THEORY OF COMPUTATION	PP	100	40	40	P C	14. MANAGEMENT INFORMATION SYSTEMS	PP	100	40	46	P					
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	40	P C	15. PROGRAMMING PARADIGMS	PP	100	40	40	P					
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	40	P C	16. DESIGN & ANALYSIS OF ALGORITHMS	PP	100	40	10	F					
05. SOFTWARE ENGINEERING	PP	100	40	40	P C	17. HUMAN COMPU.INTERACTION & USABI.	PP	100	40	44	P					
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	15	P C	18. SOFTWARE DESIGN LABORATORY	TW	50	20	20	P C					
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	15	F	19. SOFTWARE DESIGN LABORATORY	PR	50	20	23	P					
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	15	P C	20. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	30	P C					
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	41	P C	21. SOFTWARE DEVELOPMENT TOOLS LAB.	OR	50	20	10	F					
10. NETWORK LABORATORY	TW	25	10	16	P C	22. SEMINAR AND TECHNICAL COMMUN.	TW	50	20	37	P C					
11. NETWORK LABORATORY	OR	50	20	32	P C											
12. SOFT SKILLS LABORATORY	TW	25	10	21	P C											

GRAND TOTAL = 668/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

T80058643	BORDE ROHAN SATISHRAO				UJWALA					, 71045398B		T8058514		PICT		T80058643
01. OPERATING SYSTEM	PP	100	40	51	P C	13. SYSTEM SOFTWARE PROGRAMMING	PP	100	40	66	P C					
02. THEORY OF COMPUTATION	PP	100	40	61	P C	14. MANAGEMENT INFORMATION SYSTEMS	PP	100	40	59	P C					
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	68	P C	15. PROGRAMMING PARADIGMS	PP	100	40	66	P C					
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	65	P C	16. DESIGN & ANALYSIS OF ALGORITHMS	PP	100	40	63	P C					
05. SOFTWARE ENGINEERING	PP	100	40	59	P C	17. HUMAN COMPU.INTERACTION & USABI.	PP	100	40	58	P C					
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	19	P C	18. SOFTWARE DESIGN LABORATORY	TW	50	20	33	P C					
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	30	P C	19. SOFTWARE DESIGN LABORATORY	PR	50	20	40	P					
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	19	P C	20. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	44	P C					
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	25	P C	21. SOFTWARE DEVELOPMENT TOOLS LAB.	OR	50	20	33	P C					
10. NETWORK LABORATORY	TW	25	10	19	P C	22. SEMINAR AND TECHNICAL COMMUN.	TW	50	20	35	P C					
11. NETWORK LABORATORY	OR	50	20	40	P C											
12. SOFT SKILLS LABORATORY	TW	25	10	20	P C											

GRAND TOTAL = 973/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80058644	CHAVAN PUSHPAK VILAS				SUREKHA				, 71134947K				T8058522		PICT			T80058644
01. OPERATING SYSTEM	PP	100	40	57	P C	13. SYSTEM SOFTWARE PROGRAMMING	PP	100	40	63	P C							
02. THEORY OF COMPUTATION	PP	100	40	48	P C	14. MANAGEMENT INFORMATION SYSTEMS	PP	100	40	56	P C							
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	64	P C	15. PROGRAMMING PARADIGMS	PP	100	40	74	P C							
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	57	P C	16. DESIGN & ANALYSIS OF ALGORITHMS	PP	100	40	49	P C							
05. SOFTWARE ENGINEERING	PP	100	40	69	P C	17. HUMAN COMPU.INTERACTION & USABI.	PP	100	40	54	P C							
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	19	P C	18. SOFTWARE DESIGN LABORATORY	TW	50	20	33	P C							
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	34	P C	19. SOFTWARE DESIGN LABORATORY	PR	50	20	32	P							
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	17	P C	20. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	41	P C							
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	22	P C	21. SOFTWARE DEVELOPMENT TOOLS LAB.	OR	50	20	25	P C							
10. NETWORK LABORATORY	TW	25	10	17	P C	22. SEMINAR AND TECHNICAL COMMUN.	TW	50	20	40	P C							
11. NETWORK LABORATORY	OR	50	20	35	P C													
12. SOFT SKILLS LABORATORY	TW	25	10	21	P C													

GRAND TOTAL = 927/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

T80058645	DEORE NIKHIL BAPUSAHEB				MANDAKINI				, 70801397L				T8058525		PICT			T80058645
01. OPERATING SYSTEM	PP	100	40	42	P C	13. SYSTEM SOFTWARE PROGRAMMING	PP	100	40	59	P C							
02. THEORY OF COMPUTATION	PP	100	40	40	P C	14. MANAGEMENT INFORMATION SYSTEMS	PP	100	40	40	P C							
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	50	P C	15. PROGRAMMING PARADIGMS	PP	100	40	40	P							
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	41	P C	16. DESIGN & ANALYSIS OF ALGORITHMS	PP	100	40	40	P							
05. SOFTWARE ENGINEERING	PP	100	40	43	P C	17. HUMAN COMPU.INTERACTION & USABI.	PP	100	40	42	P C							
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	14	P C	18. SOFTWARE DESIGN LABORATORY	TW	50	20	20	P C							
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	20	P C	19. SOFTWARE DESIGN LABORATORY	PR	50	20	10	F							
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	13	P C	20. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	20	P C							
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	35	P C	21. SOFTWARE DEVELOPMENT TOOLS LAB.	OR	50	20	08	F							
10. NETWORK LABORATORY	TW	25	10	12	P C	22. SEMINAR AND TECHNICAL COMMUN.	TW	50	20	20	P C							
11. NETWORK LABORATORY	OR	50	20	24	P C													
12. SOFT SKILLS LABORATORY	TW	25	10	20	P C													

GRAND TOTAL = 653/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

T80058646	DIKE ASHISH VIJAY				RAJANI				, 71045418L				T8058530		PICT			T80058646
01. OPERATING SYSTEM	PP	100	40	59	P C	13. SYSTEM SOFTWARE PROGRAMMING	PP	100	40	55	P C							
02. THEORY OF COMPUTATION	PP	100	40	46	P C	14. MANAGEMENT INFORMATION SYSTEMS	PP	100	40	43	P C							
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	45	P C	15. PROGRAMMING PARADIGMS	PP	100	40	50	P C							
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	40	P C	16. DESIGN & ANALYSIS OF ALGORITHMS	PP	100	40	40	P C							
05. SOFTWARE ENGINEERING	PP	100	40	46	P C	17. HUMAN COMPU.INTERACTION & USABI.	PP	100	40	41	P C							
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	12	P C	18. SOFTWARE DESIGN LABORATORY	TW	50	20	20	P C							
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	20	P C	19. SOFTWARE DESIGN LABORATORY	PR	50	20	AA	F							
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	10	P C	20. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	25	P C							
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	21	P	21. SOFTWARE DEVELOPMENT TOOLS LAB.	OR	50	20	12	F							
10. NETWORK LABORATORY	TW	25	10	10	P C	22. SEMINAR AND TECHNICAL COMMUN.	TW	50	20	20	P C							
11. NETWORK LABORATORY	OR	50	20	23	P C													
12. SOFT SKILLS LABORATORY	TW	25	10	15	P C													

GRAND TOTAL = 653/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80058647	GAIKWAD AKSHAY SHAHAJI				MAHANANDA		, 71045430K		T8058535		PICT		T80058647
01. OPERATING SYSTEM	PP	100	40	45	P C	13. SYSTEM SOFTWARE PROGRAMMING	PP	100	40	57	P C		
02. THEORY OF COMPUTATION	PP	100	40	48	P C	14. MANAGEMENT INFORMATION SYSTEMS	PP	100	40	51	P C		
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	51	P C	15. PROGRAMMING PARADIGMS	PP	100	40	59	P C		
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	52	P C	16. DESIGN & ANALYSIS OF ALGORITHMS	PP	100	40	50	P C		
05. SOFTWARE ENGINEERING	PP	100	40	44	P C	17. HUMAN COMPU.INTERACTION & USABI.	PP	100	40	47	P C		
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	10	P C	18. SOFTWARE DESIGN LABORATORY	TW	50	20	20	P C		
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	20	P C	19. SOFTWARE DESIGN LABORATORY	PR	50	20	01	F		
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	10	P C	20. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	25	P C		
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	21	P	21. SOFTWARE DEVELOPMENT TOOLS LAB.	OR	50	20	22	P C		
10. NETWORK LABORATORY	TW	25	10	10	P C	22. SEMINAR AND TECHNICAL COMMUN.	TW	50	20	20	P C		
11. NETWORK LABORATORY	OR	50	20	29	P C								
12. SOFT SKILLS LABORATORY	TW	25	10	15	P C								

GRAND TOTAL = 707/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

T80058648	JADHAV DEEPANJAN MADHAV				ARUNA		, 70925445J		T8058662		PICT		T80058648
01. OPERATING SYSTEM	PP	100	40	45	P C	13. SYSTEM SOFTWARE PROGRAMMING	PP	100	40	59	P C		
02. THEORY OF COMPUTATION	PP	100	40	52	P C	14. MANAGEMENT INFORMATION SYSTEMS	PP	100	40	47	P C		
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	72	P C	15. PROGRAMMING PARADIGMS	PP	100	40	40	P C		
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	53	P C	16. DESIGN & ANALYSIS OF ALGORITHMS	PP	100	40	43	P C		
05. SOFTWARE ENGINEERING	PP	100	40	47	P C	17. HUMAN COMPU.INTERACTION & USABI.	PP	100	40	55	P C		
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	13	P C	18. SOFTWARE DESIGN LABORATORY	TW	50	20	24	P C		
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	AA	F	19. SOFTWARE DESIGN LABORATORY	PR	50	20	22	P		
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	12	P C	20. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	20	P C		
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	22	P C	21. SOFTWARE DEVELOPMENT TOOLS LAB.	OR	50	20	20	P		
10. NETWORK LABORATORY	TW	25	10	16	P C	22. SEMINAR AND TECHNICAL COMMUN.	TW	50	20	34	P C		
11. NETWORK LABORATORY	OR	50	20	42	P C								
12. SOFT SKILLS LABORATORY	TW	25	10	16	P C								

GRAND TOTAL = 754/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

T80058649	KAKADE POOJA DILIPRAO				SUNITA		, 71134965H		T8058554		PICT		T80058649
01. OPERATING SYSTEM	PP	100	40	59	P C	13. SYSTEM SOFTWARE PROGRAMMING	PP	100	40	74	P C		
02. THEORY OF COMPUTATION	PP	100	40	64	P C	14. MANAGEMENT INFORMATION SYSTEMS	PP	100	40	45	P C		
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	74	P C	15. PROGRAMMING PARADIGMS	PP	100	40	61	P C		
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	58	P C	16. DESIGN & ANALYSIS OF ALGORITHMS	PP	100	40	63	P C		
05. SOFTWARE ENGINEERING	PP	100	40	55	P C	17. HUMAN COMPU.INTERACTION & USABI.	PP	100	40	54	P C		
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	23	P C	18. SOFTWARE DESIGN LABORATORY	TW	50	20	45	P C		
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	37	P C	19. SOFTWARE DESIGN LABORATORY	PR	50	20	37	P C		
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	24	P C	20. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	38	P C		
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	32	P C	21. SOFTWARE DEVELOPMENT TOOLS LAB.	OR	50	20	22	P		
10. NETWORK LABORATORY	TW	25	10	22	P C	22. SEMINAR AND TECHNICAL COMMUN.	TW	50	20	38	P C		
11. NETWORK LABORATORY	OR	50	20	36	P C								
12. SOFT SKILLS LABORATORY	TW	25	10	23	P C								

GRAND TOTAL = 984/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80058650	KHANDAGALE SURAJ PANDURANG			SUNITA		, 70925476J		T8058562		PICT		T80058650
01. OPERATING SYSTEM	PP	100	40	45	P C	13. SYSTEM SOFTWARE PROGRAMMING	PP	100	40	61	P C	
02. THEORY OF COMPUTATION	PP	100	40	57	P C	14. MANAGEMENT INFORMATION SYSTEMS	PP	100	40	51	P C	
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	62	P C	15. PROGRAMMING PARADIGMS	PP	100	40	45	P C	
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	51	P C	16. DESIGN & ANALYSIS OF ALGORITHMS	PP	100	40	51	P C	
05. SOFTWARE ENGINEERING	PP	100	40	40	P C	17. HUMAN COMPU.INTERACTION & USABI.	PP	100	40	44	P C	
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	19	P C	18. SOFTWARE DESIGN LABORATORY	TW	50	20	35	P C	
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	28	P C	19. SOFTWARE DESIGN LABORATORY	PR	50	20	38	P	
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	19	P C	20. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	37	P C	
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	40	P C	21. SOFTWARE DEVELOPMENT TOOLS LAB.	OR	50	20	27	P C	
10. NETWORK LABORATORY	TW	25	10	18	P C	22. SEMINAR AND TECHNICAL COMMUN.	TW	50	20	40	P C	
11. NETWORK LABORATORY	OR	50	20	37	P C							
12. SOFT SKILLS LABORATORY	TW	25	10	17	P C							

GRAND TOTAL = 862/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

T80058651	KOTHAWALE AKASH SANJAY			ANITA		, 71045486E		T8058563		PICT		T80058651
01. OPERATING SYSTEM	PP	100	40	52	P C	13. SYSTEM SOFTWARE PROGRAMMING	PP	100	40	45	P C	
02. THEORY OF COMPUTATION	PP	100	40	61	P C	14. MANAGEMENT INFORMATION SYSTEMS	PP	100	40	42	P C	
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	69	P C	15. PROGRAMMING PARADIGMS	PP	100	40	43	P C	
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	42	P C	16. DESIGN & ANALYSIS OF ALGORITHMS	PP	100	40	53	P C	
05. SOFTWARE ENGINEERING	PP	100	40	51	P C	17. HUMAN COMPU.INTERACTION & USABI.	PP	100	40	48	P C	
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	23	P C	18. SOFTWARE DESIGN LABORATORY	TW	50	20	39	P C	
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	26	P C	19. SOFTWARE DESIGN LABORATORY	PR	50	20	25	P	
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	22	P C	20. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	36	P C	
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	41	P C	21. SOFTWARE DEVELOPMENT TOOLS LAB.	OR	50	20	40	P C	
10. NETWORK LABORATORY	TW	25	10	22	P C	22. SEMINAR AND TECHNICAL COMMUN.	TW	50	20	41	P C	
11. NETWORK LABORATORY	OR	50	20	37	P C							
12. SOFT SKILLS LABORATORY	TW	25	10	22	P C							

GRAND TOTAL = 880/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

T80058652	KULKARNI PRAMOD SHIVAJIRAO			SUMATI		, 71045672H		T8058565		PICT		T80058652
01. OPERATING SYSTEM	PP	100	40	40	P C	13. SYSTEM SOFTWARE PROGRAMMING	PP	100	40	60	P C	
02. THEORY OF COMPUTATION	PP	100	40	40	P C	14. MANAGEMENT INFORMATION SYSTEMS	PP	100	40	44	P C	
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	61	P C	15. PROGRAMMING PARADIGMS	PP	100	40	42	P C	
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	49	P C	16. DESIGN & ANALYSIS OF ALGORITHMS	PP	100	40	41	P C	
05. SOFTWARE ENGINEERING	PP	100	40	40	P C	17. HUMAN COMPU.INTERACTION & USABI.	PP	100	40	46	P C	
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	12	P C	18. SOFTWARE DESIGN LABORATORY	TW	50	20	25	P C	
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	20	P C	19. SOFTWARE DESIGN LABORATORY	PR	50	20	12	F	
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	10	P C	20. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	22	P C	
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	25	P C	21. SOFTWARE DEVELOPMENT TOOLS LAB.	OR	50	20	25	P	
10. NETWORK LABORATORY	TW	25	10	10	P C	22. SEMINAR AND TECHNICAL COMMUN.	TW	50	20	20	P C	
11. NETWORK LABORATORY	OR	50	20	26	P C							
12. SOFT SKILLS LABORATORY	TW	25	10	12	P C							

GRAND TOTAL = 682/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80058653	MAHAJAN SACHIN RAVINDRA				SARALA				, 70701521K				T8058569		PICT		T80058653
01. OPERATING SYSTEM	PP	100	40	43	P C	13. SYSTEM SOFTWARE PROGRAMMING	PP	100	40	64	P C						
02. THEORY OF COMPUTATION	PP	100	40	AA	F	14. MANAGEMENT INFORMATION SYSTEMS	PP	100	40	52	P C						
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	61	P C	15. PROGRAMMING PARADIGMS	PP	100	40	43	P C						
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	42	P C	16. DESIGN & ANALYSIS OF ALGORITHMS	PP	100	40	44	P						
05. SOFTWARE ENGINEERING	PP	100	40	40	P C	17. HUMAN COMPU.INTERACTION & USABI.	PP	100	40	47	P C						
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	10	P C	18. SOFTWARE DESIGN LABORATORY	TW	50	20	20	P C						
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	20	P C	19. SOFTWARE DESIGN LABORATORY	PR	50	20	AA	F						
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	10	P C	20. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	20	P C						
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	25	P C	21. SOFTWARE DEVELOPMENT TOOLS LAB.	OR	50	20	20	P						
10. NETWORK LABORATORY	TW	25	10	10	P C	22. SEMINAR AND TECHNICAL COMMUN.	TW	50	20	20	P C						
11. NETWORK LABORATORY	OR	50	20	32	P C												
12. SOFT SKILLS LABORATORY	TW	25	10	10	P C												

GRAND TOTAL = 633/1500, RESULT: FAILS A.T.K.T.

RESULT RESERVED FOR BKLG

ORDN. 1 MARKS :

T80058654	MULAY AMIT YESHWANT				SNEHAL				, 71045530F				T8058575		PICT		T80058654
01. OPERATING SYSTEM	PP	100	40	40	P C	13. SYSTEM SOFTWARE PROGRAMMING	PP	100	40	40	P C						
02. THEORY OF COMPUTATION	PP	100	40	52	P C	14. MANAGEMENT INFORMATION SYSTEMS	PP	100	40	67	P						
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	58	P C	15. PROGRAMMING PARADIGMS	PP	100	40	50	P C						
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	58	P C	16. DESIGN & ANALYSIS OF ALGORITHMS	PP	100	40	50	P C						
05. SOFTWARE ENGINEERING	PP	100	40	46	P C	17. HUMAN COMPU.INTERACTION & USABI.	PP	100	40	46	P C						
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	15	P C	18. SOFTWARE DESIGN LABORATORY	TW	50	20	20	P C						
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	23	P	19. SOFTWARE DESIGN LABORATORY	PR	50	20	AA	F						
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	15	P C	20. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	21	P C						
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	20	P C	21. SOFTWARE DEVELOPMENT TOOLS LAB.	OR	50	20	10	F						
10. NETWORK LABORATORY	TW	25	10	14	P C	22. SEMINAR AND TECHNICAL COMMUN.	TW	50	20	20	P C						
11. NETWORK LABORATORY	OR	50	20	23	P C												
12. SOFT SKILLS LABORATORY	TW	25	10	10	P C												

GRAND TOTAL = 698/1500, RESULT: FAILS A.T.K.T.

RESULT RESERVED FOR BKLG

ORDN. 1 MARKS :

T80058655	NAIKNAWARE UTKARSH MARUTI				MAHANANDA				, 71045538M				T8058579		PICT		T80058655
01. OPERATING SYSTEM	PP	100	40	40	P C	13. SYSTEM SOFTWARE PROGRAMMING	PP	100	40	64	P C						
02. THEORY OF COMPUTATION	PP	100	40	48	P C	14. MANAGEMENT INFORMATION SYSTEMS	PP	100	40	59	P C						
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	52	P C	15. PROGRAMMING PARADIGMS	PP	100	40	62	P C						
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	50	P C	16. DESIGN & ANALYSIS OF ALGORITHMS	PP	100	40	40	P C						
05. SOFTWARE ENGINEERING	PP	100	40	40	P C	17. HUMAN COMPU.INTERACTION & USABI.	PP	100	40	41	P C						
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	23	P C	18. SOFTWARE DESIGN LABORATORY	TW	50	20	37	P C						
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	32	P C	19. SOFTWARE DESIGN LABORATORY	PR	50	20	22	P						
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	20	P C	20. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	38	P C						
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	30	P C	21. SOFTWARE DEVELOPMENT TOOLS LAB.	OR	50	20	42	P C						
10. NETWORK LABORATORY	TW	25	10	22	P C	22. SEMINAR AND TECHNICAL COMMUN.	TW	50	20	40	P C						
11. NETWORK LABORATORY	OR	50	20	35	P C												
12. SOFT SKILLS LABORATORY	TW	25	10	21	P C												

GRAND TOTAL = 858/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80058656	NY GARIN				YI MALIVATTEY		, 70925519F		T8058583		PICT		T80058656
01. OPERATING SYSTEM	PP	100	40	48	P C	13. SYSTEM SOFTWARE PROGRAMMING	PP	100	40	61	P C		
02. THEORY OF COMPUTATION	PP	100	40	44	P C	14. MANAGEMENT INFORMATION SYSTEMS	PP	100	40	48	P C		
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	46	P C	15. PROGRAMMING PARADIGMS	PP	100	40	56	P C		
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	40	P C	16. DESIGN & ANALYSIS OF ALGORITHMS	PP	100	40	45	P C		
05. SOFTWARE ENGINEERING	PP	100	40	46	P C	17. HUMAN COMPU.INTERACTION & USABI.	PP	100	40	46	P C		
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	19	P C	18. SOFTWARE DESIGN LABORATORY	TW	50	20	28	P C		
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	27	P C	19. SOFTWARE DESIGN LABORATORY	PR	50	20	21	P		
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	20	P C	20. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	32	P C		
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	31	P C	21. SOFTWARE DEVELOPMENT TOOLS LAB.	OR	50	20	36	P C		
10. NETWORK LABORATORY	TW	25	10	18	P C	22. SEMINAR AND TECHNICAL COMMUN.	TW	50	20	41	P C		
11. NETWORK LABORATORY	OR	50	20	23	P C								
12. SOFT SKILLS LABORATORY	TW	25	10	19	P C								

GRAND TOTAL = 795/1500, RESULT: SECOND CLASS

ORDN. 1 MARKS :

T80058657	PANSARE ROHAN SAMPAT				MANGAL		, 71045554C		T8058585		PICT		T80058657
01. OPERATING SYSTEM	PP	100	40	50	P C	13. SYSTEM SOFTWARE PROGRAMMING	PP	100	40	65	P C		
02. THEORY OF COMPUTATION	PP	100	40	49	P C	14. MANAGEMENT INFORMATION SYSTEMS	PP	100	40	54	P C		
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	69	P C	15. PROGRAMMING PARADIGMS	PP	100	40	75	P C		
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	60	P C	16. DESIGN & ANALYSIS OF ALGORITHMS	PP	100	40	45	P C		
05. SOFTWARE ENGINEERING	PP	100	40	49	P C	17. HUMAN COMPU.INTERACTION & USABI.	PP	100	40	55	P C		
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	22	P C	18. SOFTWARE DESIGN LABORATORY	TW	50	20	36	P C		
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	39	P C	19. SOFTWARE DESIGN LABORATORY	PR	50	20	40	P		
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	20	P C	20. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	38	P C		
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	28	P C	21. SOFTWARE DEVELOPMENT TOOLS LAB.	OR	50	20	34	P C		
10. NETWORK LABORATORY	TW	25	10	20	P C	22. SEMINAR AND TECHNICAL COMMUN.	TW	50	20	41	P C		
11. NETWORK LABORATORY	OR	50	20	36	P C								
12. SOFT SKILLS LABORATORY	TW	25	10	21	P C								

GRAND TOTAL = 946/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

T80058658	PARTH SARTHI PIPLANI				SUREKHA		, 71045560H		T8058589		PICT		T80058658
01. OPERATING SYSTEM	PP	100	40	45	P C	13. SYSTEM SOFTWARE PROGRAMMING	PP	100	40	54	P C		
02. THEORY OF COMPUTATION	PP	100	40	40	P C	14. MANAGEMENT INFORMATION SYSTEMS	PP	100	40	48	P C		
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	40	P C	15. PROGRAMMING PARADIGMS	PP	100	40	46	P		
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	40	P C	16. DESIGN & ANALYSIS OF ALGORITHMS	PP	100	40	53	P C		
05. SOFTWARE ENGINEERING	PP	100	40	41	P C	17. HUMAN COMPU.INTERACTION & USABI.	PP	100	40	40	P C		
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	16	P C	18. SOFTWARE DESIGN LABORATORY	TW	50	20	20	P C		
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	22	P	19. SOFTWARE DESIGN LABORATORY	PR	50	20	20	P		
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	10	P C	20. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	24	P C		
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	21	P	21. SOFTWARE DEVELOPMENT TOOLS LAB.	OR	50	20	32	P C		
10. NETWORK LABORATORY	TW	25	10	12	P C	22. SEMINAR AND TECHNICAL COMMUN.	TW	50	20	22	P C		
11. NETWORK LABORATORY	OR	50	20	32	P C								
12. SOFT SKILLS LABORATORY	TW	25	10	15	P C								

GRAND TOTAL = 693/1500, RESULT: PASS CLASS

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80058659 PATHAK ANIKET SANJAY ASHA , 70925542L , T8058672 , PICT , T80058659

01. OPERATING SYSTEM	PP	100	40	40	P C	13. SYSTEM SOFTWARE PROGRAMMING	PP	100	40	56	P C
02. THEORY OF COMPUTATION	PP	100	40	49	P	14. MANAGEMENT INFORMATION SYSTEMS	PP	100	40	49	P C
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	57	P C	15. PROGRAMMING PARADIGMS	PP	100	40	40	P C
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	40	P C	16. DESIGN & ANALYSIS OF ALGORITHMS	PP	100	40	42	P C
05. SOFTWARE ENGINEERING	PP	100	40	47	P C	17. HUMAN COMPU.INTERACTION & USABI.	PP	100	40	51	P C
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	12	P C	18. SOFTWARE DESIGN LABORATORY	TW	50	20	26	P C
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	20	P C	19. SOFTWARE DESIGN LABORATORY	PR	50	20	32	P C
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	12	P C	20. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	24	P C
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	33	P C	21. SOFTWARE DEVELOPMENT TOOLS LAB.	OR	50	20	36	P C
10. NETWORK LABORATORY	TW	25	10	12	P C	22. SEMINAR AND TECHNICAL COMMUN.	TW	50	20	32	P C
11. NETWORK LABORATORY	OR	50	20	30	P C						
12. SOFT SKILLS LABORATORY	TW	25	10	18	P C						

GRAND TOTAL = 758/1500, RESULT: SECOND CLASS

ORDN. 1 MARKS :

T80058660 POLE AKSHAYKUMAR RAHUL SHEELA , 71045576D , T8058596 , PICT , T80058660

01. OPERATING SYSTEM	PP	100	40	40	P C	13. SYSTEM SOFTWARE PROGRAMMING	PP	100	40	51	P C
02. THEORY OF COMPUTATION	PP	100	40	40	P C	14. MANAGEMENT INFORMATION SYSTEMS	PP	100	40	52	P C
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	55	P C	15. PROGRAMMING PARADIGMS	PP	100	40	54	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	42	P C	16. DESIGN & ANALYSIS OF ALGORITHMS	PP	100	40	47	P C
05. SOFTWARE ENGINEERING	PP	100	40	40	P C	17. HUMAN COMPU.INTERACTION & USABI.	PP	100	40	52	P C
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	12	P C	18. SOFTWARE DESIGN LABORATORY	TW	50	20	37	P C
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	20	P C	19. SOFTWARE DESIGN LABORATORY	PR	50	20	22	P C
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	10	P C	20. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	32	P C
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	28	P C	21. SOFTWARE DEVELOPMENT TOOLS LAB.	OR	50	20	38	P C
10. NETWORK LABORATORY	TW	25	10	19	P C	22. SEMINAR AND TECHNICAL COMMUN.	TW	50	20	41	P C
11. NETWORK LABORATORY	OR	50	20	26	P C						
12. SOFT SKILLS LABORATORY	TW	25	10	20	P C						

GRAND TOTAL = 778/1500, RESULT: SECOND CLASS

ORDN. 1 MARKS :

T80058661 POTEY AKSHAY MADHUKAR NILIMA , 71045578L , T8058598 , PICT , T80058661

01. OPERATING SYSTEM	PP	100	40	AA	F						
02. THEORY OF COMPUTATION	PP	100	40	42	P C						
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	66	P C						
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	47	P C						
05. SOFTWARE ENGINEERING	PP	100	40	40	P C						
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	18	P C						
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	30	P C						
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	15	P C						
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	AA	F						
10. NETWORK LABORATORY	TW	25	10	11	P C						
11. NETWORK LABORATORY	OR	50	20	25	P						
12. SOFT SKILLS LABORATORY	TW	25	10	15	P C						

FIRST TERM TOTAL = 309/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80058662	PUNTAMBEKAR SHREYA SHAILESH				SEEMA				, 71045584E				T8058600		PICT			T80058662
01. OPERATING SYSTEM	PP	100	40	45	P				13. SYSTEM SOFTWARE PROGRAMMING	PP	100	40	50	P	C			
02. THEORY OF COMPUTATION	PP	100	40	28	F				14. MANAGEMENT INFORMATION SYSTEMS	PP	100	40	47	P	C			
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	46	P	C			15. PROGRAMMING PARADIGMS	PP	100	40	41	P	C			
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	40	P	C			16. DESIGN & ANALYSIS OF ALGORITHMS	PP	100	40	AA	F				
05. SOFTWARE ENGINEERING	PP	100	40	40	P	C			17. HUMAN COMPU.INTERACTION & USABI.	PP	100	40	43	P	C			
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	18	P	C			18. SOFTWARE DESIGN LABORATORY	TW	50	20	29	P	C			
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	20	P	C			19. SOFTWARE DESIGN LABORATORY	PR	50	20	AA	F				
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	18	P	C			20. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	33	P	C			
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	38	P	C			21. SOFTWARE DEVELOPMENT TOOLS LAB.	OR	50	20	40	P	C			
10. NETWORK LABORATORY	TW	25	10	19	P	C			22. SEMINAR AND TECHNICAL COMMUN.	TW	50	20	40	P	C			
11. NETWORK LABORATORY	OR	50	20	28	P	C												
12. SOFT SKILLS LABORATORY	TW	25	10	17	P	C												

GRAND TOTAL = 680/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

T80058663	RAJOLE VISHAL VILASRAO				SUNITA				, 71134976C				T8058602		PICT			T80058663
01. OPERATING SYSTEM	PP	100	40	53	P	C			13. SYSTEM SOFTWARE PROGRAMMING	PP	100	40	68	P	C			
02. THEORY OF COMPUTATION	PP	100	40	72	P	C			14. MANAGEMENT INFORMATION SYSTEMS	PP	100	40	61	P	C			
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	71	P	C			15. PROGRAMMING PARADIGMS	PP	100	40	70	P	C			
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	60	P	C			16. DESIGN & ANALYSIS OF ALGORITHMS	PP	100	40	60	P	C			
05. SOFTWARE ENGINEERING	PP	100	40	60	P	C			17. HUMAN COMPU.INTERACTION & USABI.	PP	100	40	49	P	C			
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	22	P	C			18. SOFTWARE DESIGN LABORATORY	TW	50	20	39	P	C			
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	41	P	C			19. SOFTWARE DESIGN LABORATORY	PR	50	20	39	P				
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	21	P	C			20. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	36	P	C			
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	40	P	C			21. SOFTWARE DEVELOPMENT TOOLS LAB.	OR	50	20	45	P	C			
10. NETWORK LABORATORY	TW	25	10	22	P	C			22. SEMINAR AND TECHNICAL COMMUN.	TW	50	20	45	P	C			
11. NETWORK LABORATORY	OR	50	20	35	P	C												
12. SOFT SKILLS LABORATORY	TW	25	10	20	P	C												

GRAND TOTAL = 1029/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

T80058664	RISHABH PANDITA				USHA				, 71045589F				T8058603		PICT			T80058664
01. OPERATING SYSTEM	PP	100	40	52	P	C			13. SYSTEM SOFTWARE PROGRAMMING	PP	100	40	65	P	C			
02. THEORY OF COMPUTATION	PP	100	40	59	P	C			14. MANAGEMENT INFORMATION SYSTEMS	PP	100	40	58	P	C			
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	67	P	C			15. PROGRAMMING PARADIGMS	PP	100	40	43	P	C			
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	60	P	C			16. DESIGN & ANALYSIS OF ALGORITHMS	PP	100	40	61	P	C			
05. SOFTWARE ENGINEERING	PP	100	40	46	P	C			17. HUMAN COMPU.INTERACTION & USABI.	PP	100	40	56	P	C			
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	21	P	C			18. SOFTWARE DESIGN LABORATORY	TW	50	20	21	P	C			
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	41	P	C			19. SOFTWARE DESIGN LABORATORY	PR	50	20	36	P				
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	21	P	C			20. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	34	P	C			
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	30	P	C			21. SOFTWARE DEVELOPMENT TOOLS LAB.	OR	50	20	43	P	C			
10. NETWORK LABORATORY	TW	25	10	20	P	C			22. SEMINAR AND TECHNICAL COMMUN.	TW	50	20	41	P	C			
11. NETWORK LABORATORY	OR	50	20	44	P	C												
12. SOFT SKILLS LABORATORY	TW	25	10	21	P	C												

GRAND TOTAL = 940/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80058665	ROHAN MARWADI				MEENAKSHI				, 71045590K			T8058604		PICT		T80058665
01. OPERATING SYSTEM	PP	100	40	56	P C	13. SYSTEM SOFTWARE PROGRAMMING	PP	100	40	60	P C					
02. THEORY OF COMPUTATION	PP	100	40	57	P C	14. MANAGEMENT INFORMATION SYSTEMS	PP	100	40	42	P C					
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	40	P C	15. PROGRAMMING PARADIGMS	PP	100	40	58	P					
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	52	P C	16. DESIGN & ANALYSIS OF ALGORITHMS	PP	100	40	42	P C					
05. SOFTWARE ENGINEERING	PP	100	40	45	P C	17. HUMAN COMPU.INTERACTION & USABI.	PP	100	40	43	P C					
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	17	P C	18. SOFTWARE DESIGN LABORATORY	TW	50	20	21	P C					
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	32	P C	19. SOFTWARE DESIGN LABORATORY	PR	50	20	21	P					
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	19	P C	20. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	30	P C					
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	40	P C	21. SOFTWARE DEVELOPMENT TOOLS LAB.	OR	50	20	40	P C					
10. NETWORK LABORATORY	TW	25	10	19	P C	22. SEMINAR AND TECHNICAL COMMUN.	TW	50	20	35	P C					
11. NETWORK LABORATORY	OR	50	20	22	P C											
12. SOFT SKILLS LABORATORY	TW	25	10	18	P C											

GRAND TOTAL = 809/1500, RESULT: SECOND CLASS

ORDN. 1 MARKS :

T80058666	SARTHAK MAJITHIA				POOJA				, 71045598E			T8058608		PICT		T80058666
01. OPERATING SYSTEM	PP	100	40	40	P C	13. SYSTEM SOFTWARE PROGRAMMING	PP	100	40	AA	F					
02. THEORY OF COMPUTATION	PP	100	40	45	P C	14. MANAGEMENT INFORMATION SYSTEMS	PP	100	40	61	P C					
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	40	P C	15. PROGRAMMING PARADIGMS	PP	100	40	47	P C					
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	51	P C	16. DESIGN & ANALYSIS OF ALGORITHMS	PP	100	40	55	P C					
05. SOFTWARE ENGINEERING	PP	100	40	AA	F	17. HUMAN COMPU.INTERACTION & USABI.	PP	100	40	50	P C					
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	11	P C	18. SOFTWARE DESIGN LABORATORY	TW	50	20	20	P C					
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	22	P C	19. SOFTWARE DESIGN LABORATORY	PR	50	20	AA	F					
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	12	P C	20. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	22	P C					
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	AA	F	21. SOFTWARE DEVELOPMENT TOOLS LAB.	OR	50	20	32	P C					
10. NETWORK LABORATORY	TW	25	10	10	P C	22. SEMINAR AND TECHNICAL COMMUN.	TW	50	20	22	P C					
11. NETWORK LABORATORY	OR	50	20	24	P C											
12. SOFT SKILLS LABORATORY	TW	25	10	18	P C											

GRAND TOTAL = 582/1500, RESULT: FAILS A.T.K.T.

RESULT RESERVED FOR BKLG

ORDN. 1 MARKS :

T80058667	SATPUTE SWAPNIL BHASKAR				SUREKHA				, 71045600L			T8058611		PICT		T80058667
01. OPERATING SYSTEM	PP	100	40	46	P C	13. SYSTEM SOFTWARE PROGRAMMING	PP	100	40	50	P C					
02. THEORY OF COMPUTATION	PP	100	40	42	P C	14. MANAGEMENT INFORMATION SYSTEMS	PP	100	40	47	P C					
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	50	P C	15. PROGRAMMING PARADIGMS	PP	100	40	40	P C					
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	44	P C	16. DESIGN & ANALYSIS OF ALGORITHMS	PP	100	40	40	P C					
05. SOFTWARE ENGINEERING	PP	100	40	40	P C	17. HUMAN COMPU.INTERACTION & USABI.	PP	100	40	40	P C					
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	15	P C	18. SOFTWARE DESIGN LABORATORY	TW	50	20	20	P C					
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	38	P C	19. SOFTWARE DESIGN LABORATORY	PR	50	20	20	P					
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	15	P C	20. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	30	P C					
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	30	P C	21. SOFTWARE DEVELOPMENT TOOLS LAB.	OR	50	20	35	P C					
10. NETWORK LABORATORY	TW	25	10	15	P C	22. SEMINAR AND TECHNICAL COMMUN.	TW	50	20	21	P C					
11. NETWORK LABORATORY	OR	50	20	35	P C											
12. SOFT SKILLS LABORATORY	TW	25	10	19	P C											

GRAND TOTAL = 732/1500, RESULT: PASS CLASS

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80058668	SHIVADE PRATHAMESH SHRIKANT				NILIMA				, 70925608G				T8058621		PICT		T80058668
01. OPERATING SYSTEM	PP	100	40	47	P C	13. SYSTEM SOFTWARE PROGRAMMING	PP	100	40	59	P C						
02. THEORY OF COMPUTATION	PP	100	40	43	P C	14. MANAGEMENT INFORMATION SYSTEMS	PP	100	40	41	P C						
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	50	P C	15. PROGRAMMING PARADIGMS	PP	100	40	45	P C						
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	40	P C	16. DESIGN & ANALYSIS OF ALGORITHMS	PP	100	40	45	P						
05. SOFTWARE ENGINEERING	PP	100	40	40	P C	17. HUMAN COMPU.INTERACTION & USABI.	PP	100	40	52	P C						
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	14	P C	18. SOFTWARE DESIGN LABORATORY	TW	50	20	20	P C						
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	40	P C	19. SOFTWARE DESIGN LABORATORY	PR	50	20	20\$	P C						
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	15	P C	20. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	29	P C						
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	41	P C	21. SOFTWARE DEVELOPMENT TOOLS LAB.	OR	50	20	38	P C						
10. NETWORK LABORATORY	TW	25	10	14	P C	22. SEMINAR AND TECHNICAL COMMUN.	TW	50	20	34	P C						
11. NETWORK LABORATORY	OR	50	20	24	P C												
12. SOFT SKILLS LABORATORY	TW	25	10	20	P C												

GRAND TOTAL = 771/1500, RESULT: SECOND CLASS [\$ 0.1]

ORDN. 1 MARKS : (19)2,

T80058669	SURYAWANSHI NIRAJ NITIN				NEHA				, 70701667D				T8058630		PICT		T80058669
01. OPERATING SYSTEM	PP	100	40	AA	F	13. SYSTEM SOFTWARE PROGRAMMING	PP	100	40	AA	F						
02. THEORY OF COMPUTATION	PP	100	40	AA	F	14. MANAGEMENT INFORMATION SYSTEMS	PP	100	40	40	P C						
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	40	P C	15. PROGRAMMING PARADIGMS	PP	100	40	40	P C						
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	44	P C	16. DESIGN & ANALYSIS OF ALGORITHMS	PP	100	40	AA	F						
05. SOFTWARE ENGINEERING	PP	100	40	AA	F	17. HUMAN COMPU.INTERACTION & USABI.	PP	100	40	15	F						
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	12	P C	18. SOFTWARE DESIGN LABORATORY	TW	50	20	28	P C						
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	AA	F	19. SOFTWARE DESIGN LABORATORY	PR	50	20	AA	F						
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	14	P C	20. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	32	P C						
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	20	P C	21. SOFTWARE DEVELOPMENT TOOLS LAB.	OR	50	20	38	P C						
10. NETWORK LABORATORY	TW	25	10	11	P C	22. SEMINAR AND TECHNICAL COMMUN.	TW	50	20	22	P C						
11. NETWORK LABORATORY	OR	50	20	28	P C												
12. SOFT SKILLS LABORATORY	TW	25	10	18	P C												

GRAND TOTAL = 402/1500, RESULT: FAILS

RESULT RESERVED FOR BKLG

ORDN. 1 MARKS :

T80058670	SUYASH PANDEY				SHAILA				, 70925625G				T8058631		PICT		T80058670
01. OPERATING SYSTEM	PP	100	40	55	P												
02. THEORY OF COMPUTATION	PP	100	40	24	F												
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	46	P												
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	40	P C												
05. SOFTWARE ENGINEERING	PP	100	40	40	P C												
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	11	P C												
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	30	P												
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	14	P C												
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	28	P C												
10. NETWORK LABORATORY	TW	25	10	10	P C												
11. NETWORK LABORATORY	OR	50	20	32	P												
12. SOFT SKILLS LABORATORY	TW	25	10	15	P C												

FIRST TERM TOTAL = 345/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80058671	TANAWADE AMOL SHAMRAO				MANDA				, 70801648M				, T8058677		PICT			, T80058671
01. OPERATING SYSTEM	PP	100	40	40	P C	13. SYSTEM SOFTWARE PROGRAMMING	PP	100	40	59	P C							
02. THEORY OF COMPUTATION	PP	100	40	40	P C	14. MANAGEMENT INFORMATION SYSTEMS	PP	100	40	45	P C							
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	43	P C	15. PROGRAMMING PARADIGMS	PP	100	40	41	P C							
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	40	P C	16. DESIGN & ANALYSIS OF ALGORITHMS	PP	100	40	40	P C							
05. SOFTWARE ENGINEERING	PP	100	40	40	P C	17. HUMAN COMPU.INTERACTION & USABI.	PP	100	40	57	P C							
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	12	P C	18. SOFTWARE DESIGN LABORATORY	TW	50	20	39	P C							
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	13	F	19. SOFTWARE DESIGN LABORATORY	PR	50	20	30	P C							
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	12	P C	20. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	36	P C							
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	27	P C	21. SOFTWARE DEVELOPMENT TOOLS LAB.	OR	50	20	36	P C							
10. NETWORK LABORATORY	TW	25	10	10	P C	22. SEMINAR AND TECHNICAL COMMUN.	TW	50	20	41	P C							
11. NETWORK LABORATORY	OR	50	20	33	P C													
12. SOFT SKILLS LABORATORY	TW	25	10	14	P C													

GRAND TOTAL = 748/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

T80058672	THAKUR BHAVANA PRAKASH				ARCHANA				, 70925633H				, T8058634		PICT			, T80058672
01. OPERATING SYSTEM	PP	100	40	43	P C	13. SYSTEM SOFTWARE PROGRAMMING	PP	100	40	42	P C							
02. THEORY OF COMPUTATION	PP	100	40	52	P C	14. MANAGEMENT INFORMATION SYSTEMS	PP	100	40	40	P							
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	46	P C	15. PROGRAMMING PARADIGMS	PP	100	40	48	P							
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	41	P C	16. DESIGN & ANALYSIS OF ALGORITHMS	PP	100	40	44	P C							
05. SOFTWARE ENGINEERING	PP	100	40	41	P C	17. HUMAN COMPU.INTERACTION & USABI.	PP	100	40	52	P							
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	17	P C	18. SOFTWARE DESIGN LABORATORY	TW	50	20	31	P C							
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	35	P C	19. SOFTWARE DESIGN LABORATORY	PR	50	20	38	P C							
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	17	P C	20. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	30	P C							
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	27	P C	21. SOFTWARE DEVELOPMENT TOOLS LAB.	OR	50	20	36	P C							
10. NETWORK LABORATORY	TW	25	10	18	P C	22. SEMINAR AND TECHNICAL COMMUN.	TW	50	20	33	P C							
11. NETWORK LABORATORY	OR	50	20	24	P C													
12. SOFT SKILLS LABORATORY	TW	25	10	21	P C													

GRAND TOTAL = 776/1500, RESULT: SECOND CLASS

ORDN. 1 MARKS :

T80058673	THOMBRE PRIYANKA MILIND				JAYMALA				, 71045649C				, T8058635		PICT			, T80058673
01. OPERATING SYSTEM	PP	100	40	43	P C	13. SYSTEM SOFTWARE PROGRAMMING	PP	100	40	52	P C							
02. THEORY OF COMPUTATION	PP	100	40	56	P C	14. MANAGEMENT INFORMATION SYSTEMS	PP	100	40	40	P C							
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	40	P C	15. PROGRAMMING PARADIGMS	PP	100	40	45	P C							
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	44	P C	16. DESIGN & ANALYSIS OF ALGORITHMS	PP	100	40	46	P C							
05. SOFTWARE ENGINEERING	PP	100	40	42	P C	17. HUMAN COMPU.INTERACTION & USABI.	PP	100	40	54	P							
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	17	P C	18. SOFTWARE DESIGN LABORATORY	TW	50	20	32	P C							
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	35	P C	19. SOFTWARE DESIGN LABORATORY	PR	50	20	33	P C							
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	15	P C	20. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	31	P C							
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	38	P C	21. SOFTWARE DEVELOPMENT TOOLS LAB.	OR	50	20	37	P C							
10. NETWORK LABORATORY	TW	25	10	18	P C	22. SEMINAR AND TECHNICAL COMMUN.	TW	50	20	20	P C							
11. NETWORK LABORATORY	OR	50	20	29	P C													
12. SOFT SKILLS LABORATORY	TW	25	10	20	P C													

GRAND TOTAL = 787/1500, RESULT: SECOND CLASS

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80058674	UPPOD BALAJI SAMBHAJI				SHOBHA				, 71045653M			T8058638		PICT		T80058674
01. OPERATING SYSTEM	PP	100	40	40	P C	13. SYSTEM SOFTWARE PROGRAMMING	PP	100	40	41	P C					
02. THEORY OF COMPUTATION	PP	100	40	40	P C	14. MANAGEMENT INFORMATION SYSTEMS	PP	100	40	40	P C					
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	54	P C	15. PROGRAMMING PARADIGMS	PP	100	40	59	P					
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	41	P C	16. DESIGN & ANALYSIS OF ALGORITHMS	PP	100	40	43	P C					
05. SOFTWARE ENGINEERING	PP	100	40	44	P	17. HUMAN COMPU.INTERACTION & USABI.	PP	100	40	46	P C					
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	15	P C	18. SOFTWARE DESIGN LABORATORY	TW	50	20	20	P C					
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	28	P C	19. SOFTWARE DESIGN LABORATORY	PR	50	20	12	F					
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	17	P C	20. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	20	P C					
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	41	P C	21. SOFTWARE DEVELOPMENT TOOLS LAB.	OR	50	20	38	P C					
10. NETWORK LABORATORY	TW	25	10	15	P C	22. SEMINAR AND TECHNICAL COMMUN.	TW	50	20	20	P C					
11. NETWORK LABORATORY	OR	50	20	29	P C											
12. SOFT SKILLS LABORATORY	TW	25	10	20	P C											

GRAND TOTAL = 723/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

T80058675	VED PRAKASH SINGH				RAJ LAXMI				, 70801665M			T8058679		PICT		T80058675
01. OPERATING SYSTEM	PP	100	40	AA	F	13. SYSTEM SOFTWARE PROGRAMMING	PP	100	40	40	P C					
02. THEORY OF COMPUTATION	PP	100	40	52	P C	14. MANAGEMENT INFORMATION SYSTEMS	PP	100	40	46	P C					
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	46	P C	15. PROGRAMMING PARADIGMS	PP	100	40	AA	F					
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	AA	F	16. DESIGN & ANALYSIS OF ALGORITHMS	PP	100	40	40	P C					
05. SOFTWARE ENGINEERING	PP	100	40	40	P C	17. HUMAN COMPU.INTERACTION & USABI.	PP	100	40	44	P C					
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	10	P C	18. SOFTWARE DESIGN LABORATORY	TW	50	20	21	P C					
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	20	P C	19. SOFTWARE DESIGN LABORATORY	PR	50	20	AA	F					
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	10	P C	20. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	20	P C					
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	21	P C	21. SOFTWARE DEVELOPMENT TOOLS LAB.	OR	50	20	AA	F					
10. NETWORK LABORATORY	TW	25	10	10	P C	22. SEMINAR AND TECHNICAL COMMUN.	TW	50	20	22	P C					
11. NETWORK LABORATORY	OR	50	20	26	P C											
12. SOFT SKILLS LABORATORY	TW	25	10	10	P C											

GRAND TOTAL = 478/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

T80058676	VIVEK KUMAR				SUMAN				, 70925651F			T8058643		PICT		T80058676
01. OPERATING SYSTEM	PP	100	40	46	P C	13. SYSTEM SOFTWARE PROGRAMMING	PP	100	40	49	P C					
02. THEORY OF COMPUTATION	PP	100	40	53	P C	14. MANAGEMENT INFORMATION SYSTEMS	PP	100	40	55	P C					
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	59	P C	15. PROGRAMMING PARADIGMS	PP	100	40	43	P C					
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	52	P C	16. DESIGN & ANALYSIS OF ALGORITHMS	PP	100	40	50	P					
05. SOFTWARE ENGINEERING	PP	100	40	40	P C	17. HUMAN COMPU.INTERACTION & USABI.	PP	100	40	47	P C					
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	21	P C	18. SOFTWARE DESIGN LABORATORY	TW	50	20	27	P C					
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	39	P C	19. SOFTWARE DESIGN LABORATORY	PR	50	20	27	P					
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	18	P C	20. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	34	P C					
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	28	P C	21. SOFTWARE DEVELOPMENT TOOLS LAB.	OR	50	20	38	P C					
10. NETWORK LABORATORY	TW	25	10	20	P C	22. SEMINAR AND TECHNICAL COMMUN.	TW	50	20	40	P C					
11. NETWORK LABORATORY	OR	50	20	30	P C											
12. SOFT SKILLS LABORATORY	TW	25	10	20	P C											

GRAND TOTAL = 836/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :