u by	veryr Di'i Di	Editor Version 2.0														
		UNIVERSITY	OF PUNE	,S.E.(2008	PAT.)(ELE	CTRONICS &	TELECOM.)	cols05	5					
	DATE : 1	18 AUG. 2011	CENT	RE : P	UNE I	NSTI	TUTE (OF COMPUTE	R TECHNOLOGY	, PUNE.		PAGE	NO.	01	(3	25)
	NOTE: FIRST	LINE : SEAT NO., NAM	IE OF THE	CANDI	DATE,	MO	THER,	PERMANENT	REG. NO.,	PREVIOUS SE	EAT NO., C	OLLEGE	Ξ,	SEAT N	Ю.	
	OTHER	R LINES: HEAD OF PASSIN	IG, MAX.	MARKS	, MI	N. P	ASS M	MARKS, MAR	KS OBTAINED,	P/F:PASS/	FAIL, C:P	REVIOL	JS CAI	RRY OV	ER/	
	s8053001	ABDULE MONALI MUKUND				SANI	DHYA		, 7104535	4L , S8	3053001 ,	PICT		, s80	5300	1
	01. SIGNAL	AND SYSTEMS	PP	100	40	40	РС	11.	ENGINEERING	MATHEMATIC	CS III	PP	100	40	75	Р
	02. SIGNAL	AND SYSTEMS	OR	50	20	32	P C	12.	ENGINEERING	MATHEMATIC	CS III	TW	25	10	24	Р
	03. SOLID S	STATES DEVICES AND CIRC	UITSPP	100	40	40	P C	13.	INTEGRATED	CIRCUITS AF	PPLICATIONS	PP	100	40	40	Р
	04 601 70 6	TATES DEVISES AND STRE	UTTCDD	Ε0	20	40	ъ с	1.4						20	20	_

04. SOLID STATES DEVICES AND CIRCUITSPR 14. INTEGRATED CIRCUITS APPLICATIONS PR 20 40 P C 50 20 30 P 05. NETWORK ANALYSIS 100 40 40 P C 15. ELECTROMAGNETIC 100 40 55 P 06. DIGITAL LOGIC DESIGN 100 40 49 P C 16. ELECTROMAGNETIC TW 25 10 16 P 07. DIGITAL LOGIC DESIGN 50 20 35 P C 17. DATA STRUCTURES PP 100 40 56 P 08. POWER DEVICES AND MACHINES 100 50 20 34 P 42 P C 18. DATA STRUCTURES 19. COMMUNICATION THEORY 09. NETWORK AND POWER LAB. TW 50 20 42 P C 100 40 42 P 50 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 20 41 P C 20. COMMUNICATION THEORY 20 32 P 21. CIRCUIT SIMULATION AND TOOLS 50 20 39 P TW

GRAND TOTAL = 844/1500, RESULT: HIGHER SECOND CLASS ORDN. 1 MARKS:

S8053002 ABHINIT KUMAR			ASHA DEVI	, 71050778L , S8053002 ,	PICT		, s80)53002	2
O1. SIGNAL AND SYSTEMS PP	100	40	70 P C	11. ENGINEERING MATHEMATICS III	PP	100	40	75	Р
02. SIGNAL AND SYSTEMS OR	50	20	30 P C	12. ENGINEERING MATHEMATICS III	TW	25	10	23	Р
03. SOLID STATES DEVICES AND CIRCUITSPP	100	40	50 P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	63	Р
04. SOLID STATES DEVICES AND CIRCUITSPR	50	20	20 P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	29	Р
05. NETWORK ANALYSIS PP	100	40	50 P C	15. ELECTROMAGNETIC	PP	100	40	60	Р
06. DIGITAL LOGIC DESIGN PP	100	40	50 P C	16. ELECTROMAGNETIC	TW	25	10	24	Р

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07. DIGITAL LOGIC DESIGN	PR	50	20	21	Р	17. DATA STRUCTURES PP 100 40 43 P
08. POWER DEVICES AND MACHINES	PP	100	40	45	РС	18. DATA STRUCTURES PR 50 20 38 P
09. NETWORK AND POWER LAB.	TW	50	20	39	РС	19. COMMUNICATION THEORY PP 100 40 68 P
10. ELECTRONIC INSTRUMENTS AND TOOLS	5 TW	50	20	36	РС	20. COMMUNICATION THEORY OR 50 20 40 P
						21. CIRCUIT SIMULATION AND TOOLS TW 50 20 36 P

GRAND TOTAL = 910/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS:

S8053003 ABHISHEK ANAND				IND	RANI	, 71045356G , S8053003 , PICT , S8053003	
01. SIGNAL AND SYSTEMS	PP	100	40	46	P C	11. ENGINEERING MATHEMATICS III PP 100 40 44	Р
02. SIGNAL AND SYSTEMS	OR	50	20	34	P C	12. ENGINEERING MATHEMATICS III TW 25 10 16	Р
03. SOLID STATES DEVICES AND CIRCUITS	SPP	100	40	40	P C	13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 47	Р
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	20	Р	14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 35	Р
05. NETWORK ANALYSIS	PP	100	40	40	P C	15. ELECTROMAGNETIC PP 100 40 48	Р
06. DIGITAL LOGIC DESIGN	PP	100	40	40	P C	16. ELECTROMAGNETIC TW 25 10 17	Р
07. DIGITAL LOGIC DESIGN	PR	50	20	30	P C	17. DATA STRUCTURES PP 100 40 49	Р
08. POWER DEVICES AND MACHINES	PP	100	40	46	Р	18. DATA STRUCTURES PR 50 20 35	Р
09. NETWORK AND POWER LAB.	TW	50	20	33	P C	19. COMMUNICATION THEORY PP 100 40 44	Р
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	35	P C	20. COMMUNICATION THEORY OR 50 20 38	Р
						21. CIRCUIT SIMULATION AND TOOLS TW 50 20 28	Р

GRAND TOTAL = 765/1500, RESULT: SECOND CLASS ORDN. 1 MARKS :

UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(ELECTRONICS & TELECOM.)

DATE: 18 AUG. 2011 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 02 (326)

NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

cols0	15
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S8053004 AM	IT KUMAR				RITA	4		, 71045367E	в ,	s8053004 ,	PICT		, s80	53004	4
01. SIGNAL ANI	D SYSTEMS	PP	100	40	83	РС	11.	ENGINEERING M	МАТНЕМАТ	ICS III	PP	100	40	91	Р
02. SIGNAL AN	D SYSTEMS	OR	50	20	30	Р	12.	ENGINEERING M	МАТНЕМАТ	ICS III	TW	25	10	18	Р
03. SOLID STA	TES DEVICES AND CIRCUITS	SPP	100	40	63	P C	13.	INTEGRATED C	IRCUITS	APPLICATIONS	PP	100	40	70	Р
04. SOLID STA	TES DEVICES AND CIRCUITS	SPR	50	20	21	P C	14.	INTEGRATED C	IRCUITS	APPLICATIONS	PR	50	20	23	Р
05. NETWORK A	NALYSIS	PP	100	40	57	P C	15.	ELECTROMAGNET	TIC		PP	100	40	57	Р
06. DIGITAL L	OGIC DESIGN	PP	100	40	45	P C	16.	ELECTROMAGNET	TIC		TW	25	10	20	Р
07. DIGITAL L	OGIC DESIGN	PR	50	20	39	P C	17.	DATA STRUCTUR	RES		PP	100	40	65	Р
08. POWER DEV	ICES AND MACHINES	PP	100	40	49	P C	18.	DATA STRUCTUR	RES		PR	50	20	32	Р
09. NETWORK A	ND POWER LAB.	TW	50	20	35	P C	19.	COMMUNICATION	N THEORY	,	PP	100	40	77	Р
10. ELECTRONIC	C INSTRUMENTS AND TOOLS	TW	50	20	35	P C	20.	COMMUNICATION	N THEORY	•	OR	50	20	38	Р
							21.	CIRCUIT SIMUL	LATION A	ND TOOLS	TW	50	20	32	Р

GRAND TOTAL = 980/1500, RESULT: FIRST CLASS
ORDN. 1 MARKS :

s8053005	ANAGH PAL				MANI	DIRA		, 71045368L	, s8053005 ,	PICT		, s80	5300!	5
01. SIGNAL	AND SYSTEMS	PP	100	40	88	P C	11.	ENGINEERING MA	THEMATICS III	PP	100	40	75	Р
02. SIGNAL	AND SYSTEMS	OR	50	20	43	P C	12.	ENGINEERING MA	THEMATICS III	TW	25	10	19	Р
03. SOLID	STATES DEVICES AND CIRCUIT	TSPP	100	40	67	P C	13.	INTEGRATED CIR	CUITS APPLICATIONS	PP	100	40	54	Р
04. SOLID	STATES DEVICES AND CIRCUIT	TSPR	50	20	41	P C	14.	INTEGRATED CIR	CUITS APPLICATIONS	PR	50	20	35	Р
05. NETWORK	K ANALYSIS	PP	100	40	70	P C	15.	ELECTROMAGNETI	С	PP	100	40	72	Р
06. DIGITA	L LOGIC DESIGN	PP	100	40	51	P C	16.	ELECTROMAGNETI	С	TW	25	10	23	Р
07. DIGITA	L LOGIC DESIGN	PR	50	20	44	P C	17.	DATA STRUCTURE	S	PP	100	40	49	Р
08. POWER I	DEVICES AND MACHINES	PP	100	40	53	P C	18.	DATA STRUCTURE	S	PR	50	20	48	Р
09. NETWORK	K AND POWER LAB.	TW	50	20	37	P C	19.	COMMUNICATION	THEORY	PP	100	40	53	Р
10. ELECTRO	ONIC INSTRUMENTS AND TOOLS	S TW	50	20	41	P C	20.	COMMUNICATION	THEORY	OR	50	20	25	Р
							21.	CIRCUIT SIMULA	TION AND TOOLS	TW	50	20	41	Р

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GRAND TOTAL = 1029/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS :

S8053006 ANANNYA MAZUMDER				NEE	ELA		, 71045369J , S8053006 ,	PIC	т	, S8	05300	6
01. SIGNAL AND SYSTEMS	PP	100	40	74	РC	11	ENGINEERING MATHEMATICS III	PP	100	40	90	Р
02. SIGNAL AND SYSTEMS	OR	50	20	33	P C	12	ENGINEERING MATHEMATICS III	TW	25	10	20	Р
03. SOLID STATES DEVICES AND CIRCUI	ITSPP	100	40	67	P C	13	INTEGRATED CIRCUITS APPLICATIONS	S PP	100	40	62	Р
04. SOLID STATES DEVICES AND CIRCUI	ITSPR	50	20	34	P C	14	INTEGRATED CIRCUITS APPLICATIONS	5 PR	50	20	40	Р
05. NETWORK ANALYSIS	PP	100	40	57	PС	15	ELECTROMAGNETIC	PP	100	40	79	Р
06. DIGITAL LOGIC DESIGN	PP	100	40	62	P C	16	ELECTROMAGNETIC	TW	25	10	22	Р
07. DIGITAL LOGIC DESIGN	PR	50	20	30	P C	17	DATA STRUCTURES	PP	100	40	67	Р
08. POWER DEVICES AND MACHINES	PP	100	40	65	P C	18	DATA STRUCTURES	PR	50	20	46	Р
09. NETWORK AND POWER LAB.	TW	50	20	40	P C	19	COMMUNICATION THEORY	PP	100	40	71	Р
10. ELECTRONIC INSTRUMENTS AND TOOL	_S TW	50	20	38	P C	20	COMMUNICATION THEORY	OR	50	20	35	Р
						21	CIRCUIT SIMULATION AND TOOLS	TW	50	20	38	Р
GRAND TOTAL = 1070/1500, RESULT: FIRST	ST CLAS	SS WITI	H DIS	TINCT	ION							
UNIVERSITY 0	 F PUNE	 S.E.	(2008	 PAT	 .)(ELEC	TRONICS	& TELECOM.)					• •
DATE : 18 AUG. 2011	CEN	TRE : I	PUNE :	INSTI	TUTE O	F COMPUTE	ER TECHNOLOGY, PUNE.	PA	GE NO.	03	(3	27)
NOTE: FIRST LINE : SEAT NO., NAME	OF THE	E CAND	IDATE	, MC	THER,	PERMANEN ⁻	REG. NO., PREVIOUS SEAT NO., C	COLLE	GE,	SEAT I	NO.	
OTHER LINES: HEAD OF PASSING,	, MAX	. MARKS	S, M	IN. F	PASS MAI	RKS, MAI	RKS OBTAINED, P/F:PASS/FAIL, C:F	PREVI(OUS CA	RRY O'	VER	
S8053007 ATTAR BENAZIR MEHBOOB				HAS	SINA		, 71129924c , s8053007 ,	PIC	т	, 58	05300	7
01. SIGNAL AND SYSTEMS	PP	100	40	70	P C	11	ENGINEERING MATHEMATICS III	PP	100	40	73	Р
02. SIGNAL AND SYSTEMS	OR	50	20	30	РC	12	ENGINEERING MATHEMATICS III	TW	25	10	22	Р

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03. SOLID STATES DEVICES AND CIRCUIT	SPP	100	40	52	P C	13	INTEGRATED CIRCUITS APPLICATIONS	S PP	100	40	68	Р
04. SOLID STATES DEVICES AND CIRCUIT	SPR	50	20	35	P C	14	INTEGRATED CIRCUITS APPLICATIONS	S PR	50	20	32	Р
05. NETWORK ANALYSIS	PP	100	40	61	P C	15	ELECTROMAGNETIC	PP	100	40	49	Р
06. DIGITAL LOGIC DESIGN	PP	100	40	42	P C	16	ELECTROMAGNETIC	TW	25	10	20	Р
07. DIGITAL LOGIC DESIGN	PR	50	20	38	P C	17	DATA STRUCTURES	PP	100	40	46	Р
08. POWER DEVICES AND MACHINES	PP	100	40	58	P C	18	DATA STRUCTURES	PR	50	20	32	Р
09. NETWORK AND POWER LAB.	TW	50	20	41	P C	19	COMMUNICATION THEORY	PP	100	40	72	Р
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	38	P C	20	COMMUNICATION THEORY	OR	50	20	42	Р
						21	CIRCUIT SIMULATION AND TOOLS	TW	50	20	42	Р

GRAND TOTAL = 963/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS:

S8053008 AWASARE VRUSHALI VALMIK			URMILA		, 71045379F , S8053008 ,	PICT		, s80°	53008	8
O1. SIGNAL AND SYSTEMS PP	100	40	58 P	11. E	NGINEERING MATHEMATICS III	PP 1	L00	40	47	Р
02. SIGNAL AND SYSTEMS OR	50	20	28 P	12. EI	NGINEERING MATHEMATICS III	TW	25	10	19	Р
03. SOLID STATES DEVICES AND CIRCUITSPP	100	40	51 P	13. II	NTEGRATED CIRCUITS APPLICATIONS	PP 1	L00	40	40	Р
04. SOLID STATES DEVICES AND CIRCUITSPR	50	20	40 P	14. II	NTEGRATED CIRCUITS APPLICATIONS	PR	50	20	32	Р
05. NETWORK ANALYSIS PP	100	40	56 P	15. EI	LECTROMAGNETIC	PP 1	L00	40	69	Р
06. DIGITAL LOGIC DESIGN PP	100	40	53 P	16. EI	LECTROMAGNETIC	TW	25	10	23	Р
07. DIGITAL LOGIC DESIGN PR	50	20	38 P	17. D	ATA STRUCTURES	PP 1	L00	40	40	Р
08. POWER DEVICES AND MACHINES PP	100	40	50 P	18. D	ATA STRUCTURES	PR	50	20	38	Р
09. NETWORK AND POWER LAB. TW	50	20	37 P	19. co	OMMUNICATION THEORY	PP 1	L00	40	27	F
10. ELECTRONIC INSTRUMENTS AND TOOLS TW	50	20	42 P	20. cc	OMMUNICATION THEORY	OR	50	20	29	Р
				21. C	IRCUIT SIMULATION AND TOOLS	TW	50	20	41	Р

GRAND TOTAL = 858/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS:

S8053009 BAGMAR RITESH SURESH				ANI	TA		, 71045381н	cols05 , s8053009 ,	PICT		, s80	5300	9
01. SIGNAL AND SYSTEMS	PP	100	40	81	P C	11.	ENGINEERING MAT	HEMATICS III	PP	100	40	65	Р
02. SIGNAL AND SYSTEMS	OR	50	20	28	P C	12.	ENGINEERING MAT	HEMATICS III	TW	25	10	23	Р
03. SOLID STATES DEVICES AND CIRCUIT	ГЅРР	100	40	52	P C	13.	INTEGRATED CIRC	UITS APPLICATIONS	PP	100	40	63	Р
04. SOLID STATES DEVICES AND CIRCUIT	ΓSPR	50	20	38	РС	14.	INTEGRATED CIRC	UITS APPLICATIONS	PR	50	20	32	Р
05. NETWORK ANALYSIS	PP	100	40	44	P C	15.	ELECTROMAGNETIC		PP	100	40	61	Р
06. DIGITAL LOGIC DESIGN	PP	100	40	45	P C	16.	ELECTROMAGNETIC		TW	25	10	22	Р
07. DIGITAL LOGIC DESIGN	PR	50	20	28	P C	17.	DATA STRUCTURES		PP	100	40	62	Р
08. POWER DEVICES AND MACHINES	PP	100	40	55	P C	18.	DATA STRUCTURES		PR	50	20	34	Р
09. NETWORK AND POWER LAB.	TW	50	20	38	P C	19.	COMMUNICATION T	HEORY	PP	100	40	68	Р
10. ELECTRONIC INSTRUMENTS AND TOOLS	5 TW	50	20	43	P C	20.	COMMUNICATION T	HEORY	OR	50	20	35	Р
						21.	CIRCUIT SIMULAT	ION AND TOOLS	TW	50	20	37	Р
DATE: 18 AUG. 2011	CENT	RE : P	PUNE I	NSTI	TUTE (F COMPUTE	R TECHNOLOGY, PU			 E NO.			 28)
	CENT DF THE MAX.	RE : F CANDI MARKS	PUNE I DATE, MI	NSTI MO	TUTE (THER, ASS MA	F COMPUTE · · · · · PERMANENT RKS, MARI	R TECHNOLOGY, PU REG. NO., PREV KS OBTAINED, P/		 OLLEG REVIO	 E, S	SEAT N	 IO. /ER	
DATE: 18 AUG. 2011	CENT DF THE MAX.	RE : F CANDI MARKS	PUNE I DATE, MI	NSTI MO	TUTE (THER, ASS MA	F COMPUTE · · · · · PERMANENT RKS, MARI	R TECHNOLOGY, PU REG. NO., PREV KS OBTAINED, P/	IOUS SEAT NO., COF:PASS/FAIL, C:P	 OLLEG REVIO	E, S US CAF	SEAT N	 IO . /ER	
DATE: 18 AUG. 2011	CENT DF THE MAX.	RE : F CANDI MARKS	PUNE I DATE, MI	MO' N. P	TUTE (THER, ASS MA	F COMPUTEI PERMANENT RKS, MARI	R TECHNOLOGY, PU REG. NO., PREV KS OBTAINED, P/		 OLLEG REVIO	E, S US CAF	SEAT N	 IO. /ER	0
DATE: 18 AUG. 2011	CENT THE MAX.	RE : F	PUNE I	MOTAL PADI	TUTE (THER, ASS MA	F COMPUTEI PERMANENT RKS, MARI	R TECHNOLOGY, PU REG. NO., PREV KS OBTAINED, P/ 71045382F	IOUS SEAT NO., COF:PASS/FAIL, C:P, \$8053010 ,	OLLEGREVIO	 E, S US CAF	SEAT N	 /ER 	 0
DATE: 18 AUG. 2011 NOTE: FIRST LINE: SEAT NO., NAME OF OTHER LINES: HEAD OF PASSING, S8053010 BAHETI BHAKTI VILAS 01. SIGNAL AND SYSTEMS	CENT OF THE MAX. PP OR	CANDI MARKS	PUNE I	MOON PADI	TUTE (THER, ASS MA	F COMPUTEI PERMANENT RKS, MARI	R TECHNOLOGY, PU REG. NO., PREV KS OBTAINED, P/ , 71045382F ENGINEERING MAT ENGINEERING MAT	IOUS SEAT NO., COF:PASS/FAIL, C:P, \$8053010 ,	OLLEG REVIO PICT PP TW		GEAT N RRY OV , S80	 NO. /ER 05301	 0 P
DATE: 18 AUG. 2011 NOTE: FIRST LINE: SEAT NO., NAME OF OTHER LINES: HEAD OF PASSING, S8053010 BAHETI BHAKTI VILAS 01. SIGNAL AND SYSTEMS 02. SIGNAL AND SYSTEMS	CENT CENT PP OR CSPP	CANDI MARKS	PUNE I EDATE, 5, MI 40	MO :N. P PADI 80 35 69	TUTE (THER, ASS MA MA P C P C	F COMPUTER PERMANENT RKS, MARR T11. 12. 13.	R TECHNOLOGY, PU REG. NO., PREV KS OBTAINED, P/ 71045382F ENGINEERING MAT ENGINEERING MAT INTEGRATED CIRC	IOUS SEAT NO., CORENT NO., COR	OLLEG REVIO PICT PP TW PP		GEAT N RRY OV , S80 40 10	 VER 05301 89 23	
DATE: 18 AUG. 2011 NOTE: FIRST LINE: SEAT NO., NAME OF CONTROL LINES: HEAD OF PASSING, S8053010 BAHETI BHAKTI VILAS 01. SIGNAL AND SYSTEMS 02. SIGNAL AND SYSTEMS 03. SOLID STATES DEVICES AND CIRCUIT	CENT CENT PP OR CSPP	TRE : F CANDI MARKS 100 50 100	PUNE I DATE, 40 20 40	NSTITE MOTE NO. PADI 80 35 69 44	TUTE (THER, ASS MA MA P C P C P C	F COMPUTER PERMANENT RKS, MARR 11. 12. 13. 14.	R TECHNOLOGY, PU REG. NO., PREV KS OBTAINED, P/ 71045382F ENGINEERING MAT ENGINEERING MAT INTEGRATED CIRC	IOUS SEAT NO., CONTRIBUTIONS F:PASS/FAIL, C:P , S8053010 , HEMATICS III HEMATICS III UITS APPLICATIONS UITS APPLICATIONS	OLLEG REVIO PICT PP TW PP		SEAT N RRY OV , S80 40 10 40	 VER 05301 89 23 57	
DATE: 18 AUG. 2011 NOTE: FIRST LINE: SEAT NO., NAME OF CONTROL LINES: HEAD OF PASSING, S8053010 BAHETI BHAKTI VILAS 01. SIGNAL AND SYSTEMS 02. SIGNAL AND SYSTEMS 03. SOLID STATES DEVICES AND CIRCUIT 04. SOLID STATES DEVICES AND CIRCUIT	CENT CENT PF OR CSPP CSPR	TRE : F CANDI MARKS 100 50 100 50	PUNE I DATE, 40 20 40 20	**NSTITE** **MOTE** **N. PADI *** **PADI *** **80 **35 **69 **44 **58	TUTE (THER, ASS MA MA P C P C P C	F COMPUTER PERMANENT RKS, MARI 11. 12. 13. 14. 15.	R TECHNOLOGY, PU REG. NO., PREV KS OBTAINED, P/ 71045382F ENGINEERING MAT ENGINEERING MAT INTEGRATED CIRC	IOUS SEAT NO., CORESPASS/FAIL, C:P, \$8053010 , HEMATICS III HEMATICS III UITS APPLICATIONS UITS APPLICATIONS	OLLEG REVIO PICT PP TW PP PR		SEAT N RRY OV , S80 40 10 40 20		0 P P P

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						cols05
08. POWER DEVICES AND MACHINES	PP	100	40	55	РС	18. DATA STRUCTURES PR 50 20 47 P
09. NETWORK AND POWER LAB.	TW	50	20	42	P C	19. COMMUNICATION THEORY PP 100 40 69 P
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	44	P C	20. COMMUNICATION THEORY OR 50 20 37 P
						21. CIRCUIT SIMULATION AND TOOLS TW 50 20 41 P

GRAND TOTAL = 1084/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS :

S8053011 BANSAL SHRUTI SANJAY				MEEN	NA	, 71129925	5M , S8053011 ,	PICT		, s80	53011	L
01. SIGNAL AND SYSTEMS	PP	100	40	63	P C	11. ENGINEERING	MATHEMATICS III	PP	100	40	59	Р
02. SIGNAL AND SYSTEMS	OR	50	20	32	P C	12. ENGINEERING	MATHEMATICS III	TW	25	10	21	Р
03. SOLID STATES DEVICES AND CIRCUITS	SPP	100	40	62	P C	13. INTEGRATED (CIRCUITS APPLICATIONS	PP	100	40	54	Р
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	33	P C	14. INTEGRATED (CIRCUITS APPLICATIONS	PR	50	20	35	Р
05. NETWORK ANALYSIS	PP	100	40	54	P C	15. ELECTROMAGNE	ETIC	PP	100	40	43	Р
06. DIGITAL LOGIC DESIGN	PP	100	40	61	P C	16. ELECTROMAGNE	ETIC	TW	25	10	22	Р
07. DIGITAL LOGIC DESIGN	PR	50	20	32	P C	17. DATA STRUCTU	URES	PP	100	40	58	Р
08. POWER DEVICES AND MACHINES	PP	100	40	48	P C	18. DATA STRUCTU	URES	PR	50	20	38	Р
09. NETWORK AND POWER LAB.	TW	50	20	37	P C	19. COMMUNICATIO	ON THEORY	PP	100	40	64	Р
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	41	P C	20. COMMUNICATIO	ON THEORY	OR	50	20	42	Р
						21. CIRCUIT SIMU	ULATION AND TOOLS	TW	50	20	40	Р

GRAND TOTAL = 939/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

S8053012 BHAGAT ABHILASHA RAMDAS			MADHURI	, 71045386J , S8053012 , P	PICT	, s80	053012
01. SIGNAL AND SYSTEMS PP	100	40	40 P C	11. ENGINEERING MATHEMATICS III P	PP 100	40	49 P
02. SIGNAL AND SYSTEMS OR	50	20	35 P C	12. ENGINEERING MATHEMATICS III T	W 25	10	22 P
03. SOLID STATES DEVICES AND CIRCUITSPP	100	40	43 P C	13. INTEGRATED CIRCUITS APPLICATIONS P	P 100	40	40 P
04. SOLID STATES DEVICES AND CIRCUITSPR	50	20	32 P C	14. INTEGRATED CIRCUITS APPLICATIONS P Page 7	PR 50	20	28 P

05. NETWORK ANALYSIS	PP	100	40	40	РС	15.	ELECTROMAGNETIC		PP	100	40	40	Р
06. DIGITAL LOGIC DESIGN	PP	100	40	50	РС	16.	ELECTROMAGNETIC		TW	25	10	19	Р
07. DIGITAL LOGIC DESIGN	PR	50	20	21	РС	17.	DATA STRUCTURES		PP	100	40	59	Р
08. POWER DEVICES AND MACHINES	PP	100	40	47	РС	18.	DATA STRUCTURES		PR	50	20	34	Р
09. NETWORK AND POWER LAB.	TW	50	20	33	РС	19.	COMMUNICATION THE	ORY	PP	100	40	54	Р
10. ELECTRONIC INSTRUMENTS AND TOOL	S TW	50	20	35	РС	20.	COMMUNICATION THE	ORY	OR	50	20	28	Р
						21.	CIRCUIT SIMULATIO	ON AND TOOLS	TW	50	20	31	Р
GRAND TOTAL = 780/1500, RESULT: SECO	ND CL	ASS											
ORDN. 1 MARKS :													
UNIVERSITY OF	 PUNE	 . S.E.	(2008	 PAT	 .)(EL	 ECTRONICS &							
							R TECHNOLOGY, PUNE	i.	PAG	GE NO.	05	(3	29)
NOTE: FIRST LINE : SEAT NO., NAME	OF TH	E CAND	IDATE,	, MC	THER,	PERMANENT	REG. NO., PREVIO	OUS SEAT NO.,	COLLEG	GE, S	SEAT N	١٥.	
OTHER LINES: HEAD OF PASSING,	MAX	. MARKS	S, MI	[N. P	PASS N	IARKS, MAR	KS OBTAINED, P/F:	PASS/FAIL, C:	PREVIO	DUS CAF	RRY O\	/ER	
S8053013 BHANDALKAR VISHAL MAHADE	n			LAX	/MT		, 70801362н	, s8053013 ,	PTC ⁻	Γ	, s80	15301	3
SOUSSOIS BHANDALKAK VISHAL MAHADE	O			LAV	W-1-T		, 7000130211	, 30033013 ,	110	•	, 500) J J O I .	,
01. SIGNAL AND SYSTEMS	PP	100	40	13	F								
02. SIGNAL AND SYSTEMS	OR	50	20	AA	F								
03. SOLID STATES DEVICES AND CIRCUI	TSPP	100	40	13	F								
04. SOLID STATES DEVICES AND CIRCUI	TSPR	50	20	AA	F								
05. NETWORK ANALYSIS	PP	100	40	AA	F								
06. DIGITAL LOGIC DESIGN	PP	100	40	16	F								
07. DIGITAL LOGIC DESIGN	PR	50	20	AA	F								
08. POWER DEVICES AND MACHINES	PP	100	40	31	F								
09. NETWORK AND POWER LAB.	TW	50	20	24	P C								
10. ELECTRONIC INSTRUMENTS AND TOOL	S TW	50	20	20	РС								

FIRST TERM TOTAL = 117/750.

ORDN. 1 MARKS :

S8053014 BHAT TUSHAR GOVIND				VEE	NA		, 71045388E	, s8053014 ,	PICT		, s80	5301	4
01. SIGNAL AND SYSTEMS	PP	100	40	84	P C	11.	ENGINEERING MATHEM	ATICS III	PP	100	40	77	Р
02. SIGNAL AND SYSTEMS	OR	50	20	38	P C	12.	ENGINEERING MATHEM	ATICS III	TW	25	10	23	Р
03. SOLID STATES DEVICES AND CIRCU	UITSPP	100	40	56	P C	13.	INTEGRATED CIRCUIT	S APPLICATIONS	PP	100	40	71	Р
04. SOLID STATES DEVICES AND CIRCU	UITSPR	50	20	40	P C	14.	INTEGRATED CIRCUIT	S APPLICATIONS	PR	50	20	28	Р
05. NETWORK ANALYSIS	PP	100	40	62	P C	15.	ELECTROMAGNETIC		PP	100	40	78	Р
06. DIGITAL LOGIC DESIGN	PP	100	40	61	P C	16.	ELECTROMAGNETIC		TW	25	10	20	Р
07. DIGITAL LOGIC DESIGN	PR	50	20	32	P C	17.	DATA STRUCTURES		PP	100	40	69	Р
08. POWER DEVICES AND MACHINES	PP	100	40	52	P C	18.	DATA STRUCTURES		PR	50	20	42	Р
09. NETWORK AND POWER LAB.	TW	50	20	42	РС	19.	COMMUNICATION THEO	RY	PP	100	40	81	Р
10. ELECTRONIC INSTRUMENTS AND TOO	OLS TW	50	20	44	P C	20.	COMMUNICATION THEO	RY	OR	50	20	35	Р
						21.	CIRCUIT SIMULATION	AND TOOLS	TW	50	20	39	Р

GRAND TOTAL = 1074/1500, RESULT: FIRST CLASS WITH DISTINCTION
ORDN. 1 MARKS :

S8053015 BHOITE AJINKYA CHANDRAKA	ANT			MADI	HAVI	, 71045392C , S8053015 , PICT , S805301	.5
01. SIGNAL AND SYSTEMS	PP	100	40	84	P C	11. ENGINEERING MATHEMATICS III PP 100 40 75	Р
02. SIGNAL AND SYSTEMS	OR	50	20	38	P C	12. ENGINEERING MATHEMATICS III TW 25 10 23	Р
03. SOLID STATES DEVICES AND CIRCU	ITSPP	100	40	71	P C	13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 50	Р
04. SOLID STATES DEVICES AND CIRCU	ITSPR	50	20	27	P C	14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 33	Р
05. NETWORK ANALYSIS	PP	100	40	54	P C	15. ELECTROMAGNETIC PP 100 40 78	Р
06. DIGITAL LOGIC DESIGN	PP	100	40	66	P C	16. ELECTROMAGNETIC TW 25 10 20	Р
07. DIGITAL LOGIC DESIGN	PR	50	20	32	P C	17. DATA STRUCTURES PP 100 40 70	Р
08. POWER DEVICES AND MACHINES	PP	100	40	54	P C	18. DATA STRUCTURES PR 50 20 38	Р
09. NETWORK AND POWER LAB.	TW	50	20	44	P C	19. COMMUNICATION THEORY PP 100 40 58 Page 9	Р

							cols05					
10. ELECTRONIC INSTRUMENTS AND TOO	LS TW	50	20	38	P C	20.	COMMUNICATION THEORY	OR	50	20	32	Р
						21.	CIRCUIT SIMULATION AND TOOLS	TW	50	20	41	Р
GRAND TOTAL = 1026/1500, RESULT: FIR:	ST CLA	SS WTTI	ı DTS	TTNCT	TON							
ORDN. 1 MARKS :	JI CLA	33 WIII	1 013	11101	1011							
ONDIT. I PARKS .												
UNIVERSITY O	F PUNE	,S.E.	(2008	PAT	.)(ELE	CTRONICS &	k TELECOM.)					
DATE : 18 AUG. 2011	CEN.	TRE : I	PUNE :	INSTI	TUTE	OF COMPUTE	R TECHNOLOGY, PUNE.	PA	GE NO.	06	(3	30)
NOTE: FIRST LINE : SEAT NO., NAME	OF TH	E CAND	IDATE	, MC	THER,	PERMANENT	REG. NO., PREVIOUS SEAT NO.,	COLLE	GE,	SEAT I	١٥.	
OTHER LINES: HEAD OF PASSING	, MAX	. MARKS	5, M	IN. F	PASS M	ARKS, MAR	KS OBTAINED, P/F:PASS/FAIL, C	PREVI	OUS CA	RRY O	√ER	
S8053016 BHOSALE PRASHANT DNYANA	DEO			RΔN	IJANA		, 71045393M , S8053016 ,	PIC	т	581	05301	6
SOUSSOIL BIOSKEE FIGURIANT BRITAIN				10 11	137 (147)		, 710 133334 , 30033010 ,	, 10	•	, 50	,,,,,,	.0
01. SIGNAL AND SYSTEMS	PP	100	40	71	P C	11.	ENGINEERING MATHEMATICS III	PP	100	40	85	Р
02. SIGNAL AND SYSTEMS	OR	50	20	35	P C	12.	ENGINEERING MATHEMATICS III	TW	25	10	22	Р
03. SOLID STATES DEVICES AND CIRCU	ITSPP	100	40	57	P C	13.	INTEGRATED CIRCUITS APPLICATION	S PP	100	40	47	Р
04. SOLID STATES DEVICES AND CIRCU	ITSPR	50	20	33	P C	14.	INTEGRATED CIRCUITS APPLICATION	S PR	50	20	26	Р
05. NETWORK ANALYSIS	PP	100	40	59	P C	15.	ELECTROMAGNETIC	PP	100	40	67	Р
06. DIGITAL LOGIC DESIGN	PP	100	40	52	P C	16.	ELECTROMAGNETIC	TW	25	10	19	Р
07. DIGITAL LOGIC DESIGN	PR	50	20	20	P C	17.	DATA STRUCTURES	PP	100	40	45	Р
08. POWER DEVICES AND MACHINES	PP	100	40	42	РС	18.	DATA STRUCTURES	PR	50	20	21	Р
09. NETWORK AND POWER LAB.	TW	50	20	34	РС	19.	COMMUNICATION THEORY	PP	100	40	72	Р
10. ELECTRONIC INSTRUMENTS AND TOO	LS TW	50	20	41	P C	20.	COMMUNICATION THEORY	OR	50	20	27	Р
						21.	CIRCUIT SIMULATION AND TOOLS	TW	50	20	36	Р
GRAND TOTAL = 911/1500, RESULT: FIR	ST CLA	SS										
ORDN. 1 MARKS :												

S8053017 BICHU TANMAY NITIN DHANASHREE , 71045396F , S8053017 , PICT , S8053017

Page 10

01.	SIGNAL AND SYSTEMS	PP	100	40	83	PC	11.	ENGINEERING MATHEMATICS III	PP	100	40	92	Р
02.	SIGNAL AND SYSTEMS	OR	50	20	48	P C	12.	ENGINEERING MATHEMATICS III	TW	25	10	24	Р
03.	SOLID STATES DEVICES AND CIRCUIT	SPP	100	40	73	P C	13.	INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	79	Р
04.	SOLID STATES DEVICES AND CIRCUIT	SPR	50	20	45	P C	14.	INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	44	Р
05.	NETWORK ANALYSIS	PP	100	40	72	P C	15.	ELECTROMAGNETIC	PP	100	40	68	Р
06.	DIGITAL LOGIC DESIGN	PP	100	40	72	P C	16.	ELECTROMAGNETIC	TW	25	10	22	Р
07.	DIGITAL LOGIC DESIGN	PR	50	20	44	P C	17.	DATA STRUCTURES	PP	100	40	69	Р
08.	POWER DEVICES AND MACHINES	PP	100	40	67	P C	18.	DATA STRUCTURES	PR	50	20	42	Р
09.	NETWORK AND POWER LAB.	TW	50	20	45	P C	19.	COMMUNICATION THEORY	PP	100	40	78	Р
10.	ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	42	P C	20.	COMMUNICATION THEORY	OR	50	20	42	Р
							21.	CIRCUIT SIMULATION AND TOOLS	TW	50	20	43	Р

GRAND TOTAL = 1194/1500, RESULT: FIRST CLASS WITH DISTINCTION
ORDN. 1 MARKS :

S8053018 CHAVAN TRUPTI GANESH			LATA	, 71045406G , S8053018 , PICT	, s8053018
01. SIGNAL AND SYSTEMS PP	100	40	85 P C	11. ENGINEERING MATHEMATICS III PP 1	00 40 79 P
02. SIGNAL AND SYSTEMS OR	50	20	42 P C	12. ENGINEERING MATHEMATICS III TW	25 10 23 P
03. SOLID STATES DEVICES AND CIRCUITSPP	100	40	71 P C	13. INTEGRATED CIRCUITS APPLICATIONS PP 1	00 40 63 P
04. SOLID STATES DEVICES AND CIRCUITSPR	50	20	35 P C	14. INTEGRATED CIRCUITS APPLICATIONS PR	50 20 44 P
05. NETWORK ANALYSIS PP	100	40	53 P C	15. ELECTROMAGNETIC PP 1	00 40 52 P
06. DIGITAL LOGIC DESIGN PP	100	40	71 P C	16. ELECTROMAGNETIC TW	25 10 24 P
07. DIGITAL LOGIC DESIGN PR	50	20	30 P C	17. DATA STRUCTURES PP 1	00 40 68 P
08. POWER DEVICES AND MACHINES PP	100	40	59 P C	18. DATA STRUCTURES PR	50 20 45 P
09. NETWORK AND POWER LAB. TW	50	20	43 P C	19. COMMUNICATION THEORY PP 1	00 40 68 P
10. ELECTRONIC INSTRUMENTS AND TOOLS TW	50	20	43 P C	20. COMMUNICATION THEORY OR	50 20 35 P
				21. CIRCUIT SIMULATION AND TOOLS TW	50 20 40 P

GRAND TOTAL = 1073/1500, RESULT: FIRST CLASS WITH DISTINCTION
ORDN. 1 MARKS :

DATE : 18 AUG. 2011	CENT	RE : P	UNE I	NSTI	TUTE	OF COMPUTE	R TECHNOLOGY, PUNE		PAG	E NO.	07	(3	31)
NOTE: FIRST LINE : SEAT NO., NAME O	F THE	CANDI	DATE,	MO ⁻	THER,	, PERMANENT	REG. NO., PREVIO	US SEAT NO., C	OLLEG	E, S	EAT N	0.	
OTHER LINES: HEAD OF PASSING,	MAX.	MARKS	, MII	N. P.	ASS M	MARKS, MAR	KS OBTAINED, P/F:	PASS/FAIL, C:P	REVIO	US CAR	RY OV	ER	
S8053019 DABHADE ROHIDAS RAGHUNATH				BEB	EE		, 71045409м	, s8053019 ,	PICT		, s80	5301	9
01. SIGNAL AND SYSTEMS	PP	100	40	63	P C	11.	ENGINEERING MATHE	MATICS III	PP	100	40	63	Р
02. SIGNAL AND SYSTEMS	OR	50	20	24	Р	12.	ENGINEERING MATHE	MATICS III	TW	25	10	24	Р
03. SOLID STATES DEVICES AND CIRCUIT	SPP	100	40	49	P C	13.	INTEGRATED CIRCUI	TS APPLICATIONS	PP	100	40	51	Р
04. SOLID STATES DEVICES AND CIRCUIT	SPR	50	20	25	P C	14.	INTEGRATED CIRCUI	TS APPLICATIONS	PR	50	20	28	Р
05. NETWORK ANALYSIS	PP	100	40	56	РС	15.	ELECTROMAGNETIC		PP	100	40	68	Р
06. DIGITAL LOGIC DESIGN	PP	100	40	59	РС	16.	ELECTROMAGNETIC		TW	25	10	17	Р
07. DIGITAL LOGIC DESIGN	PR	50	20	33	РС	17.	DATA STRUCTURES		PP	100	40	53	Р
08. POWER DEVICES AND MACHINES	PP	100	40	60	P C	18.	DATA STRUCTURES		PR	50	20	46	Р
09. NETWORK AND POWER LAB.	TW	50	20	37	P C	19.	COMMUNICATION THE	ORY	PP	100	40	44	Р
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	39	РС	20.	COMMUNICATION THE	ORY	OR	50	20	30	Р
						21.	CIRCUIT SIMULATIO	N AND TOOLS	TW	50	20	38	Р
GRAND TOTAL = 907/1500, RESULT: FIRST	CLAS	SS											
ORDN. 1 MARKS :													
S8053020 DESAI SHWETA ARUN				ANJ	ANI		, 71129926к	, \$8053020 ,	PICT		, s80	53020	0
01. SIGNAL AND SYSTEMS	PP	100	40	42	P C	11.	ENGINEERING MATHE	MATICS III	PP	100	40	45	Р
02. SIGNAL AND SYSTEMS	OR	50	20	30	P C	12.	ENGINEERING MATHE	MATICS III	TW	25	10	20	Р
03. SOLID STATES DEVICES AND CIRCUIT	SPP	100	40	47	P C	13.	INTEGRATED CIRCUI	TS APPLICATIONS	PP	100	40	59	Р
04. SOLID STATES DEVICES AND CIRCUIT	SPR	50	20	35	P C	14.	INTEGRATED CIRCUI	TS APPLICATIONS	PR	50	20	35	Р
05. NETWORK ANALYSIS	PP	100	40	40	P C	15.	ELECTROMAGNETIC Pa	nge 12	PP	100	40	53	Р

UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(ELECTRONICS & TELECOM.)

06. DIGITAL LOGIC DESIGN	PP	100	40	40	РС	16.	ELECTROMAGNETIC	TW	25	10	20	Р
07. DIGITAL LOGIC DESIGN	PR	50	20	25	P C	17.	DATA STRUCTURES	PP	100	40	61	Р
08. POWER DEVICES AND MACHINES	PP	100	40	50	P C	18.	DATA STRUCTURES	PR	50	20	34	Р
09. NETWORK AND POWER LAB.	TW	50	20	43	P C	19.	COMMUNICATION THEORY	PP	100	40	50	Р
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	39	P C	20.	COMMUNICATION THEORY	OR	50	20	38	Р
						21.	CIRCUIT SIMULATION AND TOOLS	TW	50	20	38	Р

GRAND TOTAL = 844/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS:

S8053021 DHANNE ANJALI BASAVRAJ			MADHUI	JRI	, 71129927н , S8053021 ,	PICT		, s80	53021	1
01. SIGNAL AND SYSTEMS PP	100	40	57 P	P C 1	1. ENGINEERING MATHEMATICS III	PP	100	40	55	Р
02. SIGNAL AND SYSTEMS OR	50	20	27 P	, c 1	2. ENGINEERING MATHEMATICS III	TW	25	10	22	Р
03. SOLID STATES DEVICES AND CIRCUITSPP	100	40	53 P	C 1	3. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	70	Р
04. SOLID STATES DEVICES AND CIRCUITSPR	50	20	33 P	, c 1	4. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	30	Р
05. NETWORK ANALYSIS PP	100	40	45 P	C 1	5. ELECTROMAGNETIC	PP	100	40	50	Р
06. DIGITAL LOGIC DESIGN PP	100	40	45 P	C 1	6. ELECTROMAGNETIC	TW	25	10	20	Р
07. DIGITAL LOGIC DESIGN PR	50	20	35 P	2 1	7. DATA STRUCTURES	PP	100	40	51	Р
08. POWER DEVICES AND MACHINES PP	100	40	59 P	P C 1	.8. DATA STRUCTURES	PR	50	20	37	Р
09. NETWORK AND POWER LAB. TW	50	20	38 P	P C 1	9. COMMUNICATION THEORY	PP	100	40	62	Р
10. ELECTRONIC INSTRUMENTS AND TOOLS TW	50	20	43 P	P C 2	0. COMMUNICATION THEORY	OR	50	20	37	Р
				2	1. CIRCUIT SIMULATION AND TOOLS	TW	50	20	36	Р

GRAND TOTAL = 905/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS:

UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(ELECTRONICS & TELECOM.)

DATE: 18 AUG. 2011 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 08 (332)

NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

Page 13

cols05

cols05

100

TW

40

50 20 29 P

50 20 34 P

60 P

19. COMMUNICATION THEORY

20. COMMUNICATION THEORY

21. CIRCUIT SIMULATION AND TOOLS

OTHER LINES: HEAD OF PASSING	MAX. MARKS, MIN. PASS	MARKS, MARKS OBTAINED, P/F:PASS	/FAIL, C:PREVIOUS CARRY OVER
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, 71045414н S8053022 DHIKALE JITENDRA BABAN USHA , S8053022 , PICT , S8053022 01. SIGNAL AND SYSTEMS PP 100 40 93 P C 11. ENGINEERING MATHEMATICS III PP 100 40 96 P 02. SIGNAL AND SYSTEMS 50 20 40 P C 12. ENGINEERING MATHEMATICS III 25 10 25 P OR TW 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 63 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 42 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 36 P 05. NETWORK ANALYSIS 100 40 75 P C 15. ELECTROMAGNETIC 100 40 75 P 100 40 59 P C 06. DIGITAL LOGIC DESIGN 16. ELECTROMAGNETIC TW 25 10 23 P 07. DIGITAL LOGIC DESIGN 20 38 P C 17. DATA STRUCTURES 100 40 60 P 08. POWER DEVICES AND MACHINES 100 40 65 P C 18. DATA STRUCTURES PR 50 20 48 P

20

50 20 40 P C

42 P C

GRAND TOTAL = 1108/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS:

09. NETWORK AND POWER LAB.

10. ELECTRONIC INSTRUMENTS AND TOOLS TW

s80	53023	DIGHE GANESH DA	ATTATRAY			SHO	ВНА		, 7104541	7в ,	s8053023 ,	PICT		, s80	5302	3
01.	SIGNAL	AND SYSTEMS	PP	100	40	67	P C	11.	. ENGINEERING	MATHEMAT	TICS III	PP	100	40	85	Р
02.	SIGNAL	AND SYSTEMS	OR	50	20	28	P C	12.	. ENGINEERING	MATHEMAT	TICS III	TW	25	10	17	Р
03.	SOLID S	STATES DEVICES A	AND CIRCUITSPP	100	40	57	P C	13.	. INTEGRATED (CIRCUITS	APPLICATIONS	PP	100	40	58	Р
04.	SOLID S	STATES DEVICES A	AND CIRCUITSPR	50	20	36	P C	14.	. INTEGRATED (CIRCUITS	APPLICATIONS	PR	50	20	29	Р
05.	NETWORI	K ANALYSIS	PP	100	40	53	P C	15.	. ELECTROMAGNI	ETIC		PP	100	40	65	Р
06.	DIGITA	L LOGIC DESIGN	PP	100	40	43	P C	16.	. ELECTROMAGNI	ETIC		TW	25	10	18	Р
07.	DIGITA	L LOGIC DESIGN	PR	50	20	39	P C	17.	. DATA STRUCTI	URES		PP	100	40	64	Р
08.	POWER I	DEVICES AND MACH	HINES PP	100	40	51	P C	18.	. DATA STRUCTI	URES		PR	50	20	48	Р
09.	NETWORI	K AND POWER LAB.	. TW	50	20	40	P C	19.	. COMMUNICATIO	ON THEORY	(PP	100	40	65	Р
10.	ELECTRO	ONIC INSTRUMENTS	S AND TOOLS TW	50	20	39	P C	20.	. COMMUNICATIO	ON THEORY Page		OR	50	20	39	Р

, 71045427K , S8053025 , PICT

, s8053025

21. CIRCUIT SIMULATION AND TOOLS TW 50 20 35 P

GRAND TOTAL = 976/1500, RESULT: FIRST CLASS
ORDN. 1 MARKS :

S8053024 FIRAKE PARESH RAVINDRA				LAX	(MI	, 71045425C , S8053024 , PICT , S8053024	1
01. SIGNAL AND SYSTEMS	PP	100	40	66	РС	11. ENGINEERING MATHEMATICS III PP 100 40 92	Р
02. SIGNAL AND SYSTEMS	OR	50	20	38	P C	12. ENGINEERING MATHEMATICS III TW 25 10 23	Р
03. SOLID STATES DEVICES AND CIRCUI	TSPP	100	40	71	P C	13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 59	Р
04. SOLID STATES DEVICES AND CIRCUI	TSPR	50	20	45	P C	14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 38	Р
05. NETWORK ANALYSIS	PP	100	40	70	P C	15. ELECTROMAGNETIC PP 100 40 58	Р
06. DIGITAL LOGIC DESIGN	PP	100	40	61	P C	16. ELECTROMAGNETIC TW 25 10 23	Р
07. DIGITAL LOGIC DESIGN	PR	50	20	46	P C	17. DATA STRUCTURES PP 100 40 61	Р
08. POWER DEVICES AND MACHINES	PP	100	40	62	P C	18. DATA STRUCTURES PR 50 20 48	Р
09. NETWORK AND POWER LAB.	TW	50	20	42	P C	19. COMMUNICATION THEORY PP 100 40 66	Р
10. ELECTRONIC INSTRUMENTS AND TOOL	S TW	50	20	42	P C	20. COMMUNICATION THEORY OR 50 20 43	Р
						21. CIRCUIT SIMULATION AND TOOLS TW 50 20 42	Р

GRAND TOTAL = 1096/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS :

S8053025 GADHE GANESH VITTHALRAO

0	UNIVERSITY OF	PUNE ,S.E.(2008 PAT.)(ELECTRON		
DATE : 18 AUG.	2011	CENTRE : PUNE INSTITUTE OF COM	MPUTER TECHNOLOGY, PUNE.	PAGE NO. 09 (333)
NOTE: FIRST LINE	: SEAT NO., NAME O	OF THE CANDIDATE, MOTHER, PERMA	ANENT REG. NO., PREVIOUS SEAT NO	., COLLEGE, SEAT NO.
OTHER LINES	: HEAD OF PASSING,	MAX. MARKS, MIN. PASS MARKS,	MARKS OBTAINED, P/F:PASS/FAIL,	C:PREVIOUS CARRY OVER

01. SIGNAL AND SYSTEMS PP 100 40 42 P C 11. ENGINEERING MATHEMATICS III PP 100 40 55 P Page 15

KEVALBAI

02.	SIGNAL AND SYSTEMS	OR	50	20	30	Р	12.	ENGINEERING MATHEMATICS III	TW	25	10	23	Р
03.	SOLID STATES DEVICES AND CIRCUITS	SPP	100	40	41	P C	13.	INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	47	Р
04.	SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	20	P C	14.	INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	29	Р
05.	NETWORK ANALYSIS	PP	100	40	40	P C	15.	ELECTROMAGNETIC	PP	100	40	45	Р
06.	DIGITAL LOGIC DESIGN	PP	100	40	45	P C	16.	ELECTROMAGNETIC	TW	25	10	18	Р
07.	DIGITAL LOGIC DESIGN	PR	50	20	35	Р	17.	DATA STRUCTURES	PP	100	40	44	Р
08.	POWER DEVICES AND MACHINES	PP	100	40	40	P C	18.	DATA STRUCTURES	PR	50	20	37	Р
09.	NETWORK AND POWER LAB.	TW	50	20	36	P C	19.	COMMUNICATION THEORY	PP	100	40	40	Р
10.	ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	40	P C	20.	COMMUNICATION THEORY	OR	50	20	32	Р
							21.	CIRCUIT SIMULATION AND TOOLS	TW	50	20	35	Р

GRAND TOTAL = 774/1500, RESULT: SECOND CLASS
ORDN. 1 MARKS :

s8053026	GAVNEKAR SHREYAS SANJIV				NIL	IMA		, 71045435L	, s8053026 ,	PICT		, s80	53020	6
01. SIGNAL	. AND SYSTEMS	PP	100	40	59	P C	11.	ENGINEERING MATH	HEMATICS III	PP	100	40	72	Р
02. SIGNAL	AND SYSTEMS	OR	50	20	29	P C	12.	ENGINEERING MATH	HEMATICS III	TW	25	10	23	Р
03. SOLID	STATES DEVICES AND CIRCUIT	SPP	100	40	48	P C	13.	INTEGRATED CIRCU	JITS APPLICATIONS	, PP	100	40	60	Р
04. SOLID	STATES DEVICES AND CIRCUIT	SPR	50	20	29	P C	14.	INTEGRATED CIRCU	JITS APPLICATIONS	PR	50	20	37	Р
05. NETWOR	RK ANALYSIS	PP	100	40	40	P C	15.	ELECTROMAGNETIC		PP	100	40	40	Р
06. DIGITA	AL LOGIC DESIGN	PP	100	40	43	P C	16.	ELECTROMAGNETIC		TW	25	10	20	Р
07. DIGITA	AL LOGIC DESIGN	PR	50	20	30	P C	17.	DATA STRUCTURES		PP	100	40	47	Р
08. POWER	DEVICES AND MACHINES	PP	100	40	46	P C	18.	DATA STRUCTURES		PR	50	20	38	Р
09. NETWOR	RK AND POWER LAB.	TW	50	20	38	P C	19.	COMMUNICATION TH	IEORY	PP	100	40	28	F
10. ELECTR	RONIC INSTRUMENTS AND TOOLS	TW	50	20	39	P C	20.	COMMUNICATION TH	IEORY	OR	50	20	26	Р
							21.	CIRCUIT SIMULATI	ON AND TOOLS	TW	50	20	41	Р

GRAND TOTAL = 833/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS:

	cols05
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							cols05
S80	S8053027 GHOGARE SAYALI RAJESH				SUJ	ATA	, 71129928F , S8053027 , PICT , S8053027
01.	SIGNAL AND SYSTEMS	PP	100	40	62	P C	11. ENGINEERING MATHEMATICS III PP 100 40 40 P
02.	SIGNAL AND SYSTEMS	OR	50	20	26	P C	12. ENGINEERING MATHEMATICS III TW 25 10 22 P
03.	SOLID STATES DEVICES AND CIRC	UITSPP	100	40	40	P C	13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 56 P
04.	SOLID STATES DEVICES AND CIRC	UITSPR	50	20	32	P C	14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 33 P
05.	NETWORK ANALYSIS	PP	100	40	46	P C	15. ELECTROMAGNETIC PP 100 40 41 P
06.	DIGITAL LOGIC DESIGN	PP	100	40	41	P C	16. ELECTROMAGNETIC TW 25 10 23 P
07.	DIGITAL LOGIC DESIGN	PR	50	20	38	P C	17. DATA STRUCTURES PP 100 40 55 P
08.	POWER DEVICES AND MACHINES	PP	100	40	44	P C	18. DATA STRUCTURES PR 50 20 35 P
09.	NETWORK AND POWER LAB.	TW	50	20	41	P C	19. COMMUNICATION THEORY PP 100 40 49 P
10.	ELECTRONIC INSTRUMENTS AND TO	OLS TW	50	20	38	P C	20. COMMUNICATION THEORY OR 50 20 29 P
							21. CIRCUIT SIMULATION AND TOOLS TW 50 20 38 P
	TOTAL = 829/1500, RESULT: HIGH	GHER SE	COND CI	LASS			
		 OF PUNE	 ,S.E.	 (2008	 PAT	 .)(ELE	

UNIVERSITY	 OF PUNE	 .,S.E.	 (2008	 PAT.)(ELE	ECTRONICS	& TELECOM.)						
DATE : 18 AUG. 2011	CEN	TRE : I	PUNE I	INSTI	TUTE (OF COMPUT	ER TECHNOLOGY, PUNE.		PAGE	NO.	10	(33	34)
NOTE: FIRST LINE : SEAT NO., NAME	OF TH	E CAND	IDATE,	, MO	THER,	PERMANEN [*]	REG. NO., PREVIOUS	SEAT NO., CO	DLLEGE	:, S	EAT N	Ο.	
OTHER LINES: HEAD OF PASSING	, MAX	. MARKS	S, MI	[N. P.	ASS M	MARKS, MAI	RKS OBTAINED, P/F:PAS	S/FAIL, C:PF	REVIOL	IS CAR	RY OV	ER	
S8053028 GHONE PRASAD SHARAD				HEM	LATA		, 71045437G , s	88053028 ,	PICT		, s80	53028	8
01. SIGNAL AND SYSTEMS	PP	100	40	62	P C	11	ENGINEERING MATHEMAT	ICS III	PP	100	40	59	Р
02. SIGNAL AND SYSTEMS	OR	50	20	29	РС	12	ENGINEERING MATHEMAT	ICS III	TW	25	10	22	Р
03. SOLID STATES DEVICES AND CIRCU	JITSPP	100	40	49	P C	13	INTEGRATED CIRCUITS	APPLICATIONS	PP	100	40	57	Р
04. SOLID STATES DEVICES AND CIRCU	JITSPR	50	20	26	P C	14	INTEGRATED CIRCUITS	APPLICATIONS	PR	50	20	30	Р
05. NETWORK ANALYSIS	PP	100	40	51	P C	15	ELECTROMAGNETIC		PP	100	40	59	Р
06. DIGITAL LOGIC DESIGN	PP	100	40	41	P C	16	ELECTROMAGNETIC		TW	25	10	17	Р

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07. DIGITAL LOGIC DESIGN	PR	50	20	27	РС	17. DATA STRUCTURES	PP	100	40	60	Р
08. POWER DEVICES AND MACHINES	PP	100	40	50	P C	18. DATA STRUCTURES	PR	50	20	48	Р
09. NETWORK AND POWER LAB.	TW	50	20	34	P C	19. COMMUNICATION THEORY	PP	100	40	56	Р
10. ELECTRONIC INSTRUMENTS AND TOO	LS TW	50	20	34	P C	20. COMMUNICATION THEORY	OR	50	20	22	Р
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	33	Р

GRAND TOTAL = 866/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS:

S8053029 GUJAR AKASH SANJAY			ROHINI	, 71045438E , S8053029 , PICT , S8053029
01. SIGNAL AND SYSTEMS PP	100	40	79 P C	11. ENGINEERING MATHEMATICS III PP 100 40 72 P
02. SIGNAL AND SYSTEMS OR	50	20	31 P C	12. ENGINEERING MATHEMATICS III TW 25 10 20 P
03. SOLID STATES DEVICES AND CIRCUITSPP	100	40	57 P C	13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 62 P
04. SOLID STATES DEVICES AND CIRCUITSPR	50	20	22 P C	14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 35 P
05. NETWORK ANALYSIS PP	100	40	52 P C	15. ELECTROMAGNETIC PP 100 40 41 P
06. DIGITAL LOGIC DESIGN PP	100	40	63 P C	16. ELECTROMAGNETIC TW 25 10 18 P
07. DIGITAL LOGIC DESIGN PR	50	20	28 P C	17. DATA STRUCTURES PP 100 40 54 P
08. POWER DEVICES AND MACHINES PP	100	40	46 P C	18. DATA STRUCTURES PR 50 20 46 P
09. NETWORK AND POWER LAB. TW	50	20	37 P C	19. COMMUNICATION THEORY PP 100 40 66 P
10. ELECTRONIC INSTRUMENTS AND TOOLS TW	50	20	39 P C	20. COMMUNICATION THEORY OR 50 20 29 P
				21. CIRCUIT SIMULATION AND TOOLS TW 50 20 38 P

GRAND TOTAL = 935/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

S8053030 GULAVANI ADITYA VISHRAM				MEGHA	, 71045439C , S8053030 , F	PICT	, s80)53030)
01. SIGNAL AND SYSTEMS	PP	100	40	81 P C	11. ENGINEERING MATHEMATICS III	PP 100	40	72	Р
02. SIGNAL AND SYSTEMS	OR	50	20	34 P C	12. ENGINEERING MATHEMATICS III	TW 25	10	15	Р

cols05

03. SOLID STATES DEVICES AND CIRCUITSPP	100	40	50	PC	13	. INTEGRATED CIRCUITS APPLICATION	S PP	100	40	54	Р
04. SOLID STATES DEVICES AND CIRCUITSPR	50	20	30	P C	14	. INTEGRATED CIRCUITS APPLICATION	S PR	50	20	42	Р
05. NETWORK ANALYSIS PP	100	40	55	P C	15	. ELECTROMAGNETIC	PP	100	40	71	Р
06. DIGITAL LOGIC DESIGN PP	100	40	70	PC	16	. ELECTROMAGNETIC	TW	25	10	19	Р
07. DIGITAL LOGIC DESIGN PR	50	20	34	P C	17	. DATA STRUCTURES	PP	100	40	58	Р
08. POWER DEVICES AND MACHINES PP	100	40	48	P C	18	. DATA STRUCTURES	PR	50	20	46	Р
09. NETWORK AND POWER LAB. TW	50	20	39	P C	19	. COMMUNICATION THEORY	PP	100	40	61	Р
10. ELECTRONIC INSTRUMENTS AND TOOLS TW	50	20	40	P C	20	. COMMUNICATION THEORY	OR	50	20	35	Р
					21	. CIRCUIT SIMULATION AND TOOLS	TW	50	20	37	Р
001/1500											

GRAND TOTAL = 991/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS:

П	ITVERSTTY OF PLINE S E (2008 PAT) (FLECTRONICS & TELECOM)	

UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(ELECTRONICS & TELECOM.)

DATE : 18 AUG. 2011 CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 11 (335)

NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S80)53031	GULSHEEN KAUR AHU	JJA			BHU	P INDER		, 71045440G , S8053031 ,	PICT		, s805	3031	1
01.	SIGNAL	AND SYSTEMS	PP	100	40	78	P C	11.	ENGINEERING MATHEMATICS III	PP	100	40	96	Р
02.	SIGNAL	AND SYSTEMS	OR	50	20	48	P C	12.	ENGINEERING MATHEMATICS III	TW	25	10	23	Р
03.	SOLID	STATES DEVICES AND	CIRCUITSPP	100	40	72	P C	13.	INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	59	Р
04.	SOLID	STATES DEVICES AND	CIRCUITSPR	50	20	45	P C	14.	INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	45	Р
05.	NETWOR	K ANALYSIS	PP	100	40	61	P C	15.	ELECTROMAGNETIC	PP	100	40	57	Р
06.	DIGITA	L LOGIC DESIGN	PP	100	40	61	P C	16.	ELECTROMAGNETIC	TW	25	10	24	Р
07.	DIGITA	L LOGIC DESIGN	PR	50	20	43	P C	17.	DATA STRUCTURES	PP	100	40	71	Р
08.	POWER	DEVICES AND MACHIN	IES PP	100	40	45	P C	18.	DATA STRUCTURES	PR	50	20	48	Р
09.	NETWOR	K AND POWER LAB.	TW	50	20	45	P C	19.	COMMUNICATION THEORY	PP	100	40	63	Р
10.	ELECTR	ONIC INSTRUMENTS A	AND TOOLS TW	50	20	44	P C	20.	COMMUNICATION THEORY	OR	50	20	42	Р
								21.	CIRCUIT SIMULATION AND TOOLS Page 19	TW	50	20	40	Р

GRAND TOTAL = 1110/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS :

s8053032	GUNDI NACHIKET DINESH				VINE	EETA		, 71045441E , S8053032 ,	PICT		, s805	53032	<u>}</u>
01. SIGNAL	AND SYSTEMS	PP	100	40	73	P C	11.	ENGINEERING MATHEMATICS III	PP	100	40	73	Р
02. SIGNAL	AND SYSTEMS	OR	50	20	33	P C	12.	ENGINEERING MATHEMATICS III	TW	25	10	25	Р
03. SOLID	STATES DEVICES AND CIRCUIT	SPP	100	40	53	P C	13.	INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	47	Р
04. SOLID	STATES DEVICES AND CIRCUIT	SPR	50	20	35	P C	14.	INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	25	Р
05. NETWOR	K ANALYSIS	PP	100	40	59	P C	15.	ELECTROMAGNETIC	PP	100	40	57	Р
06. DIGITA	L LOGIC DESIGN	PP	100	40	55	P C	16.	ELECTROMAGNETIC	TW	25	10	19	Р
07. DIGITA	L LOGIC DESIGN	PR	50	20	31	P C	17.	DATA STRUCTURES	PP	100	40	42	Р
08. POWER	DEVICES AND MACHINES	PP	100	40	49	P C	18.	DATA STRUCTURES	PR	50	20	37	Р
09. NETWOR	K AND POWER LAB.	TW	50	20	41	P C	19.	COMMUNICATION THEORY	PP	100	40	40	Р
10. ELECTRO	ONIC INSTRUMENTS AND TOOLS	TW	50	20	42	P C	20.	COMMUNICATION THEORY	OR	50	20	32	Р
							21.	CIRCUIT SIMULATION AND TOOLS	TW	50	20	38	Р

GRAND TOTAL = 906/1500, RESULT: FIRST CLASS
ORDN. 1 MARKS :

s8053033	HIMANSHU KAMAT				ROOF	PASHRI		, 71045446F , S8053033 ,	PICT		, s805	3033	3
01. SIGNAL	AND SYSTEMS	PP	100	40	67	P C	11.	ENGINEERING MATHEMATICS III	PP	100	40	63	Р
02. SIGNAL	AND SYSTEMS	OR	50	20	25	P C	12.	ENGINEERING MATHEMATICS III	TW	25	10	20	Р
03. SOLID S	STATES DEVICES AND CIR	CUITSPP	100	40	54	P C	13.	INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	44	Р
04. SOLID S	STATES DEVICES AND CIR	CUITSPR	50	20	40	P C	14.	INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	36	Р
05. NETWORK	K ANALYSIS	PP	100	40	40	P C	15.	ELECTROMAGNETIC	PP	100	40	56	Р
06. DIGITAL	L LOGIC DESIGN	PP	100	40	46	P C	16.	ELECTROMAGNETIC	TW	25	10	20	Р
07. DIGITAL	L LOGIC DESIGN	PR	50	20	33	P C	17.	DATA STRUCTURES	PP	100	40	65	Р

08. POWER DEVICES AND MACHINES	PP	100	40	45	РС	- -	18.	cols05	PR	50	20	43	Р
09. NETWORK AND POWER LAB.	TW	50	20	37	РС	<u>.</u>	19.	COMMUNICATION THEORY	PP	100	40	46	Р
10. ELECTRONIC INSTRUMENTS AND TOO	LS TW	50	20	41	РС	Ž	20.	COMMUNICATION THEORY	OR	50	20	32	Р
						2	21.	CIRCUIT SIMULATION AND TOOLS	TW	50	20	33	Р
206 (4500													
GRAND TOTAL = 886/1500, RESULT: HIG	HER SE	COND C	LASS										
ORDN. 1 MARKS :													
UNIVERSITY O	F PUNE	,S.E.	(2008	PAT	.)(EL	ECTRONIC	S &	TELECOM.)				•	
DATE : 18 AUG. 2011	CEN.	TRE :	PUNE I	NSTI	TUTE	OF COMPL	UTER	TECHNOLOGY, PUNE.	PAG	E NO.	12	(3	36)
NOTE: FIRST LINE : SEAT NO., NAME	OF TH	E CAND	IDATE,	MC	THER,	, PERMANI	ENT	REG. NO., PREVIOUS SEAT NO., C	OLLEG	iE, S	SEAT N	10.	
OTHER LINES: HEAD OF PASSING	, MAX	. MARK	S, MI	IN. F	PASS N	MARKS, M	MARK	S OBTAINED, P/F:PASS/FAIL, C:P	REVIO	US CAR	RRY ON	/ER	
S8053034 HISARIYA RISHI RAMMOHAN				SAN	NDHYA			, 71129929D , S8053034 ,	PICT		, s80	05303	4
01. SIGNAL AND SYSTEMS	PP	100	40	56	РC	-	11.	ENGINEERING MATHEMATICS III	PP	100	40	58	Р
02. SIGNAL AND SYSTEMS	OR	50	20	20	РС	-	12.	ENGINEERING MATHEMATICS III	TW	25	10	19	Р
03. SOLID STATES DEVICES AND CIRCU	ITSPP	100	40	42	РС	-	13.	INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	59	Р
04. SOLID STATES DEVICES AND CIRCU	ITSPR	50	20	30	РС	-	14.	INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	35	Р
05. NETWORK ANALYSIS	PP	100	40	40	P C	=	15.	ELECTROMAGNETIC	PP	100	40	46	Р
06. DIGITAL LOGIC DESIGN	PP	100	40	49	P C	=	16.	ELECTROMAGNETIC	TW	25	10	20	Р
07. DIGITAL LOGIC DESIGN	PR	50	20	35	Р	=	17.	DATA STRUCTURES	PP	100	40	55	Р
08. POWER DEVICES AND MACHINES	PP	100	40	55	P C	=	18.	DATA STRUCTURES	PR	50	20	48	Р
09. NETWORK AND POWER LAB.	TW	50	20	38	P C	=	19.	COMMUNICATION THEORY	PP	100	40	46	Р
10. ELECTRONIC INSTRUMENTS AND TOO	LS TW	50	20	42	P C	2	20.	COMMUNICATION THEORY	OR	50	20	38	Р
						2	21.	CIRCUIT SIMULATION AND TOOLS	TW	50	20	36	Р
GRAND TOTAL = 867/1500, RESULT: HIG	UED SE	COND C	1 466										
ORDN. 1 MARKS :	IILK 3L	COND	LAJJ										
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S8053035 INGLE CHETAN VASUDEO			SHALINI	, 71129930н , S8053035 , РІСТ	, s8053035
01. SIGNAL AND SYSTEMS PP	100	40	40 P C	11. ENGINEERING MATHEMATICS III PP 100	40 70 P
02. SIGNAL AND SYSTEMS OR	50	20	20 P	12. ENGINEERING MATHEMATICS III TW 25	10 22 P
03. SOLID STATES DEVICES AND CIRCUITSPP	100	40	56 P C	13. INTEGRATED CIRCUITS APPLICATIONS PP 100	40 66 P
04. SOLID STATES DEVICES AND CIRCUITSPR	50	20	22 P C	14. INTEGRATED CIRCUITS APPLICATIONS PR 50	20 30 P
05. NETWORK ANALYSIS PP	100	40	40 P C	15. ELECTROMAGNETIC PP 100	40 66 P
06. DIGITAL LOGIC DESIGN PP	100	40	44 P C	16. ELECTROMAGNETIC TW 25	10 19 P
07. DIGITAL LOGIC DESIGN PR	50	20	29 P C	17. DATA STRUCTURES PP 100	40 61 P
08. POWER DEVICES AND MACHINES PP	100	40	53 P C	18. DATA STRUCTURES PR 50	20 48 P
09. NETWORK AND POWER LAB. TW	50	20	42 P C	19. COMMUNICATION THEORY PP 100	40 76 P
10. ELECTRONIC INSTRUMENTS AND TOOLS TW	50	20	39 P C	20. COMMUNICATION THEORY OR 50	20 38 P
				21. CIRCUIT SIMULATION AND TOOLS TW 50	20 35 P

GRAND TOTAL = 916/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS:

S	8053036	ISHTE PRAKASH BHAGN	/AN			KAMA	٩L		, 70925442	D ,	s8053036 ,	PICT		, s80	5303(6
0	1. SIGNAL	AND SYSTEMS	PP	100	40	40	Р	11.	ENGINEERING	MATHEMAT	ICS III	PP	100	40	22	F
0	2. SIGNAL	AND SYSTEMS	OR	50	20	24	P C	12.	ENGINEERING	МАТНЕМАТ	ICS III	TW	25	10	18	Р
0	3. SOLID	STATES DEVICES AND C	CIRCUITSPP	100	40	40	P C	13.	INTEGRATED C	IRCUITS	APPLICATIONS	PP	100	40	32	F
0	4. SOLID	STATES DEVICES AND C	CIRCUITSPR	50	20	20	Р	14.	INTEGRATED C	IRCUITS	APPLICATIONS	PR	50	20	25	Р
0	5. NETWOR	K ANALYSIS	PP	100	40	27	F	15.	ELECTROMAGNE	TIC		PP	100	40	57	Р
0	6. DIGITA	L LOGIC DESIGN	PP	100	40	20	F	16.	ELECTROMAGNE	TIC		TW	25	10	10	Р
0	7. DIGITA	L LOGIC DESIGN	PR	50	20	11	F	17.	DATA STRUCTU	RES		PP	100	40	29	F
0	8. POWER	DEVICES AND MACHINES	S PP	100	40	AA	F	18.	DATA STRUCTU	RES		PR	50	20	43	Р
0	9. NETWOR	K AND POWER LAB.	TW	50	20	32	P C	19.	COMMUNICATIO	N THEORY	′	PP	100	40	40	Р
1	0. ELECTR	ONIC INSTRUMENTS AND	TOOLS TW	50	20	35	P C	20.	COMMUNICATIO	N THEORY	′	OR	50	20	22	Р
								21.	CIRCUIT SIMU	LATION A	ND TOOLS	TW	50	20	26	Р

GRAND TOTAL = 573/1500, RESULT: FAILS

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UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(ELECTRONICS & TELECOM.)												
DATE : 18 AUG. 2011	CENTR	RE : PU	JNE IN	ISTIT	UTE OF	COMPUTE	R TECHNOLOGY, PUNE.	PAGE	E NO.	13	(33	37)
NOTE: FIRST LINE : SEAT NO., NAME OF	F THE	CANDI	DATE,	МОТ	HER, PE	ERMANENT	REG. NO., PREVIOUS SEAT NO., CO	DLLEGE	E, S	EAT N	Ο.	
OTHER LINES: HEAD OF PASSING,	MAX.	MARKS,	MIN	1. P/	ASS MARK	KS, MARI	S OBTAINED, P/F:PASS/FAIL, C:PI	REVIO	JS CAR	RY OV	ER	
S8053037 JADHAV ARTI GULAB				VIMA	AL		, 71129931F , S8053037 ,	PICT		, s80	53037	7
01. SIGNAL AND SYSTEMS	PP	100	40	57	P C	11.	ENGINEERING MATHEMATICS III	PP	100	40	71	Р
02. SIGNAL AND SYSTEMS	OR	50	20	34	P C	12.	ENGINEERING MATHEMATICS III	TW	25	10	20	Р
03. SOLID STATES DEVICES AND CIRCUITS	SPP	100	40	65	P C	13.	INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	71	Р
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	28	P C	14.	INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	34	Р
05. NETWORK ANALYSIS	PP	100	40	58	P C	15.	ELECTROMAGNETIC	PP	100	40	75	Р
06. DIGITAL LOGIC DESIGN	PP	100	40	75	P C	16.	ELECTROMAGNETIC	TW	25	10	20	Р
07. DIGITAL LOGIC DESIGN	PR	50	20	33	P C	17.	DATA STRUCTURES	PP	100	40	70	Р
08. POWER DEVICES AND MACHINES	PP	100	40	60	P C	18.	DATA STRUCTURES	PR	50	20	47	Р
09. NETWORK AND POWER LAB.	TW	50	20	39	P C	19.	COMMUNICATION THEORY	PP	100	40	59	Р
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	42	P C	20.	COMMUNICATION THEORY	OR	50	20	21	Р
						21.	CIRCUIT SIMULATION AND TOOLS	TW	50	20	39	Р
GRAND TOTAL = 1018/1500, RESULT: FIRST	CLASS	WITH	DISTI	[NCT]	ON							
ORDN. 1 MARKS :												
S8053038 JAIN AMEY MANOJ				NIL	IMA		, 71045455E , S8053038 ,	PICT		, s80	53038	3
01. SIGNAL AND SYSTEMS	PP	100	40	40	P C	11.	ENGINEERING MATHEMATICS III	PP	100	40	70	Р
02. SIGNAL AND SYSTEMS	OR	50	20	28	P C	12.	ENGINEERING MATHEMATICS III	TW	25	10	19	Р
03. SOLID STATES DEVICES AND CIRCUITS	SPP	100	40	40	P C	13.	INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	51	P

						cols05
04. SOLID STATES DEVICES AND CIRCUIT	SPR	50	20	25	P C	14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 30 P
05. NETWORK ANALYSIS	PP	100	40	40	P C	15. ELECTROMAGNETIC PP 100 40 54 P
06. DIGITAL LOGIC DESIGN	PP	100	40	48	P C	16. ELECTROMAGNETIC TW 25 10 17 P
07. DIGITAL LOGIC DESIGN	PR	50	20	30	P C	17. DATA STRUCTURES PP 100 40 57 P
08. POWER DEVICES AND MACHINES	PP	100	40	44	P C	18. DATA STRUCTURES PR 50 20 46 P
09. NETWORK AND POWER LAB.	TW	50	20	35	P C	19. COMMUNICATION THEORY PP 100 40 40 P
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	35	P C	20. COMMUNICATION THEORY OR 50 20 30 P
						21. CIRCUIT SIMULATION AND TOOLS TW 50 20 34 P

GRAND TOTAL = 813/1500, RESULT: SECOND CLASS

ORDN. 1 MARKS:

S8053039 JAJOO POONAM ASHOK			JAYWANTI	, 71045459н , S8053039 , РІС	СТ	, s8053	3039
01. SIGNAL AND SYSTEMS PP	100	40	74 P C	11. ENGINEERING MATHEMATICS III PP	100	40	63 P
02. SIGNAL AND SYSTEMS OR	50	20	33 P C	12. ENGINEERING MATHEMATICS III TW	25	10 2	23 P
03. SOLID STATES DEVICES AND CIRCUITSPP	100	40	42 P C	13. INTEGRATED CIRCUITS APPLICATIONS PP	100	40 !	56 P
04. SOLID STATES DEVICES AND CIRCUITSPR	50	20	39 P C	14. INTEGRATED CIRCUITS APPLICATIONS PR	50	20 4	42 P
05. NETWORK ANALYSIS PP	100	40	54 P C	15. ELECTROMAGNETIC PP	100	40	49 P
06. DIGITAL LOGIC DESIGN PP	100	40	73 P C	16. ELECTROMAGNETIC TW	25	10 2	24 P
07. DIGITAL LOGIC DESIGN PR	50	20	35 P C	17. DATA STRUCTURES PP	100	40	65 P
08. POWER DEVICES AND MACHINES PP	100	40	50 P C	18. DATA STRUCTURES PR	50	20 4	45 P
09. NETWORK AND POWER LAB. TW	50	20	40 P C	19. COMMUNICATION THEORY PP	100	40	62 P
10. ELECTRONIC INSTRUMENTS AND TOOLS TW	50	20	44 P C	20. COMMUNICATION THEORY OR	50	20	34 P
				21. CIRCUIT SIMULATION AND TOOLS TW	50	20 4	40 P

GRAND TOTAL = 987+05/1500, RESULT: FIRST CLASS[0.163]

ORDN. 1 MARKS:

UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(ELECTRONICS & TELECOM.)

DATE: 18 AUG. 2011 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.

PAGE NO. 14 (338)

NOTE: FIRST LINE : SEAT NO., NAME	OF THE	CANDI	DATE,	MO	THER,	, PERMANENT	REG. NO., PREVI	OUS SEAT NO., C	OLLEG	Ε, :	SEAT N	10.	
OTHER LINES: HEAD OF PASSING,	MAX.	MARKS	, MI	N. P.	ASS M	MARKS, MAR	KS OBTAINED, P/F	:PASS/FAIL, C:P	REVIO	US CAI	RRY OV	/ER	
S8053040 JOSHI PURVA AVINASH				AAR	TI		, 71045463F	, s8053040 ,	PICT		, s80)5304	0
01. SIGNAL AND SYSTEMS	PP	100	40	81	P C	11.	ENGINEERING MATH	EMATICS III	PP	100	40	82	Р
02. SIGNAL AND SYSTEMS	OR	50	20	37	РС	12.	ENGINEERING MATH	EMATICS III	TW	25	10	24	Р
03. SOLID STATES DEVICES AND CIRCUI	TSPP	100	40	61	РС	13.	INTEGRATED CIRCU	ITS APPLICATIONS	PP	100	40	59	Р
04. SOLID STATES DEVICES AND CIRCUI	TSPR	50	20	42	P C	14.	INTEGRATED CIRCU	ITS APPLICATIONS	PR	50	20	40	Р
05. NETWORK ANALYSIS	PP	100	40	58	РС	15.	ELECTROMAGNETIC		PP	100	40	79	Р
06. DIGITAL LOGIC DESIGN	PP	100	40	78	P C	16.	ELECTROMAGNETIC		TW	25	10	23	Р
07. DIGITAL LOGIC DESIGN	PR	50	20	31	P C	17.	DATA STRUCTURES		PP	100	40	72	Р
08. POWER DEVICES AND MACHINES	PP	100	40	51	P C	18.	DATA STRUCTURES		PR	50	20	42	Р
09. NETWORK AND POWER LAB.	TW	50	20	42	P C	19.	COMMUNICATION TH	EORY	PP	100	40	75	Р
10. ELECTRONIC INSTRUMENTS AND TOOL	S TW	50	20	44	P C	20.	COMMUNICATION TH	EORY	OR	50	20	41	Р
						21.	CIRCUIT SIMULATI	ON AND TOOLS	TW	50	20	44	Р

GRAND TOTAL = 1106/1500, RESULT: FIRST CLASS WITH DISTINCTION
ORDN. 1 MARKS :

S8053041 KAINGADE SARTHAK RANJEET			RINISHA	, 71045464D , S8053041 , PICT ,	S8053041
01. SIGNAL AND SYSTEMS PP	100	40	83 P C	11. ENGINEERING MATHEMATICS III PP 100	40 83 P
02. SIGNAL AND SYSTEMS OR	50	20	32 P C	12. ENGINEERING MATHEMATICS III TW 25	10 24 P
03. SOLID STATES DEVICES AND CIRCUITSPP	100	40	80 P C	13. INTEGRATED CIRCUITS APPLICATIONS PP 100	40 75 P
04. SOLID STATES DEVICES AND CIRCUITSPR	50	20	41 P C	14. INTEGRATED CIRCUITS APPLICATIONS PR 50	20 40 P
05. NETWORK ANALYSIS PP	100	40	70 P C	15. ELECTROMAGNETIC PP 100	40 73 P
06. DIGITAL LOGIC DESIGN PP	100	40	90 P C	16. ELECTROMAGNETIC TW 25	10 23 P
07. DIGITAL LOGIC DESIGN PR	50	20	46 P C	17. DATA STRUCTURES PP 100	40 71 P
08. POWER DEVICES AND MACHINES PP	100	40	63 P C	18. DATA STRUCTURES PR 50	20 48 P

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09. NETWORK AND POWER LAB.	TW	50	20	43	P C	c 19.	cols05 COMMUNICATION THEORY	PP	100	40	72	Р
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	42	PC	c 20.	COMMUNICATION THEORY	OR	50	20	47	Р
						21.	CIRCUIT SIMULATION AND TOOLS	TW	50	20	40	Р
CDAND TOTAL 110C /1500 DEGULTS FIRST	C1 4 C	.c. 1	DICT	THET	TO 11							
GRAND TOTAL = 1186/1500, RESULT: FIRST	CLAS	S WITH	DISI	INCI	ION							
ORDN. 1 MARKS :												
				• •								
S8053042 KAKADE ABHIJIT SURESH				VAN	DANA	A	, 71045465B , S8053042 ,	PICT		, s80	53042	2
01. SIGNAL AND SYSTEMS	PP	100	40	66	P C	c 11.	ENGINEERING MATHEMATICS III	PP	100	40	71	Р
02. SIGNAL AND SYSTEMS	OR	50	20	36	PC	c 12.	ENGINEERING MATHEMATICS III	TW	25	10	19	Р
03. SOLID STATES DEVICES AND CIRCUIT	SPP	100	40	56	PC	c 13.	INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	41	Р
04. SOLID STATES DEVICES AND CIRCUIT	SPR	50	20	30	PC	c 14.	INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	37	Р
05. NETWORK ANALYSIS	PP	100	40	70	PC	c 15.	ELECTROMAGNETIC	PP	100	40	81	Р
06. DIGITAL LOGIC DESIGN	PP	100	40	73	PC	c 16.	ELECTROMAGNETIC	TW	25	10	21	Р
07. DIGITAL LOGIC DESIGN	PR	50	20	28	PC	c 17.	DATA STRUCTURES	PP	100	40	65	Р
08. POWER DEVICES AND MACHINES	PP	100	40	45	PC	c 18.	DATA STRUCTURES	PR	50	20	44	Р
09. NETWORK AND POWER LAB.	TW	50	20	39	PC	c 19.	COMMUNICATION THEORY	PP	100	40	30*	Р
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	41	PC	c 20.	COMMUNICATION THEORY	OR	50	20	32	Р
						21.	CIRCUIT SIMULATION AND TOOLS	TW	50	20	42	Р
GRAND TOTAL = 967/1500, RESULT: FIRST	CLAS	is *	[0.4]									
ORDN. 1 MARKS :												
UNIVERSITY OF												
DATE : 18 AUG. 2011	CENT	RE : P	UNE I	NSTI	TUTE	E OF COMPUTE	R TECHNOLOGY, PUNE.	PAGI	E NO.	15	(33	39)
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SHUBHANGI

S8053043 KAMPU DHANANJAY ANANTRAO

, 71045467」 , S8053043 , PICT Page 26

, \$8053043

01.	SIGNAL AND SYSTEMS	PP	100	40	71	PC	11.	ENGINEERING MATHEMATICS III	PP	100	40	68	Р
02.	SIGNAL AND SYSTEMS	OR	50	20	33	P C	12.	ENGINEERING MATHEMATICS III	TW	25	10	18	Р
03.	SOLID STATES DEVICES AND CIRCUIT	TSPP	100	40	69	P C	13.	INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	52	Р
04.	SOLID STATES DEVICES AND CIRCUIT	TSPR	50	20	32	P C	14.	INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	28	Р
05.	NETWORK ANALYSIS	PP	100	40	44	P C	15.	ELECTROMAGNETIC	PP	100	40	80	Р
06.	DIGITAL LOGIC DESIGN	PP	100	40	67	P C	16.	ELECTROMAGNETIC	TW	25	10	20	Р
07.	DIGITAL LOGIC DESIGN	PR	50	20	42	P C	17.	DATA STRUCTURES	PP	100	40	60	Р
08.	POWER DEVICES AND MACHINES	PP	100	40	48	P C	18.	DATA STRUCTURES	PR	50	20	46	Р
09.	NETWORK AND POWER LAB.	TW	50	20	41	P C	19.	COMMUNICATION THEORY	PP	100	40	51	Р
10.	ELECTRONIC INSTRUMENTS AND TOOLS	S TW	50	20	43	P C	20.	COMMUNICATION THEORY	OR	50	20	32	Р
							21.	CIRCUIT SIMULATION AND TOOLS	TW	50	20	36	Р

GRAND TOTAL = 981/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS:

S	3053044	KARE NACHIKET CHA	NDRASHEKHAR			NAND	INI		, 71045473C	, s8053044 ,	PICT		, s80	5304	4
0.	L. SIGNAL	AND SYSTEMS	PP	100	40	86	РС	11	ENGINEERING MAT	THEMATICS III	PP	100	40	83	Р
02	2. SIGNAL	AND SYSTEMS	OR	50	20	39	РС	12	ENGINEERING MAT	THEMATICS III	TW	25	10	25	Р
03	3. SOLID	STATES DEVICES AND	CIRCUITSPP	100	40	76	РС	13	INTEGRATED CIRC	CUITS APPLICATIONS	PP	100	40	59	Р
04	1. SOLID	STATES DEVICES AND	CIRCUITSPR	50	20	29	РС	14	INTEGRATED CIRC	CUITS APPLICATIONS	PR	50	20	33	Р
0!	. NETWOR	K ANALYSIS	PP	100	40	58	РС	15	ELECTROMAGNETIC		PP	100	40	61	Р
06	6. DIGITA	L LOGIC DESIGN	PP	100	40	67	РС	16	ELECTROMAGNETIC]	TW	25	10	22	Р
07	7. DIGITA	L LOGIC DESIGN	PR	50	20	39	РС	17	DATA STRUCTURES	5	PP	100	40	69	Р
08	3. POWER	DEVICES AND MACHIN	IES PP	100	40	58	РС	18	DATA STRUCTURES	5	PR	50	20	48	Р
09	. NETWOR	K AND POWER LAB.	TW	50	20	42	РС	19	COMMUNICATION 7	THEORY	PP	100	40	71	Р
10). ELECTR	ONIC INSTRUMENTS A	AND TOOLS TW	50	20	41	РС	20	COMMUNICATION 7	THEORY	OR	50	20	43	Р
								21	CIRCUIT SIMULAT	TION AND TOOLS	TW	50	20	42	Р

GRAND TOTAL = 1091/1500, RESULT: FIRST CLASS WITH DISTINCTION
ORDN. 1 MARKS :

S8053045 KULKARNI AKSHADA VIJAY				VID	YA		, 71045490C ,	s8053045 ,	PICT		, s80	05304	15
01. SIGNAL AND SYSTEMS	PP	100	40	88	P C	11.	ENGINEERING MATHEMAT	TICS III	PP	100	40	84	Р
02. SIGNAL AND SYSTEMS	OR	50	20	38	P C	12.	ENGINEERING MATHEMAT	TICS III	TW	25	10	23	Р
03. SOLID STATES DEVICES AND CIRCU	ITSPP	100	40	64	P C	13.	INTEGRATED CIRCUITS	APPLICATIONS	PP	100	40	53	Р
04. SOLID STATES DEVICES AND CIRCU	ITSPR	50	20	35	P C	14.	INTEGRATED CIRCUITS	APPLICATIONS	PR	50	20	42	Р
05. NETWORK ANALYSIS	PP	100	40	45	P C	15.	ELECTROMAGNETIC		PP	100	40	55	Р
06. DIGITAL LOGIC DESIGN	PP	100	40	75	P C	16.	ELECTROMAGNETIC		TW	25	10	24	Р
07. DIGITAL LOGIC DESIGN	PR	50	20	32	P C	17.	DATA STRUCTURES		PP	100	40	61	Р
08. POWER DEVICES AND MACHINES	PP	100	40	42	P C	18.	DATA STRUCTURES		PR	50	20	45	Ρ
09. NETWORK AND POWER LAB.	TW	50	20	38	P C	19.	COMMUNICATION THEORY	Y	PP	100	40	56	Р
10. ELECTRONIC INSTRUMENTS AND TOOL	LS TW	50	20	42	P C	20.	COMMUNICATION THEORY	Y	OR	50	20	35	F
						21.	CIRCUIT SIMULATION A	AND TOOLS	TW	50	20	36	F
·	ST CLAS	S WIII	4 DIS	IINCI	ION								
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RDN. 1 MARKS :	 DF PUNE	 ,S.E.	(2008	 PAT.)(ELECT		TELECOM.) R TECHNOLOGY, PUNE.		 PAG	 SE NO.			
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DATE: 18 AUG. 2011	 DF PUNE CENT	, S.E. ,S.E. FRE : F	 (2008 PUNE 1	 PAT. INSTI)(ELECT TUTE OF 	COMPUTE	TELECOM.) R TECHNOLOGY, PUNE.				 16 		340
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DATE: 18 AUG. 2011		, S.E. (S.E.) (TRE : I	(2008 PUNE] 	 PAT. INSTI)(ELECT TUTE OF THER, P	COMPUTE · · · · ·	TELECOM.) R TECHNOLOGY, PUNE. REG. NO., PREVIOUS	SEAT NO., CO	 OLLEG	 iE, :	 SEAT N		. 34(
RDN. 1 MARKS: UNIVERSITY O DATE: 18 AUG. 2011 NOTE: FIRST LINE: SEAT NO., NAME	CENTO	, S.E. (S.E.) (TRE : I	(2008 PUNE] 	 PAT. INSTI)(ELECT TUTE OF THER, P ASS MAR	COMPUTE · · · · ·	TELECOM.) R TECHNOLOGY, PUNE. REG. NO., PREVIOUS KS OBTAINED, P/F:PAS	SEAT NO., CO	 OLLEG	 US CA	 SEAT N	 NO. VER	
UNIVERSITY O DATE: 18 AUG. 2011 NOTE: FIRST LINE: SEAT NO., NAME OTHER LINES: HEAD OF PASSING S8053046 KULKARNI GAURAV PRADEEP	CENTO	, S.E. (S.E.) (TRE : I	(2008 PUNE] 	PAT. INSTI , MO IN. P , NIS)(ELECT TUTE OF THER, P ASS MAR	COMPUTE ERMANENT KS, MAR	TELECOM.) R TECHNOLOGY, PUNE. REG. NO., PREVIOUS KS OBTAINED, P/F:PAS	SEAT NO., CO SS/FAIL, C:PO 	 OLLEG REVIO	 US CA	 SEAT N RRY ON	 NO. VER	
DN. 1 MARKS: UNIVERSITY O DATE: 18 AUG. 2011 NOTE: FIRST LINE: SEAT NO., NAME OTHER LINES: HEAD OF PASSING S8053046 KULKARNI GAURAV PRADEEP 01. SIGNAL AND SYSTEMS	OF PUNE CENT OF THE , MAX	,S.E., TRE : IF CANDO	(2008 PUNE I IDATE	PAT. INSTI MO IN. P NIS	.)(ELECT TUTE OF THER, P ASS MAR	COMPUTE ERMANENT KS, MAR	TELECOM.) R TECHNOLOGY, PUNE. REG. NO., PREVIOUS KS OBTAINED, P/F:PAS , 71059376H	SEAT NO., CO SS/FAIL, C:PO S8053046 ,	OLLEGREVIO	 SUS CAI	 SEAT 1 RRY 0\ 	 NO. VER 	
DATE: 18 AUG. 2011 NOTE: FIRST LINE: SEAT NO., NAME OTHER LINES: HEAD OF PASSING	OF THE , MAX.	,S.E., TRE : F CANDO MARKS	. (2008 PUNE 1 IDATE 1	PAT. INSTI MO IN. P NIS 60 31		COMPUTE ERMANENT KS, MAR 11.	TELECOM.) R TECHNOLOGY, PUNE. REG. NO., PREVIOUS KS OBTAINED, P/F:PAS , 71059376H ENGINEERING MATHEMAT	SEAT NO., COSS/FAIL, C:POSS/FAIL, C:POSS/FAI	OLLEGREVIO		SEAT NRRY ON	 VER	

						cols05	
05. NETWORK ANALYSIS	PP	100	40	40	РС	15. ELECTROMAGNETIC PP 100 40 72 P	,
06. DIGITAL LOGIC DESIGN	PP	100	40	56	P C	16. ELECTROMAGNETIC TW 25 10 22 P	,
07. DIGITAL LOGIC DESIGN	PR	50	20	33	P C	17. DATA STRUCTURES PP 100 40 65 P	,
08. POWER DEVICES AND MACHINES	PP	100	40	61	P C	18. DATA STRUCTURES PR 50 20 47 P	,
09. NETWORK AND POWER LAB.	TW	50	20	41	P C	19. COMMUNICATION THEORY PP 100 40 62 P	,
10. ELECTRONIC INSTRUMENTS AND TOOLS	5 TW	50	20	39	P C	20. COMMUNICATION THEORY OR 50 20 35 P	,
						21. CIRCUIT SIMULATION AND TOOLS TW 50 20 42 P	,

GRAND TOTAL = 1007/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS:

S8053047 KULKARNI HARSHAD RAVINDRA				SADI	HANA	, 71045495D , S8053047 , PICT , S8053047	
01. SIGNAL AND SYSTEMS	PP	100	40	74	P C	11. ENGINEERING MATHEMATICS III PP 100 40 64 P	
02. SIGNAL AND SYSTEMS	OR	50	20	40	P C	12. ENGINEERING MATHEMATICS III TW 25 10 22 P	
03. SOLID STATES DEVICES AND CIRCUITS	SPP	100	40	58	P C	13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 54 P	
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	40	P C	14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 33 P	
05. NETWORK ANALYSIS	PP	100	40	55	P C	15. ELECTROMAGNETIC PP 100 40 54 P	
06. DIGITAL LOGIC DESIGN	PP	100	40	60	P C	16. ELECTROMAGNETIC TW 25 10 17 P	
07. DIGITAL LOGIC DESIGN	PR	50	20	34	P C	17. DATA STRUCTURES PP 100 40 74 P	
08. POWER DEVICES AND MACHINES	PP	100	40	44	P C	18. DATA STRUCTURES PR 50 20 46 P	
09. NETWORK AND POWER LAB.	TW	50	20	40	P C	19. COMMUNICATION THEORY PP 100 40 53 P	
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	41	P C	20. COMMUNICATION THEORY OR 50 20 38 P	
						21. CIRCUIT SIMULATION AND TOOLS TW 50 20 39 P	

GRAND TOTAL = 980/1500, RESULT: FIRST CLASS ORDN. 1 MARKS :

S8053048 KULKARNI SAURABH CHANDRAKANT SWARALI , 71045497L , S8053048 , PICT , S8053048

01. SIGNAL AND SYSTEMS PP 100 40 73 P C 11. ENGINEERING MATHEMATICS III PP 100 40 84 P Page 29

03. SOLID STATES DEVICES AND CIRCU												
	JITSPP	100	40	68	P C	13.	INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	55	Р
04. SOLID STATES DEVICES AND CIRCU	JITSPR	50	20	22	P C	14.	INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	35	Р
05. NETWORK ANALYSIS	PP	100	40	56	P C	15.	ELECTROMAGNETIC	PP	100	40	82	Р
06. DIGITAL LOGIC DESIGN	PP	100	40	63	P C	16.	ELECTROMAGNETIC	TW	25	10	19	Р
07. DIGITAL LOGIC DESIGN	PR	50	20	35	P C	17.	DATA STRUCTURES	PP	100	40	69	Р
08. POWER DEVICES AND MACHINES	PP	100	40	45	P C	18.	DATA STRUCTURES	PR	50	20	38	Р
09. NETWORK AND POWER LAB.	TW	50	20	43	P C	19.	COMMUNICATION THEORY	PP	100	40	69	Р
10. ELECTRONIC INSTRUMENTS AND TOO	DLS TW	50	20	43	P C	20.	COMMUNICATION THEORY	OR	50	20	31	Р
						21.	CIRCUIT SIMULATION AND TOOLS	TW	50	20	41	Р
GRAND TOTAL = 1031/1500, RESULT: FIR	عد دا ۵د	S WTTH	I DTST	TNCT.	TON							
ORDN. 1 MARKS:	CLAS	J WIIII	I DIST.	INC I	ION							
SIGNI I PARICE I												
UNIVERSITY (•					•					
DATE : 18 AUG. 2011	CENT	DE • D	DINE T	NCTT-	TUTE 05	COMPLITE	D TECHNOLOGY BUNE	DAC	E NO	17	(3	11)
DATE : 16 AUG. 2011	CLIVI	KL . F	ONE I	NSII	TOTE OF	COMPOIL	R TECHNOLOGY, PUNE.	PAG	ie NO.	17	()	41)
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NOTE: FIRST LINE : SEAT NO., NAME	OF THE		 DATE,	 МО ⁻	 THER, PE	RMANENT		 OLLEG	 E, S	 SEAT 1	 NO.	
NOTE: FIRST LINE : SEAT NO., NAME	OF THE	CANDI	 DATE,	 МО ⁻ N. Р/	 THER, PE ASS MARK	 RMANENT	REG. NO., PREVIOUS SEAT NO., CO	 OLLEG	E, S	 SEAT 1 RRY O	 NO. /ER	
NOTE: FIRST LINE : SEAT NO., NAME	OF THE	CANDI	 DATE,	 МО ⁻ N. Р/	 THER, PE ASS MARK 	 RMANENT	REG. NO., PREVIOUS SEAT NO., CO	· · · OLLEG REVIO	E, S	 SEAT 1 RRY O	 NO. /ER 	
NOTE: FIRST LINE : SEAT NO., NAME OTHER LINES: HEAD OF PASSING	OF THE	CANDI	 DATE,	 MO ⁻ N. P <i>i</i> VIN:	 THER, PE ASS MARK 	RMANENT	REG. NO., PREVIOUS SEAT NO., CO	· · · OLLEG REVIO	 E, S US CAF	 SEAT N RRY ON	 NO. /ER 	
NOTE: FIRST LINE : SEAT NO., NAME OTHER LINES: HEAD OF PASSING	OF THE	CANDI MARKS	 DATE, , MI	 MO ⁻ N. P <i>i</i> VIN:	 THER, PE ASS MARK 	RMANENT S, MAR	REG. NO., PREVIOUS SEAT NO., CO KS OBTAINED, P/F:PASS/FAIL, C:PO , 71045499G , S8053049 ,	OLLEGREVIO	E, S	SEAT N	 NO. /ER 	 9
NOTE: FIRST LINE : SEAT NO., NAME OTHER LINES: HEAD OF PASSING	OF THE G, MAX. PP OR	CANDI MARKS		MO ⁻ N. P/ VIN:	THER, PE ASS MARK ITA P C	RMANENT S, MAR	REG. NO., PREVIOUS SEAT NO., CONTAINED, P/F:PASS/FAIL, C:PM, 71045499G , S8053049 , ENGINEERING MATHEMATICS III	OLLEGREVIO	E, SOUS CAR	SEAT NRRY ON	 /ER 05304	 9 P
NOTE: FIRST LINE : SEAT NO., NAME OTHER LINES: HEAD OF PASSING S8053049 KULKARNI YASH VIJAY O1. SIGNAL AND SYSTEMS O2. SIGNAL AND SYSTEMS	PP OR OF THE	CANDI MARKS		MO ⁻ N. PA VIN ⁻ 57 43 40	THER, PE ASS MARK ITA P C P C	11. 12. 13.	REG. NO., PREVIOUS SEAT NO., CONTROL NO., PREVIOUS SEAT NO., CONTROL N	OLLEG REVIO PICT PP TW	100 25	SEAT NRRY ON	 /ER 05304 40 18	 9 P F
NOTE: FIRST LINE : SEAT NO., NAME OTHER LINES: HEAD OF PASSING S8053049 KULKARNI YASH VIJAY 01. SIGNAL AND SYSTEMS 02. SIGNAL AND SYSTEMS 03. SOLID STATES DEVICES AND CIRCLE	PP OR OF THE	CANDI MARKS		MO ⁻ N. PA VIN ⁻ 57 43 40	THER, PE ASS MARK ITA PC PC	11. 12. 13.	REG. NO., PREVIOUS SEAT NO., CONTROL NO., PREVIOUS SEAT NO., CONTROL N	OLLEG REVIO PICT PP TW	100 25	SEAT NRRY ON	 /ER 05304 40 18 27	 9 P P
NOTE: FIRST LINE : SEAT NO., NAME OTHER LINES: HEAD OF PASSING S8053049 KULKARNI YASH VIJAY 01. SIGNAL AND SYSTEMS 02. SIGNAL AND SYSTEMS 03. SOLID STATES DEVICES AND CIRCU	PP OR DITSPR	CANDI MARKS 100 50 100 50		MO ⁻ N. PA VIN: 57 43 40 29	THER, PE ASS MARK THA PC PC PC		REG. NO., PREVIOUS SEAT NO., COKS OBTAINED, P/F:PASS/FAIL, C:PKS OBTAINED, P/F:PASS/FAIL,	OLLEGREVIO	100 25 100 50	SEAT NRRY OV	 NO . /ER D5304 40 18 27 26	
NOTE: FIRST LINE: SEAT NO., NAME OTHER LINES: HEAD OF PASSING S8053049 KULKARNI YASH VIJAY O1. SIGNAL AND SYSTEMS O2. SIGNAL AND SYSTEMS O3. SOLID STATES DEVICES AND CIRCU O4. SOLID STATES DEVICES AND CIRCU O5. NETWORK ANALYSIS	PP OR DITSPR PP	CANDI MARKS 100 50 100 50 100		MO ⁻ N. PA - VIN: 57 43 40 29 40 45	THER, PE ASS MARK TTA PC PC PC PC		REG. NO., PREVIOUS SEAT NO., CONSIDER OF THE PROPERTY OF THE P	OLLEGREVIO	100 25 100 50	SEAT NRRY ON	 VER D5304 40 18 27 26 61	
NOTE: FIRST LINE : SEAT NO., NAME OTHER LINES: HEAD OF PASSING S8053049 KULKARNI YASH VIJAY O1. SIGNAL AND SYSTEMS O2. SIGNAL AND SYSTEMS O3. SOLID STATES DEVICES AND CIRCU O4. SOLID STATES DEVICES AND CIRCU O5. NETWORK ANALYSIS O6. DIGITAL LOGIC DESIGN	PP OR DITSPP PP	CANDI MARKS 100 50 100 50 100 100		MO ⁻ N. PA - VIN: 57 43 40 29 40 45	THER, PE ASS MARK THA PC PC PC PC PC	11. 12. 13. 14. 15. 16.	REG. NO., PREVIOUS SEAT NO., CONSIDER NO., PF:PASS/FAIL, C:PASS/FAIL,	OLLEGREVIO PICT PP TW PP PR PP TW	100 25 100 50 100 25	SEAT NRRY ON	 VER D5304 40 18 27 26 61 20	
NOTE: FIRST LINE: SEAT NO., NAME OTHER LINES: HEAD OF PASSING S8053049 KULKARNI YASH VIJAY O1. SIGNAL AND SYSTEMS O2. SIGNAL AND SYSTEMS O3. SOLID STATES DEVICES AND CIRCU O4. SOLID STATES DEVICES AND CIRCU O5. NETWORK ANALYSIS O6. DIGITAL LOGIC DESIGN O7. DIGITAL LOGIC DESIGN	PP OR DITSPP PP PP	CANDI MARKS 100 50 100 50 100 100 50	40 20 40 20 40 40 40 20	MO ⁻ N. PA - VIN: 57 43 40 29 40 45 32	THER, PE ASS MARK THA PC PC PC PC PC PC	11. 12. 13. 14. 15. 16. 17.	REG. NO., PREVIOUS SEAT NO., CONSIDER NO., PF:PASS/FAIL, C:PASS/FAIL,	OLLEGREVIO PICT PP TW PP PR PP TW PP	100 25 100 25 100 25 100	SEAT NRRY ON	NO . /ER	

02. SIGNAL AND SYSTEMS OR 50 20 37 P C 12. ENGINEERING MATHEMATICS III TW 25 10 23 P

10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50	20 29 P C	cols05 20. COMMUNICATION THEORY	OR 50	20 35 P
		21. CIRCUIT SIMULATION AND TOOLS	TW 50	20 36 Р
GRAND TOTAL = 783/1500, RESULT: FAILS A.T.K.T.				
ORDN. 1 MARKS :				
S8053050 KUMBHAKARN MANSI ARUN	SULBHA	, 71045500D , S8053050 ,	PICT	, s8053050
01 07000 000 0007000 07 100	40 00 P.G	11	100	40 03 5

S8053050	KUMBHAKARN MANSI ARUN				SULB	ВНА		, 71045500D , S8053050 ,	PICT		, S805	53050)
01. SIGNAL	AND SYSTEMS	PP	100	40	89	P C	11.	ENGINEERING MATHEMATICS III	PP	100	40	82	Р
02. SIGNAL	AND SYSTEMS	OR	50	20	40	P C	12.	ENGINEERING MATHEMATICS III	TW	25	10	23	Р
03. SOLID	STATES DEVICES AND CIRCUI	TSPP	100	40	66	P C	13.	INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	45	Р
04. SOLID	STATES DEVICES AND CIRCUI	TSPR	50	20	35	P C	14.	INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	45	Р
05. NETWOR	K ANALYSIS	PP	100	40	54	P C	15.	ELECTROMAGNETIC	PP	100	40	51	Р
06. DIGITA	L LOGIC DESIGN	PP	100	40	68	P C	16.	ELECTROMAGNETIC	TW	25	10	20	Р
07. DIGITA	L LOGIC DESIGN	PR	50	20	34	P C	17.	DATA STRUCTURES	PP	100	40	75	Р
08. POWER	DEVICES AND MACHINES	PP	100	40	52	P C	18.	DATA STRUCTURES	PR	50	20	42	Р
09. NETWOR	K AND POWER LAB.	TW	50	20	44	P C	19.	COMMUNICATION THEORY	PP	100	40	68	Р
10. ELECTRO	ONIC INSTRUMENTS AND TOOL	S TW	50	20	44	P C	20.	COMMUNICATION THEORY	OR	50	20	40	Р
							21.	CIRCUIT SIMULATION AND TOOLS	TW	50	20	39	Р

GRAND TOTAL = 1056/1500, RESULT: FIRST CLASS WITH DISTINCTION
ORDN. 1 MARKS :

s8053051	LIMBHORE NITIN SH	ILAVI			SANG	SITA			, 71129932D	, s8053051 ,	PICT		, s80	5305	1
01. SIGNAL	_ AND SYSTEMS	PP	100	40	58	РС	1	L1.	ENGINEERING MATHE	MATICS III	PP	100	40	84	Р
02. SIGNAL	_ AND SYSTEMS	OR	50	20	20	РС	1	L2.	ENGINEERING MATHE	MATICS III	TW	25	10	23	Р
03. SOLID	STATES DEVICES AND	CIRCUITSPP	100	40	40	РС	1	L3.	INTEGRATED CIRCUI	TS APPLICATIONS	PP	100	40	53	Р
04. SOLID	STATES DEVICES AND	CIRCUITSPR	50	20	25	P C	1	L4.	INTEGRATED CIRCUI	TS APPLICATIONS	PR	50	20	33	Р
05. NETWOR	RK ANALYSIS	PP	100	40	40	P C	1	L5.	ELECTROMAGNETIC		PP	100	40	51	Р
06. DIGITA	AL LOGIC DESIGN	PP	100	40	58	P C	1	L6.	ELECTROMAGNETIC	aga 21	TW	25	10	17	Р

Page 31

PP 100 40 71 P

O' DIGITAL LOGIC DESIGN	•			, ,		_, .	DATA STRUCTURES	• • •	-00	.0	, –	•
08. POWER DEVICES AND MACHINES	Р	P 10	00 40) 51	РC	18.	DATA STRUCTURES	PR	50	20	45	Р
09. NETWORK AND POWER LAB.	Т	W 5	50 20	40	PС	19.	COMMUNICATION THEORY	PP	100	40	66	Р
10. ELECTRONIC INSTRUMENTS AND	TOOLS T	W 5	50 20	35	PС	20.	COMMUNICATION THEORY	OR	50	20	29	Р
						21.	CIRCUIT SIMULATION AND TOOLS	TW	50	20	41	Р
GRAND TOTAL = 915/1500, RESULT: ORDN. 1 MARKS :	FIRST C	LASS										
UNIVERSIT	 TY OF PU	 JNE ,S	 .E.(20	 08 рат	 .)(EL	 ECTRONICS &	TELECOM.)					
DATE : 18 AUG. 2011	C	ENTRE	: PUNE	E INST	ITUTE	OF COMPUTE	R TECHNOLOGY, PUNE.	PAG	GE NO.	18	(34	42)
·				MIN.	-		REG. NO., PREVIOUS SEAT NO., CONTROL OF PREVIOUS SEAT NO., CONTROL		OUS CAI	RRY 0\ 		
01. SIGNAL AND SYSTEMS	Р	P 10	00 40	81	РC	11.	ENGINEERING MATHEMATICS III	PP	100	40	76	Р
02. SIGNAL AND SYSTEMS	0	R :	50 20	26	РC	12.	ENGINEERING MATHEMATICS III	TW	25	10	22	Р
03. SOLID STATES DEVICES AND CI	RCUITSP	P 10	00 40	65	РС	13.	INTEGRATED CIRCUITS APPLICATIONS	S PP	100	40	47	Р
04. SOLID STATES DEVICES AND CI	RCUITSP	R :	50 20) 32	РС	14.						
				, ,_			INTEGRATED CIRCUITS APPLICATIONS	S PR	50	20	34	Р
05. NETWORK ANALYSIS	Р	P 10			P C		INTEGRATED CIRCUITS APPLICATIONS ELECTROMAGNETIC	S PR PP	50 100	20 40	34 55	
05. NETWORK ANALYSIS06. DIGITAL LOGIC DESIGN	P P		00 40) 42		15.						Р
	Р	Р 10	00 40) 42) 64	P C	15. 16.	ELECTROMAGNETIC	PP	100	40	55	P P
06. DIGITAL LOGIC DESIGN	P P	Р 10	00 40 00 40 50 20) 42) 64) 33	P C	15. 16. 17.	ELECTROMAGNETIC ELECTROMAGNETIC	PP TW	100 25	40 10	55 20	P P P
06. DIGITAL LOGIC DESIGN07. DIGITAL LOGIC DESIGN	P P	P 10 R 5 P 10	00 40 00 40 50 20	42643347	P C P C	15. 16. 17. 18.	ELECTROMAGNETIC ELECTROMAGNETIC DATA STRUCTURES	PP TW PP	100 25 100	40 10 40	55 20 64	P P P
06. DIGITAL LOGIC DESIGN07. DIGITAL LOGIC DESIGN08. POWER DEVICES AND MACHINES	P P T	P 10 R 5 P 10 W 5	00 40 00 40 50 20 00 40	4264334737	P C P C P C	15. 16. 17. 18. 19.	ELECTROMAGNETIC ELECTROMAGNETIC DATA STRUCTURES DATA STRUCTURES	PP TW PP PR	100 25 100 50	40 10 40 20	55 20 64 38	P P P
06. DIGITAL LOGIC DESIGN07. DIGITAL LOGIC DESIGN08. POWER DEVICES AND MACHINES09. NETWORK AND POWER LAB.	P P T	P 10 R 5 P 10 W 5	00 40 00 40 50 20 00 40	4264334737	P C P C P C P C	15. 16. 17. 18. 19.	ELECTROMAGNETIC ELECTROMAGNETIC DATA STRUCTURES DATA STRUCTURES COMMUNICATION THEORY	PP TW PP PR PP	100 25 100 50 100	40 10 40 20 40	55 20 64 38 60	P P P P

07. DIGITAL LOGIC DESIGN PR 50 20 35 P C 17. DATA STRUCTURES

GRAND TOTAL = 955+05/1500, RESULT: FIRST CLASS[0.163]
ORDN. 1 MARKS :

s8	053053	LUCKY KUMAR				SARI	ITA DEVI		, 71051136в , ѕ8053053 ,	PICT		, s80	5305	3
01	. SIGNAL	. AND SYSTEMS	PP	100	40	91	P C	11.	ENGINEERING MATHEMATICS III	PP	100	40	88	Р
02	. SIGNAL	. AND SYSTEMS	OR	50	20	38	P C	12.	ENGINEERING MATHEMATICS III	TW	25	10	18	Р
03	. SOLID	STATES DEVICES AND C	IRCUITSPP	100	40	78	P C	13.	INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	55	Р
04	. SOLID	STATES DEVICES AND C	IRCUITSPR	50	20	38	P C	14.	INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	38	Р
05	. NETWOR	K ANALYSIS	PP	100	40	57	P C	15.	ELECTROMAGNETIC	PP	100	40	62	Р
06	. DIGITA	L LOGIC DESIGN	PP	100	40	79	P C	16.	ELECTROMAGNETIC	TW	25	10	20	Р
07	. DIGITA	L LOGIC DESIGN	PR	50	20	45	P C	17.	DATA STRUCTURES	PP	100	40	60	Р
08	. POWER	DEVICES AND MACHINES	PP	100	40	58	P C	18.	DATA STRUCTURES	PR	50	20	43	Р
09	. NETWOR	K AND POWER LAB.	TW	50	20	41	P C	19.	COMMUNICATION THEORY	PP	100	40	51	Р
10	. ELECTR	ONIC INSTRUMENTS AND	TOOLS TW	50	20	40	P C	20.	COMMUNICATION THEORY	OR	50	20	37	Р
								21.	CIRCUIT SIMULATION AND TOOLS	TW	50	20	35	Р

GRAND TOTAL = 1072/1500, RESULT: FIRST CLASS WITH DISTINCTION
ORDN. 1 MARKS :

S8053054 MAGDUM FAREEN JABBAR			YASMIN	, 71045506C , S8053054 , PICT , S8053054
O1. SIGNAL AND SYSTEMS PP	100	40	60 P C	11. ENGINEERING MATHEMATICS III PP 100 40 44 P
02. SIGNAL AND SYSTEMS OR	50	20	20 P C	12. ENGINEERING MATHEMATICS III TW 25 10 21 P
03. SOLID STATES DEVICES AND CIRCUITSPP	100	40	65 P	13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 P
04. SOLID STATES DEVICES AND CIRCUITSPR	50	20	26 P C	14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 39 P
05. NETWORK ANALYSIS PP	100	40	40 P	15. ELECTROMAGNETIC PP 100 40 55 P
06. DIGITAL LOGIC DESIGN PP	100	40	57 P C	16. ELECTROMAGNETIC TW 25 10 19 P
07. DIGITAL LOGIC DESIGN PR	50	20	21 P C	17. DATA STRUCTURES PP 100 40 45 P
08. POWER DEVICES AND MACHINES PP	100	40	40 P C	18. DATA STRUCTURES PR 50 20 44 P
09. NETWORK AND POWER LAB. TW	50	20	39 P C	19. COMMUNICATION THEORY PP 100 40 52 P
10. ELECTRONIC INSTRUMENTS AND TOOLS TW	50	20	41 P C	20. COMMUNICATION THEORY OR 50 20 32 P
				21. CIRCUIT SIMULATION AND TOOLS TW 50 20 42 P Page 33

21. CIRCUIT SIMULATION AND TOOLS TW 50 20 34 P

GRAND TOTAL = 842/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS:

ORDIN. I MARKS .														
DATE: 18 AUG. 2011						ECTRONICS &	 TELECOM.)				 GE NO.			43)
NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.														
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER														
S8053055 MAHAJAN TUSHAR GOPAL				744			710455				-			
S8053055 MAHAJAN TUSHAR GOPAL				JAN	NABAI		, 710455	, ,	S8053055	, PICT		, 500)5305	5
01. SIGNAL AND SYSTEMS	PP	100	40	67	P C	11.	ENGINEERIN	IG MATHEMAT	TICS III	PP	100	40	53	Р
02. SIGNAL AND SYSTEMS	OR	50	20	33	P C	12.	ENGINEERIN	IG MATHEMAT	TICS III	TW	25	10	22	Р
03. SOLID STATES DEVICES AND CIRC	UITSPP	100	40	59	P C	13.	INTEGRATED	CIRCUITS	APPLICATI	ONS PP	100	40	58	Р
04. SOLID STATES DEVICES AND CIRC	UITSPR	50	20	38	P C	14.	INTEGRATED	CIRCUITS	APPLICATI	ONS PR	50	20	30	Р
05. NETWORK ANALYSIS	PP	100	40	49	P C	15.	ELECTROMAG	NETIC		PP	100	40	69	Р
06. DIGITAL LOGIC DESIGN	PP	100	40	43	P C	16.	ELECTROMAG	NETIC		TW	25	10	13	Р
07. DIGITAL LOGIC DESIGN	PR	50	20	32	P C	17.	DATA STRUC	TURES		PP	100	40	50	Р
08. POWER DEVICES AND MACHINES	PP	100	40	50	P C	18.	DATA STRUC	TURES		PR	50	20	34	Р
09. NETWORK AND POWER LAB.	TW	50	20	38	P C	19.	COMMUNICAT	ION THEORY	(PP	100	40	54	Р
10. ELECTRONIC INSTRUMENTS AND TO	OLS TW	50	20	35	P C	20.	COMMUNICAT	ION THEORY	1	OR	50	20	32	Р

GRAND TOTAL = 893+07/1500, RESULT: FIRST CLASS [0.2]
ORDN. 1 MARKS :

S8053056 MAHENDRA CHOUDHARY	3053056 MAHENDRA CHOUDHARY			HEERA	, 71045511K , S8053056 , PICT ,	, s8053056		
01. SIGNAL AND SYSTEMS	PP	100	40	64 P C	11. ENGINEERING MATHEMATICS III PP 100	40 71 P		
02. SIGNAL AND SYSTEMS	OR	50	20	43 P C	12. ENGINEERING MATHEMATICS III TW 25 Page 34	10 17 P		

03.	SOLID STATES DEVICES AND CIRCUITS	SPP	100	40	50	РС	13	. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 54	Р
04.	SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	26	P C	14	. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 32	Р
05.	NETWORK ANALYSIS	PP	100	40	47	P C	15	ELECTROMAGNETIC PP 100 40 70	Р
06.	DIGITAL LOGIC DESIGN	PP	100	40	63	P C	16	ELECTROMAGNETIC TW 25 10 14	Р
07.	DIGITAL LOGIC DESIGN	PR	50	20	32	P C	17	DATA STRUCTURES PP 100 40 57	Р
08.	POWER DEVICES AND MACHINES	PP	100	40	43	P C	18	DATA STRUCTURES PR 50 20 32	Р
09.	NETWORK AND POWER LAB.	TW	50	20	37	P C	19	COMMUNICATION THEORY PP 100 40 45	Р
10.	ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	39	P C	20	. COMMUNICATION THEORY OR 50 20 29	Р
							21	. CIRCUIT SIMULATION AND TOOLS TW 50 20 38	Р

GRAND TOTAL = 903/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS:

S805305	7 MAHESHWARI SNEHAL RAVI	NDRA			AART	ΓI		, 71045512н , ѕ8053057 ,	PICT		, S805	3057	7
01. SIG	NAL AND SYSTEMS	PP	100	40	58	P C	11.	ENGINEERING MATHEMATICS III	PP	100	40	57	Р
02. SIG	NAL AND SYSTEMS	OR	50	20	20	P C	12.	ENGINEERING MATHEMATICS III	TW	25	10	21	Р
03. SOL	ID STATES DEVICES AND CIRC	UITSPP	100	40	48	P C	13.	INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	41	Р
04. SOL	ID STATES DEVICES AND CIRC	UITSPR	50	20	25	P	14.	INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	30	Р
05. NET	WORK ANALYSIS	PP	100	40	42	P C	15.	ELECTROMAGNETIC	PP	100	40	40	Р
06. DIG	ITAL LOGIC DESIGN	PP	100	40	48	P C	16.	ELECTROMAGNETIC	TW	25	10	20	Р
07. DIG	ITAL LOGIC DESIGN	PR	50	20	30	P C	17.	DATA STRUCTURES	PP	100	40	48	Р
08. POW	ER DEVICES AND MACHINES	PP	100	40	40	P C	18.	DATA STRUCTURES	PR	50	20	38	Р
09. NET	WORK AND POWER LAB.	TW	50	20	40	P C	19.	COMMUNICATION THEORY	PP	100	40	46	Р
10. ELE	CTRONIC INSTRUMENTS AND TO	OLS TW	50	20	43	P C	20.	COMMUNICATION THEORY	OR	50	20	30	Р
							21.	CIRCUIT SIMULATION AND TOOLS	TW	50	20	36	Р

GRAND TOTAL = 801/1500, RESULT: SECOND CLASS

ORDN. 1 MARKS :

UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(ELECTRONICS & TELECOM.)

DATE : 18 AUG.	. 2011	CENT	RE : Pl	JNE IN	ISTIT	TUTE OF COM	PUTEI	R TECHNOLOGY, PUNE.	PAGI	E NO.	20	(34	44)
NOTE: FIRST LINE	: SEAT NO., NAME (OF THE	CANDI	DATE,	МОТ	THER, PERMAI	NENT	REG. NO., PREVIOUS SEAT NO., CO	OLLEGI	E, S	EAT N	ο.	
OTHER LINES	S: HEAD OF PASSING,	MAX.	MARKS	MIN	N. PA	ASS MARKS,	MARI	KS OBTAINED, P/F:PASS/FAIL, C:PI	REVIO	US CAR	RY OV	ER	
S8053058 MALDOD	DE RAHUL SUDHAKAR				SANC	GEETA		, 71045514D , S8053058 ,	PICT		, s80	5305	8
01. SIGNAL AND SY	STEMS	PP	100	40	51	P C	11.	ENGINEERING MATHEMATICS III	PP	100	40	40	Р
02. SIGNAL AND SY	/STEMS	OR	50	20	26	P C	12.	ENGINEERING MATHEMATICS III	TW	25	10	21	Р
03. SOLID STATES	DEVICES AND CIRCUIT	ГЅРР	100	40	56	P C	13.	INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	61	Р
04. SOLID STATES	DEVICES AND CIRCUIT	ΓSPR	50	20	22	P C	14.	INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	30	Р
05. NETWORK ANALY	/SIS	PP	100	40	40	P C	15.	ELECTROMAGNETIC	PP	100	40	66	Р
06. DIGITAL LOGIC	DESIGN	PP	100	40	55	P C	16.	ELECTROMAGNETIC	TW	25	10	19	Р
07. DIGITAL LOGIC	DESIGN	PR	50	20	25	P C	17.	DATA STRUCTURES	PP	100	40	56	Р
08. POWER DEVICES	S AND MACHINES	PP	100	40	52	P C	18.	DATA STRUCTURES	PR	50	20	38	Р
09. NETWORK AND F	POWER LAB.	TW	50	20	41	P C	19.	COMMUNICATION THEORY	PP	100	40	65	Р
10. ELECTRONIC IN	STRUMENTS AND TOOLS	5 TW	50	20	41	P C	20.	COMMUNICATION THEORY	OR	50	20	27	Р
							21.	CIRCUIT SIMULATION AND TOOLS	TW	50	20	39	Р
GRAND TOTAL = 871/	/1500, RESULT: HIGHE	ER SEC	OND CLA	ASS									
ORDN. 1 MARKS :	ŕ												
S8053059 MANE D	DEEPALI LIMBAJI				REKH	łΑ		, 71045517J , S8053059 ,	PICT		, s80	5305	9
01. SIGNAL AND SY	STEMS	PP	100	40	69	P C	11.	ENGINEERING MATHEMATICS III	PP	100	40	72	Р
02. SIGNAL AND SY	/STEMS	OR	50	20	40	P C	12.	ENGINEERING MATHEMATICS III	TW	25	10	21	Р
03. SOLID STATES	DEVICES AND CIRCUIT	ГЅРР	100	40	56	P C	13.	INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	51	Р
04. SOLID STATES	DEVICES AND CIRCUIT	ΓSPR	50	20	32	P C	14.	INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	38	Р
05. NETWORK ANALY	/SIS	PP	100	40	52	P C	15.	ELECTROMAGNETIC	PP	100	40	63	Р
06. DIGITAL LOGIC	DESIGN	PP	100	40	60	P C	16.	ELECTROMAGNETIC	TW	25	10	21	Р
07. DIGITAL LOGIC	DESIGN	PR	50	20	29	P C	17.	DATA STRUCTURES Page 36	PP	100	40	70	Р

08. POWER DEVICES AND MACHINES	PP	100	40	54	P C	18.	DATA STRUCTURES	PR	50	20	43	Р
09. NETWORK AND POWER LAB.	TW	50	20	37	P C	19.	COMMUNICATION THEORY	PP	100	40	61	Р
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	43	P C	20.	COMMUNICATION THEORY	OR	50	20	35	Р
						21.	CIRCUIT SIMULATION AND TOOLS	TW	50	20	35	Р
GRAND TOTAL = 982/1500, RESULT: FIRST	. СГ У	. c										
ORDN. 1 MARKS :	CLAS	33										
ORDIV. I MARKS .												
S8053060 MANE GAURANGI LALASAHEB				KAL	PANA		, 71045518G , S8053060 ,	PICT		, s80)5306	0
01. SIGNAL AND SYSTEMS	PP	100	40	52	P C	11.	ENGINEERING MATHEMATICS III	PP	100	40	69	Р
02. SIGNAL AND SYSTEMS	OR	50	20	26	РС	12.	ENGINEERING MATHEMATICS III	TW	25	10	23	Р
03. SOLID STATES DEVICES AND CIRCUIT	SPP	100	40	42	P C	13.	INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	42	Р
04. SOLID STATES DEVICES AND CIRCUIT	SPR	50	20	35	P C	14.	INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	35	Р
05. NETWORK ANALYSIS	PP	100	40	48	P C	15.	ELECTROMAGNETIC	PP	100	40	45	Р
06. DIGITAL LOGIC DESIGN	PP	100	40	53	P C	16.	ELECTROMAGNETIC	TW	25	10	20	Р
07. DIGITAL LOGIC DESIGN	PR	50	20	29	P C	17.	DATA STRUCTURES	PP	100	40	64	Р
08. POWER DEVICES AND MACHINES	PP	100	40	40	P C	18.	DATA STRUCTURES	PR	50	20	43	Р
09. NETWORK AND POWER LAB.	TW	50	20	40	P C	19.	COMMUNICATION THEORY	PP	100	40	49	Р
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	40	P C	20.	COMMUNICATION THEORY	OR	50	20	33	Р
						21.	CIRCUIT SIMULATION AND TOOLS	TW	50	20	42	Р
GRAND TOTAL = 870/1500, RESULT: HIGHE	R SEC	COND CL	ASS									
ORDN. 1 MARKS :												
			'' <u>.</u>		. <i>.</i> .							
UNIVERSITY OF	PUNE	,S.E.	(2008	PAT.	.)(ELE	ECTRONICS &	TELECOM.)					
DATE : 18 AUG. 2011	CENT	ΓRE : P	PUNE I	NSTI	TUTE	OF COMPUTE	R TECHNOLOGY, PUNE.	PAGI	E NO.	21	(3	45)
NOTE: ETROT LINE : CELT NO							DEC. NO. DREWTONS CELTURE					
NOTE: FIRST LINE : SEAT NO., NAME O									-	SEAT N		
OTHER LINES: HEAD OF PASSING,	MAX.	. MARKS	, MI	N. P	ASS M	IAKKS, MAR	(S OBTAINED, P/F:PASS/FAIL, C:P	KEVIO	US CAR	KRY OV	EK	

S8053061 MANTRI SHRUTI BAJRANG			SUREKHA	, 71045519E , S8053061 , PICT , S8053061
01. SIGNAL AND SYSTEMS	P 100	40	79 P C	11. ENGINEERING MATHEMATICS III PP 100 40 80 P
02. SIGNAL AND SYSTEMS	R 50	20	45 P C	12. ENGINEERING MATHEMATICS III TW 25 10 21 P
03. SOLID STATES DEVICES AND CIRCUITS	P 100	40	47 P C	13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 48 P
04. SOLID STATES DEVICES AND CIRCUITS	r 50	20	25 P C	14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 28 P
05. NETWORK ANALYSIS	P 100	40	58 P C	15. ELECTROMAGNETIC PP 100 40 49 P
06. DIGITAL LOGIC DESIGN	P 100	40	63 P C	16. ELECTROMAGNETIC TW 25 10 18 P
07. DIGITAL LOGIC DESIGN	r 50	20	24 P C	17. DATA STRUCTURES PP 100 40 49 P
08. POWER DEVICES AND MACHINES	P 100	40	57 P C	18. DATA STRUCTURES PR 50 20 34 P
09. NETWORK AND POWER LAB.	w 50	20	41 P C	19. COMMUNICATION THEORY PP 100 40 42 P
10. ELECTRONIC INSTRUMENTS AND TOOLS T	w 50	20	43 P C	20. COMMUNICATION THEORY OR 50 20 27 P
				21. CIRCUIT SIMULATION AND TOOLS TW 50 20 36 P

GRAND TOTAL = 914/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS:

s8053	3062	MERCHANT ALIAKBAR YA	HAYA			MEEI	LA		, 71129933В	, s8053062 ,	PICT		, s80	53062	2
01. 9	SIGNAL	AND SYSTEMS	PP	100	40	62	РС	11.	ENGINEERING MAT	HEMATICS III	PP	100	40	66	Р
02. 9	SIGNAL	AND SYSTEMS	OR	50	20	33	РС	12.	ENGINEERING MAT	HEMATICS III	TW	25	10	21	Р
03. 9	SOLID S	STATES DEVICES AND CI	RCUITSPP	100	40	52	РС	13.	INTEGRATED CIRC	UITS APPLICATIONS	PP	100	40	70	Р
04. 9	SOLID S	STATES DEVICES AND CI	RCUITSPR	50	20	40	P C	14.	INTEGRATED CIRC	UITS APPLICATIONS	PR	50	20	35	Р
05. 1	NETWORK	ANALYSIS	PP	100	40	52	P C	15.	ELECTROMAGNETIC		PP	100	40	76	Р
06. I	DIGITAL	. LOGIC DESIGN	PP	100	40	50	P C	16.	ELECTROMAGNETIC		TW	25	10	17	Р
07. [DIGITAL	LOGIC DESIGN	PR	50	20	39	P C	17.	DATA STRUCTURES		PP	100	40	62	Р
08. 1	POWER D	DEVICES AND MACHINES	PP	100	40	51	P C	18.	DATA STRUCTURES		PR	50	20	43	Р
09. 1	NETWORK	AND POWER LAB.	TW	50	20	38	P C	19.	COMMUNICATION T	HEORY	PP	100	40	65	Р
10. I	ELECTRO	ONIC INSTRUMENTS AND	TOOLS TW	50	20	44	P C	20.	COMMUNICATION T	HEORY	OR	50	20	29	Р
								21.	CIRCUIT SIMULAT	ION AND TOOLS	TW	50	20	38	Р

21. CIRCUIT SIMULATION AND TOOLS

50

TW

13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 61 P

Page 39

20

38 P

GRAND TOTAL = 983/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS:

, 71045525K , s8053063 , , s8053063 S8053063 MISHRA ABHISHEK ASHUTOSH MALA PICT 01. SIGNAL AND SYSTEMS 100 40 P C 11. ENGINEERING MATHEMATICS III 02. SIGNAL AND SYSTEMS 50 20 36 P C 12. ENGINEERING MATHEMATICS III 25 10 17 P TW 100 54 P C 03. SOLID STATES DEVICES AND CIRCUITSPP 13. INTEGRATED CIRCUITS APPLICATIONS PP 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 40 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 33 P 05. NETWORK ANALYSIS 100 40 40 P 15. ELECTROMAGNETIC 100 40 52 P 25 06. DIGITAL LOGIC DESIGN 100 40 48 P C 10 20 P 16. ELECTROMAGNETIC TW 07. DIGITAL LOGIC DESIGN 50 20 29 P C 17. DATA STRUCTURES 100 40 43 P 08. POWER DEVICES AND MACHINES 100 40 51 P C 18. DATA STRUCTURES 50 20 46 P 09. NETWORK AND POWER LAB. 19. COMMUNICATION THEORY 100 40 51 P TW 50 20 40 P C 20 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 39 P C 20. COMMUNICATION THEORY 30 P

GRAND TOTAL = 835/1500, RESULT: HIGHER SECOND CLASS
ORDN. 1 MARKS :

03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 75 P C

UNIVERSITY OF		. (2008	 PAT.)(ELECTRON			
DATE : 18 AUG. 2011	CENTRE :	PUNE :	INSTITUTE OF CO	MPUTER TECHNOLOGY, PUNE.	PAGE N	10. 22 (346)
NOTE: FIRST LINE : SEAT NO., NAME	OF THE CAN	DIDATE	, MOTHER, PERM	ANENT REG. NO., PREVIOUS SEAT NO.	, COLLEGE,	SEAT NO.
OTHER LINES: HEAD OF PASSING,	MAX. MARI	KS, M	IN. PASS MARKS,	MARKS OBTAINED, P/F:PASS/FAIL,	C:PREVIOUS	CARRY OVER
S8053064 MOHAMMED HABIBULLAH BAIG	NASRULLAH	BAIG	SADIYA BEGUM	, 71045673F , S8053064	, PICT	, s8053064
01. SIGNAL AND SYSTEMS	PP 100	40	94 P C	11. ENGINEERING MATHEMATICS III	PP 10	0 40 86 P
02. SIGNAL AND SYSTEMS	OR 50	20	42 P C	12. ENGINEERING MATHEMATICS III	TW 2	5 10 18 P

04. SOLID STATES DEVICES AND CIRCU	ITSPR	50	20	35	P C	14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 38
05. NETWORK ANALYSIS	PP	100	40	47	P C	15. ELECTROMAGNETIC PP 100 40 87
06. DIGITAL LOGIC DESIGN	PP	100	40	65	P C	16. ELECTROMAGNETIC TW 25 10 22
07. DIGITAL LOGIC DESIGN	PR	50	20	38	P C	17. DATA STRUCTURES PP 100 40 63
08. POWER DEVICES AND MACHINES	PP	100	40	52	P C	18. DATA STRUCTURES PR 50 20 48
09. NETWORK AND POWER LAB.	TW	50	20	39	P C	19. COMMUNICATION THEORY PP 100 40 64
10. ELECTRONIC INSTRUMENTS AND TOO	LS TW	50	20	35	P C	20. COMMUNICATION THEORY OR 50 20 35
						21. CIRCUIT SIMULATION AND TOOLS TW 50 20 39

GRAND TOTAL = 1083/1500, RESULT: FIRST CLASS WITH DISTINCTION
ORDN. 1 MARKS :

S8053065 MORE VIKAS CHANDRABHAN			SUMAN	, 71045527F , S8053065 , PICT , S	8053065
O1. SIGNAL AND SYSTEMS PP	100	40	44 P C	11. ENGINEERING MATHEMATICS III PP 100 40) 40 P
02. SIGNAL AND SYSTEMS OR	50	20	22 P	12. ENGINEERING MATHEMATICS III TW 25 10) 15 P
03. SOLID STATES DEVICES AND CIRCUITSPP	100	40	40 P C	13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40) 30 F
04. SOLID STATES DEVICES AND CIRCUITSPR	50	20	15 F	14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20) AA F
05. NETWORK ANALYSIS PP	100	40	27 F	15. ELECTROMAGNETIC PP 100 40) 22 F
06. DIGITAL LOGIC DESIGN PP	100	40	40 P C	16. ELECTROMAGNETIC TW 25 10) 17 P
07. DIGITAL LOGIC DESIGN PR	50	20	25 P C	17. DATA STRUCTURES PP 100 40) 27 F
08. POWER DEVICES AND MACHINES PP	100	40	42 P C	18. DATA STRUCTURES PR 50 20) 32 P
09. NETWORK AND POWER LAB. TW	50	20	39 P C	19. COMMUNICATION THEORY PP 100 40) 40 P
10. ELECTRONIC INSTRUMENTS AND TOOLS TW	50	20	38 P C	20. COMMUNICATION THEORY OR 50 20) 29 P
				21. CIRCUIT SIMULATION AND TOOLS TW 50 20) 33 P

GRAND TOTAL = 617/1500, RESULT: FAILS

ORDN. 1 MARKS :

\$8053066 MRIDUL NAGAR MANJU , 71045528D , \$8053066 , PICT , \$8053066 Page 40

01. SIGNAL AND SYSTEMS	PP	100	40	52	P C	11.	ENGINEERING MATHEMATICS III	PP	100	40	60	Р
02. SIGNAL AND SYSTEMS	OR	50	20	35	P C	12	ENGINEERING MATHEMATICS III	TW	25	10	18	Р
03. SOLID STATES DEVICES AND CIRC	CUITSPP	100	40	46	P C	13	INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	26	F
04. SOLID STATES DEVICES AND CIRC	CUITSPR	50	20	30	P C	14	INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	30	Р
05. NETWORK ANALYSIS	PP	100	40	40	P C	15	ELECTROMAGNETIC	PP	100	40	53	Р
06. DIGITAL LOGIC DESIGN	PP	100	40	40	P C	16	ELECTROMAGNETIC	TW	25	10	13	Р
07. DIGITAL LOGIC DESIGN	PR	50	20	30	P C	17	DATA STRUCTURES	PP	100	40	48	Р
08. POWER DEVICES AND MACHINES	PP	100	40	44	P C	18	DATA STRUCTURES	PR	50	20	28	Р
09. NETWORK AND POWER LAB.	TW	50	20	39	P C	19	COMMUNICATION THEORY	PP	100	40	48	Р
10. ELECTRONIC INSTRUMENTS AND TO	OOLS TW	50	20	35	P C	20	COMMUNICATION THEORY	OR	50	20	21	Р
						21.	CIRCUIT SIMULATION AND TOOLS	TW	50	20	26	Р
(

GRAND TOTAL = 762/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

07. DIGITAL LOGIC DESIGN

08. POWER DEVICES AND MACHINES

		UNIVERSITY O	 F PUNE	 ,S.E.	(2008	PAT.		 :cs &	TELECOM.)					
	DATE :	18 AUG. 2011	CENT	RE : P	UNE IN	NSTI ⁻	TUTE OF COM	PUTEI	R TECHNOLOGY, PUNE.	PAGE	E NO.	23	(34	17)
					,		•		REG. NO., PREVIOUS SEAT NO., CO		•	 EAT NO RY OVE		
•	s8053067	MUJAWAR SAMIR HANIF				RUB			, 71045529B , S8053067 ,	PICT		 , s80!	53067	7
	01. SIGNAL	AND SYSTEMS	PP	100	40	66	P C	11.	ENGINEERING MATHEMATICS III	PP	100	40	49	Р
	02. SIGNAL	AND SYSTEMS	OR	50	20	36	P C	12.	ENGINEERING MATHEMATICS III	TW	25	10	18	Р
	03. SOLID	STATES DEVICES AND CIRCU	ITSPP	100	40	46	P C	13.	INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	40	Р
	04. SOLID	STATES DEVICES AND CIRCU	ITSPR	50	20	26	P C	14.	INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	25	Р
	05. NETWOR	K ANALYSIS	PP	100	40	40	P C	15.	ELECTROMAGNETIC	PP	100	40	59	Р
	06. DIGITA	L LOGIC DESIGN	PP	100	40	40	P C	16.	ELECTROMAGNETIC	TW	25	10	18	Р

50 20 28 P C

PP 100 40 40 P C

Page 41

PP 100 40 50 P PR 50 20 34 P

17. DATA STRUCTURES

18. DATA STRUCTURES

99. NETWORK AND POWER LAB. TW 50 20 39 P.C. 19. COMMUNICATION THEORY PP 100 40 60 P 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 22 P 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 34 P GRAND TOTAL = 791/1500, RESULT: SECOND CLASS ORDN. 1 MARKS: *** *** *** *** *** *** ***									601303					
CRAND TOTAL = 791/1500, RESULT: SECOND CLASS ORDN. 1 MARKS: ARIFA BRIFA ARIFA ARIFA ARIFA ARIFA BRIFA ARIFA BRIFA ARIFA BRIFA ARIFA BRIFA ARIFA ARIFA ARIFA BRIFA BRIFA ARIFA ARIFA ARIFA BRIFA ARIFA BRIFA BRIFA BRIFA BRIFA BRIFA BRIFA BRIFA BRIFA ARIFA BRIFA BRI	09	NETWORK AND POWER LAB.	TW	50	20	39	РС	19	COMMUNICATION THEORY	PP	100	40	40	Р
GRAND TOTAL = 791/1500, RESULT: SECOND CLASS ORDN. 1 MARKS : S8053068 MULLA SHARIQA AZAMALI ARIFA 71045531D , S8053068 PICT , S8053068 01. SIGNAL AND SYSTEMS PP 100 40 67 P C 11. ENGINEERING MATHEMATICS III PP 100 40 73 P 02. SIGNAL AND SYSTEMS OR 50 20 35 P C 12. ENGINEERING MATHEMATICS III TW 25 10 21 P 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 62 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 40 P 04. SOLID STATES DEVICES AND CIRCUITSPP 50 20 29 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 35 P 05. NETWORK ANALYSIS PP 100 40 68 P C 15. ELECTROMAGNETIC PP 100 40 56 P 06. DIGITAL LOGIC DESIGN PP 100 40 46 P C 16. ELECTROMAGNETIC TW 25 10 13 P 07. DIGITAL LOGIC DESIGN PR 50 20 25 P C 17. DATA STRUCTURES PP 100 40 43 P 08. POWER DEVICES AND MACHINES PP 100 40 58 P C 18. DATA STRUCTURES PR 50 20 43 P 09. NETWORK AND POWER LAB. TW 50 20 37 P C 19. COMMUNICATION THEORY PP 100 40 64 P 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 37 P C 20. COMMUNICATION THEORY PP 100 40 64 P 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 34 P GRAND TOTAL = 946/1500, RESULT: FIRST CLASS GRA	10	ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	41	P C	20	COMMUNICATION THEORY	OR	50	20	22	Р
S8053068 MULLA SHARIQA AZAMALI ARIFA 7,71045531D 8,88053068 MULLA SHARIQA AZAMALI ARIFA 7,71045531D 8,88053068 PICT 8,88053069 PICT 9,88053069 PICT 9,88053068 PICT 9,88053068 PICT 9,88053068 PICT 9,88053068 P								21	CIRCUIT SIMULATION AND TOOLS	TW	50	20	34	Р
S8053068 MULLA SHARIQA AZAMALI ARIFA 7,71045531D 8,88053068 MULLA SHARIQA AZAMALI ARIFA 7,71045531D 8,88053068 PICT 8,88053069 PICT 9,88053069 PICT 9,88053068 PICT 9,88053068 PICT 9,88053068 PICT 9,88053068 P	CDANI	701/1500 PECH T. CECONE) (LA	CC										
S8053068 MULLA SHARIQA AZAMALI ARIFA 7,71045531D 8,8053068, PICT 8,8053068 01. SIGNAL AND SYSTEMS PP 100 40 67 P C 11. ENGINEERING MATHEMATICS III PP 100 40 73 P 02. SIGNAL AND SYSTEMS OR 50 20 35 P C 12. ENGINEERING MATHEMATICS III TW 25 10 21 P 03. SOLID STATES DEVICES AND CIRCUITSPP 04. 62 P C 15. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 68 P C 05. NETWORK ANALYSIS PP 100 40 68 P C 16. ELECTROMAGNETIC TW 25 10 13 P 07. DIGITAL LOGIC DESIGN PR 50 20 25 P C 17. DATA STRUCTURES PP 100 40 63 P 08. POWER DEVICES AND MACHINES PP 100 40 58 P C 18. DATA STRUCTURES PP 100 40 64 P 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 37 P C 20. COMMUNICATION THEORY PP 100 40 64 P 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 34 P CRAND TOTAL = 946/1500, RESULT: FIRST CLASS ORDN. 1 MARKS: S8053069 MUNDANKAR SUSHANT SANJAY SHAILA 7,71045532B 7,8053069, PICT 7, S8053069 MUNDANKAR SUSHANT SANJAY SHAILA 7,71045532B 7,8053069, PICT 7, S8053069 MUNDANKAR SUSHANT SANJAY SHAILA 7,71045532B 7,8053069, PICT 7, S8053069 MUNDANKAR SUSHANT SANJAY SHAILA 7,71045532B 7,8053069, PICT 7, S8053069			CLA	55										
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08. POWER DEVICES AND MACHINES														
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21. CIRCUIT SIMULATION AND TOOLS TW 50 20 34 P GRAND TOTAL = 946/1500, RESULT: FIRST CLASS ORDN. 1 MARKS: S8053069 MUNDANKAR SUSHANT SANJAY SHAILA 7,71045532B , S8053069 , PICT , S8053069 01. SIGNAL AND SYSTEMS PP 100 40 83 P C 11. ENGINEERING MATHEMATICS III PP 100 40 86 P 02. SIGNAL AND SYSTEMS OR 50 20 37 P C 12. ENGINEERING MATHEMATICS III TW 25 10 18 P														
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01. SIGNAL AND SYSTEMS PP 100 40 83 PC 11. ENGINEERING MATHEMATICS III PP 100 40 86 P 02. SIGNAL AND SYSTEMS OR 50 20 37 PC 12. ENGINEERING MATHEMATICS III TW 25 10 18 P														
01. SIGNAL AND SYSTEMS PP 100 40 83 PC 11. ENGINEERING MATHEMATICS III PP 100 40 86 P 02. SIGNAL AND SYSTEMS OR 50 20 37 PC 12. ENGINEERING MATHEMATICS III TW 25 10 18 P														
01. SIGNAL AND SYSTEMS PP 100 40 83 PC 11. ENGINEERING MATHEMATICS III PP 100 40 86 P 02. SIGNAL AND SYSTEMS OR 50 20 37 PC 12. ENGINEERING MATHEMATICS III TW 25 10 18 P	C 9 (052060 MUNDANKAD SUSHANT SANJAY				спи	T 1 A		710455226 69052060	DTCT		C 8 N	5206	٥
02. SIGNAL AND SYSTEMS OR 50 20 37 P C 12. ENGINEERING MATHEMATICS III TW 25 10 18 P	300	MUNDANKAK SUSHANI SANJAT				ЗПА	ILA		, 710433326 , 36033009 ,	PICI		, 300	3300	9
	01	SIGNAL AND SYSTEMS	PP	100	40	83	РС	11	ENGINEERING MATHEMATICS III	PP	100	40	86	Р
03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 56 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 58 P	02	SIGNAL AND SYSTEMS	OR	50	20	37	РС	12	ENGINEERING MATHEMATICS III	TW	25	10	18	Р
	03	SOLID STATES DEVICES AND CIRCUITS	SPP	100	40	56	P C	13	INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	58	Р

50 20 35 P C

04. SOLID STATES DEVICES AND CIRCUITSPR

Page 42

14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 33 P

05. NETWORK ANALYSIS	PP	100	40	61	РC	cols05	PP	100	40	65	Р
06. DIGITAL LOGIC DESIGN	PP	100	40	62	P C	16. ELECTROMAGNETIC	TW	25	10	20	Р
07. DIGITAL LOGIC DESIGN	PR	50	20	30	P C	17. DATA STRUCTURES	PP	100	40	59	Р
08. POWER DEVICES AND MACHINES	PP	100	40	57	P C	18. DATA STRUCTURES	PR	50	20	36	Р
09. NETWORK AND POWER LAB.	TW	50	20	37	P C	19. COMMUNICATION THEORY	PP	100	40	40	Р
10. ELECTRONIC INSTRUMENTS AND TOOL	S TW	50	20	37	P C	20. COMMUNICATION THEORY	OR	50	20	35	Р
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	35	Р
GRAND TOTAL = 980/1500, RESULT: FIRS	ST CLAS	SS									
	 F PUNE	 ,S.E.	 (2008	 PAT.	 .)(ELE						
DATE : 18 AUG. 2011	CEN	TRE : I	PUNE]	INSTI	TUTE (F COMPUTER TECHNOLOGY, PUNE.	PAGI	E NO.	24	(3	48)

NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8053070 NAGHATE ANKIT VIJAY			MADHURI	, 71045537C , S8053070 , PICT	, s8053070
01. SIGNAL AND SYSTEMS PR	100	40	80 P C	11. ENGINEERING MATHEMATICS III PP 100	40 72 P
02. SIGNAL AND SYSTEMS OF	50	20	35 P C	12. ENGINEERING MATHEMATICS III TW 25	10 21 P
03. SOLID STATES DEVICES AND CIRCUITSPE	100	40	40 P C	13. INTEGRATED CIRCUITS APPLICATIONS PP 100	40 48 P
04. SOLID STATES DEVICES AND CIRCUITSPE	50	20	20 P C	14. INTEGRATED CIRCUITS APPLICATIONS PR 50	20 40 P
05. NETWORK ANALYSIS PR	100	40	49 P C	15. ELECTROMAGNETIC PP 100	40 66 P
06. DIGITAL LOGIC DESIGN PR	100	40	78 P C	16. ELECTROMAGNETIC TW 25	10 20 P
07. DIGITAL LOGIC DESIGN PR	50	20	44 P C	17. DATA STRUCTURES PP 100	40 73 P
08. POWER DEVICES AND MACHINES PR	100	40	45 P C	18. DATA STRUCTURES PR 50	20 48 P
09. NETWORK AND POWER LAB.	50	20	37 P C	19. COMMUNICATION THEORY PP 100	40 51 P
10. ELECTRONIC INSTRUMENTS AND TOOLS TO	50	20	39 P C	20. COMMUNICATION THEORY OR 50	20 33 P
				21. CIRCUIT SIMULATION AND TOOLS TW 50	20 41 P

GRAND TOTAL = 980/1500, RESULT: FIRST CLASS

S8053071 NERLEKAR SHWETA PRAKASH			SUGANDHA	, 71045546B , S8053071 ,	PICT		, s80	5307	1
01. SIGNAL AND SYSTEMS PP	100	40	46 P C	11. ENGINEERING MATHEMATICS III	PP	100	40	66	Р
02. SIGNAL AND SYSTEMS OR	50	20	22 P C	12. ENGINEERING MATHEMATICS III	TW	25	10	21	Р
03. SOLID STATES DEVICES AND CIRCUITSPP	100	40	40 P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	18	F
04. SOLID STATES DEVICES AND CIRCUITSPR	50	20	25 P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	35	Р
05. NETWORK ANALYSIS PP	100	40	40 P C	15. ELECTROMAGNETIC	PP	100	40	20	F
06. DIGITAL LOGIC DESIGN PP	100	40	24 F	16. ELECTROMAGNETIC	TW	25	10	19	Р
07. DIGITAL LOGIC DESIGN PR	50	20	25 P	17. DATA STRUCTURES	PP	100	40	50	Р
08. POWER DEVICES AND MACHINES PP	100	40	40 P C	18. DATA STRUCTURES	PR	50	20	36	Р
09. NETWORK AND POWER LAB. TW	50	20	40 P C	19. COMMUNICATION THEORY	PP	100	40	32	F
10. ELECTRONIC INSTRUMENTS AND TOOLS TW	50	20	41 P C	20. COMMUNICATION THEORY	OR	50	20	27	Р
				21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	37	Р

GRAND TOTAL = 704/1500, RESULT: FAILS

ORDN. 1 MARKS:

S8053072 NEVASE PRAJAKTA MARUTI			NANDA	, 71129934L , S8053072 ,	PICT		, s8053072		
01. SIGNAL AND SYSTEMS PP	100	40	52 P C	11. ENGINEERING MATHEMATICS III	PP	100	40	40	Р
02. SIGNAL AND SYSTEMS OR	50	20	16 F	12. ENGINEERING MATHEMATICS III	TW	25	10	23	Р
03. SOLID STATES DEVICES AND CIRCUITSPP	100	40	40 P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	57	Р
04. SOLID STATES DEVICES AND CIRCUITSPR	50	20	26 P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	34	Р
05. NETWORK ANALYSIS PP	100	40	29 F	15. ELECTROMAGNETIC	PP	100	40	43	Р
06. DIGITAL LOGIC DESIGN PP	100	40	68 P C	16. ELECTROMAGNETIC	TW	25	10	20	Р
07. DIGITAL LOGIC DESIGN PR	50	20	34 P C	17. DATA STRUCTURES	PP	100	40	63	Р
08. POWER DEVICES AND MACHINES PP	100	40	54 P C	18. DATA STRUCTURES	PR	50	20	35	Р
09. NETWORK AND POWER LAB. TW	50	20	41 P C	19. COMMUNICATION THEORY	PP	100	40	47	Р

10. ELECTRONIC INSTRUMENTS AND TOOL	_S TW	50	20	37	P C	20.	cols05 COMMUNICATION THEORY	OR	50	20	22	Р
						21.	CIRCUIT SIMULATION AND TOOLS	S TW	50	20	40	Р
GRAND TOTAL = 821/1500, RESULT: FAIL ORDN. 1 MARKS :	₋S A.T	.К.Т.										
						TROUTES "						
UNIVERSITY O							R TECHNOLOGY, PUNE.	D	AGE NO	25	(3	49)
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NOTE: FIRST LINE : SEAT NO., NAME							DEC NO DREVIOUS SEAT NO					
OTHER LINES: HEAD OF PASSING												
OTHER LINES. HEAD OF PASSING	, MAX	• MAKK	J, MI	LIN. F	ASS MAN	KS, MAK	NS OBTAINED, F/F.FASS/FAIL,	C.PREV.	1003 CA	KKI U	/ EK	
				• •								
S8053073 NIKAM JEEVAN SHANKAR				SAN	IGITA		, 71045547L , S8053073	3 , PI	CT	, s8	05307	3
01. SIGNAL AND SYSTEMS	PP	100	40	19	F	11.	ENGINEERING MATHEMATICS III	PP	100	40	25	F
02. SIGNAL AND SYSTEMS	OR	50	20	20	Р	12.	ENGINEERING MATHEMATICS III	TW	25	10	23	Р
03. SOLID STATES DEVICES AND CIRCUI	TSPP	100	40	40	Р	13.	INTEGRATED CIRCUITS APPLICAT	TIONS PP	100	40	25	F
04. SOLID STATES DEVICES AND CIRCUI	TSPR	50	20	20	Р	14.	INTEGRATED CIRCUITS APPLICAT	TIONS PR	50	20	25	Р
05. NETWORK ANALYSIS	PP	100	40	24	F	15.	ELECTROMAGNETIC	PP	100	40	28	F
06. DIGITAL LOGIC DESIGN	PP	100	40	40	PC	16.	ELECTROMAGNETIC	TW	25	10	15	Р
07. DIGITAL LOGIC DESIGN	PR	50	20	29	PC	17.	DATA STRUCTURES	PP	100	40	40	Р
08. POWER DEVICES AND MACHINES	PP	100	40	40	Р	18.	DATA STRUCTURES	PR	50	20	28	Р
09. NETWORK AND POWER LAB.	TW	50	20	37	PC	19.	COMMUNICATION THEORY	PP	100	40	23	F
10. ELECTRONIC INSTRUMENTS AND TOOL	_S TW	50	20	39	PC	20.	COMMUNICATION THEORY	OR	50	20	23	Р
						21.	CIRCUIT SIMULATION AND TOOLS	5 TW	50	20	37	Р
GRAND TOTAL = 600/1500, RESULT: FAIL ORDN. 1 MARKS :	_S											
S8053074 NIRMAL MAYUR VINAYAKRAO				SUN	IITA		, 71045548J , S8053074	ł, PI	CT	, s8	05307	4

01. SIGNAL AND SYSTEMS	PP	100	40	86	РС	cols05 11. ENGINEERING MATHEMATICS III PP 100 40 79 P
02. SIGNAL AND SYSTEMS	OR	50	20	40	PС	12. ENGINEERING MATHEMATICS III TW 25 10 23 P
03. SOLID STATES DEVICES AND CIR	CUITSPP	100	40	72	РС	13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 69 P
04. SOLID STATES DEVICES AND CIR	CUITSPR	50	20	34	P C	14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 39 P
05. NETWORK ANALYSIS	PP	100	40	49	PС	15. ELECTROMAGNETIC PP 100 40 61 P
06. DIGITAL LOGIC DESIGN	PP	100	40	66	P C	16. ELECTROMAGNETIC TW 25 10 21 P
07. DIGITAL LOGIC DESIGN	PR	50	20	24	P C	17. DATA STRUCTURES PP 100 40 52 P
08. POWER DEVICES AND MACHINES	PP	100	40	58	P C	18. DATA STRUCTURES PR 50 20 38 P
09. NETWORK AND POWER LAB.	TW	50	20	40	PС	19. COMMUNICATION THEORY PP 100 40 66 P
10. ELECTRONIC INSTRUMENTS AND T	OOLS TW	50	20	44	P C	20. COMMUNICATION THEORY OR 50 20 32 P
						21. CIRCUIT SIMULATION AND TOOLS TW 50 20 37 P

GRAND TOTAL = 1030/1500, RESULT: FIRST CLASS WITH DISTINCTION
ORDN. 1 MARKS :

S8053075 PACHANGE ASHISH NARAYAN			KALPANA	, 71045551j , S8053075 , PICT	, s8053075
01. SIGNAL AND SYSTEMS PE	100	40	80 P C	11. ENGINEERING MATHEMATICS III PP 100	40 64 P
02. SIGNAL AND SYSTEMS OF	50	20	34 P C	12. ENGINEERING MATHEMATICS III TW 25	10 22 P
03. SOLID STATES DEVICES AND CIRCUITSPE	100	40	53 P C	13. INTEGRATED CIRCUITS APPLICATIONS PP 100	40 51 P
04. SOLID STATES DEVICES AND CIRCUITSPE	50	20	34 P C	14. INTEGRATED CIRCUITS APPLICATIONS PR 50	20 30 P
05. NETWORK ANALYSIS PR	100	40	64 P	15. ELECTROMAGNETIC PP 100	40 43 P
06. DIGITAL LOGIC DESIGN PF	100	40	77 P C	16. ELECTROMAGNETIC TW 25	10 10 P
07. DIGITAL LOGIC DESIGN PR	50	20	39 P C	17. DATA STRUCTURES PP 100	40 64 P
08. POWER DEVICES AND MACHINES PR	100	40	58 P C	18. DATA STRUCTURES PR 50	20 46 P
09. NETWORK AND POWER LAB.	50	20	38 P C	19. COMMUNICATION THEORY PP 100	40 58 P
10. ELECTRONIC INSTRUMENTS AND TOOLS TO	50	20	26 P C	20. COMMUNICATION THEORY OR 50	20 34 P
				21. CIRCUIT SIMULATION AND TOOLS TW 50	20 34 P

GRAND TOTAL = 959/1500, RESULT: FIRST CLASS
ORDN. 1 MARKS :

	UNIVERSITY OF	 PUNE	 ,S.E.(2008	PAT.		 ONICS &	TELECOM.)					
	DATE : 18 AUG. 2011	CENT	RE : P	UNE IN	ISTI	TUTE OF (COMPUTER	R TECHNOLOGY, PUNE.	PAGI	E NO.	26	(3	50)
NOT	TE: FIRST LINE : SEAT NO., NAME OF	F THE	CANDI	DATE,	MO	THER, PEF	RMANENT	REG. NO., PREVIOUS SEAT NO., CO	OLLEGI	E, S	EAT N	Ο.	
	OTHER LINES: HEAD OF PASSING,	MAX.	MARKS	, MIN	1. PA	ASS MARKS	S, MARK	KS OBTAINED, P/F:PASS/FAIL, C:PF	REVIO	JS CAR	RY OV	ER	
S80	053076 PARODE ASHWINI SANJAY				JAIS	SHREE		, 71045559D , S8053076 ,	PICT		, s80	5307	6
01.	. SIGNAL AND SYSTEMS	PP	100	40	68	P C	11.	ENGINEERING MATHEMATICS III	PP	100	40	67	Р
02.	. SIGNAL AND SYSTEMS	OR	50	20	20	P C	12.	ENGINEERING MATHEMATICS III	TW	25	10	25	Р
03.	. SOLID STATES DEVICES AND CIRCUITS	SPP	100	40	40	P C	13.	INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	40	Р
04.	. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	30	P C	14.	INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	35	Р
05.	. NETWORK ANALYSIS	PP	100	40	40	P C	15.	ELECTROMAGNETIC	PP	100	40	46	Р
06.	. DIGITAL LOGIC DESIGN	PP	100	40	57	P C	16.	ELECTROMAGNETIC	TW	25	10	20	Р
07.	. DIGITAL LOGIC DESIGN	PR	50	20	33	P C	17.	DATA STRUCTURES	PP	100	40	64	Р
08.	. POWER DEVICES AND MACHINES	PP	100	40	45	P C	18.	DATA STRUCTURES	PR	50	20	44	Р
09.	. NETWORK AND POWER LAB.	TW	50	20	43	P C	19.	COMMUNICATION THEORY	PP	100	40	43	Р
10.	. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	42	P C	20.	COMMUNICATION THEORY	OR	50	20	30	Р
							21.	CIRCUIT SIMULATION AND TOOLS	TW	50	20	42	Р
GRANI	D TOTAL = 874/1500, RESULT: HIGHER	R SEC	OND CL	ASS									
	. 1 MARKS :												
S80	053077 PATIL GAURAV VISHRAM				PRAM	MILA		, 71045564L , S8053077 ,	PICT		, s80	5307	7
01.	. SIGNAL AND SYSTEMS	PP	100	40	53	P C	11.	ENGINEERING MATHEMATICS III	PP	100	40	42	Р
02.	. SIGNAL AND SYSTEMS	OR	50	20	20	P C	12.	ENGINEERING MATHEMATICS III	TW	25	10	20	Р
03.	. SOLID STATES DEVICES AND CIRCUITS	SPP	100	40	52	Р	13.	INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	51	Р
04.	. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	28	P C	14.	INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	27	Р
05.	. NETWORK ANALYSIS	PP	100	40	40	P C	15.	ELECTROMAGNETIC	PP	100	40	69	Р
								Davis 47					

						cols05				
06. DIGITAL LOGIC DESIGN	PP	100	40	52	PC	16. ELECTROMAGNETIC TW	25	10	17	Р
07. DIGITAL LOGIC DESIGN	PR	50	20	32	P C	17. DATA STRUCTURES PP	100	40	47	Р
08. POWER DEVICES AND MACHINES	PP	100	40	59	P C	18. DATA STRUCTURES PR	50	20	44	Р
09. NETWORK AND POWER LAB.	TW	50	20	36	P C	19. COMMUNICATION THEORY PP	100	40	53	Р
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	39	P C	20. COMMUNICATION THEORY OR	50	20	24	Р
						21. CIRCUIT SIMULATION AND TOOLS TW	50	20	34	Р

GRAND TOTAL = 839/1500, RESULT: HIGHER SECOND CLASS ORDN. 1 MARKS:

S8053078 PATIL OMKAR VISHWAS	VANITA			, 71045566G , S8053078 ,	PICT		, s8053078		8	
O1. SIGNAL AND SYSTEMS PP	100	40	85 P	11.	ENGINEERING MATHEMATICS III	PP	100	40	79	Р
02. SIGNAL AND SYSTEMS OR	50	20	37 P	12.	ENGINEERING MATHEMATICS III	TW	25	10	21	Р
03. SOLID STATES DEVICES AND CIRCUITSPP	100	40	50 P	13.	INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	64	Р
04. SOLID STATES DEVICES AND CIRCUITSPR	50	20	38 P	14.	INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	35	Р
05. NETWORK ANALYSIS PP	100	40	45 P	15.	ELECTROMAGNETIC	PP	100	40	64	Р
06. DIGITAL LOGIC DESIGN PP	100	40	57 P	16.	ELECTROMAGNETIC	TW	25	10	21	Р
07. DIGITAL LOGIC DESIGN PR	50	20	32 P	17.	DATA STRUCTURES	PP	100	40	60	Р
08. POWER DEVICES AND MACHINES PP	100	40	49 P	18.	DATA STRUCTURES	PR	50	20	38	Р
09. NETWORK AND POWER LAB. TW	50	20	40 P	19.	COMMUNICATION THEORY	PP	100	40	59	Р
10. ELECTRONIC INSTRUMENTS AND TOOLS TW	50	20	43 P	20.	COMMUNICATION THEORY	OR	50	20	32	Р
				21.	CIRCUIT SIMULATION AND TOOLS	TW	50	20	42	Р

GRAND TOTAL = 991/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS :

UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(ELECTRONICS & TELECOM.)

DATE: 18 AUG. 2011 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 27 (351)

NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

				cols05	
OTHER LINES: HEAD OF PASSING,	MAX. MARKS,	MIN. PASS MARKS,	MARKS OBTAINED,	P/F:PASS/FAIL,	C:PREVIOUS CARRY OVER

S8053079 PATIL SONAM SHASHIKANT			SHUBHANGI	, 71129935J , S8053079 ,	PICT		, s80	053079)
01. SIGNAL AND SYSTEMS PP	100	40	73 P C	11. ENGINEERING MATHEMATICS III	PP	100	40	76	Р
02. SIGNAL AND SYSTEMS OR	50	20	32 P C	12. ENGINEERING MATHEMATICS III	TW	25	10	21	Р
03. SOLID STATES DEVICES AND CIRCUITSPP	100	40	45 P C	13. INTEGRATED CIRCUITS APPLICATIONS	, PP	100	40	56	Р
04. SOLID STATES DEVICES AND CIRCUITSPR	50	20	35 P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	35	Р
05. NETWORK ANALYSIS PP	100	40	40 P C	15. ELECTROMAGNETIC	PP	100	40	64	Р
06. DIGITAL LOGIC DESIGN PP	100	40	60 P C	16. ELECTROMAGNETIC	TW	25	10	19	Р
07. DIGITAL LOGIC DESIGN PR	50	20	29 P C	17. DATA STRUCTURES	PP	100	40	64	Р
08. POWER DEVICES AND MACHINES PP	100	40	64 P C	18. DATA STRUCTURES	PR	50	20	42	Р
09. NETWORK AND POWER LAB. TW	50	20	38 P C	19. COMMUNICATION THEORY	PP	100	40	65	Р
10. ELECTRONIC INSTRUMENTS AND TOOLS TW	50	20	42 P C	20. COMMUNICATION THEORY	OR	50	20	35	Р
				21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	41	Р

GRAND TOTAL = 976/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS:

S8053080 PATIL TEJAS LAXMAN	SUREKHA		SUREKHA	, 71045568C , S8053080 ,	PICT	, s8	053080
O1. SIGNAL AND SYSTEMS PP	100	40	76 P C	11. ENGINEERING MATHEMATICS III	PP 100	40	66 P
02. SIGNAL AND SYSTEMS OR	50	20	40 P C	12. ENGINEERING MATHEMATICS III	TW 2	10	21 P
03. SOLID STATES DEVICES AND CIRCUITSPP	100	40	64 P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP 100	40	32* P
04. SOLID STATES DEVICES AND CIRCUITSPR	50	20	34 P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR 50	20	33 P
05. NETWORK ANALYSIS PP	100	40	43 P C	15. ELECTROMAGNETIC	PP 100	40	50 P
06. DIGITAL LOGIC DESIGN PP	100	40	63 P C	16. ELECTROMAGNETIC	TW 2	5 10	19 P
07. DIGITAL LOGIC DESIGN PR	50	20	33 P C	17. DATA STRUCTURES	PP 100	40	52 P
08. POWER DEVICES AND MACHINES PP	100	40	50 P C	18. DATA STRUCTURES	PR 50	20	44 P
09. NETWORK AND POWER LAB. TW	50	20	40 P C	19. COMMUNICATION THEORY	PP 100	40	47 P
10. ELECTRONIC INSTRUMENTS AND TOOLS TW	50	20	43 P C	20. COMMUNICATION THEORY	OR 50	20	40 P

cols05 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 37 P

GRAND TOTAL = 927/1500, RESULT: FIRST CLASS * [0.4] ORDN. 1 MARKS:

s8053081	PAWADE NITIN NARAYAN				REN	JKA		, 71045569м	, s8053081 ,	PICT		, s80	53082	1
01. SIGNAL	AND SYSTEMS	PP	100	40	71	P C	11.	ENGINEERING MAT	HEMATICS III	PP	100	40	56	Р
02. SIGNAL	AND SYSTEMS	OR	50	20	35	РС	12.	ENGINEERING MAT	THEMATICS III	TW	25	10	23	Р
03. SOLID	STATES DEVICES AND CIRCUIT	SPP	100	40	55	РС	13.	INTEGRATED CIRC	CUITS APPLICATIONS	PP	100	40	40	Р
04. SOLID	STATES DEVICES AND CIRCUIT	SPR	50	20	38	РС	14.	INTEGRATED CIRC	CUITS APPLICATIONS	PR	50	20	30	Р
05. NETWOR	K ANALYSIS	PP	100	40	40	РС	15.	ELECTROMAGNETIC		PP	100	40	67	Р
06. DIGITA	L LOGIC DESIGN	PP	100	40	76	РС	16.	ELECTROMAGNETIC		TW	25	10	20	Р
07. DIGITA	L LOGIC DESIGN	PR	50	20	34	РС	17.	DATA STRUCTURES	;	PP	100	40	52	Р
08. POWER	DEVICES AND MACHINES	PP	100	40	49	РС	18.	DATA STRUCTURES	;	PR	50	20	39	Р
09. NETWOR	K AND POWER LAB.	TW	50	20	44	РС	19.	COMMUNICATION 1	HEORY	PP	100	40	68	Р
10. ELECTR	ONIC INSTRUMENTS AND TOOLS	TW	50	20	43	РС	20.	COMMUNICATION 1	HEORY	OR	50	20	27	Р
							21.	CIRCUIT SIMULAT	ION AND TOOLS	TW	50	20	38	Р

GRAND TOTAL = 945/1500, RESULT: FIRST CLASS ORDN. 1 MARKS:

UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(ELECTRONICS & TELECOM.)

DATE: 18 AUG. 2011 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 28 (352)

NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8053082 PAWAR PRADIPKUMAR RAOSAHEB SANGITA , 71045572M , S8053082 , PICT , S8053082

01. SIGNAL AND SYSTEMS PP 100 40 64 P C 11. ENGINEERING MATHEMATICS III PP 100 40 70 P

						co1s05	
02. SIGNAL AND SYSTEMS	OR	50	20	20	РС	12. ENGINEERING MATHEMATICS III TW 25 10 22 P	
03. SOLID STATES DEVICES AND CI	RCUITSPP	100	40	46	P C	13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 51 P	
04. SOLID STATES DEVICES AND CI	RCUITSPR	50	20	30	P C	14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 30 P	
05. NETWORK ANALYSIS	PP	100	40	49	P C	15. ELECTROMAGNETIC PP 100 40 68 P	
06. DIGITAL LOGIC DESIGN	PP	100	40	59	P C	16. ELECTROMAGNETIC TW 25 10 17 P	
07. DIGITAL LOGIC DESIGN	PR	50	20	34	P C	17. DATA STRUCTURES PP 100 40 58 P	
08. POWER DEVICES AND MACHINES	PP	100	40	41	P C	18. DATA STRUCTURES PR 50 20 32 P	
09. NETWORK AND POWER LAB.	TW	50	20	39	P C	19. COMMUNICATION THEORY PP 100 40 46 P	
10. ELECTRONIC INSTRUMENTS AND	TOOLS TW	50	20	34	P C	20. COMMUNICATION THEORY OR 50 20 27 P	
						21. CIRCUIT SIMULATION AND TOOLS TW 50 20 35 P	

GRAND TOTAL = 872/1500, RESULT: HIGHER SECOND CLASS ORDN. 1 MARKS:

S8053083 PAWAR SURAJ SUBHASH			VIDYA	, 71045573к , s8053083 ,	PICT	, S8	053083
O1. SIGNAL AND SYSTEMS PP	100	40	52 P C	11. ENGINEERING MATHEMATICS III	PP 100	40	47 P
02. SIGNAL AND SYSTEMS OR	50	20	26 P C	12. ENGINEERING MATHEMATICS III	TW 25	10	19 P
03. SOLID STATES DEVICES AND CIRCUITSPP	100	40	44 P	13. INTEGRATED CIRCUITS APPLICATIONS	PP 100	40	28 F
04. SOLID STATES DEVICES AND CIRCUITSPR	50	20	28 P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR 50	20	25 P
05. NETWORK ANALYSIS PP	100	40	40 P	15. ELECTROMAGNETIC	PP 100	40	40 P
06. DIGITAL LOGIC DESIGN PP	100	40	25 F	16. ELECTROMAGNETIC	TW 25	10	18 P
07. DIGITAL LOGIC DESIGN PR	50	20	24 P C	17. DATA STRUCTURES	PP 100	40	45 P
08. POWER DEVICES AND MACHINES PP	100	40	40 P C	18. DATA STRUCTURES	PR 50	20	43 P
09. NETWORK AND POWER LAB. TW	50	20	35 P C	19. COMMUNICATION THEORY	PP 100	40	20 F
10. ELECTRONIC INSTRUMENTS AND TOOLS TW	50	20	36 P C	20. COMMUNICATION THEORY	OR 50	20	27 P
				21. CIRCUIT SIMULATION AND TOOLS	TW 50	20	35 P

GRAND TOTAL = 697/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS:

S8053084 PITRODA UTSAV RAJENDRA				KAI	LASH		, 71129936G , S8053084 ,	PICT		, s80	5308	4
01. SIGNAL AND SYSTEMS	PP	100	40	40	P C	11.	ENGINEERING MATHEMATICS III	PP	100	40	18	F
02. SIGNAL AND SYSTEMS	OR	50	20	21	Р	12.	ENGINEERING MATHEMATICS III	TW	25	10	18	Р
03. SOLID STATES DEVICES AND CIRCUI	TSPP	100	40	49	Р	13.	INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	51	Р
04. SOLID STATES DEVICES AND CIRCUI	TSPR	50	20	28	P C	14.	INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	30	Р
05. NETWORK ANALYSIS	PP	100	40	19	F	15.	ELECTROMAGNETIC	PP	100	40	40	Р
06. DIGITAL LOGIC DESIGN	PP	100	40	40	P C	16.	ELECTROMAGNETIC	TW	25	10	16	Р
07. DIGITAL LOGIC DESIGN	PR	50	20	24	P C	17.	DATA STRUCTURES	PP	100	40	43	Р
08. POWER DEVICES AND MACHINES	PP	100	40	48	P C	18.	DATA STRUCTURES	PR	50	20	45	Р
09. NETWORK AND POWER LAB.	TW	50	20	41	P C	19.	COMMUNICATION THEORY	PP	100	40	40	Р
10. ELECTRONIC INSTRUMENTS AND TOOL	S TW	50	20	38	P C	20.	COMMUNICATION THEORY	OR	50	20	20	Р
						21.	CIRCUIT SIMULATION AND TOOLS	TW	50	20	42	Р
ORDN. 1 MARKS :												
UNIVERSITY OF							TELECOM.) R TECHNOLOGY, PUNE.		 E NO.	29		 53)
DATE: 18 AUG. 2011	CENT	RE : P	PUNE II	NSTI [.]	TUTE OF C	COMPUTER	TELECOM.) R TECHNOLOGY, PUNE.	PAG		 29		 53)
DATE: 18 AUG. 2011 NOTE: FIRST LINE: SEAT NO., NAME	CENTI · · · OF THE	RE : P	PUNE II	NSTI ⁻	TUTE OF C	COMPUTER	TELECOM.) R TECHNOLOGY, PUNE	PAG · · ·	 E, S	 SEAT N		 53)
DATE: 18 AUG. 2011 NOTE: FIRST LINE: SEAT NO., NAME	CENTI · · · OF THE	RE : P	PUNE II	NSTI ⁻	TUTE OF C	COMPUTER	TELECOM.) R TECHNOLOGY, PUNE.	PAG · · ·	 E, S	 SEAT N		 53)
DATE: 18 AUG. 2011 NOTE: FIRST LINE: SEAT NO., NAME	CENTI OF THE MAX.	RE : P CANDI MARKS	PUNE IN	NSTI [*] MO [*] N. P	TUTE OF C THER, PER	COMPUTER RMANENT S, MARK	TELECOM.) R TECHNOLOGY, PUNE.	PAG · · · OLLEG REVIO	 E, S US CAR	 SEAT N		 53)
DATE: 18 AUG. 2011 NOTE: FIRST LINE: SEAT NO., NAME OTHER LINES: HEAD OF PASSING,	CENTI OF THE MAX.	RE : P CANDI MARKS	PUNE IN	NSTI [*] MO [*] N. P	TUTE OF C THER, PER ASS MARKS	COMPUTER RMANENT S, MARK	TELECOM.) R TECHNOLOGY, PUNE.	PAG OLLEG REVIO	E, S US CAR	 SEAT N	 IO . 'ER 	
DATE: 18 AUG. 2011 NOTE: FIRST LINE: SEAT NO., NAME OTHER LINES: HEAD OF PASSING,	CENTI OF THE MAX.	RE : P CANDI MARKS	PUNE IN	MOTN. PA	TUTE OF C THER, PER ASS MARKS	COMPUTER RMANENT MARK	TELECOM.) R TECHNOLOGY, PUNE.	PAG OLLEG REVIO	E, S US CAR	EAT N RRY OV	 IO . 'ER 	
DATE: 18 AUG. 2011 NOTE: FIRST LINE: SEAT NO., NAME OTHER LINES: HEAD OF PASSING, S8053085 POL AMIT KISHOR	CENT OF THE MAX. 	RE : P CANDI MARKS	PUNE IN	MOTAL NSTITE	TUTE OF C THER, PER ASS MARKS	COMPUTER RMANENT MARK 11.	TELECOM.) R TECHNOLOGY, PUNE	PAG OLLEG REVIO	E, S US CAR	EAT NRRY OV	ER	 5
UNIVERSITY OF DATE: 18 AUG. 2011 NOTE: FIRST LINE: SEAT NO., NAME OTHER LINES: HEAD OF PASSING, S8053085 POL AMIT KISHOR 01. SIGNAL AND SYSTEMS	CENTION OF THE MAX	CANDI MARKS	PUNE IN DATE, , MIN	NSTITUTE MOTE NO. PARAMETER DAYA	TUTE OF C THER, PER ASS MARKS	COMPUTER RMANENT MARK 11.	TELECOM.) R TECHNOLOGY, PUNE	PAG OLLEG REVIO PICT PP TW	E, S US CAR 		 IO . /ER 953089	 5 P
UNIVERSITY OF DATE: 18 AUG. 2011 NOTE: FIRST LINE: SEAT NO., NAME OTHER LINES: HEAD OF PASSING, S8053085 POL AMIT KISHOR 01. SIGNAL AND SYSTEMS 02. SIGNAL AND SYSTEMS	CENTI OF THE MAX PP OR TSPP	CANDI MARKS	PUNE IN	NSTITE	TUTE OF C THER, PER ASS MARKS A P C P C	COMPUTER RMANENT MARK 11. 12. 13.	TELECOM.) R TECHNOLOGY, PUNE	PAG OLLEG REVIO PICT PP TW PP	E, S US CAR 	 SEAT N RRY OV , S80 40 10		 5 P
UNIVERSITY OF DATE: 18 AUG. 2011 NOTE: FIRST LINE: SEAT NO., NAME OTHER LINES: HEAD OF PASSING, S8053085 POL AMIT KISHOR 01. SIGNAL AND SYSTEMS 02. SIGNAL AND SYSTEMS 03. SOLID STATES DEVICES AND CIRCUIT	CENTI OF THE MAX PP OR TSPP	TRE : P CANDI MARKS 100 50 100	PUNE IN	NSTITE	TUTE OF C THER, PER ASS MARKS A P C P C P C	COMPUTER RMANENT 5, MARK 11. 12. 13. 14.	TELECOM.) R TECHNOLOGY, PUNE	PAG OLLEG REVIO PICT PP TW PP	100 25	 SEAT N RRY OV , S80 40 10 40		
UNIVERSITY OF DATE: 18 AUG. 2011 NOTE: FIRST LINE: SEAT NO., NAME OTHER LINES: HEAD OF PASSING, S8053085 POL AMIT KISHOR 01. SIGNAL AND SYSTEMS 02. SIGNAL AND SYSTEMS 03. SOLID STATES DEVICES AND CIRCUI 04. SOLID STATES DEVICES AND CIRCUI	CENTI	TRE : P CANDI MARKS 100 50 100 50	PUNE IN	NSTITE	TUTE OF C THER, PER ASS MARKS A P C P C P C P C	COMPUTER RMANENT 5, MARK 11. 12. 13. 14. 15.	TELECOM.) R TECHNOLOGY, PUNE	PAG OLLEG REVIO PICT PP TW PP PR	100 25 100 50	 SEAT N RRY OV , S80 40 10 40 20	95 24 44 36	

Page 52

07. DIGITAL LOGIC DESIGN	PR	50	20	29	РС	cols05 17. DATA STRUCTURES PP 100 40 6	5 P
08. POWER DEVICES AND MACHINES	PP	100	40	47	P C	18. DATA STRUCTURES PR 50 20 4	6 P
09. NETWORK AND POWER LAB.	TW	50	20	41	P C	19. COMMUNICATION THEORY PP 100 40 46	3 P
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	38	P C	20. COMMUNICATION THEORY OR 50 20 23	LΡ
						21. CIRCUIT SIMULATION AND TOOLS TW 50 20 40) Р

GRAND TOTAL = 982/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS:

S	3053086	PRABHUDESAI PRATHAMESH	PRAKASH			ADIT	ΓI		, 71045579J , S8053086 ,	PICT		, s80	53086	6
0.	L. SIGNAL	AND SYSTEMS	PP	100	40	78	P C	11.	ENGINEERING MATHEMATICS III	PP	100	40	63	Р
02	2. SIGNAL	AND SYSTEMS	OR	50	20	42	P C	12.	ENGINEERING MATHEMATICS III	TW	25	10	21	Р
03	3. SOLID	STATES DEVICES AND CIRCU	ITSPP	100	40	67	P C	13.	INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	56	Р
04	1. SOLID	STATES DEVICES AND CIRCU	ITSPR	50	20	36	P C	14.	INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	41	Р
0!	. NETWOR	K ANALYSIS	PP	100	40	45	P C	15.	ELECTROMAGNETIC	PP	100	40	70	Р
06	6. DIGITA	L LOGIC DESIGN	PP	100	40	69	P C	16.	ELECTROMAGNETIC	TW	25	10	17	Р
07	7. DIGITA	L LOGIC DESIGN	PR	50	20	42	P C	17.	DATA STRUCTURES	PP	100	40	62	Р
08	3. POWER	DEVICES AND MACHINES	PP	100	40	61	P C	18.	DATA STRUCTURES	PR	50	20	46	Р
09). NETWOR	K AND POWER LAB.	TW	50	20	42	P C	19.	COMMUNICATION THEORY	PP	100	40	67	Р
10). ELECTR	ONIC INSTRUMENTS AND TOO	LS TW	50	20	39	P C	20.	COMMUNICATION THEORY	OR	50	20	35	Р
								21.	CIRCUIT SIMULATION AND TOOLS	TW	50	20	40	Р

GRAND TOTAL = 1039/1500, RESULT: FIRST CLASS WITH DISTINCTION
ORDN. 1 MARKS :

s8053087	PRIYANKA TOMAR				SHA	SHI		, 7104558	33G , S8053087 ,	PICT		, s80	5308	7
01. SIGNAL	AND SYSTEMS	PP	100	40	77	P C	11.	ENGINEERING	G MATHEMATICS III	PP	100	40	51	Р
02. SIGNAL	AND SYSTEMS	OR	50	20	32	P C	12.	ENGINEERING	G MATHEMATICS III	TW	25	10	18	Р
03. SOLID	STATES DEVICES AND CIRCUIT	SPP	100	40	40	P C	13.	INTEGRATED	CIRCUITS APPLICATIONS Page 53	PP	100	40	40	Р

05. NETWORK ANALYSIS	PP	100									
		TOO	40	42	PC	15. ELECTROMAGNETIC	PP	100	40	54	Р
06. DIGITAL LOGIC DESIGN	PP	100	40	49	P C	16. ELECTROMAGNETIC	TW	25	10	11	Р
07. DIGITAL LOGIC DESIGN	PR	50	20	30	P C	17. DATA STRUCTURES	PP	100	40	61	Р
08. POWER DEVICES AND MACHINES	PP	100	40	40	P C	18. DATA STRUCTURES	PR	50	20	46	Р
09. NETWORK AND POWER LAB.	TW	50	20	38	P C	19. COMMUNICATION THEORY	PP	100	40	40	Р
10. ELECTRONIC INSTRUMENTS AND TOOL	S TW	50	20	44	P C	20. COMMUNICATION THEORY	OR	50	20	34	Р
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	26	Р
GRAND TOTAL = 838/1500, RESULT: HIGH ORDN. 1 MARKS :	ER SEC	COND CL	.ASS								
UNIVERSITY OF	 F PUNE	 ,S.E.	(2008	 PAT.	 .)(ELECTRO						
DATE : 18 AUG. 2011	CENT	RE : F	PUNE I	NSTI	TUTE OF CO	MPUTER TECHNOLOGY, PUNE.	PAG	SE NO.	30	(3	54)
NOTE: FIRST LINE : SEAT NO., NAME	OF THE	CAND1	DATE,	МО	THER, PERM	MANENT REG. NO., PREVIOUS SEAT NO.,	COLLEG	iΕ,	SEAT N	١٥.	
OTHER LINES: HEAD OF PASSING,	MAX.	MARKS	s, MI	N. P	ASS MARKS.	MARKS OBTAINED, P/F:PASS/FAIL, O	:PREVIC	NIS CAI	RRY O	/FR	
					,	, , , ,		705 CAI			
S8053089 ROOPAL OBEROI				 AMA		, 71045591н , ѕ8053089 ,			 , s80		 9
S8053089 ROOPAL OBEROI 01. SIGNAL AND SYSTEMS	 PP	100	40								
	PP OR	100	40		RJEET P C	, 71045591н , \$8053089 ,	PICT	-	 , s80)5308	Р
01. SIGNAL AND SYSTEMS	OR			63	RJEET P C	, 71045591H , S8053089 ,	PICTPP	100	 , s80 40		Р
01. SIGNAL AND SYSTEMS02. SIGNAL AND SYSTEMS	OR TSPP	50	20	63 32	RJEET PC PC	, 71045591H , S8053089 , 11. ENGINEERING MATHEMATICS III 12. ENGINEERING MATHEMATICS III	PICT PP TW ONS PP	100	, s80 40 10	 05308 60 21	P P
01. SIGNAL AND SYSTEMS02. SIGNAL AND SYSTEMS03. SOLID STATES DEVICES AND CIRCUIT	OR TSPP	50 100	20 40	63 32 48	RJEET PC PC PC	, 71045591H , S8053089 , 11. ENGINEERING MATHEMATICS III 12. ENGINEERING MATHEMATICS III 13. INTEGRATED CIRCUITS APPLICATION	PICT PP TW ONS PP	100 25 100	, s80 40 10 40	60 21 40	P P P
01. SIGNAL AND SYSTEMS02. SIGNAL AND SYSTEMS03. SOLID STATES DEVICES AND CIRCUI04. SOLID STATES DEVICES AND CIRCUI	OR TSPP TSPR	50 100 50	20 40 20	63 32 48 29 42	RJEET PC PC PC	, 71045591H , S8053089 , 11. ENGINEERING MATHEMATICS III 12. ENGINEERING MATHEMATICS III 13. INTEGRATED CIRCUITS APPLICATION 14. INTEGRATED CIRCUITS APPLICATION	PICT PP TW ONS PP	100 25 100 50	, s80 40 10 40 20	 05308 60 21 40 38	P P P
01. SIGNAL AND SYSTEMS02. SIGNAL AND SYSTEMS03. SOLID STATES DEVICES AND CIRCUI04. SOLID STATES DEVICES AND CIRCUI05. NETWORK ANALYSIS	OR TSPP TSPR PP	50 100 50 100	20 40 20 40	63 32 48 29 42	RJEET PC PC PC	, 71045591H , S8053089 , 11. ENGINEERING MATHEMATICS III 12. ENGINEERING MATHEMATICS III 13. INTEGRATED CIRCUITS APPLICATION 14. INTEGRATED CIRCUITS APPLICATION 15. ELECTROMAGNETIC	PICT PP TW ONS PP ONS PR PP	100 25 100 50 100	, s80 40 10 40 20 40	 05308 60 21 40 38 54	P P P P
 01. SIGNAL AND SYSTEMS 02. SIGNAL AND SYSTEMS 03. SOLID STATES DEVICES AND CIRCUI 04. SOLID STATES DEVICES AND CIRCUI 05. NETWORK ANALYSIS 06. DIGITAL LOGIC DESIGN 	OR TSPP TSPR PP	50 100 50 100 100	20 40 20 40 40	63 32 48 29 42 40	RJEET PC PC PC PC	, 71045591H , S8053089 , 11. ENGINEERING MATHEMATICS III 12. ENGINEERING MATHEMATICS III 13. INTEGRATED CIRCUITS APPLICATION 14. INTEGRATED CIRCUITS APPLICATION 15. ELECTROMAGNETIC 16. ELECTROMAGNETIC	PICT PP TW ONS PP ONS PR PP TW	100 25 100 50 100 25	, s80 40 10 40 20 40	 05308 60 21 40 38 54 17	P P P P P
<pre>01. SIGNAL AND SYSTEMS 02. SIGNAL AND SYSTEMS 03. SOLID STATES DEVICES AND CIRCUI 04. SOLID STATES DEVICES AND CIRCUI 05. NETWORK ANALYSIS 06. DIGITAL LOGIC DESIGN 07. DIGITAL LOGIC DESIGN</pre>	OR TSPP TSPR PP PR	50 100 50 100 100 50	20 40 20 40 40 20	63 32 48 29 42 40 24	RJEET PC PC PC PC	, 71045591H , S8053089 , 11. ENGINEERING MATHEMATICS III 12. ENGINEERING MATHEMATICS III 13. INTEGRATED CIRCUITS APPLICATION 14. INTEGRATED CIRCUITS APPLICATION 15. ELECTROMAGNETIC 16. ELECTROMAGNETIC 17. DATA STRUCTURES	PICT PP TW ONS PP ONS PR PP TW PP	100 25 100 50 100 25 100	, s80 40 10 40 20 40 10 40	 05308 60 21 40 38 54 17 47	P P P P P
<pre>01. SIGNAL AND SYSTEMS 02. SIGNAL AND SYSTEMS 03. SOLID STATES DEVICES AND CIRCUI 04. SOLID STATES DEVICES AND CIRCUI 05. NETWORK ANALYSIS 06. DIGITAL LOGIC DESIGN 07. DIGITAL LOGIC DESIGN 08. POWER DEVICES AND MACHINES</pre>	OR TSPP TSPR PP PR PR TW	50 100 50 100 100 50 100	20 40 20 40 40 20 40	63 32 48 29 42 40 24	RJEET PC PC PC PC PC	, 71045591H , S8053089 , 11. ENGINEERING MATHEMATICS III 12. ENGINEERING MATHEMATICS III 13. INTEGRATED CIRCUITS APPLICATION 14. INTEGRATED CIRCUITS APPLICATION 15. ELECTROMAGNETIC 16. ELECTROMAGNETIC 17. DATA STRUCTURES 18. DATA STRUCTURES	PICT PP TW ONS PP ONS PR PP TW PP	100 25 100 50 100 25 100 50	40 10 40 20 40 10 40 20	54 17 47 32	P P P P P
01. SIGNAL AND SYSTEMS 02. SIGNAL AND SYSTEMS 03. SOLID STATES DEVICES AND CIRCUI 04. SOLID STATES DEVICES AND CIRCUI 05. NETWORK ANALYSIS 06. DIGITAL LOGIC DESIGN 07. DIGITAL LOGIC DESIGN 08. POWER DEVICES AND MACHINES 09. NETWORK AND POWER LAB.	OR TSPP TSPR PP PR PR TW	50 100 50 100 100 50 100 50	20 40 20 40 40 20 40 20	63 32 48 29 42 40 24 43 40	RJEET PC PC PC PC PC	, 71045591H , S8053089 , 11. ENGINEERING MATHEMATICS III 12. ENGINEERING MATHEMATICS III 13. INTEGRATED CIRCUITS APPLICATION 14. INTEGRATED CIRCUITS APPLICATION 15. ELECTROMAGNETIC 16. ELECTROMAGNETIC 17. DATA STRUCTURES 18. DATA STRUCTURES 19. COMMUNICATION THEORY	PICT PP TW ONS PP ONS PR PP TW PP PR	100 25 100 50 100 25 100 50	40 40 40 40 40 40 40 40 40 40		P P P P P P

04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 28 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 37 P

GRAND TOTAL = 808/1500, RESULT: SECOND CLASS

ORDN. 1 MARKS:

S8053090 SAURABH SANJAY MANE			PALL	AVI	, 71045601J , S8053090 , PICT	, s8053090
01. SIGNAL AND SYSTEMS PP	100	40	74	P C	11. ENGINEERING MATHEMATICS III PP 100	40 52 P
02. SIGNAL AND SYSTEMS OR	50	20	35	P C	12. ENGINEERING MATHEMATICS III TW 25	10 17 P
03. SOLID STATES DEVICES AND CIRCUITSPP	100	40	47	P C	13. INTEGRATED CIRCUITS APPLICATIONS PP 100	40 51 P
04. SOLID STATES DEVICES AND CIRCUITSPR	50	20	33	P C	14. INTEGRATED CIRCUITS APPLICATIONS PR 50	20 34 P
05. NETWORK ANALYSIS PP	100	40	47	P C	15. ELECTROMAGNETIC PP 100	40 61 P
06. DIGITAL LOGIC DESIGN PP	100	40	49	P C	16. ELECTROMAGNETIC TW 25	10 18 P
07. DIGITAL LOGIC DESIGN PR	50	20	29	P C	17. DATA STRUCTURES PP 100	40 50 P
08. POWER DEVICES AND MACHINES PP	100	40	52	P C	18. DATA STRUCTURES PR 50	20 44 P
09. NETWORK AND POWER LAB. TW	50	20	42	P C	19. COMMUNICATION THEORY PP 100	40 63 P
10. ELECTRONIC INSTRUMENTS AND TOOLS TW	50	20	42	P C	20. COMMUNICATION THEORY OR 50	20 22 P
					21. CIRCUIT SIMULATION AND TOOLS TW 50	20 37 P

GRAND TOTAL = 899+01/1500, RESULT: FIRST CLASS [0.2]
ORDN. 1 MARKS:

S8053091 SAYED SAIF IFTEKAR				SHA	KILA	, 71045604C , S8053091 , PICT , S8053091
01. SIGNAL AND SYSTEMS	PP	100	40	48	P C	11. ENGINEERING MATHEMATICS III PP 100 40 43 P
02. SIGNAL AND SYSTEMS	OR	50	20	27	P C	12. ENGINEERING MATHEMATICS III TW 25 10 20 P
03. SOLID STATES DEVICES AND CIRCUIT	ГЅРР	100	40	42	Р	13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 14 F
04. SOLID STATES DEVICES AND CIRCUIT	ΓSPR	50	20	36	P C	14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 43 P
05. NETWORK ANALYSIS	PP	100	40	40	Р	15. ELECTROMAGNETIC PP 100 40 48 P
06. DIGITAL LOGIC DESIGN	PP	100	40	40	PС	16. ELECTROMAGNETIC TW 25 10 17 P
07. DIGITAL LOGIC DESIGN	PR	50	20	30	P C	17. DATA STRUCTURES PP 100 40 49 P
08. POWER DEVICES AND MACHINES	PP	100	40	40	P C	18. DATA STRUCTURES PR 50 20 45 P

TW	50	20	41	P C	19. COMMUNICATION THEORY	PP	100	40	40	Р
S TW	50	20	41	PС	20. COMMUNICATION THEORY	OR	50	20	28	Р
					21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	37	Р
S A.T	.к.т.									
 F PUNE	 ,S.E.	 (2008	 PAT	 .)(ELEC						
CEN	ΓRE :	PUNE]	INSTI	TUTE O	COMPUTER TECHNOLOGY, PUNE.	PA	GE NO.	31	(3	55)
OF THI	E CAND	IDATE,	, MC	THER,	ERMANENT REG. NO., PREVIOUS SEAT NO.,	COLLE	GE,	SEAT	١0.	
MAX	. MARK	5, MI	EN. F	PASS MAI	KS, MARKS OBTAINED, P/F:PASS/FAIL, C:	PREVI	OUS CA	RRY O'	√ER	
			D.E.I		71120027- 60052002	DIC	-	6.0	25200	
			KEF	IANA	, /1129937E , \$8053092 ,	PIC	ı	, 58	J5309	12
PP	100	40	83	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	72	Р
OR	50	20	38	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	21	Р
TSPP	100	40	62	P C	13. INTEGRATED CIRCUITS APPLICATION	S PP	100	40	53	Р
TSPR	50	20	39	P C	14. INTEGRATED CIRCUITS APPLICATION	S PR	50	20	37	Р
PP	100	40	42	P C	15. ELECTROMAGNETIC	PP	100	40	61	Р
PP	100	40	70	P C	16. ELECTROMAGNETIC	TW	25	10	22	Р
PR	50	20	32	PС	17. DATA STRUCTURES	PP	100	40	66	Р
PP	100	40	53	РС	18. DATA STRUCTURES	PR	50	20	45	Р
TW	50	20	39	РС	19. COMMUNICATION THEORY	PP	100	40	61	Р
	Ε0	20	43	РС	20. COMMUNICATION THEORY	OR	50	20	38	Р
S TW	50	20								
	S A.T F PUNE CENT OF THI MAX PP OR TSPP TSPR PP PR PP	PP 100	PP 100 40 OR 50 20 PP 100 40 OR 50 20 PP 100 40 PP 100 40	S A.T.K.T. F PUNE ,S.E. (2008 PAT CENTRE : PUNE INSTITUTE CANDIDATE, MC MAX. MARKS, MIN. FREE PP 100 40 83 OR 50 20 38 CTSPP 100 40 62 CTSPR 50 20 39 PP 100 40 42 PP 100 40 70 PR 50 20 32 PP 100 40 53	S A.T.K.T. F PUNE ,S.E. (2008 PAT.) (ELECT CENTRE : PUNE INSTITUTE OF	21. CIRCUIT SIMULATION AND TOOLS S A.T.K.T. F PUNE ,S.E.(2008 PAT.)(ELECTRONICS & TELECOM.) CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C: REHANA , 71129937E , S8053092 , PP 100 40 83 P C 11. ENGINEERING MATHEMATICS III OR 50 20 38 P C 12. ENGINEERING MATHEMATICS III TSPP 100 40 62 P C 13. INTEGRATED CIRCUITS APPLICATION TTSPR 50 20 39 P C 14. INTEGRATED CIRCUITS APPLICATION PP 100 40 42 P C 15. ELECTROMAGNETIC PP 100 40 70 P C 16. ELECTROMAGNETIC PR 50 20 32 P C 17. DATA STRUCTURES PP 100 40 53 P C 18. DATA STRUCTURES	21. CIRCUIT SIMULATION AND TOOLS TW S A.T.K.T. F PUNE ,S.E.(2008 PAT.)(ELECTRONICS & TELECOM.) CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PA OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLE MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVI REHANA , 71129937E , S8053092 , PIC OR 50 20 38 P C 11. ENGINEERING MATHEMATICS III PP OR 50 20 38 P C 12. ENGINEERING MATHEMATICS III TW ITSPP 100 40 62 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP TSPR 50 20 39 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR PP 100 40 70 P C 16. ELECTROMAGNETIC PP PP 100 40 70 P C 16. ELECTROMAGNETIC TW PR 50 20 32 P C 17. DATA STRUCTURES PP	21. CIRCUIT SIMULATION AND TOOLS TW 50 S A.T.K.T. F PUNE ,S.E. (2008 PAT.) (ELECTRONICS & TELECOM.) CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CA REHANA , 71129937E , S8053092 , PICT PP 100 40 83 P C 11. ENGINEERING MATHEMATICS III PP 100 OR 50 20 38 P C 12. ENGINEERING MATHEMATICS III TW 25 TSPP 100 40 62 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 TSPR 50 20 39 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 PP 100 40 42 P C 15. ELECTROMAGNETIC PP 100 PP 100 40 70 P C 16. ELECTROMAGNETIC PP 100 PP 100 40 70 P C 16. ELECTROMAGNETIC TW 25 PR 50 20 32 P C 17. DATA STRUCTURES PP 100 PP 100 40 53 P C 18. DATA STRUCTURES PR 50	21. CIRCUIT SIMULATION AND TOOLS TW 50 20 S A.T.K.T. F PUNE ,S.E. (2008 PAT.) (ELECTRONICS & TELECOM.) CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 31 OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO., MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OF NO., COLLEGE, SEAT NO., COLLEGE, SEAT NO., COLLEGE, SEAT NO., MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OF NO., COLLEGE, SEAT	21. CIRCUIT SIMULATION AND TOOLS TW 50 20 37 S A.T.K.T. F PUNE ,S.E.(2008 PAT.)(ELECTRONICS & TELECOM.) CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 31 (3 OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER REHANA , 71129937E , S8053092 , PICT , S805309 PP 100 40 83 P C 11. ENGINEERING MATHEMATICS III PP 100 40 72 OR 50 20 38 P C 12. ENGINEERING MATHEMATICS III TW 25 10 21 ITSPP 100 40 62 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 53 TSPR 50 20 39 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 37 PP 100 40 42 P C 15. ELECTROMAGNETIC PP 100 40 61 PP 100 40 70 P C 16. ELECTROMAGNETIC TW 25 10 22 PR 50 20 32 P C 17. DATA STRUCTURES PP 100 40 66 PP 100 40 53 P C 18. DATA STRUCTURES PR 50 20 45

s8053093	SHELKE MINAXI BHARAT				PUTI	JLA			cols05 , 71129938C , S8053093 ,	PICT		, s805	3093	3
01. SIGNAL	AND SYSTEMS	PP	100	40	49	РС		11.	ENGINEERING MATHEMATICS III	PP	100	40	53	Р
02. SIGNAL	AND SYSTEMS	OR	50	20	26	РС	:	12.	ENGINEERING MATHEMATICS III	TW	25	10	23	Р
03. SOLID	STATES DEVICES AND CIRCUIT	SPP	100	40	45	РС	:	13.	INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	40	Р
04. SOLID	STATES DEVICES AND CIRCUIT	SPR	50	20	25	P C	:	14.	INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	30	Р
05. NETWOR	K ANALYSIS	PP	100	40	45	P C	:	15.	ELECTROMAGNETIC	PP	100	40	66	Р
06. DIGITA	L LOGIC DESIGN	PP	100	40	40	P C	:	16.	ELECTROMAGNETIC	TW	25	10	19	Р
07. DIGITA	L LOGIC DESIGN	PR	50	20	20	P C		17.	DATA STRUCTURES	PP	100	40	51	Р
08. POWER	DEVICES AND MACHINES	PP	100	40	63	P C		18.	DATA STRUCTURES	PR	50	20	36	Р
09. NETWOR	K AND POWER LAB.	TW	50	20	40	P C		19.	COMMUNICATION THEORY	PP	100	40	51	Р
10. ELECTR	ONIC INSTRUMENTS AND TOOLS	TW	50	20	37	P C	;	20.	COMMUNICATION THEORY	OR	50	20	30	Р
							:	21.	CIRCUIT SIMULATION AND TOOLS	TW	50	20	41	Р

GRAND TOTAL = 830/1500, RESULT: HIGHER SECOND CLASS
ORDN. 1 MARKS :

S8053094 SHENDKAR SUSHMA HANUMANT			REKHA	A	, 71129939м , ѕ8053094 ,	PICT		, s80	5309	4
01. SIGNAL AND SYSTEMS PP	100	40	55 F	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	68	Р
02. SIGNAL AND SYSTEMS OR	50	20	28 F	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	20	Р
03. SOLID STATES DEVICES AND CIRCUITSPP	100	40	40 F	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	43	Р
04. SOLID STATES DEVICES AND CIRCUITSPR	50	20	21 F	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	30	Р
05. NETWORK ANALYSIS PP	100	40	48 F	Р	15. ELECTROMAGNETIC	PP	100	40	45	Р
06. DIGITAL LOGIC DESIGN PP	100	40	43 F	P C	16. ELECTROMAGNETIC	TW	25	10	16	Р
07. DIGITAL LOGIC DESIGN PR	50	20	35 F	Р	17. DATA STRUCTURES	PP	100	40	50	Р
08. POWER DEVICES AND MACHINES PP	100	40	53 F	P C	18. DATA STRUCTURES	PR	50	20	25	Р
09. NETWORK AND POWER LAB. TW	50	20	38 F	P C	19. COMMUNICATION THEORY	PP	100	40	40	Р
10. ELECTRONIC INSTRUMENTS AND TOOLS TW	50	20	43 F	P C	20. COMMUNICATION THEORY	OR	50	20	30	Р
					21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	38	Р

GRAND TOTAL = 809/1500, RESULT: SECOND CLASS

ORDN. 1 MARKS:

DATE - 10 AUG 2011)(ELECTRO		TECHNOLOGY BUNE	D 4 6	·	22	(3	F.C.)
DATE : 18 AUG. 2011	CEN	IRE : I	ONE .	INSII	TOTE OF CO)MPU I EI	R TECHNOLOGY, PUNE.	PAG	iE NO.	32	(3	56)
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											• •	
S8053095 SHINDE MANOJ SURESH				MAN	JUSHRI		, 71045615J , S8053095 ,	PICT	-	, s80)5309	5
01. SIGNAL AND SYSTEMS	PP	100	40	78	P C	11.	ENGINEERING MATHEMATICS III	PP	100	40	76	Р
02. SIGNAL AND SYSTEMS	OR	50	20	33	P C	12.	ENGINEERING MATHEMATICS III	TW	25	10	19	Р
03. SOLID STATES DEVICES AND CIRCUIT	SPP	100	40	64	P C	13.	INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	44	Р
04. SOLID STATES DEVICES AND CIRCUIT	SPR	50	20	36	P C	14.	INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	25	Р
05. NETWORK ANALYSIS	PP	100	40	41	P C	15.	ELECTROMAGNETIC	PP	100	40	62	Р
06. DIGITAL LOGIC DESIGN	PP	100	40	64	P C	16.	ELECTROMAGNETIC	TW	25	10	13	Р
07. DIGITAL LOGIC DESIGN	PR	50	20	29	P C	17.	DATA STRUCTURES	PP	100	40	62	Р
08. POWER DEVICES AND MACHINES	PP	100	40	55	P C	18.	DATA STRUCTURES	PR	50	20	42	Р
09. NETWORK AND POWER LAB.	TW	50	20	41	P C	19.	COMMUNICATION THEORY	PP	100	40	50	Р
10. ELECTRONIC INSTRUMENTS AND TOOLS	5 TW	50	20	34	P C	20.	COMMUNICATION THEORY	OR	50	20	29	Р
						21.	CIRCUIT SIMULATION AND TOOLS	TW	50	20	31	Р
GRAND TOTAL = 928/1500, RESULT: FIRST	CLAS	SS										
ORDN. 1 MARKS :												
S8053096 SHITOLE JAYESH DASHRATH				SUL	OCHANA		, 71129940E , S8053096 ,	PICT	-	, 580)5309	6
01. SIGNAL AND SYSTEMS	PP	100	40	40	P C	11.	ENGINEERING MATHEMATICS III	PP	100	40	40	Р
02. SIGNAL AND SYSTEMS	OR	50	20	22	Р	12.	ENGINEERING MATHEMATICS III	TW	25	10	20	Р
03. SOLID STATES DEVICES AND CIRCUIT	SPP	100	40	56	Р	13.	INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	44	Р
04. SOLID STATES DEVICES AND CIRCUIT	SPR	50	20	20	P C	14.	INTEGRATED CIRCUITS APPLICATIONS Page 58	PR	50	20	30	Р

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05	5. NETWORK ANALYSIS	PP	100	40	40	Р	15. ELECTROMAGNETIC	PP	100	40	47	Р
06	5. DIGITAL LOGIC DESIGN	PP	100	40	45	PC	16. ELECTROMAGNETIC	TW	25	10	16	Р
07	7. DIGITAL LOGIC DESIGN	PR	50	20	30	PC	17. DATA STRUCTURES	PP	100	40	40	Р
08	3. POWER DEVICES AND MACHINES	PP	100	40	40	PC	18. DATA STRUCTURES	PR	50	20	34	Р
09	. NETWORK AND POWER LAB.	TW	50	20	40	PC	19. COMMUNICATION THEORY	PP	100	40	40	Р
10). ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	38	P C	20. COMMUNICATION THEORY	OR	50	20	29	Р
							21. CIRCUIT SIMULATION AND TOOL	.S TW	50	20	41	Р

GRAND TOTAL = 752/1500, RESULT: SECOND CLASS

ORDN. 1 MARKS:

S8053097 SIDDHARTHA BANGA				SUMEDHA RANI	ANI , 71045625F , S8053097 ,			, s80	5309	7
01. SIGNAL AND SYSTEMS	PP	100	40	40 P C	11. ENGINEERING MATHEMATICS III	PP 1	100	40	40	Р
02. SIGNAL AND SYSTEMS	OR	50	20	22 P C	12. ENGINEERING MATHEMATICS III	TW	25	10	19	Р
03. SOLID STATES DEVICES AND CIRC	JITSPP	100	40	40 P	13. INTEGRATED CIRCUITS APPLICATIONS	PP 1	100	40	40	Р
04. SOLID STATES DEVICES AND CIRC	JITSPR	50	20	34 P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	34	Р
05. NETWORK ANALYSIS	PP	100	40	40 P	15. ELECTROMAGNETIC	PP 1	100	40	40	Р
06. DIGITAL LOGIC DESIGN	PP	100	40	44 P C	16. ELECTROMAGNETIC	TW	25	10	17	Р
07. DIGITAL LOGIC DESIGN	PR	50	20	28 P C	17. DATA STRUCTURES	PP 1	100	40	56	Р
08. POWER DEVICES AND MACHINES	PP	100	40	40 P C	18. DATA STRUCTURES	PR	50	20	35	Р
09. NETWORK AND POWER LAB.	TW	50	20	34 P C	19. COMMUNICATION THEORY	PP 1	100	40	19	F
10. ELECTRONIC INSTRUMENTS AND TO	OLS TW	50	20	34 P C	20. COMMUNICATION THEORY	OR	50	20	31	Р
					21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	36	Р

GRAND TOTAL = 723/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS:

UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(ELECTRONICS & TELECOM.)

DATE: 18 AUG. 2011 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 33 (357)

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NOTE:	FIRST LINE	E : SEAT NO.,	NAME OF THE	CANDID	ATE,	МОТН	ER, PERMA	NENT	REG. NO.,	PREVIOUS	SEAT NO., CO	OLLEGE	:, S	EAT NO	Ο.	
	OTHER LINE	ES: HEAD OF PA	ASSING, MAX.	MARKS,	MIN	. PAS	S MARKS,	MAR	KS OBTAINED,	P/F:PAS	SS/FAIL, C:PI	REVIOL	IS CARI	RY OVI	ΞR	
S8053	098 SONAN	NANE NIKHIL B	HIMRAO			SUNAN	DA		, 7104563	320 ,	S8053098 ,	PICT		, s80	53098	3
01. S	IGNAL AND S	SYSTEMS	PP	100	40	28 F		11.	ENGINEERING	; МАТНЕМАТ	ICS III	PP	100	40	AA	F
02. S	IGNAL AND S	SYSTEMS	OR	50	20	14 F		12.	ENGINEERING	мАТНЕМАТ	ICS III	TW	25	10	10	Р
03. S	OLID STATES	S DEVICES AND	CIRCUITSPP	100	40	AA F		13.	INTEGRATED	CIRCUITS	APPLICATIONS	PP	100	40	AA	F
04. S	OLID STATES	S DEVICES AND	CIRCUITSPR	50	20	15 F		14.	INTEGRATED	CIRCUITS	APPLICATIONS	PR	50	20	AA	F
05. N	ETWORK ANAI	LYSIS	PP	100	40	AA F	•	15.	ELECTROMAGN	IETIC		PP	100	40	AA	F
06. D	IGITAL LOG	IC DESIGN	PP	100	40	AA F	•	16.	ELECTROMAGN	IETIC		TW	25	10	10	Р
07. D	IGITAL LOG	IC DESIGN	PR	50	20	30 P	С	17.	DATA STRUCT	URES		PP	100	40	AA	F

GRAND TOTAL = 224/1500, RESULT: FAILS

RESULT RESERVED FOR BKLG

40 P C

27 P C

20

50 20 30 P C

50 20 35 P C

18. DATA STRUCTURES

19. COMMUNICATION THEORY

20. COMMUNICATION THEORY

19. COMMUNICATION THEORY

Page 60

21. CIRCUIT SIMULATION AND TOOLS

50 20 AA F

40

20

50 20 20 P

AA F

100

100

40

40 P

50

ORDN. 1 MARKS:

08. POWER DEVICES AND MACHINES

10. ELECTRONIC INSTRUMENTS AND TOOLS TW

09. NETWORK AND POWER LAB.

09. NETWORK AND POWER LAB.

S8053099 SONU SINGH , 71045633G , s8053099 , , s8053099 USHA PICT 01. SIGNAL AND SYSTEMS 100 40 P C 11. ENGINEERING MATHEMATICS III 100 40 40 P 02. SIGNAL AND SYSTEMS 50 20 32 P C 12. ENGINEERING MATHEMATICS III 10 17 P 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 31 F 100 40 18 F 13. INTEGRATED CIRCUITS APPLICATIONS PP 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 20 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 34 P 100 05. NETWORK ANALYSIS 100 40 28 F 15. ELECTROMAGNETIC 40 31 F 06. DIGITAL LOGIC DESIGN 100 40 40 P C 16. ELECTROMAGNETIC 25 10 TW 16 P 07. DIGITAL LOGIC DESIGN 50 20 29 P C 17. DATA STRUCTURES 100 40 41 P 08. POWER DEVICES AND MACHINES 31 F 18. DATA STRUCTURES 50 20 35 P

					cols05
10. ELECTRONIC INSTRUMENTS AND TOOLS TW	50	20	33	P C	20. COMMUNICATION THEORY OR 50 20 29 P
					21. CIRCUIT SIMULATION AND TOOLS TW 50 20 35 P
CDAND TOTAL					
GRAND TOTAL = 655/1500, RESULT: FAILS					
ORDN. 1 MARKS :					
			• •		
S8053100 SUBAGHYA MAHAJAN			VIJ	AY	, 71045635C , S8053100 , PICT , S8053100
01. SIGNAL AND SYSTEMS PP	100	40	49	P C	11. ENGINEERING MATHEMATICS III PP 100 40 46 P
02. SIGNAL AND SYSTEMS OR	50	20	43	P C	12. ENGINEERING MATHEMATICS III TW 25 10 16 P
03. SOLID STATES DEVICES AND CIRCUITSPP	100	40	49	Р	13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 P
04. SOLID STATES DEVICES AND CIRCUITSPR	50	20	32	P C	14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 45 P
05. NETWORK ANALYSIS PP	100	40	40	P C	15. ELECTROMAGNETIC PP 100 40 52 P
06. DIGITAL LOGIC DESIGN PP	100	40	50	P C	16. ELECTROMAGNETIC TW 25 10 11 P
07. DIGITAL LOGIC DESIGN PR	50	20	29	P C	17. DATA STRUCTURES PP 100 40 49 P
08. POWER DEVICES AND MACHINES PP	100	40	52	P C	18. DATA STRUCTURES PR 50 20 47 P
09. NETWORK AND POWER LAB. TW	50	20	35	P C	19. COMMUNICATION THEORY PP 100 40 41 P
10. ELECTRONIC INSTRUMENTS AND TOOLS TW	50	20	38	P C	20. COMMUNICATION THEORY OR 50 20 30 P
					21. CIRCUIT SIMULATION AND TOOLS TW 50 20 30 P
GRAND TOTAL = 824+01/1500, RESULT: HIGHE	D SECON		cc F	0 21	
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ORDN. I MARKS .					
UNIVERSITY OF PUN	E,S.E	(2008	PAT	.)(ELE	TRONICS & TELECOM.)
DATE : 18 AUG. 2011 CE	NTRE :	PUNE 1	INSTI	TUTE (F COMPUTER TECHNOLOGY, PUNE. PAGE NO. 34 (358)
NOTE: FIRST LINE : SEAT NO., NAME OF T	HE CAND	IDATE	, MO	THER,	PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
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, 71045637к , S8053101 , РІСТ

, s8053101

01.	SIGNAL AND SYSTEMS	PP	100	40	62	P C	11.	ENGINEERING MATHEMATICS III	PP	100	40	62	Р
02.	SIGNAL AND SYSTEMS	OR	50	20	40	P C	12.	ENGINEERING MATHEMATICS III	TW	25	10	17	Р
03.	SOLID STATES DEVICES AND CIRCUIT	SPP	100	40	40	P C	13.	INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	42	Р
04.	SOLID STATES DEVICES AND CIRCUIT	SPR	50	20	32	P C	14.	INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	31	Р
05.	NETWORK ANALYSIS	PP	100	40	49	Р	15.	ELECTROMAGNETIC	PP	100	40	42	Р
06.	DIGITAL LOGIC DESIGN	PP	100	40	68	P C	16.	ELECTROMAGNETIC	TW	25	10	17	Р
07.	DIGITAL LOGIC DESIGN	PR	50	20	25	P C	17.	DATA STRUCTURES	PP	100	40	48	Р
08.	POWER DEVICES AND MACHINES	PP	100	40	53	P C	18.	DATA STRUCTURES	PR	50	20	34	Р
09.	NETWORK AND POWER LAB.	TW	50	20	37	P C	19.	COMMUNICATION THEORY	PP	100	40	40	Р
10.	ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	40	P C	20.	COMMUNICATION THEORY	OR	50	20	30	Р
							21.	CIRCUIT SIMULATION AND TOOLS	TW	50	20	37	Р

GRAND TOTAL = 846/1500, RESULT: HIGHER SECOND CLASS
ORDN. 1 MARKS :

s8053	3102 TELI REKHA MALLAPPA				KAST	ΓURI		, 710456430	ο ,	s8053102 ,	PICT		, s80	5310	2
01. 9	SIGNAL AND SYSTEMS	PP	100	40	83	P C	11.	ENGINEERING N	МАТНЕМАТ	ICS III	PP	100	40	79	Р
02. 9	SIGNAL AND SYSTEMS	OR	50	20	40	РС	12.	ENGINEERING N	МАТНЕМАТ	TCS III	TW	25	10	22	Р
03. 9	SOLID STATES DEVICES AND CIRC	CUITSPP	100	40	40	P C	13.	INTEGRATED C	IRCUITS	APPLICATIONS	PP	100	40	47	Р
04. 9	SOLID STATES DEVICES AND CIRC	CUITSPR	50	20	35	P C	14.	INTEGRATED C	IRCUITS	APPLICATIONS	PR	50	20	31	Р
05. 1	NETWORK ANALYSIS	PP	100	40	40	P C	15.	ELECTROMAGNET	TIC		PP	100	40	63	Р
06. [DIGITAL LOGIC DESIGN	PP	100	40	56	P C	16.	ELECTROMAGNET	TIC		TW	25	10	19	Р
07. [DIGITAL LOGIC DESIGN	PR	50	20	30	P C	17.	DATA STRUCTUR	RES		PP	100	40	74	Р
08. F	POWER DEVICES AND MACHINES	PP	100	40	40	P C	18.	DATA STRUCTUR	RES		PR	50	20	41	Р
09. 1	NETWORK AND POWER LAB.	TW	50	20	40	P C	19.	COMMUNICATION	N THEORY	/	PP	100	40	50	Р
10. E	ELECTRONIC INSTRUMENTS AND TO	OOLS TW	50	20	40	P C	20.	COMMUNICATION	N THEORY	′	OR	50	20	29	Р
							21.	CIRCUIT SIMUL	LATION A	AND TOOLS	TW	50	20	36	Р

GRAND TOTAL = 935/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS:

S8053103 TEMGIRE PRASAD SANTU	Т			TAI	TAI		, 71045644B ,	s8053103 ,	PICT		, s80	53103	3
01. SIGNAL AND SYSTEMS	PP	100	40	87	P C	11.	ENGINEERING MATHEMAT	TICS III	PP	100	40	84	Р
02. SIGNAL AND SYSTEMS	OR	50	20	45	P C	12.	ENGINEERING MATHEMAT	ICS III	TW	25	10	24	Р
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	62	P C	13.	INTEGRATED CIRCUITS	APPLICATIONS	PP	100	40	66	Р
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	40	P C	14.	INTEGRATED CIRCUITS	APPLICATIONS	PR	50	20	43	Р
05. NETWORK ANALYSIS	PP	100	40	55	P C	15.	ELECTROMAGNETIC		PP	100	40	80	Р
06. DIGITAL LOGIC DESIGN	PP	100	40	83	P C	16.	ELECTROMAGNETIC		TW	25	10	22	Р
07. DIGITAL LOGIC DESIGN	PR	50	20	31	P C	17.	DATA STRUCTURES		PP	100	40	50	Р
08. POWER DEVICES AND MACHINES	PP	100	40	57	P C	18.	DATA STRUCTURES		PR	50	20	44	Р
09. NETWORK AND POWER LAB.	TW	50	20	44	P C	19.	COMMUNICATION THEORY	,	PP	100	40	59	Р
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	43	P C	20.	COMMUNICATION THEORY	•	OR	50	20	35	Р
						21.	CIRCUIT SIMULATION A	ND TOOLS	TW	50	20	43	Р
UNIVERSITY OF	PUNE	,S.E.(2008	PAT.)(ELECTRON:	CS &							
DATE : 18 AUG. 2011	CENTI	KE : PI	JNE I	NSII	TOTE OF COM	PUTER	R TECHNOLOGY, PUNE.		PAGI	E NO.	33	(33	19)
NOTE: FIRST LINE : SEAT NO., NAME OF OTHER LINES: HEAD OF PASSING,			•		-		·			•	EAT NO		
S8053104 THAKARE BHAGYASHREE RAJEND)RA			SUN	ITA		, 71045645L ,	S8053104 ,	PICT		, s80	53104	1
01. SIGNAL AND SYSTEMS	PP	100	40	23	F	11.	ENGINEERING MATHEMAT	ICS III	PP	100	40	40	Р
02. SIGNAL AND SYSTEMS	OR	50	20	20	P C	12.	ENGINEERING MATHEMAT	ICS III	TW	25	10	21	Р
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	29	F	13.	INTEGRATED CIRCUITS	APPLICATIONS	PP	100	40	25	F
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	20	P	14.	INTEGRATED CIRCUITS	APPLICATIONS	PR	50	20	30	Р
05. NETWORK ANALYSIS	PP	100	40	12	F	15.	ELECTROMAGNETIC Page	63	PP	100	40	07	F

06. DIGITAL LOGIC DESIGN	PP	100	40	11	F	16.	ELECTROMAGNETIC	TW	25	10	15	Р
07. DIGITAL LOGIC DESIGN	PR	50	20	28	P C	17.	DATA STRUCTURES	PP	100	40	57	Р
08. POWER DEVICES AND MACHINES	PP	100	40	29	F	18.	DATA STRUCTURES	PR	50	20	44	Р
09. NETWORK AND POWER LAB.	TW	50	20	37	P C	19.	COMMUNICATION THEORY	PP	100	40	23	F
10. ELECTRONIC INSTRUMENTS AND TOOL	S TW	50	20	39	P C	20.	COMMUNICATION THEORY	OR	50	20	20	Р
						21.	CIRCUIT SIMULATION AND TOOLS	TW	50	20	35	Р
GRAND TOTAL = 565/1500, RESULT: FAIL	S							RESUI	_T RES	SERVED	FOR	BKLG
ORDN. 1 MARKS :												
S8053105 THORAT TANVI TATYASAHEB				SUL	OCHAN	4	, 71045650G , S8053105 ,	PICT		, s80	5310	5
01. SIGNAL AND SYSTEMS	PP	100	40	55	P C	11.	ENGINEERING MATHEMATICS III	PP	100	40	75	Р
02. SIGNAL AND SYSTEMS	OR	50	20	30	P C	12.	ENGINEERING MATHEMATICS III	TW	25	10	22	Р
03. SOLID STATES DEVICES AND CIRCUI	TSPP	100	40	56	P C	13.	INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	51	Р
04. SOLID STATES DEVICES AND CIRCUI	TSPR	50	20	36	P C	14.	INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	37	Р
05. NETWORK ANALYSIS	PP	100	40	43	P C	15.	ELECTROMAGNETIC	PP	100	40	54	Р
06. DIGITAL LOGIC DESIGN	PP	100	40	52	P C	16.	ELECTROMAGNETIC	TW	25	10	22	Р
07. DIGITAL LOGIC DESIGN	PR	50	20	34	P C	17.	DATA STRUCTURES	PP	100	40	47	Р
08. POWER DEVICES AND MACHINES	PP	100	40	45	P C	18.	DATA STRUCTURES	PR	50	20	46	Р
09. NETWORK AND POWER LAB.	TW	50	20	37	P C	19.	COMMUNICATION THEORY	PP	100	40	67	Р
10. ELECTRONIC INSTRUMENTS AND TOOL	S TW	50	20	40	P C	20.	COMMUNICATION THEORY	OR	50	20	35	Р
						21.	CIRCUIT SIMULATION AND TOOLS	TW	50	20	37	Р
GRAND TOTAL = 921/1500, RESULT: FIRS	T CLA	SS										
ORDN. 1 MARKS :												
S8053106 VYAS HARIKESH PRADEEP				ВНА	VANA		, 71045661B , S8053106 ,	PICT		, s80	5310	5
01. SIGNAL AND SYSTEMS	PP	100	40	78	P C	11.	ENGINEERING MATHEMATICS III	PP	100	40	60	Р

						cols05
02. SIGNAL AND SYSTEMS	OR	50	20	35	P C	12. ENGINEERING MATHEMATICS III TW 25 10 20 P
03. SOLID STATES DEVICES AND CIR	RCUITSPP	100	40	41	P C	13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 47 P
04. SOLID STATES DEVICES AND CIR	RCUITSPR	50	20	20	P C	14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 42 P
05. NETWORK ANALYSIS	PP	100	40	40	P C	15. ELECTROMAGNETIC PP 100 40 41 P
06. DIGITAL LOGIC DESIGN	PP	100	40	49	P C	16. ELECTROMAGNETIC TW 25 10 18 P
07. DIGITAL LOGIC DESIGN	PR	50	20	34	P C	17. DATA STRUCTURES PP 100 40 60 P
08. POWER DEVICES AND MACHINES	PP	100	40	53	P C	18. DATA STRUCTURES PR 50 20 47 P
09. NETWORK AND POWER LAB.	TW	50	20	41	P C	19. COMMUNICATION THEORY PP 100 40 54 P
10. ELECTRONIC INSTRUMENTS AND	TOOLS TW	50	20	42	P C	20. COMMUNICATION THEORY OR 50 20 29 P
						21. CIRCUIT SIMULATION AND TOOLS TW 50 20 40 P

GRAND TOTAL = 891+09/1500, RESULT: FIRST CLASS [0.2]
ORDN. 1 MARKS :

UNIVERSITY OF	PUNE	, S.E.(2008	PAT.) (ELECTRON	ics &	TELECOM.)					
DATE : 18 AUG. 2011	CENT	RE : PI	JNE IN	ISTI	TUTE OF COM	ИРUТЕ	R TECHNOLOGY, PUNE.	PAGE	E NO.	36	(3	60)
NOTE: FIRST LINE : SEAT NO., NAME O	F THE	CANDII	DATE,	MO	THER, PERMA	ANENT	REG. NO., PREVIOUS SEAT NO., CO	OLLEGE	Ξ,	SEAT N	10.	
OTHER LINES: HEAD OF PASSING,	MAX.	MARKS	, MIN	1. P	ASS MARKS,	MAR	KS OBTAINED, P/F:PASS/FAIL, C:PI	REVIOL	JS CAI	RRY OV	/ER	
S8053107 WAGHMARE ABHISHEK SUBHASH	I			VID	YA		, 71045662L , S8053107 ,	PICT		, s80)5310	7
01. SIGNAL AND SYSTEMS	PP	100	40	41	P C	11.	ENGINEERING MATHEMATICS III	PP	100	40	47	Р
02. SIGNAL AND SYSTEMS	OR	50	20	20	P C	12.	ENGINEERING MATHEMATICS III	TW	25	10	20	Р
03. SOLID STATES DEVICES AND CIRCUIT	SPP	100	40	40	P C	13.	INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	31	F
04. SOLID STATES DEVICES AND CIRCUIT	SPR	50	20	35	P C	14.	INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	30	Р
05. NETWORK ANALYSIS	PP	100	40	40	Р	15.	ELECTROMAGNETIC	PP	100	40	40	Р
06. DIGITAL LOGIC DESIGN	PP	100	40	31	F	16.	ELECTROMAGNETIC	TW	25	10	17	Р
07. DIGITAL LOGIC DESIGN	PR	50	20	30	P C	17.	DATA STRUCTURES	PP	100	40	40	Р
08. POWER DEVICES AND MACHINES	PP	100	40	40	P C	18.	DATA STRUCTURES	PR	50	20	38	Р
09. NETWORK AND POWER LAB.	TW	50	20	43	P C	19.	COMMUNICATION THEORY	PP	100	40	40	Р
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	42	P C	20.	COMMUNICATION THEORY Page 65	OR	50	20	27	Р

21. CIRCUIT SIMULATION AND TOOLS TW 50 20 41 P

GRAND TOTAL = 733/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS:

S8053108 WATHORE SANKET UTTAMRAO			KAVITA		, 71045665E , S8053108 , PICT , S8053108	
01. SIGNAL AND SYSTEMS PP	100	40	41	P C	11. ENGINEERING MATHEMATICS III PP 100 40 49 P	
02. SIGNAL AND SYSTEMS OR	50	20	31	P C	12. ENGINEERING MATHEMATICS III TW 25 10 19 P	
03. SOLID STATES DEVICES AND CIRCUITSPP	100	40	40	P C	13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 40 P	
04. SOLID STATES DEVICES AND CIRCUITSPR	50	20	30	P C	14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 28 P	
05. NETWORK ANALYSIS PP	100	40	47	Р	15. ELECTROMAGNETIC PP 100 40 51 P	
06. DIGITAL LOGIC DESIGN PP	100	40	40	Р	16. ELECTROMAGNETIC TW 25 10 13 P	
07. DIGITAL LOGIC DESIGN PR	50	20	24	P C	17. DATA STRUCTURES PP 100 40 56 P	
08. POWER DEVICES AND MACHINES PP	100	40	42	P C	18. DATA STRUCTURES PR 50 20 36 P	
09. NETWORK AND POWER LAB. TW	50	20	37	P C	19. COMMUNICATION THEORY PP 100 40 40 P	
10. ELECTRONIC INSTRUMENTS AND TOOLS \ensuremath{TW}	50	20	38	P C	20. COMMUNICATION THEORY OR 50 20 34 P	
					21. CIRCUIT SIMULATION AND TOOLS TW 50 20 34 P	

GRAND TOTAL = 770/1500, RESULT: SECOND CLASS

ORDN. 1 MARKS:

s8053109	YADAV VIKAS PRABHURAO				SURE	EKHA		, 71045667м	, s8053109 ,	PICT		, s80	5310	9
01. SIGNAL	AND SYSTEMS	PP	100	40	71	P C	11.	ENGINEERING MATHE	MATICS III	PP	100	40	76	Р
02. SIGNAL	AND SYSTEMS	OR	50	20	39	P C	12.	ENGINEERING MATHE	MATICS III	TW	25	10	17	Р
03. SOLID	STATES DEVICES AND CIRCUIT	SPP	100	40	57	P C	13.	INTEGRATED CIRCUI	TS APPLICATIONS	PP	100	40	51	Р
04. SOLID	STATES DEVICES AND CIRCUIT	SPR	50	20	37	P C	14.	INTEGRATED CIRCUI	TS APPLICATIONS	PR	50	20	32	Р
05. NETWOR	K ANALYSIS	PP	100	40	57	P C	15.	ELECTROMAGNETIC		PP	100	40	52	Р
06. DIGITA	L LOGIC DESIGN	PP	100	40	54	P C	16.	ELECTROMAGNETIC		TW	25	10	15	Р

07. DIGITAL LOGIC DESIGN	PR	50	20	34	PC	1	.7.	cols05 DATA STRUCTURES	PP	100	40	55	Р
08. POWER DEVICES AND MACHINES	PP	100	40	49	PC	1	.8.	DATA STRUCTURES	PR	50	20	42	Р
09. NETWORK AND POWER LAB.	TW	50	20	38	PC	1	9.	COMMUNICATION THEORY	PP	100	40	45	Р
10. ELECTRONIC INSTRUMENTS AND TOOLS	5 TW	50	20	39	PC	2	0.	COMMUNICATION THEORY	OR	50	20	30	Р
						2	1.	CIRCUIT SIMULATION AND TOOLS	TW	50	20	33	Р
GRAND TOTAL = 923/1500, RESULT: FIRST	CLAS	SS											
ORDN. 1 MARKS :													
			·										
								TELECOM.)			a =		.
DATE : 18 AUG. 2011	CENT	ΓRE : F	PUNE I	NSTI	TUTE	OF COMPU	JTER	TECHNOLOGY, PUNE.	PAG	E NO.	37	(36	51)
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NOTE: FIRST LINE : SEAT NO., NAME C													
OTHER LINES: HEAD OF PASSING,	MAX.	. MARKS	s, MI	N. P.	ASS	MARKS, M	IARK	S OBTAINED, P/F:PASS/FAIL, C:PI	REVIO	US CAR	RY OV	ER	
							•						
S8053110 YARDI NIRANJAN DHANANJAY				KRA	NTI			, 71045668к , \$8053110 ,	PICT		, s80	53110)
01. SIGNAL AND SYSTEMS	PP	100	40	79	P C	1	1.	ENGINEERING MATHEMATICS III	PP	100	40	78	Р
02. SIGNAL AND SYSTEMS	OR	50	20	34	PC	1	2.	ENGINEERING MATHEMATICS III	TW	25	10	18	Р
03. SOLID STATES DEVICES AND CIRCUIT	SPP	100	40	40	PC	1	.3.	INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	48	Р
04. SOLID STATES DEVICES AND CIRCUIT	SPR	50	20	26	PC	1	4.	INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	40	Р
05. NETWORK ANALYSIS	PP	100	40	40	PC	1	.5.	ELECTROMAGNETIC	PP	100	40	66	Р
06. DIGITAL LOGIC DESIGN	PP	100	40	40	PC	1	.6.	ELECTROMAGNETIC	TW	25	10	17	Р
07. DIGITAL LOGIC DESIGN	PR	50	20	37	PC	1	7.	DATA STRUCTURES	PP	100	40	55	Р
08. POWER DEVICES AND MACHINES	PP	100	40	40	PC	1	.8.	DATA STRUCTURES	PR	50	20	46	Р
09. NETWORK AND POWER LAB.	TW	50	20	39	PC	1	9.	COMMUNICATION THEORY	PP	100	40	51	Р
10. ELECTRONIC INSTRUMENTS AND TOOLS	5 TW	50	20	41	PC	2	0.	COMMUNICATION THEORY	OR	50	20	29	Р
						2	1.	CIRCUIT SIMULATION AND TOOLS	TW	50	20	37	Р

GRAND TOTAL = 901/1500, RESULT: FIRST CLASS
ORDN. 1 MARKS :

cols	s05
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S80531	20 JADHAV OMKAR FULCHAND				JYOT	SNA		, 70701463J , S8053120 ,	PICT		. s805	3120)
								, , ,			,		
01. SI	GNAL AND SYSTEMS	PP	100	40	40	P C	11.	ENGINEERING MATHEMATICS III	PP	100	40	40	P C
02. SI	GNAL AND SYSTEMS	OR	50	20	20	P C	12.	ENGINEERING MATHEMATICS III	TW	25	10	16	P C
03. SO	LID STATES DEVICES AND CIRCUI	TSPP	100	40	40	P C	13.	INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	49	P C
04. SO	LID STATES DEVICES AND CIRCUI	TSPR	50	20	24	P C	14.	INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	AA	F
05. NE	TWORK ANALYSIS	PP	100	40	23	F	15.	ELECTROMAGNETIC	PP	100	40	40	P C
06. DI	GITAL LOGIC DESIGN	PP	100	40	53	P C	16.	ELECTROMAGNETIC	TW	25	10	18	P C
07. DI	GITAL LOGIC DESIGN	PR	50	20	20	P C	17.	DATA STRUCTURES	PP	100	40	40	P C
08. PO	WER DEVICES AND MACHINES	PP	100	40	40	P C	18.	DATA STRUCTURES	PR	50	20	25	РС
09. NE	TWORK AND POWER LAB.	TW	50	20	24	P C	19.	COMMUNICATION THEORY	PP	100	40	54	P C
10. EL	ECTRONIC INSTRUMENTS AND TOOL	S TW	50	20	38	P C	20.	COMMUNICATION THEORY	OR	50	20	32	РС
							21.	CIRCUIT SIMULATION AND TOOLS	TW	50	20	38	РС

GRAND TOTAL = 674/1500, RESULT: FAILS A.T.K.T.
ORDN. 1 MARKS :

s80	53121	JAGTAP SAUDAMINI	DHANANJAY			PRER	RANA		, 71070199D	, s8053121 ,	PICT	-	, s80)5312	1
01.	SIGNAL	AND SYSTEMS	PP	100	40	40	P C	11	ENGINEERING MATH	EMATICS III	PP	100	40	50	P C
02.	SIGNAL	AND SYSTEMS	OR	50	20	29	P C	12	ENGINEERING MATH	EMATICS III	TW	25	10	20	P C
03.	SOLID S	STATES DEVICES AND	CIRCUITSPP	100	40	50	Р	13	INTEGRATED CIRCU	ITS APPLICATIONS	PP	100	40	65	P C
04.	SOLID S	STATES DEVICES AND	CIRCUITSPR	50	20	36	РС	14	INTEGRATED CIRCU	ITS APPLICATIONS	PR	50	20	28	P C
05.	NETWORK	< ANALYSIS	PP	100	40	43	РС	15	ELECTROMAGNETIC		PP	100	40	51	P C
06.	DIGITAL	L LOGIC DESIGN	PP	100	40	51	РС	16	ELECTROMAGNETIC		TW	25	10	21	P C
07.	DIGITAL	L LOGIC DESIGN	PR	50	20	35	P C	17	DATA STRUCTURES		PP	100	40	57	P C
08.	POWER I	DEVICES AND MACHINE	ES PP	100	40	43	P C	18	DATA STRUCTURES		PR	50	20	47	P C
09.	NETWORK	AND POWER LAB.	TW	50	20	44	РС	19	COMMUNICATION TH	EORY	PP	100	40	70	P C
10.	ELECTRO	ONIC INSTRUMENTS AN	ND TOOLS TW	50	20	39	P C	20	COMMUNICATION TH	EORY	OR	50	20	29	P C
								21	CIRCUIT SIMULATI	ON AND TOOLS	TW	50	20	39	РС

GRAND TOTAL = 887/1500, RESULT: HIGHER SECOND CLASS ORDN. 1 MARKS:

UNIVERSITY OF PUNE, S.E. (2008 PAT.) (ELECTRONICS & TELECOM.)													
		-						TELECOM.) R TECHNOLOGY, PUNE.	PAG	E NO.	38	(31	62)
NOTE: FIRST LINE : SEAT NO., NAME													
·			-					(S OBTAINED, P/F:PASS/FAIL, C:P		-			
S8053123 KESHAV SINGHAL				P00	NAM			, 70925472F , S8053123 ,	PICT		, s80	5312	3
01. SIGNAL AND SYSTEMS	PP	100	40	69	PC	:	11.	ENGINEERING MATHEMATICS III	PP	100	40	AA	F
02. SIGNAL AND SYSTEMS	OR	50	20	27	PC		12.	ENGINEERING MATHEMATICS III	TW	25	10	10	Р
03. SOLID STATES DEVICES AND CIRCUI	TSPP	100	40	25	F		13.	INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	21	F
04. SOLID STATES DEVICES AND CIRCUI	TSPR	50	20	20	PC		14.	INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	42	Р
05. NETWORK ANALYSIS	PP	100	40	25	F		15.	ELECTROMAGNETIC	PP	100	40	AA	F
06. DIGITAL LOGIC DESIGN	PP	100	40	43	PC		16.	ELECTROMAGNETIC	TW	25	10	11	Р
07. DIGITAL LOGIC DESIGN	PR	50	20	20	PC		17.	DATA STRUCTURES	PP	100	40	28	F
08. POWER DEVICES AND MACHINES	PP	100	40	40	PC		18.	DATA STRUCTURES	PR	50	20	34	Р
09. NETWORK AND POWER LAB.	TW	50	20	21	PC		19.	COMMUNICATION THEORY	PP	100	40	20	F
10. ELECTRONIC INSTRUMENTS AND TOOL	S TW	50	20	25	PC		20.	COMMUNICATION THEORY	OR	50	20	29	Р
							21.	CIRCUIT SIMULATION AND TOOLS	TW	50	20	34	Р
GRAND TOTAL = 544/1500, RESULT: FAIL ORDN. 1 MARKS:	S												
S8053127 MOTWANI NIKITA SUDAMA				KRI	SHNA	A		, 71070201к , s8053127 ,	PICT		, s80	53127	7
01. SIGNAL AND SYSTEMS	PP	100	40	48	PC	:	11.	ENGINEERING MATHEMATICS III	PP	100	40	44	РC
02. SIGNAL AND SYSTEMS	OR	50	20	28	P C		12.	ENGINEERING MATHEMATICS III	TW	25	10	18	P C

03. SOLID STATES DEVICES AND CIRCUITSPP	100	40	40	РС	cols05 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 46 P C
04. SOLID STATES DEVICES AND CIRCUITSPR	50	20		PC	14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 27 P C
05. NETWORK ANALYSIS PP	100	40	43	Р	15. ELECTROMAGNETIC PP 100 40 40 P C
06. DIGITAL LOGIC DESIGN PP	100	40		РС	16. ELECTROMAGNETIC TW 25 10 20 P C
07. DIGITAL LOGIC DESIGN PR	50	20	36	РС	17. DATA STRUCTURES PP 100 40 45 P C
08. POWER DEVICES AND MACHINES PP	100	40	45	РС	18. DATA STRUCTURES PR 50 20 27 P C
09. NETWORK AND POWER LAB. TW	50	20	33	РС	19. COMMUNICATION THEORY PP 100 40 40 P C
10. ELECTRONIC INSTRUMENTS AND TOOLS TW	50	20	40	РС	20. COMMUNICATION THEORY OR 50 20 PC
					21. CIRCUIT SIMULATION AND TOOLS TW 50 20 43 P C
GRAND TOTAL = 743+07/1500, RESULT: SECOND ORDN. 1 MARKS:) CLASS	[0.2	2]		
S8053130 PAWAR SACHIN VINAYAKRAO			SAV	ITA	, 70925557」 , S8053130 , PICT , S8053130

S8053130	PAWAR SACHIN VINAYAKRAO				SAVI	ITA		, 70925557j , S8053130 ,	PICT		, s80!	53130	0
01. SIGNAL	AND SYSTEMS	PP	100	40	AA	F	11.	ENGINEERING MATHEMATICS III	PP	100	40	04	F
02. SIGNAL	AND SYSTEMS	OR	50	20	AA	F	12.	ENGINEERING MATHEMATICS III	TW	25	10	15	Р
03. SOLID	STATES DEVICES AND CIRCUITS	SPP	100	40	AA	F	13.	INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	24	F
04. SOLID	STATES DEVICES AND CIRCUITS	SPR	50	20	AA	F	14.	INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	40	Р
05. NETWORI	K ANALYSIS	PP	100	40	AA	F	15.	ELECTROMAGNETIC	PP	100	40	AA	F
06. DIGITAL	L LOGIC DESIGN	PP	100	40	AA	F	16.	ELECTROMAGNETIC	TW	25	10	18	Р
07. DIGITA	L LOGIC DESIGN	PR	50	20	AA	F	17.	DATA STRUCTURES	PP	100	40	AA	F
08. POWER I	DEVICES AND MACHINES	PP	100	40	AA	F	18.	DATA STRUCTURES	PR	50	20	AA	F
09. NETWORI	K AND POWER LAB.	TW	50	20	33	P C	19.	COMMUNICATION THEORY	PP	100	40	AA	F
10. ELECTRO	ONIC INSTRUMENTS AND TOOLS	TW	50	20	20	P C	20.	COMMUNICATION THEORY	OR	50	20	21	Р
							21.	CIRCUIT SIMULATION AND TOOLS	TW	50	20	32	Р

GRAND TOTAL = 207/1500, RESULT: FAILS RESULT RESERVED FOR BKLG

ORDN. 1 MARKS:

UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(ELECTRONICS & TELECOM.)

CC)

1	DATE : 18 AUG. 2011	CENT	RE : P	UNE]	NSTI	TUT	TE OF COM	1PUTE	cols05 R TECHNOLOGY, PUNE.	PAGE	Ξ NO.	39	(3	63)
NOT	E: FIRST LINE : SEAT NO., NAME O	F THE	CANDI	DATE,	MO ⁻	THE	ER, PERMA	NENT	REG. NO., PREVIOUS SEAT NO., CO)LLEGF	Ξ, Ξ	SEAT N	Ю.	
	OTHER LINES: HEAD OF PASSING,	MAX.	MARKS	, MI	IN. P	ASS	MARKS,	MAR	KS OBTAINED, P/F:PASS/FAIL, C:PR	REVIOL	JS CAF	RY OV	'ER	
S80	53131 PAWAR VISHAL ALIAS DIGVIJ	AY BA	LASAHE	В	VIJ	ΑΥ	ANTI		, 71070203F , S8053131 ,	PICT		, s80	5313	1
01.	SIGNAL AND SYSTEMS	PP	100	40	40	Р		11.	ENGINEERING MATHEMATICS III	PP	100	40	AA	F
02.	SIGNAL AND SYSTEMS	OR	50	20	20	Р	С	12.	ENGINEERING MATHEMATICS III	TW	25	10	14	Р
03.	SOLID STATES DEVICES AND CIRCUIT	SPP	100	40	50	Р		13.	INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	59	Р
04.	SOLID STATES DEVICES AND CIRCUIT	SPR	50	20	20	Р	С	14.	INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	20	Р
05.	NETWORK ANALYSIS	PP	100	40	AA	F		15.	ELECTROMAGNETIC	PP	100	40	40	Р
06.	DIGITAL LOGIC DESIGN	PP	100	40	29	F		16.	ELECTROMAGNETIC	TW	25	10	12	Р
07.	DIGITAL LOGIC DESIGN	PR	50	20	20	Р	С	17.	DATA STRUCTURES	PP	100	40	40	Р
08.	POWER DEVICES AND MACHINES	PP	100	40	56	Р	С	18.	DATA STRUCTURES	PR	50	20	28	Р
09.	NETWORK AND POWER LAB.	TW	50	20	34	Р	С	19.	COMMUNICATION THEORY	PP	100	40	53	Р
10.	ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	30	Р	С	20.	COMMUNICATION THEORY	OR	50	20	24	Р
								21.	CIRCUIT SIMULATION AND TOOLS	TW	50	20	37	Р (
	TOTAL = 626/1500, RESULT: FAILS 1 MARKS:	A.T.	к.т.											
S80	53132 PRASHANT POPAT JAGTAP				MUK	TA			, 70801568к , s8053132 ,	PICT		, s80	5313	2
01.	SIGNAL AND SYSTEMS	PP	100	40	40	Р	С	11.	ENGINEERING MATHEMATICS III	PP	100	40	58	Р
02.	SIGNAL AND SYSTEMS	OR	50	20	20	Р	С	12.	ENGINEERING MATHEMATICS III	TW	25	10	16	Р
03.	SOLID STATES DEVICES AND CIRCUIT	SPP	100	40	31*	Р		13.	INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	48	P
04.	SOLID STATES DEVICES AND CIRCUIT	SPR	50	20	22	Р	С	14.	INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	35	Р
05.	NETWORK ANALYSIS	PP	100	40	40	Р	С	15.	ELECTROMAGNETIC	PP	100	40	40	P
06.	DIGITAL LOGIC DESIGN	PP	100	40	51	Р	С	16.	ELECTROMAGNETIC	TW	25	10	10	P (
07.	DIGITAL LOGIC DESIGN	PR	50	20	20	Р	С	17.	DATA STRUCTURES	PP	100	40	56	Р (

							cols05					
08. POWER DEVICES AND MACHINES	PP	100	40	45	PC	18.	DATA STRUCTURES	PR	50	20	26	РС
09. NETWORK AND POWER LAB.	TW	50	20	22	P C	19.	COMMUNICATION THEORY	PP	100	40	40	P C
10. ELECTRONIC INSTRUMENTS AND TOOL	S TW	50	20	21	P C	20.	COMMUNICATION THEORY	OR	50	20	20	P C
						21.	CIRCUIT SIMULATION AND TOOLS	TW	50	20	31	P C
GRAND TOTAL = 692/1500, RESULT: PASS	CLAS	c *	[0.4]									
ORDN. 1 MARKS :	CLAS	5	[0.4]									
ONDA: 1 PARKS :												
												_
S8053137 THOKALE PRIYANKA MADHAV				MAN	IDAKINI		, 70925636B , S8053137 ,	PICT	Γ	, 580	05313	7
01. SIGNAL AND SYSTEMS	PP	100	40	53	P C	11.	ENGINEERING MATHEMATICS III	PP	100	40	40	РС
02. SIGNAL AND SYSTEMS	OR	50	20	32	P C	12.	ENGINEERING MATHEMATICS III	TW	25	10	18	P C
03. SOLID STATES DEVICES AND CIRCUI	TSPP	100	40	AA	F	13.	INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	AA	F
04. SOLID STATES DEVICES AND CIRCUI	TSPR	50	20	29	P C	14.	INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	AA	F
05. NETWORK ANALYSIS	PP	100	40	AA	F	15.	ELECTROMAGNETIC	PP	100	40	AA	F
06. DIGITAL LOGIC DESIGN	PP	100	40	43	P C	16.	ELECTROMAGNETIC	TW	25	10	13	P C
07. DIGITAL LOGIC DESIGN	PR	50	20	20	P C	17.	DATA STRUCTURES	PP	100	40	AA	F
08. POWER DEVICES AND MACHINES	PP	100	40	AA	F	18.	DATA STRUCTURES	PR	50	20	AA	F
09. NETWORK AND POWER LAB.	TW	50	20	32	P C	19.	COMMUNICATION THEORY	PP	100	40	AA	F
10. ELECTRONIC INSTRUMENTS AND TOOL	S TW	50	20	32	P C	20.	COMMUNICATION THEORY	OR	50	20	AA	F
						21.	CIRCUIT SIMULATION AND TOOLS	TW	50	20	41	P C
GRAND TOTAL = 353/1500, RESULT: FAIL	s											
ORDN. 1 MARKS :												
UNIVERSITY O	F PUNE	,S.E.	(2008	PAT	.)(ELEC	TRONICS &	TELECOM.)					
DATE : 18 AUG. 2011	CEN.	TRE :	PUNE]	INSTI	TUTE O	F COMPUTE	R TECHNOLOGY, PUNE.	PAC	GE NO.	40	(3	64)
NOTE: FIRST LINE : SEAT NO., NAME												
OTHER LINES: HEAD OF PASSING,	MAX	. MARK	S, MI	IN. P	PASS MA	RKS, MAR	KS OBTAINED, P/F:PASS/FAIL, C:P	REVIO	OUS CA	RRY O	√ER	

S8053139 AGARWAL VINIT SHARAD				SAN	TOSH		, 70925321E , s	58053002 ,	PICT		, s80	5313	9
01. SIGNAL AND SYSTEMS	PP	100	40	42	РС	11.	ENGINEERING MATHEMAT	ICS III	PP	100	40	83	Р
02. SIGNAL AND SYSTEMS	OR	50	20	33	РС	12.	ENGINEERING MATHEMAT	ICS III	TW	25	10	25	Р
03. SOLID STATES DEVICES AND CIRCUIT	ГЅРР	100	40	54	РС	13.	INTEGRATED CIRCUITS A	APPLICATIONS	PP	100	40	61	Р
04. SOLID STATES DEVICES AND CIRCUIT	ΓSPR	50	20	27	P C	14.	INTEGRATED CIRCUITS A	APPLICATIONS	PR	50	20	38	Р
05. NETWORK ANALYSIS	PP	100	40	44	P C	15.	ELECTROMAGNETIC		PP	100	40	77	Р
06. DIGITAL LOGIC DESIGN	PP	100	40	40	P C	16.	ELECTROMAGNETIC		TW	25	10	24	Р
07. DIGITAL LOGIC DESIGN	PR	50	20	25	P C	17.	DATA STRUCTURES		PP	100	40	54	Р
08. POWER DEVICES AND MACHINES	PP	100	40	40	P C	18.	DATA STRUCTURES		PR	50	20	36	Р
09. NETWORK AND POWER LAB.	TW	50	20	29	P C	19.	COMMUNICATION THEORY		PP	100	40	65	Р
10. ELECTRONIC INSTRUMENTS AND TOOLS	5 TW	50	20	30	P C	20.	COMMUNICATION THEORY		OR	50	20	47	Р
						21.	CIRCUIT SIMULATION AN	ND TOOLS	TW	50	20	47	Р
ORDN. 1 MARKS :													
UNIVE	 RSITY	 OF PUN	 NE ,S.	 .E.(2	 2008 PAT.))(COMPU		 2011					
DATE: 18 AUG. 2011							TER) EXAMINATION MAY		 PAG	 E NO.	01		 65)
									PAG	 E NO.	01		 65)
	CENT	RE : P	PUNE I	NSTI	TUTE OF C	COMPUTER	R TECHNOLOGY, PUNE.				 01 		 65)
DATE : 18 AUG. 2011	CENT · · · DF THE	RE : P	OUNE I	NSTI MO	TUTE OF C	COMPUTER · · ·	R TECHNOLOGY, PUNE		 OLLEG	 E, S	 SEAT N		 65)
DATE: 18 AUG. 2011	CENT · · · DF THE	RE : P	OUNE I	NSTI MO	TUTE OF C	COMPUTER · · ·	R TECHNOLOGY, PUNE		 OLLEG	 E, S	 SEAT N		 65)
DATE: 18 AUG. 2011	CENT · · · DF THE	RE : P	OUNE I	NSTI MO	TUTE OF C THER, PER ASS MARKS	COMPUTER · · ·	R TECHNOLOGY, PUNE. REG. NO., PREVIOUS S SOBTAINED, P/F:PASS		 OLLEG	 E, S US CAR	 SEAT N	 IO. 'ER	
DATE: 18 AUG. 2011	CENT · · · DF THE	RE : P	OUNE I	MO' N. P	TUTE OF C THER, PER ASS MARKS	COMPUTER CMANENT MARK	R TECHNOLOGY, PUNE. REG. NO., PREVIOUS S SOBTAINED, P/F:PASS		· · · OLLEG REVIO	 E, S US CAR	EAT N	 IO. 'ER	2
DATE: 18 AUG. 2011 NOTE: FIRST LINE: SEAT NO., NAME OF OTHER LINES: HEAD OF PASSING, S8054202 ABHIMANYU BHOSALE	CENT THE MAX.	RE : P CANDI MARKS	PUNE I DATE, . MI	MOTINAL PARAMETERS NO. PARAMETERS NO	TUTE OF C THER, PER ASS MARKS	COMPUTER CMANENT MARK	R TECHNOLOGY, PUNE. REG. NO., PREVIOUS S SOBTAINED, P/F:PASS , 71045355J , 5		OLLEGREVIO	 E, S US CAR	EAT NERY OV	 IO. /ER 	 2
DATE: 18 AUG. 2011 NOTE: FIRST LINE: SEAT NO., NAME OF DESCRIPTION OF PASSING, S8054202 ABHIMANYU BHOSALE 01. DISCRETE STRUCTURES	CENT DF THE MAX.	CANDI MARKS	PUNE I DATE, MI	NSTI MO N. P SUJ 53 40	TUTE OF C THER, PER ASS MARKS ATA P C	COMPUTER CMANENT MARK 11.	R TECHNOLOGY, PUNE. REG. NO., PREVIOUS S S OBTAINED, P/F:PASS , 71045355J , S ENGINEERING MATHEMAT		OLLEGREVIO	E, S US CAR 		 /ER 05420	 2 P
DATE: 18 AUG. 2011	CENT DF THE MAX.	CANDI MARKS 100 100	DUNE I DATE, 40 40	MO: N. P. SUJ. 53 40 46	TUTE OF C THER, PER ASS MARKS ATA P C P C	COMPUTER CMANENT MARK 11. 12. 13.	R TECHNOLOGY, PUNE. REG. NO., PREVIOUS S S OBTAINED, P/F:PASS , 71045355J , S ENGINEERING MATHEMAT: MICROPROC. & INTERFACE		OLLEGREVIO	E, S US CAR 	EAT N RRY OV , S80 40 40	 IO. /ER 05420 40 31	 2 P F
DATE: 18 AUG. 2011	CENT OF THE MAX. PP PP SNPP	TRE : P CANDI MARKS 100 100 100	PUNE I DATE, 40 40 40	NSTI MO N. P SUJ 53 40 46 59	TUTE OF C THER, PER ASS MARKS ATA P C P C P C	MANENT 11. 12. 13. 14.	R TECHNOLOGY, PUNE. REG. NO., PREVIOUS S S OBTAINED, P/F:PASS , 71045355J , S ENGINEERING MATHEMAT: MICROPROC. & INTERFAC	SEAT NO., COS/FAIL, C:POS., COS SEAT NO., CO	OLLEGREVIO PICT PP PP	E, S US CAR 	EAT N RRY OV , S80 40 40 40	 VER 05420 40 31 26	
DATE: 18 AUG. 2011	CENT OF THE MAX. PP PP GNPP PP	TRE : P CANDI MARKS 100 100 100 100	PUNE I DATE, 40 40 40 40 40	NSTI MO N. P SUJ 40 46 59 44	TUTE OF C THER, PER ASS MARKS ATA P C P C P C P C	COMPUTER	R TECHNOLOGY, PUNE. REG. NO., PREVIOUS S SOBTAINED, P/F:PASS TO THE PROPERTY OF THE PASS TO THE PASS	SEAT NO., COS/FAIL, C:POS SEAT NO., COS SEAT NO., COS SEAT NO., COS SEAT NO., COS SEAT NO.	OLLEGREVIO PICT PP PP PP	E, S US CAR	EAT N RRY OV , S80 40 40 40		

08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	16	РС	18.	MICROPROCESSORS & INTERFACING L	ABTW	50	20	27	Р
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	23	РС	19.	MICROPROCESSORS & INTERFACING L	ABPR	50	20	10	F
10. SOFT SKILLS	TW	50	20	32	P C	20.	DATA STRUCTURES LABORATORY	TW	50	20	23	Р
						21.	DATA STRUCTURES LABORATORY	PR	50	20	35	Р
CRAND TOTAL 709 /1500 PECULT. FATAL	T	и т										
GRAND TOTAL = 708/1500, RESULT: FAIL:	S A.I.	.K.I.										
ORDN. 1 MARKS :												
				• •								
S8054203 ABNAVE OMKAR RAJENDRA				SHO	ВНА		, 71045358C , S8054203 ,	PICT	-	, s80	5420	3
01. DISCRETE STRUCTURES	PP	100	40	40	P C	11.	ENGINEERING MATHEMATICS III	PP	100	40	42	Р
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	40	P C	12.	MICROPROC. & INTERFACING TECHNI	Q.PP	100	40	43	Р
03. DIGIT. ELECTRONICS & LOGIC DESIGNATION OF THE PROPERTY OF	GNPP	100	40	40	P C	13.	DATA STRUCTURES	PP	100	40	40	Р
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	40	P C	14.	COMPUTER GRAPHICS	PP	100	40	57	Р
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	41	P C	15.	COMPUTER ORGANIZATION	PP	100	40	45	P
06. PROGRAMMING LABORATORY	TW	25	10	13	P C	16.	O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	23	Р
07. PROGRAMMING LABORATORY	PR	50	20	12	F	17.	O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	05	F
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	13	P C	18.	MICROPROCESSORS & INTERFACING L	ABTW	50	20	21	Р
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	23	Р	19.	MICROPROCESSORS & INTERFACING L	ABPR	50	20	20	Р
10. SOFT SKILLS	TW	50	20	22	P C	20.	DATA STRUCTURES LABORATORY	TW	50	20	22	Р
						21.	DATA STRUCTURES LABORATORY	PR	50	20	00	F
GRAND TOTAL = 602/1500, RESULT: FAILS	S A.T.	к.т.						RESU	ILT RES	SERVED	FOR	BKLG
ORDN. 1 MARKS :												
S8054204 ADHAV ASHISH DNYANDEO				SUJ	ATA		, 71045359M , S8054204 ,	PICT	-	, s80	5420	4
01. DISCRETE STRUCTURES	PP	100	40	66	P C	11.	ENGINEERING MATHEMATICS III	PP	100	40	63	Р
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	57	P C	12.	MICROPROC. & INTERFACING TECHNI	Q.PP	100	40	50	Р
03. DIGIT. ELECTRONICS & LOGIC DESIGNATION	GNPP	100	40	56	P C	13.	DATA STRUCTURES	PP	100	40	62	Р

						cols05	
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	72	P C	14. COMPUTER GRAPHICS PP 100 40 66 P	
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	57	P C	15. COMPUTER ORGANIZATION PP 100 40 60 P	
06. PROGRAMMING LABORATORY	TW	25	10	17	P C	16. O. O. PROG. & COMP. GRAPH. LAB TW 50 20 32 P	
07. PROGRAMMING LABORATORY	PR	50	20	41	P C	17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 44 P	
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	15	P C	18. MICROPROCESSORS & INTERFACING LABTW 50 20 35 P	
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	30	P C	19. MICROPROCESSORS & INTERFACING LABPR 50 20 44 P	
10. SOFT SKILLS	TW	50	20	39	P C	20. DATA STRUCTURES LABORATORY TW 50 20 30 P	
						21. DATA STRUCTURES LABORATORY PR 50 20 33 P	
GRAND TOTAL = 969/1500, RESULT: FIRST	CLAS	SS					
ORDN. 1 MARKS :							

	 RSITY	OF PU	 NE ,S	 .E.(2	 2008	 B PAT.)(0	 COMPL	TER) EXAMINATION MAY 2011					
DATE : 18 AUG. 2011	CENT	ΓRE : F	PUNE]	INSTI	TUTI	E OF COM	IPUTE	R TECHNOLOGY, PUNE.	PAG	E NO.	02	(3	66)
NOTE: FIRST LINE : SEAT NO., NAME (OF THE	E CAND	DATE,	, мо	THE	R, PERMA	NENT	REG. NO., PREVIOUS SEAT NO.,	COLLEG	Ε, 5	SEAT N	10.	
OTHER LINES: HEAD OF PASSING,	MAX.	. MARKS	5, M3	IN. P	PASS	MARKS,	MAR	KS OBTAINED, P/F:PASS/FAIL, C:I	PREVIC	US CAI	RRY OV	/ER	
S8054205 ADHIKARI DHANASHREE HEMCH	HANDRA	4		YAY	′UTA			, 71045360E , S8054205 ,	PICT		, s80	5420	5
01. DISCRETE STRUCTURES	PP	100	40	69	Р (С	11.	ENGINEERING MATHEMATICS III	PP	100	40	71	Р
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	62	Р (С	12.	MICROPROC. & INTERFACING TECHNIC	Q.PP	100	40	58	Р
03. DIGIT. ELECTRONICS & LOGIC DESIG	GNPP	100	40	79	Р (С	13.	DATA STRUCTURES	PP	100	40	67	Р
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	68	Р (С	14.	COMPUTER GRAPHICS	PP	100	40	65	Р
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	64	Р (С	15.	COMPUTER ORGANIZATION	PP	100	40	57	Р
06. PROGRAMMING LABORATORY	TW	25	10	22	Р (С	16.	O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	43	Р
07. PROGRAMMING LABORATORY	PR	50	20	32	Р (С	17.	O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	43	Р
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	23	Р (С	18.	MICROPROCESSORS & INTERFACING LA	ABTW	50	20	43	Р
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	41	Р (С	19.	MICROPROCESSORS & INTERFACING LA	ABPR	50	20	46	Р
10. SOFT SKILLS	TW	50	20	41	Р (С	20.	DATA STRUCTURES LABORATORY	TW	50	20	45	Р
							21.	DATA STRUCTURES LABORATORY	PR	50	20	39	Р

GRAND TOTAL = 1078/1500, RESULT: FIRST CLASS WITH DISTINCTION
ORDN. 1 MARKS :

S8054206 ADITI JOSHI				SUF	RUCHI	, 71045361C , S8054206 , PICT , S8054206	
01. DISCRETE STRUCTURES	PP	100	40	69	РС	11. ENGINEERING MATHEMATICS III PP 100 40 74 P	
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	60	P C	12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 46 P	
03. DIGIT. ELECTRONICS & LOGIC DESIGNATION OF THE PROPERTY OF	GNPP	100	40	83	P C	13. DATA STRUCTURES PP 100 40 58 P	
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	84	P C	14. COMPUTER GRAPHICS PP 100 40 65 P	
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	51	P C	15. COMPUTER ORGANIZATION PP 100 40 54 P	
06. PROGRAMMING LABORATORY	TW	25	10	18	P C	16. O. O. PROG. & COMP. GRAPH. LAB TW 50 20 29 P	
07. PROGRAMMING LABORATORY	PR	50	20	35	P C	17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 44 P	
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	19	P C	18. MICROPROCESSORS & INTERFACING LABTW 50 20 31 P	
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	40	P C	19. MICROPROCESSORS & INTERFACING LABPR 50 20 28 P	
10. SOFT SKILLS	TW	50	20	34	P C	20. DATA STRUCTURES LABORATORY TW 50 20 26 P	
						21. DATA STRUCTURES LABORATORY PR 50 20 37 P	

GRAND TOTAL = 985/1500, RESULT: FIRST CLASS
ORDN. 1 MARKS :

S8054207 ADTANI VASHISHTHA MOHAN				BHAW	VANA		, 71045362M , S8054207 ,	PICT		, s80	5420	7
01. DISCRETE STRUCTURES	PP	100	40	81	P C	11.	ENGINEERING MATHEMATICS III	PP	100	40	71	Р
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	57	P C	12.	MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	40	Р
03. DIGIT. ELECTRONICS & LOGIC DESIGN	NPP	100	40	70	P C	13.	DATA STRUCTURES	PP	100	40	53	Р
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	77	P C	14.	COMPUTER GRAPHICS	PP	100	40	69	Р
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	54	P C	15.	COMPUTER ORGANIZATION	PP	100	40	61	Р
06. PROGRAMMING LABORATORY	TW	25	10	21	P C	16.	O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	43	Р
07. PROGRAMMING LABORATORY	PR	50	20	45	P C	17.	O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	36	Р
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	23	P C	18.	MICROPROCESSORS & INTERFACING LAB	3TW	50	20	44	Р

						cols05	
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	40	P C	19. MICROPROCESSORS & INTERFACING LABPR 50 20	38 P
10. SOFT SKILLS	TW	50	20	34	P C	20. DATA STRUCTURES LABORATORY TW 50 20	40 P
						21. DATA STRUCTURES LABORATORY PR 50 20	41 P
GRAND TOTAL = 1038/1500, RESULT: FIRST	T CLAS	SS WITH	H DIS	TINCT	ION		
DRDN. 1 MARKS :							
INTVE	 DSTTV	 OE DI	 INIE S	 E (2008 PAT)	COMPUTER) EXAMINATION MAY 2011	
DATE : 18 AUG. 2011			-			PUTER TECHNOLOGY, PUNE. PAGE NO. 03	(367
DATE : 10 Add: 2011	CLIVI	KL .	I ONE	111311	TOTE OF C	TAGE NO. 03	(301)
NOTE: FIRST LINE : SEAT NO NAME (OF THE	CAND				NIENT DEC. NO. DREVTOUS SEAT NO. COLLEGE SEAT N	
NOTE: FIRST LINE: SEAT NO NAME ()F IHF	· (ANI)	TDAIE	, MC	THEK, PEK	NNENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT N	ΙΟ.
· ·				TN F	ACC MARKS	MARKS ORTATNED - D/F.DASS/FATI - C.DREVTOUS CARRY OV	/EB
·				IN. F	ASS MARKS	MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OV	'ER
·				IN. F	ASS MARKS	MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OV	ER
·	MAX.				ASS MARKS		ER 054208
OTHER LINES: HEAD OF PASSING,	MAX.			 SHA			
OTHER LINES: HEAD OF PASSING, S8054208 AGRAWAL ANKIT UMESHKUMAR	MAX.	MARK:	S, M:	 SHA 69		, 71045364н , S8054208 , РІСТ , S80	54208
OTHER LINES: HEAD OF PASSING, S8054208 AGRAWAL ANKIT UMESHKUMAR O1. DISCRETE STRUCTURES	MAX.	MARK:	S, M:	 SHA 69 50	MADEVI	, 71045364H , S8054208 , PICT , S80 11. ENGINEERING MATHEMATICS III PP 100 40	 954208 68 Р
OTHER LINES: HEAD OF PASSING,	MAX.	100 100	40 40	 SHA 69 50	MADEVIP C	, 71045364H , S8054208 , PICT , S80 11. ENGINEERING MATHEMATICS III PP 100 40 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40	 954208 68 P 54 P
OTHER LINES: HEAD OF PASSING, S8054208 AGRAWAL ANKIT UMESHKUMAR O1. DISCRETE STRUCTURES O2. PROGRAMMING & PROBLEM SOLVING O3. DIGIT. ELECTRONICS & LOGIC DESIGN	MAX. PP PP	100 100 100	40 40 40	SHA 69 50 64	MADEVI PC PC PC	, 71045364H , S8054208 , PICT , S80 11. ENGINEERING MATHEMATICS III PP 100 40 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 13. DATA STRUCTURES PP 100 40	68 P 54 P 66 P
OTHER LINES: HEAD OF PASSING,	MAX. PP PP GNPP PP	100 100 100 100	40 40 40 40 40	SHA 69 50 64 78	MADEVI PC PC PC	, 71045364H , S8054208 , PICT , S80 11. ENGINEERING MATHEMATICS III PP 100 40 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 13. DATA STRUCTURES PP 100 40 14. COMPUTER GRAPHICS PP 100 40	68 P 54 P 66 P 65 P
OTHER LINES: HEAD OF PASSING,	PP PP PP	100 100 100 100 100	40 40 40 40 40 40	SHA 69 50 64 78 60	MADEVI PC PC PC PC	, 71045364H , S8054208 , PICT , S80 11. ENGINEERING MATHEMATICS III PP 100 40 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 13. DATA STRUCTURES PP 100 40 14. COMPUTER GRAPHICS PP 100 40 15. COMPUTER ORGANIZATION PP 100 40	68 P 54 P 66 P 65 P
OTHER LINES: HEAD OF PASSING,	PP PP PP TW	100 100 100 100 100 25	40 40 40 40 40 10	SHA 69 50 64 78 60 17	MADEVI PC PC PC PC PC	, 71045364H , S8054208 , PICT , S80 11. ENGINEERING MATHEMATICS III PP 100 40 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 13. DATA STRUCTURES PP 100 40 14. COMPUTER GRAPHICS PP 100 40 15. COMPUTER ORGANIZATION PP 100 40 16. O. O. PROG. & COMP. GRAPH. LAB TW 50 20	68 P 54 P 66 P 65 P 55 P
OTHER LINES: HEAD OF PASSING,	PP PP PP TW PR	100 100 100 100 100 25 50	40 40 40 40 40 40 20	SHA 69 50 64 78 60 17 30	MADEVI PCPC PCPC	, 71045364H , S8054208 , PICT , S80 11. ENGINEERING MATHEMATICS III PP 100 40 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 13. DATA STRUCTURES PP 100 40 14. COMPUTER GRAPHICS PP 100 40 15. COMPUTER ORGANIZATION PP 100 40 16. O. O. PROG. & COMP. GRAPH. LAB TW 50 20 17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20	68 P 54 P 66 P 65 P 55 P 32 P
OTHER LINES: HEAD OF PASSING,	MAX. PP PP PP PP TW PR TW	100 100 100 100 25 50 25	40 40 40 40 40 10 20	SHA 69 50 64 78 60 17 30 17 27	MADEVI PCPC PCPC	, 71045364H , S8054208 , PICT , S80 11. ENGINEERING MATHEMATICS III PP 100 40 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 13. DATA STRUCTURES PP 100 40 14. COMPUTER GRAPHICS PP 100 40 15. COMPUTER ORGANIZATION PP 100 40 16. O. O. PROG. & COMP. GRAPH. LAB TW 50 20 17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 18. MICROPROCESSORS & INTERFACING LABTW 50 20	68 P 54 P 66 P 65 P 55 P 32 P 39 P

, 71045365F , S8054209 , PICT Page 77 , s8054209 S8054209 AGRAWAL ANNU ARUN MEENA

01. DISCRETE STRUCTURES	PP	100	40	68	P C	11. ENGINEERING MATHEMATICS III PP 100 40 88 P	
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	47	P C	12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 56 P	
03. DIGIT. ELECTRONICS & LOGIC DESIG	GNPP	100	40	76	P C	13. DATA STRUCTURES PP 100 40 60 P	
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	67	P C	14. COMPUTER GRAPHICS PP 100 40 71 P	
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	47	P C	15. COMPUTER ORGANIZATION PP 100 40 61 P	
06. PROGRAMMING LABORATORY	TW	25	10	21	P C	16. O. O. PROG. & COMP. GRAPH. LAB TW 50 20 39 P	
07. PROGRAMMING LABORATORY	PR	50	20	34	P C	17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 35 P	
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	19	P C	18. MICROPROCESSORS & INTERFACING LABTW 50 20 40 P	
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	38	P C	19. MICROPROCESSORS & INTERFACING LABPR 50 20 38 P	
10. SOFT SKILLS	TW	50	20	38	P C	20. DATA STRUCTURES LABORATORY TW 50 20 35 P	
						21. DATA STRUCTURES LABORATORY PR 50 20 37 P	

GRAND TOTAL = 1015/1500, RESULT: FIRST CLASS WITH DISTINCTION
ORDN. 1 MARKS :

S8054210 AKASH DEEP AGRAWAL				KAUSA	LYA		, 71045366D	, s8054210 ,	PICT		, s80)5421	0
01. DISCRETE STRUCTURES	PP	100	40	69 P	c	11.	ENGINEERING MATH	HEMATICS III	PP	100	40	64	Р
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	48 P	C	12	MICROPROC. & INT	TERFACING TECHNIQ	.PP	100	40	52	Р
03. DIGIT. ELECTRONICS & LOGIC DESIGNATION	GNPP	100	40	63 P	C	13	DATA STRUCTURES		PP	100	40	04	F
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	72 P	C	14	COMPUTER GRAPHIC	CS	PP	100	40	68	Р
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	48 P	C	15	COMPUTER ORGANIZ	ZATION	PP	100	40	56	Р
06. PROGRAMMING LABORATORY	TW	25	10	16 P	C	16	O. O. PROG. & CO	OMP. GRAPH. LAB	TW	50	20	39	Р
07. PROGRAMMING LABORATORY	PR	50	20	28 P	C	17	O. O. PROG. & CO	OMP. GRAPH. LAB	PR	50	20	43	Р
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	17 P	C	18	MICROPROCESSORS	& INTERFACING LA	BTW	50	20	35	Р
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	28 P	C	19	MICROPROCESSORS	& INTERFACING LA	BPR	50	20	37	Р
10. SOFT SKILLS	TW	50	20	32 P	C	20	DATA STRUCTURES	LABORATORY	TW	50	20	33	Р
						21.	DATA STRUCTURES	LABORATORY	PR	50	20	32	Р

GRAND TOTAL = 884/1500, RESULT: FAILS A.T.K.T.
ORDN. 1 MARKS :

UNIVE	RSITY	OF PU	NE ,S	.E.(2	2008 F	PAT.)(COMP	UTER) EXAMINATIO	N MAY 2011					
DATE : 18 AUG. 2011	CEN.	TRE :	PUNE :	INSTI	TUTE	OF COMPUT	ER TECHNOLOGY, P	JNE.	PAG	E NO.	04	(3	68)
NOTE: FIRST LINE : SEAT NO., NAME (OF TH	E CAND	IDATE	, MO	THER,	PERMANEN	REG. NO., PRE	VIOUS SEAT NO., C	OLLEG	Ε, 5	SEAT N	10.	
OTHER LINES: HEAD OF PASSING,	MAX	. MARK	S, M	IN. P	ASS M	MARKS, MA	RKS OBTAINED, P	/F:PASS/FAIL, C:P	REVIO	US CAI	RRY O	/ER	
S8054211 ANIRUDH JODHA				ANJ	U		, 71045370в	, s8054211 ,	PICT		, s80)5421	1
01. DISCRETE STRUCTURES	PP	100	40	49	P C	11	. ENGINEERING MA	THEMATICS III	PP	100	40	42	Р
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	40	P C	12	MICROPROC. & IN	NTERFACING TECHNIQ	.PP	100	40	40	Р
03. DIGIT. ELECTRONICS & LOGIC DESIGNATION OF THE PROPERTY OF	GNPP	100	40	41	P C	13	. DATA STRUCTURES	S	PP	100	40	32	F
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	59	P C	14	. COMPUTER GRAPH:	ICS	PP	100	40	49	Р
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	46	P C	15	. COMPUTER ORGAN	IZATION	PP	100	40	40	Р
06. PROGRAMMING LABORATORY	TW	25	10	16	P C	16	O. O. PROG. & 0	COMP. GRAPH. LAB	TW	50	20	33	Р
07. PROGRAMMING LABORATORY	PR	50	20	35	P C	17	O. O. PROG. & 0	COMP. GRAPH. LAB	PR	50	20	32	Р
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	15	P C	18	. MICROPROCESSORS	S & INTERFACING LA	BTW	50	20	29	Р
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	24	P C	19	. MICROPROCESSORS	S & INTERFACING LA	BPR	50	20	30	Р
10. SOFT SKILLS	TW	50	20	35	P C	20	. DATA STRUCTURES	S LABORATORY	TW	50	20	30	Р
						21	. DATA STRUCTURES	S LABORATORY	PR	50	20	15	F
GRAND TOTAL = 732/1500, RESULT: FAILS	S A.T	.K.T.											
ORDN. 1 MARKS :													
S8054212 AVHAD PRATIK GULAB				MAM	TA		, 71045377К	, \$8054212 ,	PICT		, s80)5421	2
01. DISCRETE STRUCTURES	PP	100	40	68	P C	11	. ENGINEERING MA	THEMATICS III	PP	100	40	52	Р
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	41	P C	12	. MICROPROC. & I	NTERFACING TECHNIQ	.PP	100	40	31*	Р
03. DIGIT. ELECTRONICS & LOGIC DESIGNATION OF THE PROPERTY OF	GNPP	100	40	55	P C	13	. DATA STRUCTURES	S	PP	100	40	43	Р
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	72	P C	14	. COMPUTER GRAPH:	ICS	PP	100	40	53	Р
								Da					

05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	44	РC	cols05 15. COMPUTER ORGANIZATION PP 100 40 40 P	
06. PROGRAMMING LABORATORY	TW	25	10	18	РC	16. O. O. PROG. & COMP. GRAPH. LAB TW 50 20 29 P	
07. PROGRAMMING LABORATORY	PR	50	20	40	P C	17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 22 P	
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	19	P C	18. MICROPROCESSORS & INTERFACING LABTW 50 20 31 P	
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	22	P C	19. MICROPROCESSORS & INTERFACING LABPR 50 20 26 P	
10. SOFT SKILLS	TW	50	20	35	P C	20. DATA STRUCTURES LABORATORY TW 50 20 30 P	
						21. DATA STRUCTURES LABORATORY PR 50 20 21 P	
GRAND TOTAL = 792/1500, RESULT: SECON	אוט כו י	ΔSS :	* [0.	41			
ORDN. 1 MARKS :	ND CL	-33	[0.	- J			
THANKS .							
S8054213 AYACHIT DUSHYANT PRADEEPR	RAO			PRA	TIBHA	, 71132423K , S8054213 , PICT , S8054213	
01. DISCRETE STRUCTURES	PP	100	40	66	РС	11. FNGTNEERING MATHEMATICS III PP 100 40 46 P	

S8054213 AYACHIT DUSHYANT PRADEEP	PRAO			PRATIBHA	, 71132423K , S8054213 , PICT	, S8054213
01. DISCRETE STRUCTURES	PP	100	40	66 P C	11. ENGINEERING MATHEMATICS III PP 100	40 46 P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	59 P C	12. MICROPROC. & INTERFACING TECHNIQ.PP 100	40 57 P
03. DIGIT. ELECTRONICS & LOGIC DESI	GNPP	100	40	51 PC	13. DATA STRUCTURES PP 100	40 66 P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	58 P C	14. COMPUTER GRAPHICS PP 100	40 69 P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	40 P C	15. COMPUTER ORGANIZATION PP 100	40 58 P
06. PROGRAMMING LABORATORY	TW	25	10	21 PC	16. O. O. PROG. & COMP. GRAPH. LAB TW 50	20 40 P
07. PROGRAMMING LABORATORY	PR	50	20	36 P C	17. O. O. PROG. & COMP. GRAPH. LAB PR 50	20 32 P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	20 P C	18. MICROPROCESSORS & INTERFACING LABTW 50	20 41 P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	21 PC	19. MICROPROCESSORS & INTERFACING LABPR 50	20 38 P
10. SOFT SKILLS	TW	50	20	39 P C	20. DATA STRUCTURES LABORATORY TW 50	20 36 P
					21. DATA STRUCTURES LABORATORY PR 50	20 37 P

GRAND TOTAL = 931/1500, RESULT: FIRST CLASS
ORDN. 1 MARKS :

UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(COMPUTER) EXAMINATION MAY 2011

DATE: 18 AUG. 2011 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 05 (369)

NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8054214 BAHULIKAR CHAITALI SANJA	Y			SNE	НА	, 71045383D , S8054214 , PICT , S8	3054214
01. DISCRETE STRUCTURES	PP	100	40	63	P C	11. ENGINEERING MATHEMATICS III PP 100 40	81 P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	55	P C	12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40	65 P
03. DIGIT. ELECTRONICS & LOGIC DESI	GNPP	100	40	74	P C	13. DATA STRUCTURES PP 100 40	73 P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	83	P C	14. COMPUTER GRAPHICS PP 100 40	66 P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	57	P C	15. COMPUTER ORGANIZATION PP 100 40	53 P
06. PROGRAMMING LABORATORY	TW	25	10	23	P C	16. O. O. PROG. & COMP. GRAPH. LAB TW 50 20	44 P
07. PROGRAMMING LABORATORY	PR	50	20	42	P C	17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20	36 P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	22	P C	18. MICROPROCESSORS & INTERFACING LABTW 50 20	44 P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	42	P C	19. MICROPROCESSORS & INTERFACING LABPR 50 20	42 P
10. SOFT SKILLS	TW	50	20	43	P C	20. DATA STRUCTURES LABORATORY TW 50 20	44 P
						21. DATA STRUCTURES LABORATORY PR 50 20	35 P

GRAND TOTAL = 1087/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS :

S8054215 BANG GAURAV SUNIL				SUN	IANDA	, 71045384B , S8054215 , PICT , S8054215	
01. DISCRETE STRUCTURES	PP	100	40	64	PС	11. ENGINEERING MATHEMATICS III PP 100 40 92 P	>
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	47	PС	12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 51 P	>
03. DIGIT. ELECTRONICS & LOGIC DESIG	SNPP	100	40	66	P C	13. DATA STRUCTURES PP 100 40 67 P	•
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	67	P C	14. COMPUTER GRAPHICS PP 100 40 69 P	•
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	50	PС	15. COMPUTER ORGANIZATION PP 100 40 48 P	>
06. PROGRAMMING LABORATORY	TW	25	10	15	PС	16. O. O. PROG. & COMP. GRAPH. LAB TW 50 20 33 P	>
07. PROGRAMMING LABORATORY	PR	50	20	28	PС	17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 41 P	>
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	16	PС	18. MICROPROCESSORS & INTERFACING LABTW 50 20 30 P	>
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	25	P C	19. MICROPROCESSORS & INTERFACING LABPR 50 20 45 P	,

10. SOFT SKILLS	TW	50	20	32	РC	20	DATA	STRUCTUR	cols05 ES LABORATORY	TW	50	20	35	Р
						21	DATA	STRUCTUR	ES LABORATORY	PR	50	20	31	Р
GRAND TOTAL = 952/1500, RESULT: FIRS	T CLAS	SS												
ORDN. 1 MARKS :														
S8054216 BHADADE MOHIT DNYANESH				SHC	ВНА		,	71045385L	, s8054216 ,	PICT	Г	, s80	05421	.6
01. DISCRETE STRUCTURES	PP	100	40	76	P C	11	ENGI	NEERING M	ATHEMATICS III	PP	100	40	83	Р
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	57	P C	12	MICR	OPROC. &	INTERFACING TECHNIC	Q.PP	100	40	59	Р
03. DIGIT. ELECTRONICS & LOGIC DESI	GNPP	100	40	70	P C	13	DATA	STRUCTUR	ES	PP	100	40	73	Р
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	68	P C	14	COMP	UTER GRAF	PHICS	PP	100	40	63	Р
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	57	P C	15	COMP	UTER ORGA	NIZATION	PP	100	40	65	Р
06. PROGRAMMING LABORATORY	TW	25	10	17	P C	16	0.0	PROG. 8	COMP. GRAPH. LAB	TW	50	20	39	Р
07. PROGRAMMING LABORATORY	PR	50	20	37	P C	17	0.0	PROG. 8	COMP. GRAPH. LAB	PR	50	20	34	Р
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	21	P C	18	MICR	OPROCESSO	RS & INTERFACING LA	ABTW	50	20	35	Р
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	42	P C	19	MICR	OPROCESSO	RS & INTERFACING LA	ABPR	50	20	39	Р
10. SOFT SKILLS	TW	50	20	35	P C	20	DATA	STRUCTUR	ES LABORATORY	TW	50	20	35	Р
						21	DATA	STRUCTUR	ES LABORATORY	PR	50	20	39	Р
CRAND TOTAL 1044/1500 RECULT, ETRO	T (1.4)	C WIT	LDICI	TNCT	TON									
GRAND TOTAL = 1044/1500, RESULT: FIRST ORDN. 1 MARKS:	I CLAS	22 MTII	י מנט ד	I INC	TON									
ORDN. I MARKS .														
UNIVE	RSITY	OF PU	NE ,S	.E.(2008	PAT.)(COMP	UTER)	EXAMINAT	ION MAY 2011					
DATE : 18 AUG. 2011	CEN	TRE : I	PUNE I	INSTI	TUTE	OF COMPUT	R TEC	CHNOLOGY,	PUNE.	PAC	SE NO.	06	(3	70)
NOTE: FIRST LINE : SEAT NO., NAME	OF THE	E CAND:	IDATE	, мс	THER	, PERMANEN	REG.	NO., PR	EVIOUS SEAT NO., (COLLEC	GΕ, S	SEAT N	10.	
OTHER LINES: HEAD OF PASSING,	MAX	. MARKS	S, MI	IN. F	PASS	MARKS, MAI	RKS OB	STAINED,	P/F:PASS/FAIL, C:	PREVIO	OUS CAI	RRY O	/ER	
S8054217 BHANDARKAR PRATIK PRAVIN				KAL	.PANA		,	71132424⊦	, s8054217 ,	PICT	Г	, s80)5421	.7

						cols05
01. DISCRETE STRUCTURES	PP	100	40	64	PC	11. ENGINEERING MATHEMATICS III PP 100 40 56 P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	61	P C	12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 46 P
03. DIGIT. ELECTRONICS & LOGIC DESIGNATION OF THE PROPERTY OF	GNPP	100	40	78	P C	13. DATA STRUCTURES PP 100 40 69 P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	76	P C	14. COMPUTER GRAPHICS PP 100 40 62 P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	47	P C	15. COMPUTER ORGANIZATION PP 100 40 50 P
06. PROGRAMMING LABORATORY	TW	25	10	20	P C	16. O. O. PROG. & COMP. GRAPH. LAB TW 50 20 44 P
07. PROGRAMMING LABORATORY	PR	50	20	37	P C	17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 42 P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	21	P C	18. MICROPROCESSORS & INTERFACING LABTW 50 20 41 P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	38	P C	19. MICROPROCESSORS & INTERFACING LABPR 50 20 41 P
10. SOFT SKILLS	TW	50	20	39	P C	20. DATA STRUCTURES LABORATORY TW 50 20 39 P
						21. DATA STRUCTURES LABORATORY PR 50 20 41 P

GRAND TOTAL = 1012/1500, RESULT: FIRST CLASS WITH DISTINCTION
ORDN. 1 MARKS :

S8054218 BHANGALE PRATIK DIGAMBAR			MEENAKSHI	, 71132425F , S8054218 , PIC	Г	, s80	054218
01. DISCRETE STRUCTURES PF	100	40	60 P C	11. ENGINEERING MATHEMATICS III PP	100	40	55 P
02. PROGRAMMING & PROBLEM SOLVING PF	100	40	70 P C	12. MICROPROC. & INTERFACING TECHNIQ.PP	100	40	59 P
03. DIGIT. ELECTRONICS & LOGIC DESIGNPP	100	40	71 P C	13. DATA STRUCTURES PP	100	40	78 P
04. DATA STRUCTURES AND ALGORITHMS PF	100	40	62 P C	14. COMPUTER GRAPHICS PP	100	40	59 P
05. HUMANITIES AND SOCIAL SCIENCE PF	100	40	50 P C	15. COMPUTER ORGANIZATION PP	100	40	54 P
06. PROGRAMMING LABORATORY TW	25	10	23 P C	16. O. O. PROG. & COMP. GRAPH. LAB TW	50	20	41 P
07. PROGRAMMING LABORATORY PR	50	20	32 P C	17. O. O. PROG. & COMP. GRAPH. LAB PR	50	20	40 P
08. DIGITAL ELECTRONICS LABORATORY TW	25	10	20 P C	18. MICROPROCESSORS & INTERFACING LABTW	50	20	40 P
09. DIGITAL ELECTRONICS LABORATORY PR	50	20	37 P C	19. MICROPROCESSORS & INTERFACING LABPR	50	20	28 P
10. SOFT SKILLS	50	20	41 P C	20. DATA STRUCTURES LABORATORY TW	50	20	33 P
				21. DATA STRUCTURES LABORATORY PR	50	20	34 P

GRAND TOTAL = 987/1500, RESULT: FIRST CLASS ORDN. 1 MARKS :

				ВНА	RATI		, 71045389C , S8054219 ,	PICT		, s80)5421	9
01. DISCRETE STRUCTURES	PP	100	40	60	P C	11.	ENGINEERING MATHEMATICS III	PP	100	40	41	Р
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	45	P C	12.	MICROPROC. & INTERFACING TECHNIQ	.PP	100	40	43	Р
03. DIGIT. ELECTRONICS & LOGIC DESIG	GNPP	100	40	72	P C	13.	DATA STRUCTURES	PP	100	40	60	Р
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	65	P C	14.	COMPUTER GRAPHICS	PP	100	40	67	Р
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	47	P C	15.	COMPUTER ORGANIZATION	PP	100	40	43	Р
06. PROGRAMMING LABORATORY	TW	25	10	18	P C	16.	O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	32	Р
07. PROGRAMMING LABORATORY	PR	50	20	41	P C	17.	O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	41	Р
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	19	P C	18.	MICROPROCESSORS & INTERFACING LA	BTW	50	20	29	Р
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	40	P C	19.	MICROPROCESSORS & INTERFACING LA	BPR	50	20	30	Р
10. SOFT SKILLS	TW	50	20	34	P C	20.	DATA STRUCTURES LABORATORY	TW	50	20	27	Р
						21.	DATA STRUCTURES LABORATORY	PR	50	20	35	Р
							TER) EXAMINATION MAY 2011 R TECHNOLOGY, PUNE.		 E NO.			 71)
							ITER) EXAMINATION MAY 2011					 71)
	CENT	TRE : F	PUNE I	INSTI	TUTE OF (COMPUTE	TER) EXAMINATION MAY 2011 R TECHNOLOGY, PUNE.	PAG	E NO.	07	(3	 71)
DATE: 18 AUG. 2011	CENT DF THE	TRE : F	PUNE I IDATE,	INSTI	TUTE OF (COMPUTE · · ·	TER) EXAMINATION MAY 2011 R TECHNOLOGY, PUNE.	PAG · · ·	E NO. 	O7	(3; 	71)
DATE: 18 AUG. 2011	CENT DF THE	TRE : F	PUNE I IDATE,	INSTI	TUTE OF (COMPUTE · · ·	TER) EXAMINATION MAY 2011 R TECHNOLOGY, PUNE.	PAG · · ·	E NO. 	O7	(3; 	 71)
DATE: 18 AUG. 2011	CENT DF THE	TRE : F	PUNE I IDATE,	INSTI , MO IN. P	TUTE OF (COMPUTE · · ·	TER) EXAMINATION MAY 2011 R TECHNOLOGY, PUNE.	PAG · · ·	E NO. E, S US CAF	07 GEAT N	(3; 	
DATE: 18 AUG. 2011	CENT DF THE	TRE : F	PUNE I IDATE,	INSTI , MO IN. P	TUTE OF C THER, PEF ASS MARKS	COMPUTE RMANENT 6, MARI	TER) EXAMINATION MAY 2011 R TECHNOLOGY, PUNE. REG. NO., PREVIOUS SEAT NO., C KS OBTAINED, P/F:PASS/FAIL, C:P	PAG OLLEG REVIO	E NO. E, S US CAF	07 GEAT N	(3' NO. /ER 	
DATE: 18 AUG. 2011	CENT DF THE MAX	TRE : F	PUNE I IDATE, S, MI	INSTI , MO IN. P ANU	TUTE OF C THER, PEF ASS MARKS	COMPUTE RMANENT 6, MARI 	TER) EXAMINATION MAY 2011 R TECHNOLOGY, PUNE. REG. NO., PREVIOUS SEAT NO., C KS OBTAINED, P/F:PASS/FAIL, C:P , 71045391E , S8054220 ,	PAG OLLEG REVIO PICT	E NO. E, S US CAF	07 SEAT N RRY ON	(3°	 0
DATE: 18 AUG. 2011	CENT OF THE MAX PP	TRE : F	PUNE I IDATE, 5, MI	INSTI , MO IN . P ANU 53 40	TUTE OF C THER, PEF ASS MARKS SAYA P	COMPUTE RMANENT 6, MARI 	TTER) EXAMINATION MAY 2011 R TECHNOLOGY, PUNE.	PAG OLLEG REVIO PICT	E NO. E, S US CAF	07 SEAT N RRY ON , S80	(3 NO. /ER 054220	 0 P
DATE: 18 AUG. 2011	CENT OF THE MAX PP	TRE : F	PUNE I IDATE, 5, MI 40 40	INSTI , MO IN . P ANU 53 40 60	TUTE OF C THER, PEF ASS MARKS SAYA P P C	RMANENT S, MAR	TER) EXAMINATION MAY 2011 R TECHNOLOGY, PUNE.	PAG OLLEG REVIO PICT PP .PP	E NO. E, S US CAF	07 SEAT N RRY OV , S80 40 40	(3 NO. /ER 054220 40 26	 O P F

co1s05

06. PROGRAMMING LABORATORY	TW	25	10	16	РС	16. O. O. PROG. & COMP. GRAPH. LAB TW 50 20 32	Р
07. PROGRAMMING LABORATORY	PR	50	20	42	P C	17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 22	Р
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	20	P C	18. MICROPROCESSORS & INTERFACING LABTW 50 20 36	Р
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	25	P C	19. MICROPROCESSORS & INTERFACING LABPR 50 20 32	Р
10. SOFT SKILLS	TW	50	20	33	P C	20. DATA STRUCTURES LABORATORY TW 50 20 35	Р
						21. DATA STRUCTURES LABORATORY PR 50 20 28	Р

GRAND TOTAL = 783/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS:

S8054221 BHOSALE SHITAL MANOHAR				JAY	⁄A	, 71132426D , S8054221 , PICT , S8054221	
01. DISCRETE STRUCTURES	PP	100	40	40	P C	11. ENGINEERING MATHEMATICS III PP 100 40 11 F	
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	44	Р	12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 40 P	
03. DIGIT. ELECTRONICS & LOGIC DESIGNATION OF THE PROPERTY OF	GNPP	100	40	41	P C	13. DATA STRUCTURES PP 100 40 40 P	
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	47	P C	14. COMPUTER GRAPHICS PP 100 40 46 P	
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	40	P C	15. COMPUTER ORGANIZATION PP 100 40 40 P	
06. PROGRAMMING LABORATORY	TW	25	10	17	P C	16. O. O. PROG. & COMP. GRAPH. LAB TW 50 20 39 P	
07. PROGRAMMING LABORATORY	PR	50	20	25	Р	17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 20\$ P	
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	19	P C	18. MICROPROCESSORS & INTERFACING LABTW 50 20 34 P	
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	20	P C	19. MICROPROCESSORS & INTERFACING LABPR 50 20 08 F	
10. SOFT SKILLS	TW	50	20	34	P C	20. DATA STRUCTURES LABORATORY TW 50 20 32 P	
						21. DATA STRUCTURES LABORATORY PR 50 20 10 F	

GRAND TOTAL = 647/1500, RESULT: FAILS A.T.K.T. [\$ 0.1]

ORDN. 1 MARKS : (17)2,

S8054222 BHURKE POOJA MILIND				GEETANJALI	, 71045395н , S8054222 , РІ	CT	, s80)54222	<u>}</u>
01. DISCRETE STRUCTURES	PP	100	40	81 P C	11. ENGINEERING MATHEMATICS III PP	100	40	60	Р
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	53 P C	12. MICROPROC. & INTERFACING TECHNIQ.PP Page 85	100	40	48	Р

0	3. DIGIT. ELECTRONICS & LOGIC DESIG	NPP	100	40	50	P C	13. DATA	A STRUCTURES	PP	100	40	58	Р
04	1. DATA STRUCTURES AND ALGORITHMS	PP	100	40	54	P C	14. COMF	PUTER GRAPHICS	PP	100	40	68	Р
0	5. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	56	P C	15. COMF	PUTER ORGANIZATION	PP	100	40	50	Р
00	5. PROGRAMMING LABORATORY	TW	25	10	21	P C	16. 0. 0	O. PROG. & COMP. GRAPH. LAB	TW	50	20	30	Р
0	7. PROGRAMMING LABORATORY	PR	50	20	30	P C	17. 0. 0	O. PROG. & COMP. GRAPH. LAB	PR	50	20	27	Р
08	3. DIGITAL ELECTRONICS LABORATORY	TW	25	10	20	P C	18. MICF	ROPROCESSORS & INTERFACING L	ABTW	50	20	33	Р
09	O. DIGITAL ELECTRONICS LABORATORY	PR	50	20	24	P C	19. MICF	ROPROCESSORS & INTERFACING L	ABPR	50	20	22	Р
10). SOFT SKILLS	TW	50	20	41	P C	20. DATA	A STRUCTURES LABORATORY	TW	50	20	34	Р
							21. DATA	A STRUCTURES LABORATORY	PR	50	20	80	F
GRA	ND TOTAL = $868/1500$, RESULT: FAILS	A.T	.K.T.										
ORDI	N. 1 MARKS :												

UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(COMPUTER) EXAMINATION MAY 2011

DATE: 18 AUG. 2011 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 08 (372)

NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8054223 BISWAJIT DEY				TAPATI RANI	, 71045397D , S8054223 , PICT		, s80	05422	:3
01. DISCRETE STRUCTURES	PP	100	40	60 P C	11. ENGINEERING MATHEMATICS III PP	100	40	69	Р
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	64 P C	12. MICROPROC. & INTERFACING TECHNIQ.PP	100	40	46	Р
03. DIGIT. ELECTRONICS & LOGIC DESI	GNPP	100	40	68 P C	13. DATA STRUCTURES PP	100	40	65	Р
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	67 P C	14. COMPUTER GRAPHICS PP	100	40	60	Р
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	53 P C	15. COMPUTER ORGANIZATION PP	100	40	62	Р
06. PROGRAMMING LABORATORY	TW	25	10	20 P C	16. O. O. PROG. & COMP. GRAPH. LAB TW	50	20	45	Р
07. PROGRAMMING LABORATORY	PR	50	20	40 P C	17. O. O. PROG. & COMP. GRAPH. LAB PR	50	20	45	Р
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	22 P C	18. MICROPROCESSORS & INTERFACING LABTW	50	20	40	Р
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	41 P C	19. MICROPROCESSORS & INTERFACING LABPR	50	20	37	Р
10. SOFT SKILLS	TW	50	20	40 P C	20. DATA STRUCTURES LABORATORY TW	50	20	42	Р

cols05 21. DATA STRUCTURES LABORATORY PR 50 20 34 P

GRAND TOTAL = 1020/1500, RESULT: FIRST CLASS WITH DISTINCTION
ORDN. 1 MARKS :

S8054224 BOTHALE SANJANA ANIL				KALPANA	, 71045399L	, S8054224 ,	PICT	, S
01. DISCRETE STRUCTURES	PP	100	40	60 P C				
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	57 P C				
03. DIGIT. ELECTRONICS & LOGIC DESI	GNPP	100	40	71 P C				
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	58 P C				
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	54 P C				
06. PROGRAMMING LABORATORY	TW	25	10	14 P C				
07. PROGRAMMING LABORATORY	PR	50	20	41 P C				
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	15 P C				
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	30 P C				
10. SOFT SKILLS	TW	50	20	31 P C				

FIRST TERM TOTAL = 431/700.

ORDN. 1 MARKS:

S8054225 CHANDORKAR ANUJA ANIRUDDH	IA			ARU	NA	, 71045401F , S8054225 , PICT , S8054225	
01. DISCRETE STRUCTURES	PP	100	40	60	P C	11. ENGINEERING MATHEMATICS III PP 100 40 76	Р
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	51	P C	12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 43	Р
03. DIGIT. ELECTRONICS & LOGIC DESIG	NPP	100	40	68	PС	13. DATA STRUCTURES PP 100 40 79	Р
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	76	P C	14. COMPUTER GRAPHICS PP 100 40 71	Р
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	49	PС	15. COMPUTER ORGANIZATION PP 100 40 58	Р
06. PROGRAMMING LABORATORY	TW	25	10	21	PС	16. O. O. PROG. & COMP. GRAPH. LAB TW 50 20 42	Р
07. PROGRAMMING LABORATORY	PR	50	20	37	РС	17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 36 Page 87	Р

••												
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	22	P C	18.	MICROPROCESSORS & INTERFACING LA	ABTW	50	20	42	Р
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	40	P C	19.	MICROPROCESSORS & INTERFACING LA	ABPR	50	20	42	Р
10. SOFT SKILLS	TW	50	20	40	P C	20.	DATA STRUCTURES LABORATORY	TW	50	20	45	Р
						21.	DATA STRUCTURES LABORATORY	PR	50	20	30	Р
GRAND TOTAL = 1028/1500, RESULT: FIRST	T CLAS	SS WITH	H DIST	ГІИСТ	-ION							
ORDN. 1 MARKS :												
	 PSTTV	 OF PU	 NF S	 F (COMPLI						
DATE : 18 AUG. 2011			-				R TECHNOLOGY, PUNE.	PΛG	iE NO.	09	(3	73)
											•	
NOTE: FIRST LINE : SEAT NO., NAME (SEAT N		
							KS OBTAINED, P/F:PASS/FAIL, C:					
					•					KKI U	/ LK	
		• •										
S8054226 CHAUDHARI DIPTI KRISHNA				SHA	ARDA		, 71045404L , S8054226 ,	PICT	-	, s80)5422	6
01 DISCRETE STRUCTURES		100	40	67	РС	11	ENGINEERING MATHEMATICS III		100	40	F 2	D
01. DISCRETE STRUCTURES	PP	100	+0	07	r C	11.	ENGINEERING MATHEMATICS III	PP	100	40	52	Р
02. PROGRAMMING & PROBLEM SOLVING	PP PP	100	40	45	PC		MICROPROC. & INTERFACING TECHNIC		100	40	52	
	PP			45	-	12.						Р
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	45 67	P C	12. 13.	MICROPROC. & INTERFACING TECHNIC	Q.PP	100	40	52	P P
02. PROGRAMMING & PROBLEM SOLVING 03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP GNPP	100 100	40 40	45 67 65	P C	12. 13. 14.	MICROPROC. & INTERFACING TECHNIC	Q.PP PP	100 100	40 40	52 47	P P P
02. PROGRAMMING & PROBLEM SOLVING 03. DIGIT. ELECTRONICS & LOGIC DESIGNATION DATA STRUCTURES AND ALGORITHMS	PP GNPP PP	100 100 100	40 40 40	45 67 65	P C P C	12. 13. 14. 15.	MICROPROC. & INTERFACING TECHNIC DATA STRUCTURES COMPUTER GRAPHICS	Q.PP PP PP	100 100 100	40 40 40	52 47 54	P P P
02. PROGRAMMING & PROBLEM SOLVING 03. DIGIT. ELECTRONICS & LOGIC DESIGN 04. DATA STRUCTURES AND ALGORITHMS 05. HUMANITIES AND SOCIAL SCIENCE	PP GNPP PP	100 100 100 100	40 40 40 40	45 67 65 52 17	P C P C P C	12. 13. 14. 15.	MICROPROC. & INTERFACING TECHNIC DATA STRUCTURES COMPUTER GRAPHICS COMPUTER ORGANIZATION O. O. PROG. & COMP. GRAPH. LAB	Q.PP PP PP PP	100 100 100 100	40 40 40 40	52 47 54 55	P P P P
02. PROGRAMMING & PROBLEM SOLVING 03. DIGIT. ELECTRONICS & LOGIC DESIGN 04. DATA STRUCTURES AND ALGORITHMS 05. HUMANITIES AND SOCIAL SCIENCE 06. PROGRAMMING LABORATORY	PP GNPP PP PP TW	100 100 100 100 25	40 40 40 40 10	45 67 65 52 17 38	P C P C P C P C	12. 13. 14. 15. 16.	MICROPROC. & INTERFACING TECHNIC DATA STRUCTURES COMPUTER GRAPHICS COMPUTER ORGANIZATION O. O. PROG. & COMP. GRAPH. LAB	Q.PP PP PP PP TW PR	100 100 100 100 50	40 40 40 40 20	52 47 54 55 30	P P P P
02. PROGRAMMING & PROBLEM SOLVING 03. DIGIT. ELECTRONICS & LOGIC DESIGN 04. DATA STRUCTURES AND ALGORITHMS 05. HUMANITIES AND SOCIAL SCIENCE 06. PROGRAMMING LABORATORY 07. PROGRAMMING LABORATORY	PP GNPP PP PP TW PR	100 100 100 100 25 50	40 40 40 40 10 20	45 67 65 52 17 38	P C P C P C P C P C	12. 13. 14. 15. 16. 17.	MICROPROC. & INTERFACING TECHNIC DATA STRUCTURES COMPUTER GRAPHICS COMPUTER ORGANIZATION O. O. PROG. & COMP. GRAPH. LAB O. O. PROG. & COMP. GRAPH. LAB	Q.PP PP PP TW PR ABTW	100 100 100 100 50	40 40 40 40 20 20	52 47 54 55 30 34	P P P P P
02. PROGRAMMING & PROBLEM SOLVING 03. DIGIT. ELECTRONICS & LOGIC DESIGN 04. DATA STRUCTURES AND ALGORITHMS 05. HUMANITIES AND SOCIAL SCIENCE 06. PROGRAMMING LABORATORY 07. PROGRAMMING LABORATORY 08. DIGITAL ELECTRONICS LABORATORY	PP GNPP PP TW PR TW	100 100 100 100 25 50 25	40 40 40 40 10 20	45 67 65 52 17 38 17 25	P C P C P C P C P C P C	12. 13. 14. 15. 16. 17. 18.	MICROPROC. & INTERFACING TECHNICO DATA STRUCTURES COMPUTER GRAPHICS COMPUTER ORGANIZATION O. O. PROG. & COMP. GRAPH. LAB O. O. PROG. & COMP. GRAPH. LAB MICROPROCESSORS & INTERFACING LA	Q.PP PP PP TW PR ABTW	100 100 100 100 50 50	40 40 40 40 20 20 20	52 47 54 55 30 34 29	P P P P P
02. PROGRAMMING & PROBLEM SOLVING 03. DIGIT. ELECTRONICS & LOGIC DESIGN 04. DATA STRUCTURES AND ALGORITHMS 05. HUMANITIES AND SOCIAL SCIENCE 06. PROGRAMMING LABORATORY 07. PROGRAMMING LABORATORY 08. DIGITAL ELECTRONICS LABORATORY 09. DIGITAL ELECTRONICS LABORATORY	PP GNPP PP TW PR TW PR	100 100 100 100 25 50 25	40 40 40 40 10 20 10 20	45 67 65 52 17 38 17 25	P C P C P C P C P C P C P C	12. 13. 14. 15. 16. 17. 18. 19.	MICROPROC. & INTERFACING TECHNICO DATA STRUCTURES COMPUTER GRAPHICS COMPUTER ORGANIZATION O. O. PROG. & COMP. GRAPH. LAB O. O. PROG. & COMP. GRAPH. LAB MICROPROCESSORS & INTERFACING LAM MICROPROCESSORS & INTERFACING LAM	Q.PP PP PP TW PR ABTW	100 100 100 100 50 50 50	40 40 40 40 20 20 20 20	52 47 54 55 30 34 29 23	P P P P P P
02. PROGRAMMING & PROBLEM SOLVING 03. DIGIT. ELECTRONICS & LOGIC DESIGN 04. DATA STRUCTURES AND ALGORITHMS 05. HUMANITIES AND SOCIAL SCIENCE 06. PROGRAMMING LABORATORY 07. PROGRAMMING LABORATORY 08. DIGITAL ELECTRONICS LABORATORY 09. DIGITAL ELECTRONICS LABORATORY 10. SOFT SKILLS	PP GNPP PP TW PR TW PR TW	100 100 100 25 50 25 50	40 40 40 10 20 10 20 20	45 67 65 52 17 38 17 25	P C P C P C P C P C P C P C	12. 13. 14. 15. 16. 17. 18. 19.	MICROPROC. & INTERFACING TECHNICO DATA STRUCTURES COMPUTER GRAPHICS COMPUTER ORGANIZATION O. O. PROG. & COMP. GRAPH. LAB O. O. PROG. & COMP. GRAPH. LAB MICROPROCESSORS & INTERFACING LAM MICROPROCESSORS & INTERFACING LAM DATA STRUCTURES LABORATORY	Q.PP PP PP TW PR ABTW ABPR TW	100 100 100 100 50 50 50 50	40 40 40 40 20 20 20 20 20	52 47 54 55 30 34 29 23 30	P P P P P P
02. PROGRAMMING & PROBLEM SOLVING 03. DIGIT. ELECTRONICS & LOGIC DESIGN 04. DATA STRUCTURES AND ALGORITHMS 05. HUMANITIES AND SOCIAL SCIENCE 06. PROGRAMMING LABORATORY 07. PROGRAMMING LABORATORY 08. DIGITAL ELECTRONICS LABORATORY 09. DIGITAL ELECTRONICS LABORATORY	PP GNPP PP TW PR TW PR TW	100 100 100 25 50 25 50	40 40 40 10 20 10 20 20	45 67 65 52 17 38 17 25	P C P C P C P C P C P C P C	12. 13. 14. 15. 16. 17. 18. 19.	MICROPROC. & INTERFACING TECHNICO DATA STRUCTURES COMPUTER GRAPHICS COMPUTER ORGANIZATION O. O. PROG. & COMP. GRAPH. LAB O. O. PROG. & COMP. GRAPH. LAB MICROPROCESSORS & INTERFACING LAM MICROPROCESSORS & INTERFACING LAM DATA STRUCTURES LABORATORY	Q.PP PP PP TW PR ABTW ABPR TW	100 100 100 100 50 50 50 50	40 40 40 40 20 20 20 20 20	52 47 54 55 30 34 29 23 30	P P P P P P

S8054227 CHAUDHARI RASHMI CHANDRAKANT	•		KARUNA	, 71045405j , S8054227 , PICT	, s8054227
01. DISCRETE STRUCTURES PE	100	40	65 P C	11. ENGINEERING MATHEMATICS III PP 100	40 62 P
02. PROGRAMMING & PROBLEM SOLVING PR	100	40	49 P C	12. MICROPROC. & INTERFACING TECHNIQ.PP 100	40 51 P
03. DIGIT. ELECTRONICS & LOGIC DESIGNPE	100	40	65 P C	13. DATA STRUCTURES PP 100	40 53 P
04. DATA STRUCTURES AND ALGORITHMS PR	100	40	74 P C	14. COMPUTER GRAPHICS PP 100	40 51 P
05. HUMANITIES AND SOCIAL SCIENCE PR	100	40	40 P C	15. COMPUTER ORGANIZATION PP 100	40 60 P
06. PROGRAMMING LABORATORY TV	25	10	21 PC	16. O. O. PROG. & COMP. GRAPH. LAB TW 50	20 35 P
07. PROGRAMMING LABORATORY PR	50	20	38 P C	17. O. O. PROG. & COMP. GRAPH. LAB PR 50	20 38 P
08. DIGITAL ELECTRONICS LABORATORY TV	25	10	22 P C	18. MICROPROCESSORS & INTERFACING LABTW 50	20 41 P
09. DIGITAL ELECTRONICS LABORATORY PR	50	20	35 P C	19. MICROPROCESSORS & INTERFACING LABPR 50	20 40 P
10. SOFT SKILLS TV	50	20	41 P C	20. DATA STRUCTURES LABORATORY TW 50	20 39 P
				21. DATA STRUCTURES LABORATORY PR 50	20 32 P

GRAND TOTAL = 952/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS:

S8054228 CHAVAN ASHISH VIJAYKUMAR				RAJE	SHREE		, 70925380L	, s8054228 ,	PICT		, s80!	54228	3
01. DISCRETE STRUCTURES	PP	100	40	53	P C	11.	ENGINEERING MATHEM	MATICS III	PP	100	40	67	Р
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	40	P C	12.	MICROPROC. & INTER	RFACING TECHNIQ	.PP	100	40	42	Р
03. DIGIT. ELECTRONICS & LOGIC DESIG	SNPP	100	40	45	P C	13.	DATA STRUCTURES		PP	100	40	70	Р
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	69	P C	14.	COMPUTER GRAPHICS		PP	100	40	53	Р
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	48	P C	15.	COMPUTER ORGANIZAT	ION	PP	100	40	52	Р
06. PROGRAMMING LABORATORY	TW	25	10	18	P C	16.	O. O. PROG. & COMP	P. GRAPH. LAB	TW	50	20	37	Р
07. PROGRAMMING LABORATORY	PR	50	20	37	P C	17.	O. O. PROG. & COMP	P. GRAPH. LAB	PR	50	20	34	Р
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	19	P C	18.	MICROPROCESSORS &	INTERFACING LA	BTW	50	20	39	Р
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	25	P C	19.	MICROPROCESSORS &	INTERFACING LA	BPR	50	20	39	Р
10. SOFT SKILLS	TW	50	20	38	P C	20.	DATA STRUCTURES LA	BORATORY	TW	50	20	33	Р
						21.	DATA STRUCTURES LA	ABORATORY	PR	50	20	33	Р

GRAND TOTAL = 891+09/1500, RESULT: FIRST CLASS [0.2]
ORDN. 1 MARKS:

02. PROGRAMMING & PROBLEM SOLVING PP 100 40 48 P C

03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 40 70 P C

	<u> </u>											
DATE: 18 AUG. 2011							UTER) EXAMINATION MAY 2011 ER TECHNOLOGY, PUNE.	PAG	SE NO.	10	(3	74)
NOTE: FIRST LINE : SEAT NO., NAME	OF THI	CAND	 IDATE S, M:	 , MO IN. P	 THER, ASS M	PERMANEN		COLLEC	GE, S	 SEAT N	NO. VER	
S8054229 CHHAJED DARSHAN RAJKUMAR				KUS	UM		, 71045407E , S8054229 ,	PICT	Г	, s80	05422	9
01. DISCRETE STRUCTURES	PP	100	40	65	P C	11	. ENGINEERING MATHEMATICS III	PP	100	40	65	Р
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	55	P C	12	. MICROPROC. & INTERFACING TECHNIC	Q.PP	100	40	49	Р
03. DIGIT. ELECTRONICS & LOGIC DESI	GNPP	100	40	72	P C	13	. DATA STRUCTURES	PP	100	40	71	Р
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	68	P C	14	. COMPUTER GRAPHICS	PP	100	40	61	Р
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	51	P C	15	. COMPUTER ORGANIZATION	PP	100	40	48	Р
06. PROGRAMMING LABORATORY	TW	25	10	19	P C	16	. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	36	Р
07. PROGRAMMING LABORATORY	PR	50	20	35	P C	17	. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	28	Р
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	21	P C	18	. MICROPROCESSORS & INTERFACING LA	ABTW	50	20	35	Р
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	37	P C	19	. MICROPROCESSORS & INTERFACING LA	ABPR	50	20	37	Р
10. SOFT SKILLS	TW	50	20	36	P C	20	. DATA STRUCTURES LABORATORY	TW	50	20	37	Р
						21	. DATA STRUCTURES LABORATORY	PR	50	20	37	Р
GRAND TOTAL = 963/1500, RESULT: FIRS	T CLAS	SS										
ORDN. 1 MARKS :												
S8054230 CHHAJED NIKHIL VINOD				MAN	ISHA		, 71045408C , S8054230 ,	PIC	Г	, s80	05423	0
01. DISCRETE STRUCTURES	PP	100	40	64	P C	11	. ENGINEERING MATHEMATICS III	PP	100	40	88	Р

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13. DATA STRUCTURES

12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 52 P

PP 100 40 67 P

04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	78	PC	14. COMPUTER GRAPHICS	PP	100	40	70	Р
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	53	P C	15. COMPUTER ORGANIZATION	PP	100	40	66	Р
06. PROGRAMMING LABORATORY	TW	25	10	21	P C	16. O. O. PROG. & COMP. GRAPH. LA	3 TW	50	20	29	Р
07. PROGRAMMING LABORATORY	PR	50	20	40	P C	17. O. O. PROG. & COMP. GRAPH. LA	3 PR	50	20	32	Р
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	20	P C	18. MICROPROCESSORS & INTERFACING	LABTW	50	20	35	Р
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	40	P C	19. MICROPROCESSORS & INTERFACING	LABPR	50	20	34	Р
10. SOFT SKILLS	TW	50	20	39	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	30	Р
						21. DATA STRUCTURES LABORATORY	PR	50	20	40	Р

GRAND TOTAL = 1016/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS :

S8054231 DESHMUKH MAYURAJ PRABHAKAI	R			ANUI	RADHA	, 71045412M , S8054231 , PICT , S8054231	
01. DISCRETE STRUCTURES	PP	100	40	53	РС	11. ENGINEERING MATHEMATICS III PP 100 40 40 F	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	57	P C	12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 28 F	F
03. DIGIT. ELECTRONICS & LOGIC DESIGN	NPP	100	40	58	P C	13. DATA STRUCTURES PP 100 40 40 F	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	73	P C	14. COMPUTER GRAPHICS PP 100 40 44 F	Þ
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	52	PС	15. COMPUTER ORGANIZATION PP 100 40 47 F	Þ
06. PROGRAMMING LABORATORY	TW	25	10	16	PС	16. O. O. PROG. & COMP. GRAPH. LAB TW 50 20 21 F	Þ
07. PROGRAMMING LABORATORY	PR	50	20	39	PС	17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 22 F	Þ
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	12	PС	18. MICROPROCESSORS & INTERFACING LABTW 50 20 22 F	Þ
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	28	PС	19. MICROPROCESSORS & INTERFACING LABPR 50 20 20 F	Þ
10. SOFT SKILLS	TW	50	20	26	PС	20. DATA STRUCTURES LABORATORY TW 50 20 21 F	Þ
						21. DATA STRUCTURES LABORATORY PR 50 20 25 F	P

GRAND TOTAL = 744/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(COMPUTER) EXAMINATION MAY 2011

DATE: 18 AUG. 2011 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 11 (375)

18. MICROPROCESSORS & INTERFACING LABTW

50 20 30 P

NOTE: FIRST LINE : SEAT NO.,	NAME OF THE CANDIDATE,	MOTHER, PERMANENT REG. NO.,	PREVIOUS SEAT NO., COLLE	EGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8054232 DESHMUKH SMITA ARUN				ASH	ILESHA	, 71045413к , S8054232 , РІСТ , S8054232
01. DISCRETE STRUCTURES	PP	100	40	61	P C	11. ENGINEERING MATHEMATICS III PP 100 40 66 P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	52	P C	12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 47 P
03. DIGIT. ELECTRONICS & LOGIC DESIG	NPP	100	40	67	P C	13. DATA STRUCTURES PP 100 40 57 P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	67	P C	14. COMPUTER GRAPHICS PP 100 40 52 P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	42	P C	15. COMPUTER ORGANIZATION PP 100 40 61 P
06. PROGRAMMING LABORATORY	TW	25	10	15	P C	16. O. O. PROG. & COMP. GRAPH. LAB TW 50 20 30 P
07. PROGRAMMING LABORATORY	PR	50	20	36	PС	17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 28 P

09. DIGITAL ELECTRONICS LABORATORY PR 50 20 26 P C 19. MICROPROCESSORS & INTERFACING LABPR 50 20 28 P 10. SOFT SKILLS TW 50 20 37 P C 20. DATA STRUCTURES LABORATORY TW 50 20 33 P 21. DATA STRUCTURES LABORATORY PR 50 20 33 P

25 10 20 P C

GRAND TOTAL = 889/1500, RESULT: HIGHER SECOND CLASS ORDN. 1 MARKS:

08. DIGITAL ELECTRONICS LABORATORY TW

S8054233 DISHA DAWANI				MEE	NA		, 71045419յ	, s8054233 ,	PICT		, s80)5423	3
01. DISCRETE STRUCTURES	PP	100	40	66	P C	11	. ENGINEERING MAT	HEMATICS III	PP	100	40	65	Р
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	53	P C	12	. MICROPROC. & IN	TERFACING TECHNIQ	.PP	100	40	42	Р
03. DIGIT. ELECTRONICS & LOGIC DESIG	GNPP	100	40	62	P C	13	. DATA STRUCTURES		PP	100	40	64	Р
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	76	P C	14	. COMPUTER GRAPHI	CS	PP	100	40	68	Р
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	45	P C	15	. COMPUTER ORGANI	ZATION	PP	100	40	50	Р
06. PROGRAMMING LABORATORY	TW	25	10	20	P C	16	. O. O. PROG. & C	OMP. GRAPH. LAB	TW	50	20	43	Р
07. PROGRAMMING LABORATORY	PR	50	20	44	P C	17	. O. O. PROG. & C	OMP. GRAPH. LAB	PR	50	20	36	Р
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	21	P C	18	. MICROPROCESSORS	& INTERFACING LAI Page 92	BTW	50	20	43	Р

09.	DIGITAL ELECTRONICS LABORATORY	PR	50	20	45	РС	19	MICROPROCESSORS & INTERFACING LA	BPR	50	20	40	Р
10.	SOFT SKILLS	TW	50	20	42	РС	20	DATA STRUCTURES LABORATORY	TW	50	20	45	Р
							21	DATA STRUCTURES LABORATORY	PR	50	20	39	Р
GRAND	TOTAL = 1009/1500, RESULT: FIRST	CLAS	SS WITH	dist	ΓINCT	ION							
ORDN.	1 MARKS :												
S80	54234 DUBEY AAKRITI MANOJ				ARC	CHANA		, 71045421L , S8054234 ,	PICT		, s80)5423	4
01.	DISCRETE STRUCTURES	PP	100	40	60	P C	11	ENGINEERING MATHEMATICS III	PP	100	40	67	Р
02.	PROGRAMMING & PROBLEM SOLVING	PP	100	40	64	P C	12	MICROPROC. & INTERFACING TECHNIQ	.PP	100	40	44	Р
03.	DIGIT. ELECTRONICS & LOGIC DESIG	NPP	100	40	75	P C	13	DATA STRUCTURES	PP	100	40	71	Р
04.	DATA STRUCTURES AND ALGORITHMS	PP	100	40	77	P C	14	COMPUTER GRAPHICS	PP	100	40	67	Р
05.	HUMANITIES AND SOCIAL SCIENCE	PP	100	40	52	P C	15	COMPUTER ORGANIZATION	PP	100	40	58	Р
06.	PROGRAMMING LABORATORY	TW	25	10	23	P C	16	O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	44	Р
07.	PROGRAMMING LABORATORY	PR	50	20	40	P C	17	O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	39	Р
08.	DIGITAL ELECTRONICS LABORATORY	TW	25	10	23	P C	18	MICROPROCESSORS & INTERFACING LA	BTW	50	20	45	Р
09.	DIGITAL ELECTRONICS LABORATORY	PR	50	20	32	P C	19	MICROPROCESSORS & INTERFACING LA	BPR	50	20	35	Р
10.	SOFT SKILLS	TW	50	20	43	P C	20	DATA STRUCTURES LABORATORY	TW	50	20	45	Р
							21	DATA STRUCTURES LABORATORY	PR	50	20	38	Р
CRAND	TOTAL = 1042/1500, RESULT: FIRST	. (1 /	SC WTTL	י חדכז	гтист	TON							
	1 MARKS :	CLA	33 WIII	1 013	11101	1011							
ORDIN:	I MARKS .												
0	UNIVE	RSITY	OF PU	NE ,S	.E.(2008 F	PAT.)(COMP	UTER) EXAMINATION MAY 2011					
ı	DATE : 18 AUG. 2011	CEN	TRE : F	PUNE 3	INSTI	TUTE	OF COMPUTE	ER TECHNOLOGY, PUNE.	PAG	E NO.	12	(3	76)
NOT	E: FIRST LINE : SEAT NO., NAME C	F THI	E CAND	DATE,	, MC	THER,	PERMANEN	REG. NO., PREVIOUS SEAT NO., CO	OLLEG	Ε, :	SEAT N	10.	
	OTHER LINES: HEAD OF PASSING,	MAX	. MARKS	5, M	IN. P	PASS M	IARKS, MAI	RKS OBTAINED, P/F:PASS/FAIL, C:P	REVIO	US CAI	RRY OV	/ER	

S8054235 DUBEY SHREYA CHANDRAMA				TAF	RA	cols05 , 71045422J , S8054235 , PICT , S8054235
01. DISCRETE STRUCTURES	PP	100	40	75	P C	11. ENGINEERING MATHEMATICS III PP 100 40 66 P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	66	P C	12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 59 P
03. DIGIT. ELECTRONICS & LOGIC DESIG	GNPP	100	40	79	P C	13. DATA STRUCTURES PP 100 40 59 P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	70	P C	14. COMPUTER GRAPHICS PP 100 40 72 P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	56	P C	15. COMPUTER ORGANIZATION PP 100 40 68 P
06. PROGRAMMING LABORATORY	TW	25	10	19	P C	16. O. O. PROG. & COMP. GRAPH. LAB TW 50 20 33 P
07. PROGRAMMING LABORATORY	PR	50	20	38	P C	17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 40 P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	21	P C	18. MICROPROCESSORS & INTERFACING LABTW 50 20 37 P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	32	P C	19. MICROPROCESSORS & INTERFACING LABPR 50 20 40 P
10. SOFT SKILLS	TW	50	20	41	P C	20. DATA STRUCTURES LABORATORY TW 50 20 35 P
						21. DATA STRUCTURES LABORATORY PR 50 20 34 P

GRAND TOTAL = 1040/1500, RESULT: FIRST CLASS WITH DISTINCTION
ORDN. 1 MARKS :

S8054236 DURGESH CHAPORKAR				SUM	IEDHA	, 71045423G , S8054236 , PICT , S805	4236
01. DISCRETE STRUCTURES	PP	100	40	63	P C	11. ENGINEERING MATHEMATICS III PP 100 40	42 P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	45	P C	12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40	25 F
03. DIGIT. ELECTRONICS & LOGIC DESIG	GNPP	100	40	75	P C	13. DATA STRUCTURES PP 100 40	64 P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	66	P C	14. COMPUTER GRAPHICS PP 100 40	63 P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	53	P C	15. COMPUTER ORGANIZATION PP 100 40	45 P
06. PROGRAMMING LABORATORY	TW	25	10	15	P C	16. O. O. PROG. & COMP. GRAPH. LAB TW 50 20	26 P
07. PROGRAMMING LABORATORY	PR	50	20	32	Р	17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20	38 P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	15	P C	18. MICROPROCESSORS & INTERFACING LABTW 50 20	25 P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	21	P C	19. MICROPROCESSORS & INTERFACING LABPR 50 20	22 P
10. SOFT SKILLS	TW	50	20	28	P C	20. DATA STRUCTURES LABORATORY TW 50 20	22 P
						21. DATA STRUCTURES LABORATORY PR 50 20	28 P

GRAND TOTAL = 813/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS:

01. DISCRETE STRUCTURES

02. PROGRAMMING & PROBLEM SOLVING PP 100 40 66 P C

04. DATA STRUCTURES AND ALGORITHMS PP 100 40 82 P C

03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100

S8054237 DURUGKAR SURAJ ANIL				NIR	RMALA	, 71045424E , S8054237 , PICT , S8054237
01. DISCRETE STRUCTURES	PP	100	40	61	P C	11. ENGINEERING MATHEMATICS III PP 100 40 40 P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	45	PС	12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 29 F
03. DIGIT. ELECTRONICS & LOGIC DESI	GNPP	100	40	44	PС	13. DATA STRUCTURES PP 100 40 40 P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	70	P C	14. COMPUTER GRAPHICS PP 100 40 58 P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	40	Р	15. COMPUTER ORGANIZATION PP 100 40 46 P
06. PROGRAMMING LABORATORY	TW	25	10	11	P C	16. O. O. PROG. & COMP. GRAPH. LAB TW 50 20 22 P
07. PROGRAMMING LABORATORY	PR	50	20	34	Р	17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 26 P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	14	PС	18. MICROPROCESSORS & INTERFACING LABTW 50 20 21 P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	20	P C	19. MICROPROCESSORS & INTERFACING LABPR 50 20 08 F
10. SOFT SKILLS	TW	50	20	26	P C	20. DATA STRUCTURES LABORATORY TW 50 20 21 P
						21. DATA STRUCTURES LABORATORY PR 50 20 08 F
GRAND TOTAL = 684/1500, RESULT: FAIL	SAT	κт				
ORDN. 1 MARKS :	3 A.I	. K. I .				
ORDN. I MARKS .						
UNIVE	RSITY	OF PU	INE ,S	.E.(2008 PA	AT.)(COMPUTER) EXAMINATION MAY 2011
DATE : 18 AUG. 2011	CEN.	TRE : I	PUNE :	INSTI	TUTE O	F COMPUTER TECHNOLOGY, PUNE. PAGE NO. 13 (377)
NOTE: FIRST LINE : SEAT NO., NAME	OF TH	E CAND	IDATE	, MC	THER,	PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING,	MAX	. MARK	S, M	IN. F	PASS MA	RKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
S8054238 GADE ABHIJEET BHARAT				MAN	IGAL	, 71132427B , S8054238 , PICT , S8054238

PP 100 40 56 P C

40 72 P C

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12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 47 P

PP 100 40 42 P

PP 100 40 67 P

40 52 P

11. ENGINEERING MATHEMATICS III

13. DATA STRUCTURES

14. COMPUTER GRAPHICS

05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	70	P C	15. COMPUTER ORGANIZATION PP 100 40 67	Р
06. PROGRAMMING LABORATORY	TW	25	10	20	P C	16. O. O. PROG. & COMP. GRAPH. LAB TW 50 20 35	Р
07. PROGRAMMING LABORATORY	PR	50	20	28	P C	17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 30	Р
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	19	P C	18. MICROPROCESSORS & INTERFACING LABTW 50 20 35	Р
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	20	P C	19. MICROPROCESSORS & INTERFACING LABPR 50 20 38	Р
10. SOFT SKILLS	TW	50	20	37	P C	20. DATA STRUCTURES LABORATORY TW 50 20 29	Р
						21. DATA STRUCTURES LABORATORY PR 50 20 25	Р

GRAND TOTAL = 937/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS:

S8054239 GADIA SAURABH PRASHANT				SUN	ITA		, 71045428н	, s8054239 ,	PICT		, s80	54239	9
01. DISCRETE STRUCTURES	PP	100	40	76	P C	11.	ENGINEERING MATH	EMATICS III	PP	100	40	85	Р
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	57	P C	12.	MICROPROC. & INTI	ERFACING TECHNIQ	.PP	100	40	46	Р
03. DIGIT. ELECTRONICS & LOGIC DESI	GNPP	100	40	72	РС	13.	DATA STRUCTURES		PP	100	40	69	Р
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	79	P C	14.	COMPUTER GRAPHICS	5	PP	100	40	62	Р
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	58	РС	15.	COMPUTER ORGANIZA	ATION	PP	100	40	66	Р
06. PROGRAMMING LABORATORY	TW	25	10	17	P C	16.	O. O. PROG. & COM	MP. GRAPH. LAB	TW	50	20	31	Р
07. PROGRAMMING LABORATORY	PR	50	20	30	P C	17.	O. O. PROG. & COM	MP. GRAPH. LAB	PR	50	20	42	Р
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	17	P C	18.	MICROPROCESSORS &	& INTERFACING LA	BTW	50	20	36	Р
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	34	P C	19.	MICROPROCESSORS &	& INTERFACING LA	BPR	50	20	36	Р
10. SOFT SKILLS	TW	50	20	37	P C	20.	DATA STRUCTURES I	_ABORATORY	TW	50	20	33	Р
						21.	DATA STRUCTURES I	_ABORATORY	PR	50	20	38	Р

GRAND TOTAL = 1021/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS :

\$8054240 GADSING MAYURI LAXMAN SAVITA , 71045429F , \$8054240 , PICT , \$8054240

						cols05
01. DISCRETE STRUCTURES	PP	100	40	73	РС	11. ENGINEERING MATHEMATICS III PP 100 40 67 P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	49	P C	12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 40 P
03. DIGIT. ELECTRONICS & LOGIC DESIGNATION OF THE PROPERTY OF	GNPP	100	40	64	P C	13. DATA STRUCTURES PP 100 40 56 P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	67	P C	14. COMPUTER GRAPHICS PP 100 40 56 P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	55	P C	15. COMPUTER ORGANIZATION PP 100 40 51 P
06. PROGRAMMING LABORATORY	TW	25	10	20	P C	16. O. O. PROG. & COMP. GRAPH. LAB TW 50 20 37 P
07. PROGRAMMING LABORATORY	PR	50	20	28	P C	17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 40 P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	17	P C	18. MICROPROCESSORS & INTERFACING LABTW 50 20 38 P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	35	P C	19. MICROPROCESSORS & INTERFACING LABPR 50 20 37 P
10. SOFT SKILLS	TW	50	20	36	P C	20. DATA STRUCTURES LABORATORY TW 50 20 35 P
						21. DATA STRUCTURES LABORATORY PR 50 20 39 P

GRAND TOTAL = 940/1500, RESULT: FIRST CLASS ORDN. 1 MARKS :

[JNIVERSITY OF PUNE ,S.E.(2008 PAT.)(COMPUTER)) EXAMINATION MAY 2011

DATE: 18 AUG. 2011 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 14 (378)

NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8054241 GAIKWAD MAHESH EKNATH				LAT	A		, 71045431н	, s8054241 ,	PICT		, s80)5424:	1
01. DISCRETE STRUCTURES	PP	100	40	40	Р	11.	ENGINEERING MATHER	MATICS III	PP	100	40	40	Р
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	32	F	12.	MICROPROC. & INTE	RFACING TECHNIQ	.PP	100	40	32	F
03. DIGIT. ELECTRONICS & LOGIC DESI	GNPP	100	40	47	P C	13.	DATA STRUCTURES		PP	100	40	32	F
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	42	P C	14.	COMPUTER GRAPHICS		PP	100	40	46	Р
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	40	P C	15.	COMPUTER ORGANIZA	ΓΙΟΝ	PP	100	40	40	Р
06. PROGRAMMING LABORATORY	TW	25	10	14	P C	16.	O. O. PROG. & COM	P. GRAPH. LAB	TW	50	20	27	Р
07. PROGRAMMING LABORATORY	PR	50	20	12	F	17.	O. O. PROG. & COM	P. GRAPH. LAB	PR	50	20	04	F
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	16	P C	18.	MICROPROCESSORS &	INTERFACING LA	BTW	50	20	26	Р
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	20	P C	19.	HIZCHOI HOCESSONS a	INTERFACING LA	BPR	50	20	22	Р

10. SOFT SKILLS	TW	50	20	31	P C	20.	DATA STRUCTURES LABORATORY	TW	50	20	26	Р
						21.	DATA STRUCTURES LABORATORY	PR	50	20	25	Р
GRAND TOTAL = 614/1500, RESULT: FAIL	S A.T	.к.т.										
ORDN. 1 MARKS :												
S8054242 GAIKWAD PUSHAKAR KISHOR				VAI	SHALI		, 71045432F , S8054242 ,	PICT	-	, s80	05424	2
01. DISCRETE STRUCTURES	PP	100	40	64	P C	11.	ENGINEERING MATHEMATICS III	PP	100	40	40	Р
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	55	P C	12.	MICROPROC. & INTERFACING TECHNIQ	.PP	100	40	53	Р
03. DIGIT. ELECTRONICS & LOGIC DESIGNATION	GNPP	100	40	82	P C	13.	DATA STRUCTURES	PP	100	40	61	Р
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	76	P C	14.	COMPUTER GRAPHICS	PP	100	40	70	Р
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	65	P C	15.	COMPUTER ORGANIZATION	PP	100	40	58	Р
06. PROGRAMMING LABORATORY	TW	25	10	21	P C	16.	O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	35	Р
07. PROGRAMMING LABORATORY	PR	50	20	45	P C	17.	O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	43	Р
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	19	P C	18.	MICROPROCESSORS & INTERFACING LA	BTW	50	20	40	Р
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	44	P C	19.	MICROPROCESSORS & INTERFACING LA	BPR	50	20	44	Р
10. SOFT SKILLS	TW	50	20	38	P C	20.	DATA STRUCTURES LABORATORY	TW	50	20	38	Р
						21.	DATA STRUCTURES LABORATORY	PR	50	20	37	Р
RAND TOTAL = 1028/1500, RESULT: FIRS	T (1 A	SS WTT	H DTS	TTNCT	TON							
ORDN. 1 MARKS :	. CL/	33 WIT	. 513	11101	1011							
S8054243 GANDHI ANUJ VIDYUT				SHA	RMILA		, 71045433D , S8054243 ,	PICT	-	, s80)5424	13
01. DISCRETE STRUCTURES	PP	100	40	69	РC	11.	ENGINEERING MATHEMATICS III	PP	100	40	68	Р
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	42	РС	12.	MICROPROC. & INTERFACING TECHNIQ	. PP	100	40	55	Р
03. DIGIT. ELECTRONICS & LOGIC DESIGNATION OF THE PROPERTY OF	GNPP	100	40	56	РС		DATA STRUCTURES	PP	100	40	58	Р
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40		РC		COMPUTER GRAPHICS	PP	100	40	64	Р
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40		P C		COMPUTER ORGANIZATION	PP	100	40	65	
33. HORMATILES AND SOCIAL SCIENCE	• •	-00	.0	10		17.	CO. II OTER ORGANIZZATION		100		33	•

06. PROGRAMMING LABORATORY	TW	25	10	15	P C	cols05 16. O. O. PROG. & COMP. GRAPH. LAB TW 50 20	34 P
07. PROGRAMMING LABORATORY	PR	50	20	32	P C	17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20	34 P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	17	P C	18. MICROPROCESSORS & INTERFACING LABTW 50 20	40 P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	22	P C	19. MICROPROCESSORS & INTERFACING LABPR 50 20	46 P
10. SOFT SKILLS	TW	50	20	34	P C	20. DATA STRUCTURES LABORATORY TW 50 20	34 P
						21. DATA STRUCTURES LABORATORY PR 50 20	31 P
GRAND TOTAL = 920/1500, RESULT: FIRST	Γ CLAS	SS					
ORDN. 1 MARKS :							
						,	
			-			COMPUTER) EXAMINATION MAY 2011	(270)
DATE : 18 AUG. 2011	CENT	RE : F	PUNE I	INSTI	TUTE OF C	MPUTER TECHNOLOGY, PUNE. PAGE NO. 15 (379)
						ANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.	
						ANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVE	
				IN. P			ER
OTHER LINES: HEAD OF PASSING,				IN. P	PASS MARKS	MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVE	ER
OTHER LINES: HEAD OF PASSING, S8054244 GAUTAM KUMAR	MAX.	MARKS	S, MI	IN. P	PASS MARKS	MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVE	ER 54244
OTHER LINES: HEAD OF PASSING, S8054244 GAUTAM KUMAR 01. DISCRETE STRUCTURES	MAX.	MARKS	S, MI	EN. P BAL 65	PASS MARKS A DEVI P C	MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVE , 71045434B , S8054244 , PICT , S805 11. ENGINEERING MATHEMATICS III PP 100 40	ER 54244 82 P
OTHER LINES: HEAD OF PASSING, S8054244 GAUTAM KUMAR 01. DISCRETE STRUCTURES 02. PROGRAMMING & PROBLEM SOLVING	MAX.	100 100	S, MI 40 40	BAL 65	PASS MARKSA DEVI P C P C	MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVE , 71045434B , \$8054244 , PICT , \$805 11. ENGINEERING MATHEMATICS III PP 100 40 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40	60 P
OTHER LINES: HEAD OF PASSING, S8054244 GAUTAM KUMAR O1. DISCRETE STRUCTURES O2. PROGRAMMING & PROBLEM SOLVING O3. DIGIT. ELECTRONICS & LOGIC DESIGN	PP PP PP GNPP	100 100 100	40 40 40 40	BAL 65 51	PASS MARKS .A DEVI P C P C P C	MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVE , 71045434B , S8054244 , PICT , S805 11. ENGINEERING MATHEMATICS III PP 100 40 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 13. DATA STRUCTURES PP 100 40	64244 82 P 60 P 69 P
OTHER LINES: HEAD OF PASSING, S8054244 GAUTAM KUMAR O1. DISCRETE STRUCTURES O2. PROGRAMMING & PROBLEM SOLVING O3. DIGIT. ELECTRONICS & LOGIC DESIGNATION OF THE PROBLEM STRUCTURES AND ALGORITHMS	PP PP PP	100 100 100 100	40 40 40 40	BAL 65 51 69 72 62	PASS MARKS .A DEVI P C P C P C P C	MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVE , 71045434B , S8054244 , PICT , S805 11. ENGINEERING MATHEMATICS III PP 100 40 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 13. DATA STRUCTURES PP 100 40 14. COMPUTER GRAPHICS PP 100 40	64244 82 P 60 P 69 P
OTHER LINES: HEAD OF PASSING, S8054244 GAUTAM KUMAR O1. DISCRETE STRUCTURES O2. PROGRAMMING & PROBLEM SOLVING O3. DIGIT. ELECTRONICS & LOGIC DESIGNOUS AND ALGORITHMS O5. HUMANITIES AND SOCIAL SCIENCE	PP PP PP	100 100 100 100 100	40 40 40 40 40 40	BAL 65 51 69 72 62 19	PASS MARKS .A DEVI P C P C P C P C P C	MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVE, 71045434B , S8054244 , PICT , S805 11. ENGINEERING MATHEMATICS III PP 100 40 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 13. DATA STRUCTURES PP 100 40 14. COMPUTER GRAPHICS PP 100 40 15. COMPUTER ORGANIZATION PP 100 40 16. O. O. PROG. & COMP. GRAPH. LAB TW 50 20	82 P 60 P 69 P 71 P
OTHER LINES: HEAD OF PASSING, S8054244 GAUTAM KUMAR O1. DISCRETE STRUCTURES O2. PROGRAMMING & PROBLEM SOLVING O3. DIGIT. ELECTRONICS & LOGIC DESIGNATION O4. DATA STRUCTURES AND ALGORITHMS O5. HUMANITIES AND SOCIAL SCIENCE O6. PROGRAMMING LABORATORY	PP PP PP TW	100 100 100 100 100 25	40 40 40 40 40 40	BAL 65 51 69 72 62 19	PASS MARKS A DEVI P C P C P C P C P C P C P C P	MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVE, 71045434B , S8054244 , PICT , S805 11. ENGINEERING MATHEMATICS III PP 100 40 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 13. DATA STRUCTURES PP 100 40 14. COMPUTER GRAPHICS PP 100 40 15. COMPUTER ORGANIZATION PP 100 40 16. O. O. PROG. & COMP. GRAPH. LAB TW 50 20	82 P 60 P 69 P 71 P 59 P
OTHER LINES: HEAD OF PASSING, S8054244 GAUTAM KUMAR O1. DISCRETE STRUCTURES O2. PROGRAMMING & PROBLEM SOLVING O3. DIGIT. ELECTRONICS & LOGIC DESIGNATION O4. DATA STRUCTURES AND ALGORITHMS O5. HUMANITIES AND SOCIAL SCIENCE O6. PROGRAMMING LABORATORY O7. PROGRAMMING LABORATORY	PP PP PP TW PR	100 100 100 100 100 25 50	40 40 40 40 40 40 20	BAL 65 51 69 72 62 19 28	PASS MARKS A DEVI P C P C P C P C P C P C P C P	MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVE , 71045434B , S8054244 , PICT , S805 11. ENGINEERING MATHEMATICS III PP 100 40 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 13. DATA STRUCTURES PP 100 40 14. COMPUTER GRAPHICS PP 100 40 15. COMPUTER ORGANIZATION PP 100 40 16. O. O. PROG. & COMP. GRAPH. LAB TW 50 20 17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20	ER
OTHER LINES: HEAD OF PASSING, S8054244 GAUTAM KUMAR O1. DISCRETE STRUCTURES O2. PROGRAMMING & PROBLEM SOLVING O3. DIGIT. ELECTRONICS & LOGIC DESIGN O4. DATA STRUCTURES AND ALGORITHMS O5. HUMANITIES AND SOCIAL SCIENCE O6. PROGRAMMING LABORATORY O7. PROGRAMMING LABORATORY O8. DIGITAL ELECTRONICS LABORATORY	PP PP PP TW PR TW	100 100 100 100 100 25 50 25	40 40 40 40 40 10 20	BAL 65 51 69 72 62 19 28	PASS MARKS A DEVI P C P C P C P C P C P C P C P	MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVE , 71045434B , \$8054244 , PICT , \$805 11. ENGINEERING MATHEMATICS III PP 100 40 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 13. DATA STRUCTURES PP 100 40 14. COMPUTER GRAPHICS PP 100 40 15. COMPUTER ORGANIZATION PP 100 40 16. O. O. PROG. & COMP. GRAPH. LAB TW 50 20 17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 18. MICROPROCESSORS & INTERFACING LABTW 50 20	ER
OTHER LINES: HEAD OF PASSING, S8054244 GAUTAM KUMAR O1. DISCRETE STRUCTURES O2. PROGRAMMING & PROBLEM SOLVING O3. DIGIT. ELECTRONICS & LOGIC DESIGN O4. DATA STRUCTURES AND ALGORITHMS O5. HUMANITIES AND SOCIAL SCIENCE O6. PROGRAMMING LABORATORY O7. PROGRAMMING LABORATORY O8. DIGITAL ELECTRONICS LABORATORY O9. DIGITAL ELECTRONICS LABORATORY	PP PP PP TW PR TW PR	100 100 100 100 25 50 25	40 40 40 40 40 10 20 10	EN. F BAL 65 51 69 72 62 19 28 19	PASS MARKS A DEVI P C P C P C P C P C P C P C P	MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVE , 71045434B , S8054244 , PICT , S805 11. ENGINEERING MATHEMATICS III PP 100 40 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 13. DATA STRUCTURES PP 100 40 14. COMPUTER GRAPHICS PP 100 40 15. COMPUTER ORGANIZATION PP 100 40 16. O. O. PROG. & COMP. GRAPH. LAB TW 50 20 17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 18. MICROPROCESSORS & INTERFACING LABTW 50 20 19. MICROPROCESSORS & INTERFACING LABPR 50 20	ER

GRAND TOTAL = 977/1500, RESULT: FIRST CLASS
ORDN. 1 MARKS :

				•			•											•																				

S8054245 GOKHALE GAURAV SUHAS				SWA	TI	, 71132428L , S8054245 , PICT , S8054	245
01. DISCRETE STRUCTURES	PP	100	40	40	P C	11. ENGINEERING MATHEMATICS III PP 100 40 4	0 Р
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	48	P C	12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 4	2 P
03. DIGIT. ELECTRONICS & LOGIC DESIG	GNPP	100	40	40	P C	13. DATA STRUCTURES PP 100 40 2	9 F
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	66	P C	14. COMPUTER GRAPHICS PP 100 40 5	7 P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	58	P C	15. COMPUTER ORGANIZATION PP 100 40 5	0 Р
06. PROGRAMMING LABORATORY	TW	25	10	16	P C	16. O. O. PROG. & COMP. GRAPH. LAB TW 50 20 2	4 P
07. PROGRAMMING LABORATORY	PR	50	20	22	Р	17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 2	2 P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	18	P C	18. MICROPROCESSORS & INTERFACING LABTW 50 20 2	0 Р
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	27	P C	19. MICROPROCESSORS & INTERFACING LABPR 50 20 2	0 Р
10. SOFT SKILLS	TW	50	20	29	P C	20. DATA STRUCTURES LABORATORY TW 50 20 2	0 Р
						21. DATA STRUCTURES LABORATORY PR 50 20 0	4 F

GRAND TOTAL = 692/1500, RESULT: FAILS A.T.K.T.
ORDN. 1 MARKS :

S8054246 GUPTA TARUN SATYABHUSHAN				KAN	AK	, 71045442C , S8054246 , PICT , S	8054246
01. DISCRETE STRUCTURES	PP	100	40	74	P C	11. ENGINEERING MATHEMATICS III PP 100 40) 55 P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	47	P C	12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40) 48 P
03. DIGIT. ELECTRONICS & LOGIC DESIG	SNPP	100	40	68	P C	13. DATA STRUCTURES PP 100 40) 53 P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	73	P C	14. COMPUTER GRAPHICS PP 100 40	69 Р
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	50	P C	15. COMPUTER ORGANIZATION PP 100 40	63 P
06. PROGRAMMING LABORATORY	TW	25	10	15	P C	16. O. O. PROG. & COMP. GRAPH. LAB TW 50 20) 29 P
07. PROGRAMMING LABORATORY	PR	50	20	30	P C	17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20	37 P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	17	P C	18. MICROPROCESSORS & INTERFACING LABTW 50 20	30 P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	23	P C	19. MICROPROCESSORS & INTERFACING LABPR 50 20	32 P
10. SOFT SKILLS	TW	50	20	34	P C	20. DATA STRUCTURES LABORATORY TW 50 20	32 P

cols05 21. DATA STRUCTURES LABORATORY PR 50 20 37 P

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ORDN. 1 MARKS: UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(COMPUTER) EXAMINATION MAY 2011 DATE: 18 AUG. 2011 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 16 (380) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER S8054247 HARSHIT SINGHANIA , 71045445H , S8054247 , PICT SEEMA , s8054247 01. DISCRETE STRUCTURES PP 100 40 54 P C 11. ENGINEERING MATHEMATICS III PP 100 40 24 F 02. PROGRAMMING & PROBLEM SOLVING PP 100 40 P C 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 40 P 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 40 66 P C 13. DATA STRUCTURES 100 40 48 P 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 58 P C 14. COMPUTER GRAPHICS 100 40 56 P 05. HUMANITIES AND SOCIAL SCIENCE 100 40 40 P C 15. COMPUTER ORGANIZATION 100 40 06. PROGRAMMING LABORATORY 25 10 18 P C 16. O. O. PROG. & COMP. GRAPH. LAB TW 50 20 30 P 07. PROGRAMMING LABORATORY 50 20 41 P C 17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 31 P PR 08. DIGITAL ELECTRONICS LABORATORY TW 25 10 15 P C 18. MICROPROCESSORS & INTERFACING LABTW 50 20 29 P 09. DIGITAL ELECTRONICS LABORATORY PR 50 20 34 P C 19. MICROPROCESSORS & INTERFACING LABPR 50 20 20 P 10. SOFT SKILLS 50 20 35 P C 20. DATA STRUCTURES LABORATORY 50 20 28 P TW 21. DATA STRUCTURES LABORATORY 50 20 36 P PR GRAND TOTAL = 791/1500, RESULT: FAILS A.T.K.T. RESULT RESERVED FOR BKLG ORDN. 1 MARKS: S8054248 JADHAV PRASHANT UTTAM ANITA , 71045454G , s8054248 , PICT , s8054248 01. DISCRETE STRUCTURES 40 P C 11. ENGINEERING MATHEMATICS III PP 100 40 40 P

GRAND TOTAL = 916/1500, RESULT: FIRST CLASS

							cols05	
02.	PROGRAMMING & PROBLEM SOLVING	PP	100	40	40	Р	12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 AA F	
03.	DIGIT. ELECTRONICS & LOGIC DESIG	NPP	100	40	40	P C	13. DATA STRUCTURES PP 100 40 40 P	
04.	DATA STRUCTURES AND ALGORITHMS	PP	100	40	53	P C	14. COMPUTER GRAPHICS PP 100 40 49 P	
05.	HUMANITIES AND SOCIAL SCIENCE	PP	100	40	40	Р	15. COMPUTER ORGANIZATION PP 100 40 40 P	
06.	PROGRAMMING LABORATORY	TW	25	10	13	P C	16. O. O. PROG. & COMP. GRAPH. LAB TW 50 20 28 P	
07.	PROGRAMMING LABORATORY	PR	50	20	35	Р	17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 20\$ P	
08.	DIGITAL ELECTRONICS LABORATORY	TW	25	10	11	P C	18. MICROPROCESSORS & INTERFACING LABTW 50 20 22 P	
09.	DIGITAL ELECTRONICS LABORATORY	PR	50	20	21	P C	19. MICROPROCESSORS & INTERFACING LABPR 50 20 05 F	
10.	SOFT SKILLS	TW	50	20	30	P C	20. DATA STRUCTURES LABORATORY TW 50 20 21 P	
							21. DATA STRUCTURES LABORATORY PR 50 20 02 F	
CDAND	TOTAL 500 /1500 DEGILET FATLO		T	Г¢	o 17			
GRAND	TOTAL = $590/1500$, RESULT: FAILS	A.I	.K.I.	Ľ⊅	0.1]			
ORDN.	1 MARKS : (17)2,							

S8054249 JAIN PUJAN PRAFULLAKUMAR				JYO	TI	, 71045456C , S8054249 , PICT , S8054249
01. DISCRETE STRUCTURES	PP	100	40	57	РC	11. ENGINEERING MATHEMATICS III PP 100 40 40 P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	40	P C	12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 40 P
03. DIGIT. ELECTRONICS & LOGIC DESIG	NPP	100	40	56	P C	13. DATA STRUCTURES PP 100 40 63 P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	70	P C	14. COMPUTER GRAPHICS PP 100 40 61 P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	40	P C	15. COMPUTER ORGANIZATION PP 100 40 59 P
06. PROGRAMMING LABORATORY	TW	25	10	17	P C	16. O. O. PROG. & COMP. GRAPH. LAB TW 50 20 30 P
07. PROGRAMMING LABORATORY	PR	50	20	35	P C	17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 10 F
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	15	P C	18. MICROPROCESSORS & INTERFACING LABTW 50 20 31 P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	20	P C	19. MICROPROCESSORS & INTERFACING LABPR 50 20 37 P
10. SOFT SKILLS	TW	50	20	33	P C	20. DATA STRUCTURES LABORATORY TW 50 20 26 P
						21. DATA STRUCTURES LABORATORY PR 50 20 37 P

GRAND TOTAL = 817/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS:

NOTE: FIRST LINE : SEAT NO., NAME (DEC NO DESTRUCT			 SEAT N		•
·					•		KS OBTAINED, P/F:PASS/FAIL, C		•			
·			-				RS OBTAINED, P/F.PASS/FAIL, C					
S8054250 JAIN SAKSHI PARAS				JY0			, 71045457M , S8054250 ,			, s80		
01. DISCRETE STRUCTURES	PP	100	40	69	P C	11.	ENGINEERING MATHEMATICS III	PP	100	40	79	
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	49	P C	12.	MICROPROC. & INTERFACING TECHN	Q.PP	100	40	56	
03. DIGIT. ELECTRONICS & LOGIC DESIG	NPP	100	40	58	P C	13.	DATA STRUCTURES	PP	100	40	60	
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	71	P C	14.	COMPUTER GRAPHICS	PP	100	40	65	
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	51	P C	15.	COMPUTER ORGANIZATION	PP	100	40	61	
06. PROGRAMMING LABORATORY	TW	25	10	21	P C	16.	O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	37	
07. PROGRAMMING LABORATORY	PR	50	20	30	P C	17.	O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	12	
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	21	P C	18.	MICROPROCESSORS & INTERFACING I	ABTW	50	20	37	
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	32	P C	19.	MICROPROCESSORS & INTERFACING I	ABPR	50	20	42	
10. SOFT SKILLS	TW	50	20	38	P C	20.	DATA STRUCTURES LABORATORY	TW	50	20	34	
						21.	DATA STRUCTURES LABORATORY	PR	50	20	38	
AND TOTAL = 961/1500, RESULT: FAILS	5 A.T.	к.т.										
DN. 1 MARKS :												

, 71045458K , S8054251 , PICT S8054251 JAISWAL PRATIK PRAKASH , S8054251 KALPANA 01. DISCRETE STRUCTURES PP 100 40 58 P C 11. ENGINEERING MATHEMATICS III PP 100 02. PROGRAMMING & PROBLEM SOLVING PP 100 40 50 P C 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 52 P 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 40 50 P C 13. DATA STRUCTURES PP 100 40 61 P 04. DATA STRUCTURES AND ALGORITHMS 100 40 64 P C 14. COMPUTER GRAPHICS PP 100 40 56 P 05. HUMANITIES AND SOCIAL SCIENCE 100 65 P C 15. COMPUTER ORGANIZATION PP 100 40 52 P 06. PROGRAMMING LABORATORY 25 10 19 P C 16. O. O. PROG. & COMP. GRAPH. LAB TW 50 20 39 P

cols05 07. PROGRAMMING LABORATORY 50 20 37 P 17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 35 P 08. DIGITAL ELECTRONICS LABORATORY TW 25 10 19 P C 18. MICROPROCESSORS & INTERFACING LABTW 50 20 39 P 09. DIGITAL ELECTRONICS LABORATORY 20 20 P C 19. MICROPROCESSORS & INTERFACING LABPR 50 20 21 P 10. SOFT SKILLS 50 20 36 P C 20. DATA STRUCTURES LABORATORY 50 20 32 P TW TW 50 20 39 P 21. DATA STRUCTURES LABORATORY GRAND TOTAL = 892+08/1500, RESULT: FIRST CLASS [0.2] ORDN. 1 MARKS: S8054252 JAMES DADO ANIA , 71045460M , S8054252 , PICT , S8054252 01. DISCRETE STRUCTURES PP 100 40 40 P C 11. ENGINEERING MATHEMATICS III PP 100 40 40 P 02. PROGRAMMING & PROBLEM SOLVING 100 40 40 P C 12. MICROPROC. & INTERFACING TECHNIQ.PP 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 40 57 P 13. DATA STRUCTURES 100 40 47 P 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 65 P C 14. COMPUTER GRAPHICS 53 P 100 40 05. HUMANITIES AND SOCIAL SCIENCE 100 100 47 P 40 64 P C 15. COMPUTER ORGANIZATION 40 10 16 P C 06. PROGRAMMING LABORATORY 20 28 P 25 16. O. O. PROG. & COMP. GRAPH. LAB TW 50 07. PROGRAMMING LABORATORY 50 20 25 P C 17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 26 P 08. DIGITAL ELECTRONICS LABORATORY TW 18. MICROPROCESSORS & INTERFACING LABTW 25 10 16 P C 20 26 P 09. DIGITAL ELECTRONICS LABORATORY PR 50 20 24 P 19. MICROPROCESSORS & INTERFACING LABPR 20 10 F 10. SOFT SKILLS TW 50 20 37 P C 20. DATA STRUCTURES LABORATORY TW 50 20 22 P 50 20 02 F 21. DATA STRUCTURES LABORATORY

GRAND TOTAL = 725/1500, RESULT: FAILS A.T.K.T.
ORDN. 1 MARKS :

UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(COMPUTER) EXAMINATION MAY 2011

DATE: 18 AUG. 2011 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 18 (382)

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

cols	s05
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S8054253 JASPREET SINGH HORA				BHU	PENDER	, 71045461K , S8054253 , PICT , S8054253	3
01. DISCRETE STRUCTURES	PP	100	40	59	P C	11. ENGINEERING MATHEMATICS III PP 100 40 59	Р
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	57	P C	12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 57	Р
03. DIGIT. ELECTRONICS & LOGIC DESIG	GNPP	100	40	58	P C	13. DATA STRUCTURES PP 100 40 52	Р
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	49	P C	14. COMPUTER GRAPHICS PP 100 40 52	Р
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	51	P C	15. COMPUTER ORGANIZATION PP 100 40 49	Р
06. PROGRAMMING LABORATORY	TW	25	10	20	P C	16. O. O. PROG. & COMP. GRAPH. LAB TW 50 20 42	Р
07. PROGRAMMING LABORATORY	PR	50	20	26	P C	17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 08	F
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	18	P C	18. MICROPROCESSORS & INTERFACING LABTW 50 20 43	Р
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	36	P C	19. MICROPROCESSORS & INTERFACING LABPR 50 20 34	Р
10. SOFT SKILLS	TW	50	20	40	P C	20. DATA STRUCTURES LABORATORY TW 50 20 37	Р
						21. DATA STRUCTURES LABORATORY PR 50 20 20	Р

GRAND TOTAL = 867/1500, RESULT: FAILS A.T.K.T.
ORDN. 1 MARKS :

S8054254 JONDHALE KANCHAN BABUNATH	4			ALK	A		, 71132429յ	, s8054254 ,	PICT		, s80	5425	4
01. DISCRETE STRUCTURES	PP	100	40	62	P C	11.	ENGINEERING MATH	EMATICS III	PP	100	40	50	Р
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	41	P C	12.	MICROPROC. & INT	ERFACING TECHNIQ	.PP	100	40	59	Р
03. DIGIT. ELECTRONICS & LOGIC DESIG	SNPP	100	40	60	P C	13.	DATA STRUCTURES		PP	100	40	49	Р
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	79	P C	14.	COMPUTER GRAPHIC	S	PP	100	40	59	Р
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	47	P C	15.	COMPUTER ORGANIZ	ATION	PP	100	40	65	Р
06. PROGRAMMING LABORATORY	TW	25	10	20	P C	16.	O. O. PROG. & CO	MP. GRAPH. LAB	TW	50	20	43	Р
07. PROGRAMMING LABORATORY	PR	50	20	28	P C	17.	O. O. PROG. & CO	MP. GRAPH. LAB	PR	50	20	40	Р
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	18	P C	18.	MICROPROCESSORS	& INTERFACING LA	BTW	50	20	43	Р
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	26	P C	19.	MICROPROCESSORS	& INTERFACING LA	BPR	50	20	36	Р
10. SOFT SKILLS	TW	50	20	39	P C	20.	DATA STRUCTURES	LABORATORY	TW	50	20	37	Р
						21.	DATA STRUCTURES	LABORATORY	PR	50	20	28	Р

GRAND TOTAL = 929/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS:

S8054255 JOSHI DHANANJAY SHRINIVA	.s			SAV	/ITA	, 71132430B , S8054255 , PICT , S8054255
01. DISCRETE STRUCTURES	PP	100	40	49	РC	11. ENGINEERING MATHEMATICS III PP 100 40 51 P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	47	РС	12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 58 P
03. DIGIT. ELECTRONICS & LOGIC DESI	GNPP	100	40	61	РС	13. DATA STRUCTURES PP 100 40 66 P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	74	РС	14. COMPUTER GRAPHICS PP 100 40 69 P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	69	РС	15. COMPUTER ORGANIZATION PP 100 40 59 P
06. PROGRAMMING LABORATORY	TW	25	10	19	РС	16. O. O. PROG. & COMP. GRAPH. LAB TW 50 20 43 P
07. PROGRAMMING LABORATORY	PR	50	20	42	РС	17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 31 P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	19	РС	18. MICROPROCESSORS & INTERFACING LABTW 50 20 39 P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	40	РС	19. MICROPROCESSORS & INTERFACING LABPR 50 20 40 P
10. SOFT SKILLS	TW	50	20	37	РС	20. DATA STRUCTURES LABORATORY TW 50 20 39 P
						21. DATA STRUCTURES LABORATORY PR 50 20 40 P
GRAND TOTAL = 992/1500, RESULT: FIRS ORDN. 1 MARKS :	T CLAS	SS WIT	H DIS	TINCT	TION	
UNIVE	 ERSITY	 OF PU	 JNE ,S	(2008 PA	
DATE : 18 AUG. 2011	CEN	TRE :	PUNE :	INSTI	TUTE OF	F COMPUTER TECHNOLOGY, PUNE. PAGE NO. 19 (383)
· ·				-	•	PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. RKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
S8054256 KANADE ROHIT RAJARAM				VID	DYA	, 71132431L , S8054256 , PICT , S8054256
01. DISCRETE STRUCTURES	PP	100	40	76	P C	11. ENGINEERING MATHEMATICS III PP 100 40 58 P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	66	PC	12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 62 P

						cols05
03. DIGIT. ELECTRONICS & LOGIC DESIG	NPP	100	40	81	РС	13. DATA STRUCTURES PP 100 40 75 P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	79	P C	14. COMPUTER GRAPHICS PP 100 40 74 P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	62	P C	15. COMPUTER ORGANIZATION PP 100 40 62 P
06. PROGRAMMING LABORATORY	TW	25	10	21	P C	16. O. O. PROG. & COMP. GRAPH. LAB TW 50 20 41 P
07. PROGRAMMING LABORATORY	PR	50	20	37	P C	17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 43 P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	21	P C	18. MICROPROCESSORS & INTERFACING LABTW 50 20 39 P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	25	P C	19. MICROPROCESSORS & INTERFACING LABPR 50 20 40 P
10. SOFT SKILLS	TW	50	20	39	P C	20. DATA STRUCTURES LABORATORY TW 50 20 36 P
						21. DATA STRUCTURES LABORATORY PR 50 20 38 P

GRAND TOTAL = 1075/1500, RESULT: FIRST CLASS WITH DISTINCTION
ORDN. 1 MARKS :

s8054257	KANTROD SUKESHNI ANIL				SUN	ITA		, 71045468G	, s8054257 ,	PICT		, s80	54257	7
01. DISCRE	TE STRUCTURES	PP	100	40	53	P C	11.	ENGINEERING MATHE	EMATICS III	PP	100	40	40	Р
02. PROGRA	MMING & PROBLEM SOLVING	PP	100	40	40	P C	12.	MICROPROC. & INTE	ERFACING TECHNIQ	.PP	100	40	40	Р
03. DIGIT.	ELECTRONICS & LOGIC DESIG	NPP	100	40	60	P C	13.	DATA STRUCTURES		PP	100	40	43	Р
04. DATA S	TRUCTURES AND ALGORITHMS	PP	100	40	58	P C	14.	COMPUTER GRAPHICS	5	PP	100	40	59	Р
05. HUMANI	TIES AND SOCIAL SCIENCE	PP	100	40	40	P C	15.	COMPUTER ORGANIZA	ATION	PP	100	40	61	Р
06. PROGRA	MMING LABORATORY	TW	25	10	19	P C	16.	O. O. PROG. & COM	MP. GRAPH. LAB	TW	50	20	39	Р
07. PROGRA	MMING LABORATORY	PR	50	20	27	P C	17.	O. O. PROG. & COM	MP. GRAPH. LAB	PR	50	20	34	Р
08. DIGITA	L ELECTRONICS LABORATORY	TW	25	10	22	P C	18.	MICROPROCESSORS &	& INTERFACING LA	BTW	50	20	34	Р
09. DIGITA	L ELECTRONICS LABORATORY	PR	50	20	35	P C	19.	MICROPROCESSORS &	& INTERFACING LA	BPR	50	20	20	Р
10. SOFT S	KILLS	TW	50	20	37	P C	20.	DATA STRUCTURES I	_ABORATORY	TW	50	20	32	Р
							21.	DATA STRUCTURES I	_ABORATORY	PR	50	20	33	Р

GRAND TOTAL = 826/1500, RESULT: HIGHER SECOND CLASS ORDN. 1 MARKS :

S8054258 KAPLE MADHURA MUKESH				VAN	ITA		, 71045469E	cols05 , s8054258 ,	PICT		, s80	05425	8
01. DISCRETE STRUCTURES	PP	100	40	71	РC	11.	ENGINEERING MAT	HEMATICS III	PP	100	40	83	Р
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	45	P C	12.	MICROPROC. & IN	TERFACING TECHNIQ	. PP	100	40	53	Р
03. DIGIT. ELECTRONICS & LOGIC DESIG	INPP	100	40	75	РС	13.	DATA STRUCTURES		PP	100	40	68	Р
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	78	P C	14.	COMPUTER GRAPHI	CS	PP	100	40	55	Р
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	54	P C	15.	COMPUTER ORGANI	ZATION	PP	100	40	66	Р
06. PROGRAMMING LABORATORY	TW	25	10	21	P C	16.	O. O. PROG. & C	OMP. GRAPH. LAB	TW	50	20	39	Р
07. PROGRAMMING LABORATORY	PR	50	20	35	Р	17.	O. O. PROG. & C	OMP. GRAPH. LAB	PR	50	20	36	Р
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	20	РС	18.	MICROPROCESSORS	& INTERFACING LAI	BTW	50	20	36	Р
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	40	P C	19.	MICROPROCESSORS	& INTERFACING LAI	BPR	50	20	39	Р
10. SOFT SKILLS	TW	50	20	40	P C	20.	DATA STRUCTURES	LABORATORY	TW	50	20	42	Р
						21.	DATA STRUCTURES	LABORATORY	PR	50	20	30	Р
ORDN. 1 MARKS :													
DATE : 18 AUG. 2011			-	-		-	 TER) EXAMINATION R TECHNOLOGY, PU		 PAG	 E NO.	20		 84)
	CENT · · ·	RE : F	PUNE I	NSTI MO	TUTE OF	COMPUTE	R TECHNOLOGY, PU	NE	 OLLEG	 E, S	 SEAT N	 NO.	84)
DATE: 18 AUG. 2011	CENT · · ·	RE : F	PUNE I	NSTI MO	TUTE OF	COMPUTE	R TECHNOLOGY, PU	NE	 OLLEG	 E, S	 SEAT N	 NO.	84)
DATE: 18 AUG. 2011	CENT · · ·	RE : F	PUNE I	NSTI MO	TUTE OF THER, P ASS MAR	COMPUTE	R TECHNOLOGY, PU	NE	 OLLEG	 E, US CAF	 SEAT N RRY O\	 NO.	
DATE: 18 AUG. 2011	CENT · · ·	RE : F	PUNE I	MO N. P.	TUTE OF THER, P ASS MAR	COMPUTE ERMANENT KS, MARI	R TECHNOLOGY, PU REG. NO., PREV KS OBTAINED, P/	NE. OUS SEAT NO., CONTRIBUTION OF THE PASS/FAIL, C:PI OUS SEAT NO., CONTRIBUTION OF THE PASS/FAIL, C:PI OUS SEAT NO., CONTRIBUTION OF THE PASS/FAIL, C:PI	· · · OLLEG REVIO	 E, US CAF	 SEAT N RRY O\	NO. VER	9
DATE: 18 AUG. 2011 NOTE: FIRST LINE: SEAT NO., NAME OF OTHER LINES: HEAD OF PASSING, S8054259 KARAD NEHA DASHRATH	CENT OF THE MAX.	RE : F CANDI MARKS	PUNE I	MO N. P. USH	TUTE OF THER, P ASS MAR	COMPUTE ERMANENT KS, MARI 	R TECHNOLOGY, PU REG. NO., PREV KS OBTAINED, P/ , 71045471G ENGINEERING MAT	NE. OUS SEAT NO., CONTRIBUTION OF THE PASS/FAIL, C:PI OUS SEAT NO., CONTRIBUTION OF THE PASS/FAIL, C:PI OUS SEAT NO., CONTRIBUTION OF THE PASS/FAIL, C:PI	OLLEGREVIO	E, S US CAI	SEAT M	NO. VER 	 9
DATE: 18 AUG. 2011 NOTE: FIRST LINE: SEAT NO., NAME OF OTHER LINES: HEAD OF PASSING, S8054259 KARAD NEHA DASHRATH 01. DISCRETE STRUCTURES	CENT DF THE MAX	CANDI MARKS	PUNE I	MOON. P. USH.	TUTE OF THER, P ASS MAR A	COMPUTE ERMANENT KS, MARI 	R TECHNOLOGY, PU REG. NO., PREV KS OBTAINED, P/ , 71045471G ENGINEERING MAT	NE. OUS SEAT NO., CONTROL OF: PASS/FAIL, C:PI S8054259, HEMATICS III TERFACING TECHNIQ	OLLEGREVIO	E, S US CAR 	SEAT N RRY ON , S80	NO. VER D54259	 9 F
DATE: 18 AUG. 2011 NOTE: FIRST LINE: SEAT NO., NAME OF OTHER LINES: HEAD OF PASSING, S8054259 KARAD NEHA DASHRATH 01. DISCRETE STRUCTURES 02. PROGRAMMING & PROBLEM SOLVING	CENT DF THE MAX	CANDI MARKS	PUNE I DATE, S, MI 	MO N. P. USH. 66 46 60	TUTE OF THER, P ASS MAR A P C P C	COMPUTE ERMANENT KS, MAR 11. 12. 13.	R TECHNOLOGY, PU REG. NO., PREV KS OBTAINED, P/ , 71045471G ENGINEERING MAT MICROPROC. & IN	NE. OUS SEAT NO., COR F:PASS/FAIL, C:PR S8054259, HEMATICS III TERFACING TECHNIQ	OLLEGREVIO PICT PP	E, S US CAR 	SEAT NRRY ON	NO. VER 054259 22 40	 9 F P
DATE: 18 AUG. 2011 NOTE: FIRST LINE: SEAT NO., NAME OF OTHER LINES: HEAD OF PASSING, S8054259 KARAD NEHA DASHRATH O1. DISCRETE STRUCTURES O2. PROGRAMMING & PROBLEM SOLVING O3. DIGIT. ELECTRONICS & LOGIC DESIGNATION	CENT OF THE MAX. PP PP PP	CANDI MARKS	PUNE I DATE, 6, MI 40 40 40	MSTI MO N. P. USH 66 46 60 57	TUTE OF THER, P ASS MAR A P C P C	COMPUTE ERMANENT KS, MAR 11. 12. 13. 14.	R TECHNOLOGY, PU REG. NO., PREV S OBTAINED, P/ 71045471G ENGINEERING MAT MICROPROC. & IN DATA STRUCTURES	NE	DLLEGREVIO PICT PP PP	E, S US CAR 100 100 100	SEAT N RRY OV , S80 40 40 40	NO. VER 054259 22 40 42	
DATE: 18 AUG. 2011 NOTE: FIRST LINE: SEAT NO., NAME OF OTHER LINES: HEAD OF PASSING, S8054259 KARAD NEHA DASHRATH 01. DISCRETE STRUCTURES 02. PROGRAMMING & PROBLEM SOLVING 03. DIGIT. ELECTRONICS & LOGIC DESIGNATION OF THE PROBLEM SOLVING 04. DATA STRUCTURES AND ALGORITHMS	CENT CENT PF PP PP PP PP	CANDI MARKS 100 100 100 100	PUNE I DATE, MI 40 40 40 40 40	MSTI	TUTE OF THER, P ASS MAR A P C P C P C	COMPUTE ERMANENT KS, MAR 11. 12. 13. 14. 15.	R TECHNOLOGY, PU REG. NO., PREV S OBTAINED, P/ 71045471G ENGINEERING MAT MICROPROC. & IN DATA STRUCTURES COMPUTER GRAPHI	NE	DLLEGREVIO PICT PP PP PP	E, S US CAR 100 100 100	SEAT N RRY ON , S80 40 40 40 40	NO. VER 054259 22 40 42 49	
DATE: 18 AUG. 2011 NOTE: FIRST LINE: SEAT NO., NAME OF OTHER LINES: HEAD OF PASSING, S8054259 KARAD NEHA DASHRATH 01. DISCRETE STRUCTURES 02. PROGRAMMING & PROBLEM SOLVING 03. DIGIT. ELECTRONICS & LOGIC DESIGNATION OF THE PROBLEM SOLVING 04. DATA STRUCTURES AND ALGORITHMS 05. HUMANITIES AND SOCIAL SCIENCE	CENT CENT PF PP PP PP PP	100 100 100 100 100	PUNE I DATE, , MI 40 40 40 40 40 40	MSTI MO N. P. USH 66 46 60 57 40 19	TUTE OF THER, P ASS MAR A P C P C P C P C	COMPUTE ERMANENT KS, MAR 11. 12. 13. 14. 15. 16.	R TECHNOLOGY, PU REG. NO., PREV S OBTAINED, P/ , 71045471G ENGINEERING MAT MICROPROC. & IN DATA STRUCTURES COMPUTER GRAPHI COMPUTER ORGANI	NE. OUS SEAT NO., COR F:PASS/FAIL, C:PR S8054259, HEMATICS III TERFACING TECHNIQ CS ZATION OMP. GRAPH. LAB	DLLEG REVIO PICT PP PP PP PP	100 100 100 100 100	SEAT MRRY ON	NO. VER 054259 22 40 42 49 50	

08.	DIGITAL ELECTRONICS LABOR	RATORY TW	ı 25	10	20	P C	18	. MICROPROCESSOR	cols05 S & INTERFACING	LABTW	50	20	29	Р
09.	DIGITAL ELECTRONICS LABOR	RATORY PR	R 50	20	21	РС	19	. MICROPROCESSOR	S & INTERFACING	LABPR	50	20	21	Р
10.	SOFT SKILLS	Tw	<i>y</i> 50	20	38	P C	20	. DATA STRUCTURE	S LABORATORY	TW	50	20	26	Р
							21	. DATA STRUCTURE	S LABORATORY	PR	50	20	09	F
	TOTAL = 759/1500, RESULTION 1 MARKS :	T: FAILS A.	т.к.т.							RESU	LT RES	SERVED	FOR	BKLG
S80	54260 KARANDE RAHUL MADA	AN			SUN	ITA		, 71045472E	, s8054260	, PICT		, s80	5426	0
01.	DISCRETE STRUCTURES	PF	100	40	49	P C	11	. ENGINEERING MA	THEMATICS III	PP	100	40	40	Р

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01.	DISCRETE STRUCTURES	PP	100	40	49	PС	11.	ENGINEERING MATHE	MATICS III	PP	100	40	40	Р
02.	PROGRAMMING & PROBLEM SOLVING	PP	100	40	40	PС	12.	MICROPROC. & INTE	RFACING TECHNIQ	.PP	100	40	27	F
03.	DIGIT. ELECTRONICS & LOGIC DESIG	SNPP	100	40	54	P C	13.	DATA STRUCTURES		PP	100	40	30	F
04.	DATA STRUCTURES AND ALGORITHMS	PP	100	40	44	P C	14.	COMPUTER GRAPHICS		PP	100	40	60	Р
05.	HUMANITIES AND SOCIAL SCIENCE	PP	100	40	40	PС	15.	COMPUTER ORGANIZA	TION	PP	100	40	41	Р
06.	PROGRAMMING LABORATORY	TW	25	10	15	P C	16.	O. O. PROG. & COM	P. GRAPH. LAB	TW	50	20	24	Р
07.	PROGRAMMING LABORATORY	PR	50	20	AA	F	17.	O. O. PROG. & COM	P. GRAPH. LAB	PR	50	20	AA	F
08.	DIGITAL ELECTRONICS LABORATORY	TW	25	10	16	PС	18.	MICROPROCESSORS &	INTERFACING LA	BTW	50	20	30	Р
09.	DIGITAL ELECTRONICS LABORATORY	PR	50	20	24	P C	19.	MICROPROCESSORS &	INTERFACING LA	BPR	50	20	05	F
10.	SOFT SKILLS	TW	50	20	35	PС	20.	DATA STRUCTURES L	ABORATORY	TW	50	20	30	Р
							21.	DATA STRUCTURES L	ABORATORY	PR	50	20	06	F

GRAND TOTAL = 610/1500, RESULT: FAILS

ORDN. 1 MARKS:

S8054261 KATARIYA RAHUL PADAMKUMAR			BHARATI	, 71045476н , S8054261 , РІС	ïΤ	, s80	54261	
01. DISCRETE STRUCTURES PP	100	40	63 P C	11. ENGINEERING MATHEMATICS III PP	100	40	41 F	Р
02. PROGRAMMING & PROBLEM SOLVING PP	100	40	40 P C	12. MICROPROC. & INTERFACING TECHNIQ.PP	100	40	40 F	Р
03. DIGIT. ELECTRONICS & LOGIC DESIGNPP	100	40	62 P C	13. DATA STRUCTURES PP	100	40	57 F	Р
04. DATA STRUCTURES AND ALGORITHMS PP	100	40	61 P C	14. COMPUTER GRAPHICS PP Page 109	100	40	62 F	Ρ

05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	47	P C	15. COMPUTER ORGANIZATION PP 100 40 61	Р
06. PROGRAMMING LABORATORY	TW	25	10	21	P C	16. O. O. PROG. & COMP. GRAPH. LAB TW 50 20 41	Р
07. PROGRAMMING LABORATORY	PR	50	20	35	P C	17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 42	Р
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	21	P C	18. MICROPROCESSORS & INTERFACING LABTW 50 20 41	Р
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	43	P C	19. MICROPROCESSORS & INTERFACING LABPR 50 20 44	Р
10. SOFT SKILLS	TW	50	20	39	P C	20. DATA STRUCTURES LABORATORY TW 50 20 40	Р
						21. DATA STRUCTURES LABORATORY PR 50 20 38	Р
GRAND TOTAL = 939/1500, RESULT: FIRS	ST CLAS	SS					
ORDN. 1 MARKS :							
UNIVE	 ERSITY	 OF PU	 NE ,S	 .E.(2	 2008 PAT.)(COMPUTER) EXAMINATION MAY 2011	
						PUTER TECHNOLOGY, PUNE. PAGE NO. 21 (3	
NOTE: FIRST LINE : SEAT NO., NAME	OF THE	E CAND	IDATE,	МО	THER, PERM	NENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.	
·			•		•	NENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER	
OTHER LINES: HEAD OF PASSING,	MAX.	. MARKS	5, MI	IN. P	ASS MARKS,		
OTHER LINES: HEAD OF PASSING,	MAX.	. MARKS	5, MI	[N. P	ASS MARKS,	MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER	
OTHER LINES: HEAD OF PASSING,	MAX.	. MARKS	5, MI	IN. P	ASS MARKS,	MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER	2
OTHER LINES: HEAD OF PASSING,	MAX.	. MARKS	5, MI	EN. P KAR	ASS MARKS,	MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER	2 P
OTHER LINES: HEAD OF PASSING, S8054262 KAWADE SARANG JAIRAJ 01. DISCRETE STRUCTURES	PP	. MARKS	5, MI	KAR 67 53	PASS MARKS,	MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71045477F , S8054262 , PICT , S805426 11. ENGINEERING MATHEMATICS III PP 100 40 74	2 P P
OTHER LINES: HEAD OF PASSING, S8054262 KAWADE SARANG JAIRAJ O1. DISCRETE STRUCTURES O2. PROGRAMMING & PROBLEM SOLVING	PP PP	100 100 100	40 40 40 40	KAR 67 53	P C P C	MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71045477F , S8054262 , PICT , S805426 11. ENGINEERING MATHEMATICS III PP 100 40 74 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 49	2 P P
OTHER LINES: HEAD OF PASSING, S8054262 KAWADE SARANG JAIRAJ 01. DISCRETE STRUCTURES 02. PROGRAMMING & PROBLEM SOLVING 03. DIGIT. ELECTRONICS & LOGIC DESI	PP PP	100 100 100	40 40 40 40	KAR 67 53 75 68	P C P C	MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71045477F , \$8054262 , PICT , \$8054266 11. ENGINEERING MATHEMATICS III PP 100 40 74 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 49 13. DATA STRUCTURES PP 100 40 63	2 P P P
OTHER LINES: HEAD OF PASSING, S8054262 KAWADE SARANG JAIRAJ O1. DISCRETE STRUCTURES O2. PROGRAMMING & PROBLEM SOLVING O3. DIGIT. ELECTRONICS & LOGIC DESI O4. DATA STRUCTURES AND ALGORITHMS	PP PP PP	100 100 100 100	40 40 40 40 40	KAR 67 53 75 68 57	P C P C P C	MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71045477F , S8054262 , PICT , S805426 11. ENGINEERING MATHEMATICS III PP 100 40 74 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 49 13. DATA STRUCTURES PP 100 40 63 14. COMPUTER GRAPHICS PP 100 40 76	2 P P P
OTHER LINES: HEAD OF PASSING, S8054262 KAWADE SARANG JAIRAJ O1. DISCRETE STRUCTURES O2. PROGRAMMING & PROBLEM SOLVING O3. DIGIT. ELECTRONICS & LOGIC DESI O4. DATA STRUCTURES AND ALGORITHMS O5. HUMANITIES AND SOCIAL SCIENCE	PP PP PP PP	100 100 100 100 100	40 40 40 40 40 40	KAR 67 53 75 68 57 19	P C P C P C P C	MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71045477F , S8054262 , PICT , S8054266 11. ENGINEERING MATHEMATICS III PP 100 40 74 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 49 13. DATA STRUCTURES PP 100 40 63 14. COMPUTER GRAPHICS PP 100 40 76 15. COMPUTER ORGANIZATION PP 100 40 73	2 P P P P
OTHER LINES: HEAD OF PASSING, S8054262 KAWADE SARANG JAIRAJ O1. DISCRETE STRUCTURES O2. PROGRAMMING & PROBLEM SOLVING O3. DIGIT. ELECTRONICS & LOGIC DESI O4. DATA STRUCTURES AND ALGORITHMS O5. HUMANITIES AND SOCIAL SCIENCE O6. PROGRAMMING LABORATORY	PP PP PP TW	100 100 100 100 100 25	40 40 40 40 40 40	KAR 67 53 75 68 57 19 28	P C P C P C P C P C P C	MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71045477F , S8054262 , PICT , S805426 11. ENGINEERING MATHEMATICS III PP 100 40 74 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 49 13. DATA STRUCTURES PP 100 40 63 14. COMPUTER GRAPHICS PP 100 40 76 15. COMPUTER ORGANIZATION PP 100 40 73 16. O. O. PROG. & COMP. GRAPH. LAB TW 50 20 43	2 P P P P
OTHER LINES: HEAD OF PASSING, S8054262 KAWADE SARANG JAIRAJ 01. DISCRETE STRUCTURES 02. PROGRAMMING & PROBLEM SOLVING 03. DIGIT. ELECTRONICS & LOGIC DESI 04. DATA STRUCTURES AND ALGORITHMS 05. HUMANITIES AND SOCIAL SCIENCE 06. PROGRAMMING LABORATORY 07. PROGRAMMING LABORATORY	PP PP PP TW PR	100 100 100 100 100 25 50	40 40 40 40 40 40 20	KAR 67 53 75 68 57 19 28	P C P C P C P C P C P C P C P C	MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71045477F , S8054262 , PICT , S8054260 11. ENGINEERING MATHEMATICS III PP 100 40 74 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 49 13. DATA STRUCTURES PP 100 40 63 14. COMPUTER GRAPHICS PP 100 40 76 15. COMPUTER ORGANIZATION PP 100 40 73 16. O. O. PROG. & COMP. GRAPH. LAB TW 50 20 43 17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 41	2 P P P P P
OTHER LINES: HEAD OF PASSING, S8054262 KAWADE SARANG JAIRAJ O1. DISCRETE STRUCTURES O2. PROGRAMMING & PROBLEM SOLVING O3. DIGIT. ELECTRONICS & LOGIC DESI O4. DATA STRUCTURES AND ALGORITHMS O5. HUMANITIES AND SOCIAL SCIENCE O6. PROGRAMMING LABORATORY O7. PROGRAMMING LABORATORY O8. DIGITAL ELECTRONICS LABORATORY	PP PP PP TW PR TW	100 100 100 100 25 50 25	40 40 40 40 40 10 20	KAR 67 53 75 68 57 19 28 20 33	P C P C P C P C P C P C P C P C	MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71045477F , S8054262 , PICT , S8054266 11. ENGINEERING MATHEMATICS III PP 100 40 74 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 49 13. DATA STRUCTURES PP 100 40 63 14. COMPUTER GRAPHICS PP 100 40 76 15. COMPUTER ORGANIZATION PP 100 40 73 16. O. O. PROG. & COMP. GRAPH. LAB TW 50 20 43 17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 41 18. MICROPROCESSORS & INTERFACING LABTW 50 20 42	2 P P P P P

GRAND TOTAL = 1038/1500, RESULT: FIRST CLASS WITH DISTINCTION

20. DATA STRUCTURES LABORATORY

21. DATA STRUCTURES LABORATORY

TW 50 20 40 P

PR 50 20 41 P

ORDN. 1 MARKS :

10. SOFT SKILLS

S8054263 KAZI SABIYA FARUK				SHA	HEDA	, 71132432J , S8054263 , PICT , S8054263
01. DISCRETE STRUCTURES	PP	100	40	65	P C	11. ENGINEERING MATHEMATICS III PP 100 40 66 P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	49	P C	12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 48 P
03. DIGIT. ELECTRONICS & LOGIC DESIG	NPP	100	40	71	P C	13. DATA STRUCTURES PP 100 40 66 P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	79	P C	14. COMPUTER GRAPHICS PP 100 40 64 P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	54	P C	15. COMPUTER ORGANIZATION PP 100 40 65 P
06. PROGRAMMING LABORATORY	TW	25	10	20	P C	16. O. O. PROG. & COMP. GRAPH. LAB TW 50 20 42 P
07. PROGRAMMING LABORATORY	PR	50	20	46	P C	17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 42 P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	21	P C	18. MICROPROCESSORS & INTERFACING LABTW 50 20 43 P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	39	P C	19. MICROPROCESSORS & INTERFACING LABPR 50 20 42 P

TW 50 20 34 P C

GRAND TOTAL = 1037/1500, RESULT: FIRST CLASS WITH DISTINCTION
ORDN. 1 MARKS :

S	8054265 KHADE PRERANA BALASAHEB				TAR	ABAI		, 71045481D	, s8054265 ,	PICT		, s80	5426	5
0	1. DISCRETE STRUCTURES	PP	100	40	44	P C	11	. ENGINEERING MAT	HEMATICS III	PP	100	40	22	F
0	2. PROGRAMMING & PROBLEM SOLVING	PP	100	40	40	P C	12	MICROPROC. & IN	TERFACING TECHNIQ	.PP	100	40	31	F
0	3. DIGIT. ELECTRONICS & LOGIC DESIG	NPP	100	40	48	P C	13	. DATA STRUCTURES		PP	100	40	48	Р
0	4. DATA STRUCTURES AND ALGORITHMS	PP	100	40	64	P C	14	. COMPUTER GRAPHI	CS	PP	100	40	54	Р
0	5. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	42	P C	15	. COMPUTER ORGANI	ZATION	PP	100	40	50	Р
0	6. PROGRAMMING LABORATORY	TW	25	10	21	P C	16	O. O. PROG. & C	OMP. GRAPH. LAB	TW	50	20	40	Р
0	7. PROGRAMMING LABORATORY	PR	50	20	30	P C	17	O. O. PROG. & C	OMP. GRAPH. LAB	PR	50	20	30	Р
0	8. DIGITAL ELECTRONICS LABORATORY	TW	25	10	20	P C	18	. MICROPROCESSORS	& INTERFACING LA	BTW	50	20	40	Р
0	9. DIGITAL ELECTRONICS LABORATORY	PR	50	20	34	P C	19		& INTERFACING LA	BPR	50	20	35	Р

10. SOFT SKILLS	TW	50	20	39	PC	20.	DATA STRUCTURES LABORATORY	TW	50	20	38	Р
						21.	DATA STRUCTURES LABORATORY	PR	50	20	33	Р
GRAND TOTAL = 803/1500, RESULT: FAIL	S A.T.	K.T.										
ORDN. 1 MARKS :												
UNIVE	 RSITY	OF PU	 NE ,S	. E. (2	 2008 PAT	 Г.)(СОМРІ	UTER) EXAMINATION MAY 2011					• •
DATE : 18 AUG. 2011	CENT	RE :	PUNE :	INSTI	TUTE OF	COMPUTE	R TECHNOLOGY, PUNE.	PAG	GE NO.	22	(3	86)
NOTE: FIRST LINE : SEAT NO., NAME	OF THE	CAND	IDATE	, MC	THER, P	ERMANENT	REG. NO., PREVIOUS SEAT NO., C	OLLEG	GE,	SEAT N	NO.	
OTHER LINES: HEAD OF PASSING,	MAX.	MARK:	S, M:	IN. P	ASS MAR	KS, MAR	RKS OBTAINED, P/F:PASS/FAIL, C:F	REVIC	OUS CAI	RRY O	√ER	
S8054266 KHARDE ABHIJIT DIGAMBER				JYC	TI		, 71045483L , S8054266 ,	PICT	Г	, 580	05426	66
01. DISCRETE STRUCTURES	PP	100	40	67	P C	11.	ENGINEERING MATHEMATICS III	PP	100	40	68	Р
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	56	РC	12.	MICROPROC. & INTERFACING TECHNIC	.PP	100	40	47	Р
03. DIGIT. ELECTRONICS & LOGIC DESIGNATION	GNPP	100	40	70	P C	13.	DATA STRUCTURES	PP	100	40	60	Р
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	71	P C	14.	COMPUTER GRAPHICS	PP	100	40	66	Р
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	47	P C	15.	COMPUTER ORGANIZATION	PP	100	40	60	Р
06. PROGRAMMING LABORATORY	TW	25	10	22	P C	16.	O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	38	Р
07. PROGRAMMING LABORATORY	PR	50	20	45	P C	17.	O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	35	Р
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	21	P C	18.	MICROPROCESSORS & INTERFACING LA	BTW	50	20	36	Р
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	23	P C	19.	MICROPROCESSORS & INTERFACING LA	BPR	50	20	41	Р
10. SOFT SKILLS	TW	50	20	39	P C	20.	DATA STRUCTURES LABORATORY	TW	50	20	38	Р
						21.	DATA STRUCTURES LABORATORY	PR	50	20	40	Р
CRAND TOTAL - 000/1500 RECULT, ETRE	T (1 A)	C WITT	u DTC	TTNCT	TON							
GRAND TOTAL = 990/1500, RESULT: FIRST	I CLAS	S WIII	п ртз	I INC	ION							
ORDN. 1 MARKS :												
	-	- -	- •	- ·	·	·			- -	- •	- •	
S8054267 KHEDKAR PRITAM KISHOR				SUR	REKHA		, 71045484J , S8054267 ,	PICT	Γ	, 580	05426	57

01. DISCRETE STRUCTURES	PP	100	40	55	PС	11	ENGINEERING MATHEMATICS III	PP	100	40	65	Р
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	47	P C	12	MICROPROC. & INTERFACING TECHNIC	.PP	100	40	40	Р
03. DIGIT. ELECTRONICS & LOGIC DESIGNATION OF THE PROPERTY OF	GNPP	100	40	73	P C	13	DATA STRUCTURES	PP	100	40	56	Р
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	68	P C	14	COMPUTER GRAPHICS	PP	100	40	63	Р
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	40	P C	15	COMPUTER ORGANIZATION	PP	100	40	40	Р
06. PROGRAMMING LABORATORY	TW	25	10	18	P C	16	O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	25	Р
07. PROGRAMMING LABORATORY	PR	50	20	41	P C	17	O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	37	Р
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	17	P C	18	MICROPROCESSORS & INTERFACING LA	BTW	50	20	25	Р
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	22	P C	19	MICROPROCESSORS & INTERFACING LA	BPR	50	20	05	F
10. SOFT SKILLS	TW	50	20	38	P C	20	DATA STRUCTURES LABORATORY	TW	50	20	32	Р
						21	DATA STRUCTURES LABORATORY	PR	50	20	36	Р

GRAND TOTAL = 843/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

S8054268 KSHATRIYA PURVA HEMANT				RAJE	ESHRI		, 71045487C	, s8054268 ,	PICT		, s80	5426	8
01. DISCRETE STRUCTURES	PP	100	40	50	P C	11.	ENGINEERING MATH	EMATICS III	PP	100	40	72	Р
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	44	РС	12.	MICROPROC. & INT	ERFACING TECHNIQ	.PP	100	40	48	Р
03. DIGIT. ELECTRONICS & LOGIC DESIG	GNPP	100	40	65	РС	13.	DATA STRUCTURES		PP	100	40	56	Р
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	66	P C	14.	COMPUTER GRAPHICS	5	PP	100	40	66	Р
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	64	P C	15.	COMPUTER ORGANIZA	ATION	PP	100	40	60	Р
06. PROGRAMMING LABORATORY	TW	25	10	21	P C	16.	O. O. PROG. & COM	MP. GRAPH. LAB	TW	50	20	34	Р
07. PROGRAMMING LABORATORY	PR	50	20	40	P C	17.	O. O. PROG. & COM	MP. GRAPH. LAB	PR	50	20	36	Р
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	21	P C	18.	MICROPROCESSORS &	& INTERFACING LA	BTW	50	20	35	Р
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	27	P C	19.	MICROPROCESSORS &	& INTERFACING LA	BPR	50	20	41	Р
10. SOFT SKILLS	TW	50	20	40	РС	20.	DATA STRUCTURES	_ABORATORY	TW	50	20	31	Р
						21.	DATA STRUCTURES I	_ABORATORY	PR	50	20	38	Р

GRAND TOTAL = 955/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS:

UNIVE						.)(COMPUTER) EXAMINATION MAY 2011
DATE : 18 AUG. 2011	CENT	TRE : F	PUNE :	INSTI	TUTE OF	COMPUTER TECHNOLOGY, PUNE. PAGE NO. 23 (38
NOTE: FIRST LINE : SEAT NO., NAME O	OF THE	E CAND	DATE	, MO	THER, F	ERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING,	MAX.	MARKS	5, M	IN. P	ASS MAF	(S, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
S8054269 KSHITIZ DANGE				REK	HA RAN	, 71045488M , S8054269 , PICT , S8054269
01. DISCRETE STRUCTURES	PP	100	40	65	P C	11. ENGINEERING MATHEMATICS III PP 100 40 59 I
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	51	P C	12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 45
03. DIGIT. ELECTRONICS & LOGIC DESIG	GNPP	100	40	65	P C	13. DATA STRUCTURES PP 100 40 51 I
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	67	P C	14. COMPUTER GRAPHICS PP 100 40 70 I
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	44	P C	15. COMPUTER ORGANIZATION PP 100 40 64 I
06. PROGRAMMING LABORATORY	TW	25	10	20	P C	16. O. O. PROG. & COMP. GRAPH. LAB TW 50 20 38 I
07. PROGRAMMING LABORATORY	PR	50	20	42	P C	17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 42 1
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	16	P C	18. MICROPROCESSORS & INTERFACING LABTW 50 20 40 H
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	45	P C	19. MICROPROCESSORS & INTERFACING LABPR 50 20 42
10. SOFT SKILLS	TW	50	20	39	P C	20. DATA STRUCTURES LABORATORY TW 50 20 37
						21. DATA STRUCTURES LABORATORY PR 50 20 40 I
GRAND TOTAL = 982/1500, RESULT: FIRST	Γ (ΙΔ9	ss				
ORDN. 1 MARKS :						
S8054270 KULKARNI AKASH ABHAY				ASH	WINI	, 71045489к , S8054270 , РІСТ , S8054270
01. DISCRETE STRUCTURES	PP	100	40	54	PC	11. ENGINEERING MATHEMATICS III PP 100 40 84 I
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	64	PC	12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 61
03. DIGIT. ELECTRONICS & LOGIC DESIG	GNPP	100	40	83	PC	13. DATA STRUCTURES PP 100 40 58 I
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	63	PC	14. COMPUTER GRAPHICS PP 100 40 69 I
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	53	PC	15. COMPUTER ORGANIZATION PP 100 40 65 I Page 114

06. PROGRAMMING LABORATORY	TW	25	10	22	P C	16. O. O. PROG. & COMP. GRAPH. LAB TW 50 20 41 P
07. PROGRAMMING LABORATORY	PR	50	20	43	P C	17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 41 P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	22	P C	18. MICROPROCESSORS & INTERFACING LABTW 50 20 38 P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	39	P C	19. MICROPROCESSORS & INTERFACING LABPR 50 20 45 P
10. SOFT SKILLS	TW	50	20	39	P C	20. DATA STRUCTURES LABORATORY TW 50 20 41 P
						21. DATA STRUCTURES LABORATORY PR 50 20 44 P
4050/4500						

GRAND TOTAL = 1069/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS :

S8054271 KULKARNI CHINMAYI AVINASH	4			SHO	ВНА	, 71045493H , S8054271 , РІСТ , S8054271
01. DISCRETE STRUCTURES	PP	100	40	46	P C	11. ENGINEERING MATHEMATICS III PP 100 40 40 P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	48	P C	12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 40 P
03. DIGIT. ELECTRONICS & LOGIC DESIG	GNPP	100	40	63	P C	13. DATA STRUCTURES PP 100 40 43 P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	43	P C	14. COMPUTER GRAPHICS PP 100 40 55 P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	45	Р	15. COMPUTER ORGANIZATION PP 100 40 46 P
06. PROGRAMMING LABORATORY	TW	25	10	18	P C	16. O. O. PROG. & COMP. GRAPH. LAB TW 50 20 38 P
07. PROGRAMMING LABORATORY	PR	50	20	39	Р	17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 23 P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	16	P C	18. MICROPROCESSORS & INTERFACING LABTW 50 20 33 P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	22	P C	19. MICROPROCESSORS & INTERFACING LABPR 50 20 34 P
10. SOFT SKILLS	TW	50	20	40	P C	20. DATA STRUCTURES LABORATORY TW 50 20 36 P
						21. DATA STRUCTURES LABORATORY PR 50 20 38 P

GRAND TOTAL = 806/1500, RESULT: SECOND CLASS
ORDN. 1 MARKS :

UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(COMPUTER) EXAMINATION MAY 2011

DATE: 18 AUG. 2011 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 24 (388)

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

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cols05

cols05

OTHER LINES:	HEAD OF	F PASSING,	MAX. MARKS,	MIN. PASS MARKS,	MARKS OBTAINED,	P/F:PASS/FAIL,	C:PREVIOUS CARRY OVER	

S8054272 KULKARNI DNYANESH VIJAYRA	0			MAN	GAL		, 71045494F	, s8054272 ,	PICT		, \$80	5427	2
01. DISCRETE STRUCTURES	PP	100	40	67	P C	11	. ENGINEERING MA	ATHEMATICS III	PP	100	40	63	Р
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	56	P C	12	. MICROPROC. & I	NTERFACING TECHNIQ	.PP	100	40	40	Р
03. DIGIT. ELECTRONICS & LOGIC DESIG	INPP	100	40	77	РС	13	. DATA STRUCTURE	:S	PP	100	40	63	Р
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	61	РС	14	. COMPUTER GRAPH	IICS	PP	100	40	74	Р
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	57	РС	15	. COMPUTER ORGAN	IIZATION	PP	100	40	60	Р
06. PROGRAMMING LABORATORY	TW	25	10	21	РС	16	0. 0. PROG. &	COMP. GRAPH. LAB	TW	50	20	32	Р
07. PROGRAMMING LABORATORY	PR	50	20	38	РС	17	0. 0. PROG. &	COMP. GRAPH. LAB	PR	50	20	40	Р
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	20	РС	18	. MICROPROCESSOR	S & INTERFACING LA	BTW	50	20	35	Р
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	37	РС	19	. MICROPROCESSOR	S & INTERFACING LA	BPR	50	20	40	Р
10. SOFT SKILLS	TW	50	20	40	РС	20	. DATA STRUCTURE	S LABORATORY	TW	50	20	30	Р
						21	. DATA STRUCTURE	S LABORATORY	PR	50	20	39	Р

GRAND TOTAL = 990/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS :

S8054273 KULKARNI MANASI MILIND				JY0	TI		, 71045496в	, s8054273 ,	PICT		, s80)5427	3
01. DISCRETE STRUCTURES	PP	100	40	44	P C	11.	ENGINEERING MATH	EMATICS III	PP	100	40	40	Р
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	40	РС	12.	MICROPROC. & INT	ERFACING TECHNIQ	.PP	100	40	40	Р
03. DIGIT. ELECTRONICS & LOGIC DESI	GNPP	100	40	55	P C	13.	DATA STRUCTURES		PP	100	40	40	Р
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	40	P C	14.	COMPUTER GRAPHIC	S	PP	100	40	57	Р
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	49	РС	15.	COMPUTER ORGANIZ	ATION	PP	100	40	58	Р
06. PROGRAMMING LABORATORY	TW	25	10	19	РС	16.	O. O. PROG. & CO	MP. GRAPH. LAB	TW	50	20	36	Р
07. PROGRAMMING LABORATORY	PR	50	20	32	Р	17.	O. O. PROG. & CO	MP. GRAPH. LAB	PR	50	20	32	Р
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	20	РС	18.	MICROPROCESSORS	& INTERFACING LA	BTW	50	20	35	Р
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	35	РС	19.	MICROPROCESSORS	& INTERFACING LA	BPR	50	20	32	Р
10. SOFT SKILLS	TW	50	20	41	P C	20.	Bittiit Bittociones	LABORATORY age 116	TW	50	20	35	Р

21. DATA STRUCTURES LABORATORY PR 50 20 10 F

GRAND TOTAL = 790/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS:

S8054274 KULKARNI PRATHAMESH DEEP	AK			VII	OYA	, 71132433G , S8054274 , PICT , S8054	1274
01. DISCRETE STRUCTURES	PP	100	40	65	РС	11. ENGINEERING MATHEMATICS III PP 100 40 6	52 P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	52	P C	12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40	53 P
03. DIGIT. ELECTRONICS & LOGIC DESIGNATION OF THE PROPERTY OF	GNPP	100	40	68	P C	13. DATA STRUCTURES PP 100 40 6	52 P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	65	P C	14. COMPUTER GRAPHICS PP 100 40 7	74 P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	55	P C	15. COMPUTER ORGANIZATION PP 100 40 5	58 P
06. PROGRAMMING LABORATORY	TW	25	10	20	P C	16. O. O. PROG. & COMP. GRAPH. LAB TW 50 20	10 P
07. PROGRAMMING LABORATORY	PR	50	20	23	P C	17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20	12 P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	20	P C	18. MICROPROCESSORS & INTERFACING LABTW 50 20	39 P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	31	P C	19. MICROPROCESSORS & INTERFACING LABPR 50 20	38 P
10. SOFT SKILLS	TW	50	20	40	P C	20. DATA STRUCTURES LABORATORY TW 50 20	32 P
						21. DATA STRUCTURES LABORATORY PR 50 20 3	35 P

GRAND TOTAL = 974/1500, RESULT: FIRST CLASS ORDN. 1 MARKS :

UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(COMPUTER) EXAMINATION MAY 2011

DATE: 18 AUG. 2011 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 25 (389)

NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

\$8054275 KULKARNI SAURABH RAVINDRA SWATI , 71045498J , \$8054275 , PICT , \$8054275

01. DISCRETE STRUCTURES PP 100 40 48 P C 11. ENGINEERING MATHEMATICS III PP 100 40 73 P

02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	63	PC	12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40	50	Р
03. DIGIT. ELECTRONICS & LOGIC DESI	GNPP	100	40	69	P C	13. DATA STRUCTURES PP 100 40	50	Р
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	57	P C	14. COMPUTER GRAPHICS PP 100 40	75	Р
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	53	P C	15. COMPUTER ORGANIZATION PP 100 40	55	Р
06. PROGRAMMING LABORATORY	TW	25	10	23	P C	16. O. O. PROG. & COMP. GRAPH. LAB TW 50 20	42	Р
07. PROGRAMMING LABORATORY	PR	50	20	44	P C	17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20	43	Р
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	22	P C	18. MICROPROCESSORS & INTERFACING LABTW 50 20	37	Р
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	35	P C	19. MICROPROCESSORS & INTERFACING LABPR 50 20	44	Р
10. SOFT SKILLS	TW	50	20	43	P C	20. DATA STRUCTURES LABORATORY TW 50 20	42	Р
						21. DATA STRUCTURES LABORATORY PR 50 20	40	Р

GRAND TOTAL = 1008/1500, RESULT: FIRST CLASS WITH DISTINCTION
ORDN. 1 MARKS :

S8054276 KUNTALWAD GNYANESH GAJJA	RAM			SARASWA	ATHI	, 71045501в	, s8054276 ,	PICT		, s80	5427	6
01. DISCRETE STRUCTURES	PP	100	40	40 P C	11.	ENGINEERING MATHE	EMATICS III	PP	100	40	40	Р
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	51 P C	12.	MICROPROC. & INTE	ERFACING TECHNIQ	.PP	100	40	40	Р
03. DIGIT. ELECTRONICS & LOGIC DESI	GNPP	100	40	48 P C	13.	DATA STRUCTURES		PP	100	40	41	Р
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	40 P C	14.	COMPUTER GRAPHICS	5	PP	100	40	57	Р
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	41 P C	15.	COMPUTER ORGANIZA	ATION	PP	100	40	40	Р
06. PROGRAMMING LABORATORY	TW	25	10	16 P C	16.	O. O. PROG. & COM	1P. GRAPH. LAB	TW	50	20	22	Р
07. PROGRAMMING LABORATORY	PR	50	20	20 P	17.	O. O. PROG. & COM	1P. GRAPH. LAB	PR	50	20	23	Р
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	14 P C	18.	MICROPROCESSORS &	INTERFACING LA	BTW	50	20	25	Р
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	30 P	19.	MICROPROCESSORS &	INTERFACING LA	BPR	50	20	08	F
10. SOFT SKILLS	TW	50	20	38 P C	20.	DATA STRUCTURES L	ABORATORY	TW	50	20	34	Р
					21.	DATA STRUCTURES L	ABORATORY	PR	50	20	15	F

GRAND TOTAL = 683/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS:

cols	3 05
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TW 25 10 18 P C

06. PROGRAMMING LABORATORY

S8054277 LALHMANGAIHA				NGURTHANK	ı , 71045503」 , S8054277 , PICT , S80542	77
01. DISCRETE STRUCTURES	PP	100	40	40 P C	11. ENGINEERING MATHEMATICS III PP 100 40 01	F
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	AA F	12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 05	F
03. DIGIT. ELECTRONICS & LOGIC DESIGNATION OF THE PROPERTY OF	GNPP	100	40	40 P C	13. DATA STRUCTURES PP 100 40 06	F
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	AA F	14. COMPUTER GRAPHICS PP 100 40 23	F
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	45 P C	15. COMPUTER ORGANIZATION PP 100 40 20	F
06. PROGRAMMING LABORATORY	TW	25	10	14 P C	16. O. O. PROG. & COMP. GRAPH. LAB TW 50 20 20	Р
07. PROGRAMMING LABORATORY	PR	50	20	AA F	17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 02	F
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	10 P C	18. MICROPROCESSORS & INTERFACING LABTW 50 20 20	Р
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	20 P C	19. MICROPROCESSORS & INTERFACING LABPR 50 20 05	F
10. SOFT SKILLS	TW	50	20	24 P C	20. DATA STRUCTURES LABORATORY TW 50 20 20	Р
					21. DATA STRUCTURES LABORATORY PR 50 20 AA	F
ORDN. 1 MARKS :						
	 RSITY	OF PU	 NE ,S	 .E.(2008 PA		
					OMPUTER TECHNOLOGY, PUNE. PAGE NO. 26 (
						 390)
	CENT	TRE : F	PUNE I	NSTITUTE O		 390)
DATE: 18 AUG. 2011 NOTE: FIRST LINE: SEAT NO., NAME	CENT · · ·	TRE : F	PUNE I	NSTITUTE O	COMPUTER TECHNOLOGY, PUNE. PAGE NO. 26 (390)
DATE: 18 AUG. 2011	CENT OF THE MAX.	TRE : F	PUNE I	MOTHER,	COMPUTER TECHNOLOGY, PUNE. PAGE NO. 26 (RMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.	
DATE: 18 AUG. 2011 NOTE: FIRST LINE: SEAT NO., NAME OF PASSING,	CENT THE MAX.	TRE : F	PUNE I	MOTHER,	COMPUTER TECHNOLOGY, PUNE. PAGE NO. 26 (RMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. ARRANGE OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER	
DATE: 18 AUG. 2011 NOTE: FIRST LINE: SEAT NO., NAME OF PASSING,	CENT THE MAX.	TRE : F	PUNE I	MOTHER, N. PASS MA	COMPUTER TECHNOLOGY, PUNE. PAGE NO. 26 (RMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. S, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71045504G , S8054278 , PICT , S80542	
DATE: 18 AUG. 2011 NOTE: FIRST LINE: SEAT NO., NAME OF PASSING, OTHER LINES: HEAD OF PASSING, S8054278 LOKHANDE VINAYA VISHWANA	CENT THRAO	TRE : F	PUNE I	MOTHER, IN. PASS MAI PRATIBHA	COMPUTER TECHNOLOGY, PUNE. PAGE NO. 26 (RMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. S, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71045504G , S8054278 , PICT , S80542 11. ENGINEERING MATHEMATICS III PP 100 40 87	
DATE: 18 AUG. 2011 NOTE: FIRST LINE: SEAT NO., NAME OF PASSING, OTHER LINES: HEAD OF PASSING, S8054278 LOKHANDE VINAYA VISHWANA	CENT THRAO PP PP	TRE : F	PUNE I	MOTHER, IN. PASS MAI PRATIBHA 46 P C	COMPUTER TECHNOLOGY, PUNE. PAGE NO. 26 (RMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. S, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71045504G , S8054278 , PICT , S80542 11. ENGINEERING MATHEMATICS III PP 100 40 87 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 42	 78
DATE: 18 AUG. 2011 NOTE: FIRST LINE: SEAT NO., NAME OF PASSING, OTHER LINES: HEAD OF PASSING, S8054278 LOKHANDE VINAYA VISHWANA 01. DISCRETE STRUCTURES 02. PROGRAMMING & PROBLEM SOLVING	CENT THRAO PP PP	TRE : F	PUNE I EDATE, 5, MI 	MOTHER, EN. PASS MAI PRATIBHA 46 P C 40 P C	COMPUTER TECHNOLOGY, PUNE. PAGE NO. 26 (RMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. ARRANS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER 71045504G , S8054278 , PICT , S80542 11. ENGINEERING MATHEMATICS III PP 100 40 87 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 42 13. DATA STRUCTURES PP 100 40 60	 78 P

16. O. O. PROG. & COMP. GRAPH. LAB TW 50 20 38 P Page 119

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07. PROGRAMMING LABORATORY	PR	50	20	33	Р	17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20	35 P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	18	P C	18. MICROPROCESSORS & INTERFACING LABTW 50 20	37 P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	23	P C	19. MICROPROCESSORS & INTERFACING LABPR 50 20	36 P
10. SOFT SKILLS	TW	50	20	35	P C	20. DATA STRUCTURES LABORATORY TW 50 20	38 P
						21. DATA STRUCTURES LABORATORY PR 50 20	25 P
GRAND TOTAL = 883/1500, RESULT: HIGH	FR SFC	OND CI	ASS				
ORDN. 1 MARKS :	LIK JEC	COND CL	_,,,,,				
S8054279 MAHARANWAR NAMITA ANILRAG				 REN		, 71045510M , S8054279 , PICT , S80	54279
	 D PP	100	40		 IUKA P C	, 71045510M , S8054279 , PICT , S80 11. ENGINEERING MATHEMATICS III PP 100 40	 54279 44 P
S8054279 MAHARANWAR NAMITA ANILRAG		100	40 40	44			
S8054279 MAHARANWAR NAMITA ANILRAG	PP PP			44	P C	11. ENGINEERING MATHEMATICS III PP 100 40	44 P
S8054279 MAHARANWAR NAMITA ANILRAG 01. DISCRETE STRUCTURES 02. PROGRAMMING & PROBLEM SOLVING	PP PP	100	40	44 58 40	P C	11. ENGINEERING MATHEMATICS III PP 100 40 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40	44 P 44 P
S8054279 MAHARANWAR NAMITA ANILRAG 01. DISCRETE STRUCTURES 02. PROGRAMMING & PROBLEM SOLVING 03. DIGIT. ELECTRONICS & LOGIC DESIG	PP PP GNPP	100 100	40 40	44 58 40 52	P C P C	11. ENGINEERING MATHEMATICS III PP 100 40 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 13. DATA STRUCTURES PP 100 40	44 P44 P42 P
S8054279 MAHARANWAR NAMITA ANILRAG 01. DISCRETE STRUCTURES 02. PROGRAMMING & PROBLEM SOLVING 03. DIGIT. ELECTRONICS & LOGIC DESIGNATION OF THE PROBLEM SOLVING	PP PP GNPP PP	100 100 100	40 40 40	44 58 40 52	P C P C P C	11. ENGINEERING MATHEMATICS III PP 100 40 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 13. DATA STRUCTURES PP 100 40 14. COMPUTER GRAPHICS PP 100 40	44 P 44 P 42 P 62 P
S8054279 MAHARANWAR NAMITA ANILRAG 01. DISCRETE STRUCTURES 02. PROGRAMMING & PROBLEM SOLVING 03. DIGIT. ELECTRONICS & LOGIC DESIGNATION OF THE PROBLEM SOLVING 04. DATA STRUCTURES AND ALGORITHMS 05. HUMANITIES AND SOCIAL SCIENCE	PP PP GNPP PP	100 100 100 100	40 40 40 40	44 58 40 52 54	P C P C P C P C	11. ENGINEERING MATHEMATICS III PP 100 40 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 13. DATA STRUCTURES PP 100 40 14. COMPUTER GRAPHICS PP 100 40 15. COMPUTER ORGANIZATION PP 100 40	44 P 44 P 42 P 62 P 48 P

09. DIGITAL ELECTRONICS LABORATORY PR 50 20 25 P C 19. MICROPROCESSORS & INTERFACING LABPR 50 20 24 P 10. SOFT SKILLS TW 50 20 41 P C 20. DATA STRUCTURES LABORATORY TW 50 20 31 P 21. DATA STRUCTURES LABORATORY PR 50 20 32 P

GRAND TOTAL = 817+08/1500, RESULT: HIGHER SECOND CLASS [0.2]
ORDN. 1 MARKS:

S8054280 MALANI MUKUND PARMANAND				SUDHA	, 71045513F , S8054280 , PICT , S8054280
01. DISCRETE STRUCTURES	PP	100	40	68 P C	11. ENGINEERING MATHEMATICS III PP 100 40 51 P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	40 P C	12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 51 P
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						cols05	
03. DIGIT. ELECTRONICS & LOGIC DESIG	GNPP	100	40	66	P C		54 P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	42	P C	14. COMPUTER GRAPHICS PP 100 40	72 P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	49	PС	15. COMPUTER ORGANIZATION PP 100 40	57 P
06. PROGRAMMING LABORATORY	TW	25	10	18	PС	16. O. O. PROG. & COMP. GRAPH. LAB TW 50 20	42 P
07. PROGRAMMING LABORATORY	PR	50	20	37	Р	17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20	35 P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	16	PС	18. MICROPROCESSORS & INTERFACING LABTW 50 20	36 P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	24	P C	19. MICROPROCESSORS & INTERFACING LABPR 50 20	25 P
10. SOFT SKILLS	TW	50	20	33	P C	20. DATA STRUCTURES LABORATORY TW 50 20	33 P
						21. DATA STRUCTURES LABORATORY PR 50 20	31 P

GRAND TOTAL = 880/1500, RESULT: HIGHER SECOND CLASS ORDN. 1 MARKS:

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DATE: 18 AUG. 2011 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 27 (391)

NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8054281 MALHOTRA VIJAY GULSHARAN				ANJ	U	, 71045515B , S8054281 , PICT , S8054281
01. DISCRETE STRUCTURES	PP	100	40	74	P C	11. ENGINEERING MATHEMATICS III PP 100 40 71 I
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	44	P C	12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 58
03. DIGIT. ELECTRONICS & LOGIC DESIGNATION OF THE PROPERTY OF	GNPP	100	40	71	P C	13. DATA STRUCTURES PP 100 40 54 I
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	60	P C	14. COMPUTER GRAPHICS PP 100 40 70 I
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	52	P C	15. COMPUTER ORGANIZATION PP 100 40 50 I
06. PROGRAMMING LABORATORY	TW	25	10	21	P C	16. O. O. PROG. & COMP. GRAPH. LAB TW 50 20 37 I
07. PROGRAMMING LABORATORY	PR	50	20	37	P C	17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 36 I
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	21	P C	18. MICROPROCESSORS & INTERFACING LABTW 50 20 34 I
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	30	P C	19. MICROPROCESSORS & INTERFACING LABPR 50 20 36
10. SOFT SKILLS	TW	50	20	40	P C	20. DATA STRUCTURES LABORATORY TW 50 20 31
						21. DATA STRUCTURES LABORATORY PR 50 20 38 Page 121

GRAND TOTAL = 965/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS:

S8054282 MANDLIWALA NAFISA OANALI				RAS	HIDA	, 7	1045516L	, s8054282 ,	PICT		, s80	5428	2
01. DISCRETE STRUCTURES	PP	100	40	43	РС	11. ENGIN	EERING MATHE	MATICS III	PP	100	40	69	Р
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	40	РС	12. MICRO	PROC. & INTE	RFACING TECHNIQ	.PP	100	40	60	Р
03. DIGIT. ELECTRONICS & LOGIC DESIGNATION	GNPP	100	40	58	P C	13. DATA	STRUCTURES		PP	100	40	55	Р
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	59	P C	14. COMPU	TER GRAPHICS	i e	PP	100	40	72	Р
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	47	P C	15. COMPU	TER ORGANIZA	TION	PP	100	40	60	Р
06. PROGRAMMING LABORATORY	TW	25	10	21	P C	16. 0. 0.	PROG. & COM	IP. GRAPH. LAB	TW	50	20	43	Р
07. PROGRAMMING LABORATORY	PR	50	20	43	P C	17. 0. 0.	PROG. & COM	IP. GRAPH. LAB	PR	50	20	38	Р
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	21	P C	18. MICRO	PROCESSORS &	INTERFACING LA	BTW	50	20	38	Р
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	27	P C	19. MICRO	PROCESSORS &	INTERFACING LA	BPR	50	20	42	Р
10. SOFT SKILLS	TW	50	20	43	P C	20. DATA	STRUCTURES L	ABORATORY	TW	50	20	40	Р
						21. DATA	STRUCTURES L	ABORATORY	PR	50	20	36	Р

GRAND TOTAL = 955/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS:

S8054283 MANE POOJA VASANT				VAR	SHA		, 71132434E , S8054283 , PICT , S8054283	
01. DISCRETE STRUCTURES	PP	100	40	50	P C	11	. ENGINEERING MATHEMATICS III PP 100 40 40 P	,
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	58	P C	12	. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 57 P	
03. DIGIT. ELECTRONICS & LOGIC DESIG	SNPP	100	40	41	P C	13	DATA STRUCTURES PP 100 40 56 P	
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	48	P C	14	COMPUTER GRAPHICS PP 100 40 61 P	
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	46	P C	15	. COMPUTER ORGANIZATION PP 100 40 54 P	
06. PROGRAMMING LABORATORY	TW	25	10	19	P C	16	O.O. PROG. & COMP. GRAPH. LAB TW 50 20 43 P	,
07. PROGRAMMING LABORATORY	PR	50	20	34	Р	17	. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 36 P	

									7-05					
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	19	Р	С	18.	MICROPROCESSORS	cols05 & INTERFACING L	.ABTW	50	20	35	Р
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	23	Р	С	19.	MICROPROCESSORS	& INTERFACING L	.ABPR	50	20	37	Р
10. SOFT SKILLS	TW	50	20	37	Р	С	20.	DATA STRUCTURES	LABORATORY	TW	50	20	37	Р
							21.	DATA STRUCTURES	LABORATORY	PR	50	20	31	Р
CRAND TOTAL 963/1500 PECULT, UTCH	ED 654	SOND C	1.455											
GRAND TOTAL = 862/1500, RESULT: HIGH	EK SEC	LOND CI	LASS											
ORDN. 1 MARKS :														
UNIVE	RSITY	OF PU	NE ,S	.E.(200	8 PAT.)(COMP	JTER) EXAMINATION	N MAY 2011				• •	
DATE : 18 AUG. 2011	CENT	ΓRE :	PUNE :	[NST]	דעדז	TE OF CO	MPUTE	R TECHNOLOGY, PL	INE.	PAG	GE NO.	28	(3	92)
NOTE: FIRST LINE : SEAT NO., NAME	OF THE	E CAND:	IDATE	, MC	THE	ER, PERM	ANENT	REG. NO., PREV	OIOUS SEAT NO.,	COLLEG	GE, S	SEAT N	١٥.	
OTHER LINES: HEAD OF PASSING,	MAX.	. MARK	S, M	EN. F	PASS	MARKS,	MAR	KS OBTAINED, P/	F:PASS/FAIL, C:	PREVIO	DUS CAF	RRY O	/ER	
S8054284 MANVI BHATIA				A NI -	111			710455201	, s8054284 ,	DTC	-	c 9 (15120	1
S8054284 MANVI BHATIA				AND	, 0			, 71045520J	, 30034264 ,	PICT		, S80	J3 4 26	4
01. DISCRETE STRUCTURES	PP	100	40	69	Р	С	11.	ENGINEERING MAT	THEMATICS III	PP	100	40	80	Р
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	50	Р	С	12.	MICROPROC. & IN	ITERFACING TECHNI	Q.PP	100	40	56	Р
03. DIGIT. ELECTRONICS & LOGIC DESI	GNPP	100	40	82	Р	С	13.	DATA STRUCTURES	5	PP	100	40	57	Р
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	67	Р	С	14.	COMPUTER GRAPHI	:CS	PP	100	40	77	Р
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	52	Р	С	15.	COMPUTER ORGANI	ZATION	PP	100	40	66	Р
06. PROGRAMMING LABORATORY	TW	25	10	22	Р	С	16.	O. O. PROG. & C	COMP. GRAPH. LAB	TW	50	20	41	Р
07. PROGRAMMING LABORATORY	PR	50	20	40	Р	С	17.	O. O. PROG. & C	COMP. GRAPH. LAB	PR	50	20	44	Р
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	20	Р	С	18.	MICROPROCESSORS	& INTERFACING L	.ABTW	50	20	36	Р
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	34	Р	С	19.	MICROPROCESSORS	& INTERFACING L	.ABPR	50	20	40	Р
10. SOFT SKILLS	TW	50	20	44	Р	С	20.	DATA STRUCTURES	LABORATORY	TW	50	20	41	Р
							21.	DATA STRUCTURES	LABORATORY	PR	50	20	40	Р
GRAND TOTAL = 1058/1500, RESULT: FIRS	Τ (ΙΔς	SS WTTI	H DTS	LLNCI	LTUN	N.								
ORDN. 1 MARKS :			. 213			-								
		_		_										

S8054285 MASKE SHRUTI HIRALAL			SANGEETA	, 71045523C , S8054285 ,	PICT	, s80	054285
01. DISCRETE STRUCTURES PP	100	40	40 P C	11. ENGINEERING MATHEMATICS III	PP 100	40	52 P
02. PROGRAMMING & PROBLEM SOLVING PP	100	40	40 P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP 100	40	31* P
03. DIGIT. ELECTRONICS & LOGIC DESIGNPP	100	40	58 P C	13. DATA STRUCTURES	PP 100	40	43 P
04. DATA STRUCTURES AND ALGORITHMS PP	100	40	41 P C	14. COMPUTER GRAPHICS	PP 100	40	47 P
05. HUMANITIES AND SOCIAL SCIENCE PP	100	40	40 P C	15. COMPUTER ORGANIZATION	PP 100	40	40 P
06. PROGRAMMING LABORATORY TW	25	10	18 P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW 50	20	38 P
07. PROGRAMMING LABORATORY PR	50	20	25 P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR 50	20	33 P
08. DIGITAL ELECTRONICS LABORATORY TW	25	10	18 P C	18. MICROPROCESSORS & INTERFACING LAB	TW 50	20	34 P
09. DIGITAL ELECTRONICS LABORATORY PR	50	20	27 P C	19. MICROPROCESSORS & INTERFACING LAB	PR 50	20	30 P
10. SOFT SKILLS TW	50	20	39 P C	20. DATA STRUCTURES LABORATORY	TW 50	20	31 P
				21. DATA STRUCTURES LABORATORY	PR 50	20	26 P

GRAND TOTAL = 751/1500, RESULT: SECOND CLASS * [0.4]
ORDN. 1 MARKS:

S8054287 MUNESHWAR PRANALI PRAKASH	I			PRA	MILA	, 71045533L , S8054287 , PICT , S8054287	7
01. DISCRETE STRUCTURES	PP	100	40	50	P C	11. ENGINEERING MATHEMATICS III PP 100 40 57	Р
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	47	P C	12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 40	Р
03. DIGIT. ELECTRONICS & LOGIC DESIG	NPP	100	40	53	P C	13. DATA STRUCTURES PP 100 40 56	Р
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	40	P C	14. COMPUTER GRAPHICS PP 100 40 56	Р
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	46	P C	15. COMPUTER ORGANIZATION PP 100 40 40	Р
06. PROGRAMMING LABORATORY	TW	25	10	18	P C	16. O. O. PROG. & COMP. GRAPH. LAB TW 50 20 38	Р
07. PROGRAMMING LABORATORY	PR	50	20	37	Р	17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 30	Р
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	18	P C	18. MICROPROCESSORS & INTERFACING LABTW 50 20 30	Р
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	27	P C	19. MICROPROCESSORS & INTERFACING LABPR 50 20 10	F
10. SOFT SKILLS	TW	50	20	39	P C	20. DATA STRUCTURES LABORATORY TW 50 20 29	Р
						21. DATA STRUCTURES LABORATORY PR 50 20 20	Р

GRAND TOTAL = 781/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS:

UNIVE	 ERSITY	 OF PU	 NE ,S	 .E.(2	 2008	PAT.)(COMPU	 TER) EXAMINA	TION MAY 2011					
DATE : 18 AUG. 2011	CENT	RE :	PUNE I	NSTI	TUTE	E OF COMPUTE	R TECHNOLOGY	, PUNE.	PAG	E NO.	29	(3	93)
NOTE: FIRST LINE : SEAT NO., NAME OTHER LINES: HEAD OF PASSING,	MAX.	MARK	S, MI	N. P	ASS	MARKS, MARI	KS OBTAINED,	P/F:PASS/FAIL, C:F	PREVIO	US CAF		ER	
S8054288 MUSALE SAGAR RAJENDRA		• •			PANA		, 7104553	4J , S8054288 ,	PICT		, s80		
01. DISCRETE STRUCTURES	PP	100	40	57	P C	11.	ENGINEERING	MATHEMATICS III	PP	100	40	51	Р
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	50	PC	12.	MICROPROC.	& INTERFACING TECHNIC	Q.PP	100	40	40	Р
03. DIGIT. ELECTRONICS & LOGIC DESI	GNPP	100	40	64	PC	13.	DATA STRUCT	URES	PP	100	40	48	Р
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	50	PC	14.	COMPUTER GR	APHICS	PP	100	40	49	Р
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	43	PC	15.	COMPUTER OR	GANIZATION	PP	100	40	55	Р
06. PROGRAMMING LABORATORY	TW	25	10	19	PC	16.	O. O. PROG.	& COMP. GRAPH. LAB	TW	50	20	44	Р
07. PROGRAMMING LABORATORY	PR	50	20	42	PC	17.	O. O. PROG.	& COMP. GRAPH. LAB	PR	50	20	36	Р
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	22	PC	18.	MICROPROCES	SORS & INTERFACING LA	ABTW	50	20	41	Р
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	30	PC	19.	MICROPROCES	SORS & INTERFACING LA	ABPR	50	20	40	Р
10. SOFT SKILLS	TW	50	20	41	PC	20.	DATA STRUCT	URES LABORATORY	TW	50	20	35	Р
						21.	DATA STRUCT	URES LABORATORY	PR	50	20	41	Р
GRAND TOTAL = 898+02/1500, RESULT: FORDN. 1 MARKS:	IRST (CLASS	[0.2]										
S8054289 MUTALIK MADHUMITRA MADHA	V			MAI	TREY	YEE	, 7104553	5G , S8054289 ,	PICT		, s80	5428	9
01. DISCRETE STRUCTURES	PP	100	40	74	PC	11.	ENGINEERING	MATHEMATICS III	PP	100	40	72	Р
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	69	PC	12.	MICROPROC.	& INTERFACING TECHNIC	Q.PP	100	40	53	Р
03. DIGIT. ELECTRONICS & LOGIC DESI	GNPP	100	40	76	PC	13.	DATA STRUCT	URES	PP	100	40	77	Р
								- 125					

04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	61	РС	cols05 14. COMPUTER GRAPHICS PP 100 40 66 P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	57	РС	15. COMPUTER ORGANIZATION PP 100 40 62 P
06. PROGRAMMING LABORATORY	TW	25	10	20	P C	16. O. O. PROG. & COMP. GRAPH. LAB TW 50 20 34 P
07. PROGRAMMING LABORATORY	PR	50	20	44	P C	17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 39 P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	24	P C	18. MICROPROCESSORS & INTERFACING LABTW 50 20 38 P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	40	P C	19. MICROPROCESSORS & INTERFACING LABPR 50 20 37 P
10. SOFT SKILLS	TW	50	20	37	P C	20. DATA STRUCTURES LABORATORY TW 50 20 35 P
						21. DATA STRUCTURES LABORATORY PR 50 20 40 P

GRAND TOTAL = 1055/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS :

S8054290 MUTHA SURABHI SHASHIKUMAR				UJJ	WALA	, 71045536E , S8054290 , PICT , S8054290	
01. DISCRETE STRUCTURES	PP	100	40	68	P C	11. ENGINEERING MATHEMATICS III PP 100 40 79 P	
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	62	P C	12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 50 P	
03. DIGIT. ELECTRONICS & LOGIC DESIG	NPP	100	40	83	P C	13. DATA STRUCTURES PP 100 40 71 P	
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	59	P C	14. COMPUTER GRAPHICS PP 100 40 68 P	
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	53	P C	15. COMPUTER ORGANIZATION PP 100 40 62 P	
06. PROGRAMMING LABORATORY	TW	25	10	24	P C	16. O. O. PROG. & COMP. GRAPH. LAB TW 50 20 44 P	
07. PROGRAMMING LABORATORY	PR	50	20	45	P C	17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 42 P	
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	24	P C	18. MICROPROCESSORS & INTERFACING LABTW 50 20 42 P	
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	42	P C	19. MICROPROCESSORS & INTERFACING LABPR 50 20 46 P	
10. SOFT SKILLS	TW	50	20	38	P C	20. DATA STRUCTURES LABORATORY TW 50 20 46 P	
						21. DATA STRUCTURES LABORATORY PR 50 20 39 P	

GRAND TOTAL = 1087/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS :

UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(COMPUTER) EXAMINATION MAY 2011

DATE: 18 AUG. 2011 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 30 (394)

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NOTE: FIRST LINE : SEAT NO., NAME	OF THE	E CAND	IDATE,	МО	THER, I	ERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.	
OTHER LINES: HEAD OF PASSING,	MAX.	. MARKS	5, MI	IN. P	ASS MAI	KS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER	
S8054291 NAIR AJAY RADHAKRISHNAN				GEE	ТНА	, 71045539к , S8054291 , PICT , S8054291	
01. DISCRETE STRUCTURES	PP	100	40	63	P C	11. ENGINEERING MATHEMATICS III PP 100 40 50 P	
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	55	P C	12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 51 P	
03. DIGIT. ELECTRONICS & LOGIC DESI	GNPP	100	40	55	P C	13. DATA STRUCTURES PP 100 40 48 P	
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	51	P C	14. COMPUTER GRAPHICS PP 100 40 72 P	
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	52	P C	15. COMPUTER ORGANIZATION PP 100 40 48 P	
06. PROGRAMMING LABORATORY	TW	25	10	16	P C	16. O. O. PROG. & COMP. GRAPH. LAB TW 50 20 30 P	
07. PROGRAMMING LABORATORY	PR	50	20	40	P C	17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 37 P	
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	19	P C	18. MICROPROCESSORS & INTERFACING LABTW 50 20 27 P	
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	35	P C	19. MICROPROCESSORS & INTERFACING LABPR 50 20 39 P	
10. SOFT SKILLS	TW	50	20	31	P C	20. DATA STRUCTURES LABORATORY TW 50 20 29 P	
						21. DATA STRUCTURES LABORATORY PR 50 20 33 P	

GRAND TOTAL = 881/1500, RESULT: HIGHER SECOND CLASS ORDN. 1 MARKS:

S8054292 NAJAN KAUSTUBH GORAKSHANATH			MEERA	, 71045540c , s8054292 ,	PICT	, s80)54292
01. DISCRETE STRUCTURES PP	100	40	42 P C	11. ENGINEERING MATHEMATICS III	PP 100	40	54 P
02. PROGRAMMING & PROBLEM SOLVING PP	100	40	40 P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP 100	40	21 F
03. DIGIT. ELECTRONICS & LOGIC DESIGNPP	100	40	47 P C	13. DATA STRUCTURES	PP 100	40	40 P
04. DATA STRUCTURES AND ALGORITHMS PP	100	40	48 P	14. COMPUTER GRAPHICS	PP 100	40	41 P
05. HUMANITIES AND SOCIAL SCIENCE PP	100	40	31 F	15. COMPUTER ORGANIZATION	PP 100	40	27 F
06. PROGRAMMING LABORATORY TW	25	10	17 P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW 50	20	41 P
07. PROGRAMMING LABORATORY PR	50	20	30 P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR 50	20	30 P
08. DIGITAL ELECTRONICS LABORATORY TW	25	10	18 P C	18. MICROPROCESSORS & INTERFACING LAB	TW 50	20	40 P

00 DICITAL ELECTRONICS LABORATORY	DD	F0	20	20	ъ.	c	10	MICDODDOCECCODC	cols05 & INTERFACING LA	D D D	F.O.	20	26	В
09. DIGITAL ELECTRONICS LABORATORY	PR	50 50	20 20	20	P						50	20	36 35	
10. SOFT SKILLS	TW	30	20	34	P	C		DATA STRUCTURES		TW	50	20		
							21.	DATA STRUCTURES	LABORATORY	PR	50	20	32	Р
GRAND TOTAL = 724/1500, RESULT: FAILS	5 A.T	к.т.												
ORDN. 1 MARKS :														
S8054293 NANWANI SHRUTI RAMESH				KAM	LES	н		, 71045541M	, s8054293 ,	PICT		, s80	5429	3
01. DISCRETE STRUCTURES	PP	100	40	71	Р	С	11.	ENGINEERING MAT	HEMATICS III	PP	100	40	61	Р
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	67	Р	С	12.	MICROPROC. & IN	TERFACING TECHNIQ	.PP	100	40	46	Р
03. DIGIT. ELECTRONICS & LOGIC DESIGNATION OF THE PROPERTY OF	GNPP	100	40	84	Р	С	13.	DATA STRUCTURES		PP	100	40	75	Р
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	56	Р	С	14.	COMPUTER GRAPHI	cs	PP	100	40	67	Р
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	40	Р	С	15.	COMPUTER ORGANI	ZATION	PP	100	40	57	Р
06. PROGRAMMING LABORATORY	TW	25	10	20	Р	С	16.	O. O. PROG. & C	OMP. GRAPH. LAB	TW	50	20	33	Р
07. PROGRAMMING LABORATORY	PR	50	20	41	Р	С	17.	O. O. PROG. & C	OMP. GRAPH. LAB	PR	50	20	42	Р
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	22	Р	С	18.	MICROPROCESSORS	& INTERFACING LA	BTW	50	20	38	Р
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	40	Р	С	19.	MICROPROCESSORS	& INTERFACING LA	BPR	50	20	44	Р
10. SOFT SKILLS	TW	50	20	41	Р	С	20.	DATA STRUCTURES	LABORATORY	TW	50	20	33	Р
							21.	DATA STRUCTURES	LABORATORY	PR	50	20	33	Р
GRAND TOTAL = 1011/1500, RESULT: FIRST	T CLAS	SS WTTI	א חדכי	TTNCT	TON									
ORDN. 1 MARKS:	CLAS)5 W 111	1 013	111101	1011									
ORDIN: I PARKS !														
	RSITY	OF PU	NE ,S	.E.(2	2008	PAT.)(COMPL	TER) EXAMINATION	MAY 2011					
DATE : 18 AUG. 2011	CENT	ΓRE :	PUNE :	INSTI	TUT	E OF COM	IPUTE	R TECHNOLOGY, PU	NE.	PAG	E NO.	31	(3	95)
NOTE: FIRST LINE : SEAT NO., NAME (-		-					-	SEAT N		
OTHER LINES: HEAD OF PASSING,	MAX.	. MARK	S, M	IN. P	ASS	MARKS,	MAR	KS OBTAINED, P/	F:PASS/FAIL, C:P	REVIO	US CAF	RRY OV	ER/	

KADAM

S8054294 NARENDRA PAL SINGH

, 71045542K , S8054294 , PICT Page 128

, s8054294

01.	DISCRETE STRUCTURES	PP	100	40	53	P C	11	. ENGINEERING MATHEMATICS III	PP	100	40	72	Р
02.	PROGRAMMING & PROBLEM SOLVING	PP	100	40	48	P C	12	. MICROPROC. & INTERFACING TECHNIC	PP.	100	40	52	Р
03.	DIGIT. ELECTRONICS & LOGIC DESIG	NPP	100	40	66	P C	13	. DATA STRUCTURES	PP	100	40	69	Р
04.	DATA STRUCTURES AND ALGORITHMS	PP	100	40	51	P C	14	. COMPUTER GRAPHICS	PP	100	40	68	Р
05.	HUMANITIES AND SOCIAL SCIENCE	PP	100	40	53	P C	15	. COMPUTER ORGANIZATION	PP	100	40	58	Р
06.	PROGRAMMING LABORATORY	TW	25	10	19	P C	16	O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	46	Р
07.	PROGRAMMING LABORATORY	PR	50	20	30	P C	17	O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	27	Р
08.	DIGITAL ELECTRONICS LABORATORY	TW	25	10	20	P C	18	. MICROPROCESSORS & INTERFACING LA	ABTW	50	20	45	Р
09.	DIGITAL ELECTRONICS LABORATORY	PR	50	20	40	P C	19	. MICROPROCESSORS & INTERFACING LA	BPR	50	20	39	Р
10.	SOFT SKILLS	TW	50	20	35	P C	20	. DATA STRUCTURES LABORATORY	TW	50	20	38	Р
							21	. DATA STRUCTURES LABORATORY	PR	50	20	33	Р

GRAND TOTAL = 962/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

S8054295 NEHA RANA				SUDI	НА		, 71045543н , S8054295 , РІСТ	, s80	54295	5
01. DISCRETE STRUCTURES	PP	100	40	72	P C	11	. ENGINEERING MATHEMATICS III PP 100	40	89	Р
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	64	P C	12	. MICROPROC. & INTERFACING TECHNIQ.PP 100	40	63	Р
03. DIGIT. ELECTRONICS & LOGIC DESIG	NPP	100	40	80	P C	13	. DATA STRUCTURES PP 100	40	66	Р
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	58	P C	14	. COMPUTER GRAPHICS PP 100	40	74	Р
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	46	P C	15	. COMPUTER ORGANIZATION PP 100	40	46	Р
06. PROGRAMMING LABORATORY	TW	25	10	22	P C	16	O. O. PROG. & COMP. GRAPH. LAB TW 50	20	35	Р
07. PROGRAMMING LABORATORY	PR	50	20	42	P C	17	O. O. PROG. & COMP. GRAPH. LAB PR 50	20	37	Р
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	22	P C	18	. MICROPROCESSORS & INTERFACING LABTW 50	20	36	Р
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	30	P C	19	. MICROPROCESSORS & INTERFACING LABPR 50	20	40	Р
10. SOFT SKILLS	TW	50	20	40	P C	20	. DATA STRUCTURES LABORATORY TW 50	20	38	Р
						21	. DATA STRUCTURES LABORATORY PR 50	20	41	Р

GRAND TOTAL = 1041/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS :

S8054296 NEMADE AJAY VILAS				MAN	JUSHA		, 71045544F , S8054296 ,	PICT		, s80	54296	5
01. DISCRETE STRUCTURES	PP	100	40	58	P C	11.	ENGINEERING MATHEMATICS III	PP	100	40	73	Р
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	55	P C	12.	MICROPROC. & INTERFACING TECHNIQ	.PP	100	40	49	Р
03. DIGIT. ELECTRONICS & LOGIC DESIG	NPP	100	40	59	P C	13.	DATA STRUCTURES	PP	100	40	67	Р
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	51	P C	14.	COMPUTER GRAPHICS	PP	100	40	67	Р
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	50	P C	15.	COMPUTER ORGANIZATION	PP	100	40	56	Р
06. PROGRAMMING LABORATORY	TW	25	10	20	P C	16.	O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	38	Р
07. PROGRAMMING LABORATORY	PR	50	20	38	P C	17.	O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	38	Р
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	20	P C	18.	MICROPROCESSORS & INTERFACING LA	BTW	50	20	35	Р
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	32	P C	19.	MICROPROCESSORS & INTERFACING LA	BPR	50	20	31	Р
10. SOFT SKILLS	TW	50	20	38	P C	20.	DATA STRUCTURES LABORATORY	TW	50	20	33	Р
						21.	DATA STRUCTURES LABORATORY	PR	50	20	35	Р
ORDN. 1 MARKS :	 RSITY	 OF PUI	 NE ,S	 .E.(2	 008 PAT.)(COMPU						
DATE : 18 AUG. 2011	CENT	RE : P	PUNE I	NSTI	TUTE OF COM	ИРUТЕ	R TECHNOLOGY, PUNE.	PAG	E NO.	32	(39	96)
NOTE: FIRST LINE : SEAT NO., NAME O	F THE	CANDI	DATE,	MO ⁻	THER, PERMA	ANENT	REG. NO., PREVIOUS SEAT NO., C	OLLEG	E, S	EAT N	0.	
OTHER LINES: HEAD OF PASSING,	MAX.	MARKS	s, MI	N. P	ASS MARKS,	MAR	KS OBTAINED, P/F:PASS/FAIL, C:P	REVIO	US CAR	RY OV	ER	
S8054297 NERKAR NIKITA SUNIL				AMI [*]	TA		, 71045545D , S8054297 ,	PICT		, s80	54297	7
01. DISCRETE STRUCTURES	PP	100	40	74	P C	11.	ENGINEERING MATHEMATICS III	PP	100	40	78	Р
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	54	P C	12.	MICROPROC. & INTERFACING TECHNIQ	.PP	100	40	53	Р
03. DIGIT. ELECTRONICS & LOGIC DESIG	NPP	100	40	74	P C	13.	DATA STRUCTURES	PP	100	40	73	Р

05.	HUMANITIES AND SOCIAL SCIENCE	PP	100	40	44	P C	15.	cols05 COMPUTER ORGANIZATION	PP	100	40	54	Р
06.	PROGRAMMING LABORATORY	TW	25	10	23	P C	16.	O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	46	Р
07.	PROGRAMMING LABORATORY	PR	50	20	38	P C	17.	O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	43	Р
08.	DIGITAL ELECTRONICS LABORATORY	TW	25	10	24	P C	18.	MICROPROCESSORS & INTERFACING LA	ABTW	50	20	41	Р
09.	DIGITAL ELECTRONICS LABORATORY	PR	50	20	42	P C	19.	MICROPROCESSORS & INTERFACING LA	ABPR	50	20	44	Р
10.	SOFT SKILLS	TW	50	20	46	P C	20.	DATA STRUCTURES LABORATORY	TW	50	20	45	Р
							21.	DATA STRUCTURES LABORATORY	PR	50	20	42	Р
GRAND	TOTAL = 1069/1500, RESULT: FIRST	CLAS	SS WITH	H DIST	TINCT	ION							
ORDN.	1 MARKS :												
S80	54298 NITYA RAJ				SAR	ASWATHY		, 71045550L , S8054298 ,	PICT	-	, s80)5429	8
01.	DISCRETE STRUCTURES	PP	100	40	56	P C	11.	ENGINEERING MATHEMATICS III	PP	100	40	45	Р

S8054298	NITYA RAJ				SARA	ASWATHY		, 71045550L , S8054298 ,	PICT		, s805	54298	
01. DISCRE	TE STRUCTURES	PP	100	40	56	P C	11.	ENGINEERING MATHEMATICS III	PP	100	40	45	Р
02. PROGRA	MMING & PROBLEM SOLVING	PP	100	40	54	P C	12.	MICROPROC. & INTERFACING TECHNIQ.	. PP	100	40	48	Р
03. DIGIT.	ELECTRONICS & LOGIC DESIG	NPP	100	40	65	P C	13.	DATA STRUCTURES	PP	100	40	41	Р
04. DATA S	TRUCTURES AND ALGORITHMS	PP	100	40	51	P C	14.	COMPUTER GRAPHICS	PP	100	40	72	Р
05. HUMANI	TIES AND SOCIAL SCIENCE	PP	100	40	43	P C	15.	COMPUTER ORGANIZATION	PP	100	40	52	Р
06. PROGRA	MMING LABORATORY	TW	25	10	20	P C	16.	O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	41	Р
07. PROGRA	MMING LABORATORY	PR	50	20	38	P C	17.	O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	20\$	Р
08. DIGITA	L ELECTRONICS LABORATORY	TW	25	10	19	P C	18.	MICROPROCESSORS & INTERFACING LAB	ВТW	50	20	44	Р
09. DIGITA	L ELECTRONICS LABORATORY	PR	50	20	25	P C	19.	MICROPROCESSORS & INTERFACING LAB	BPR	50	20	36	Р
10. SOFT S	KILLS	TW	50	20	35	P C	20.	DATA STRUCTURES LABORATORY	TW	50	20	38	Р
							21.	DATA STRUCTURES LABORATORY	PR	50	20	22	Р

GRAND TOTAL = 865/1500, RESULT: HIGHER SECOND CLASS [\$ 0.1] ORDN. 1 MARKS : (17)2,

S8054299 PANDYA PARTH VIPUL ASHA , 71045553E , S8054299 , PICT , S8054299

01. DISCRETE STRUCTURES PP 100 40 51 P C 11. ENGINEERING MATHEMATICS III PP 100 40 40 P Page 131

02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	40	P C	12. MICROPROC. & INTERFACING TEC	HNIQ.PP	100	40	40	Р
03. DIGIT. ELECTRONICS & LOGIC DESIGNATION OF THE PROPERTY OF	SNPP	100	40	40	P C	13. DATA STRUCTURES	PP	100	40	40	Р
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	46	P C	14. COMPUTER GRAPHICS	PP	100	40	58	Р
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	49	P C	15. COMPUTER ORGANIZATION	PP	100	40	50	Р
06. PROGRAMMING LABORATORY	TW	25	10	19	P C	16. O. O. PROG. & COMP. GRAPH. L	AB TW	50	20	33	Р
07. PROGRAMMING LABORATORY	PR	50	20	22	P C	17. O. O. PROG. & COMP. GRAPH. L	AB PR	50	20	20	Р
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	16	P C	18. MICROPROCESSORS & INTERFACING	G LABTW	50	20	27	Р
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	21	P C	19. MICROPROCESSORS & INTERFACING	G LABPR	50	20	05	F
10. SOFT SKILLS	TW	50	20	34	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	33	Р
						21. DATA STRUCTURES LABORATORY	PR	50	20	06	F
GRAND TOTAL = 690/1500, RESULT: FAIL: ORDN. 1 MARKS:											
UNIVE	RSITY	OF PU	NE ,S	.E.(2	2008 PAT.)(COMPUTER) EXAMINATION MAY 2011					
UNIVE	RSITY	OF PU	NE ,S	.E.(2	2008 PAT.)(
DATE: 18 AUG. 2011	RSITY CENT	OF PU	NE ,S. PUNE I	.E.(2 NSTI	2008 PAT.)(COMPUTER) EXAMINATION MAY 2011	PA	GE NO.	33	(3	97)
DATE: 18 AUG. 2011	RSITY CENT	OF PUN	NE ,S. PUNE I	E.(2 NSTI	2008 PAT.)(COMPUTER) EXAMINATION MAY 2011 MPUTER TECHNOLOGY, PUNE.	PA 	GE NO.	33	(3	97)
DATE: 18 AUG. 2011 NOTE: FIRST LINE: SEAT NO., NAME O	CENT OF THE	OF PUNTRE : P	NE ,S. PUNE I 	.E.(2 NSTI MO	2008 PAT.)(TTUTE OF COM	COMPUTER) EXAMINATION MAY 2011 MPUTER TECHNOLOGY, PUNE.	PA · · · · , COLLE	GE NO. GE,	33 SEAT N	(3 	97)
DATE: 18 AUG. 2011 NOTE: FIRST LINE: SEAT NO., NAME OF PASSING,	CENT OF THE	OF PUNTRE : POST PUNTAGE : POST PUNT	PUNE I DATE, MI	NSTI MO	2008 PAT.)(TTUTE OF COM OTHER, PERMA PASS MARKS,	COMPUTER) EXAMINATION MAY 2011 MPUTER TECHNOLOGY, PUNE. ANENT REG. NO., PREVIOUS SEAT NO.	PA · · · · , COLLE C:PREVI	GE NO GE, S	33 · · · · SEAT N RRY O\	(3 NO. /ER	97) · · ·
DATE: 18 AUG. 2011 NOTE: FIRST LINE: SEAT NO., NAME OF PASSING,	CENT OF THE	OF PUNTRE : POST PUNTAGE : POST PUNT	PUNE I DATE, MI	MO	2008 PAT.)(TTUTE OF COM OTHER, PERMA PASS MARKS,	COMPUTER) EXAMINATION MAY 2011 MPUTER TECHNOLOGY, PUNE. ANENT REG. NO., PREVIOUS SEAT NO. MARKS OBTAINED, P/F:PASS/FAIL,	PA , COLLE C:PREVI	GE NO GE, .: OUS CAI	33 · · · · SEAT N RRY O\	(3 NO. /ER 	97)
DATE: 18 AUG. 2011 NOTE: FIRST LINE: SEAT NO., NAME OF COTHER LINES: HEAD OF PASSING,	CENT OF THE MAX.	OF PUNTRE : P	PUNE I CDATE, MI	MO NSTI MO N. P	2008 PAT.)(CTUTE OF COM OTHER, PERMA PASS MARKS,	COMPUTER) EXAMINATION MAY 2011 MPUTER TECHNOLOGY, PUNE. ANENT REG. NO., PREVIOUS SEAT NO. MARKS OBTAINED, P/F:PASS/FAIL, , 71132435C , S8054300	PA , COLLE C:PREVI	GE NO. GE, S OUS CAI	33 SEAT N RRY ON 	(3 NO . /ER 	97)
DATE: 18 AUG. 2011 NOTE: FIRST LINE: SEAT NO., NAME OF OTHER LINES: HEAD OF PASSING, S8054300 PATE MAYUR SATISH	CENT OF THE MAX.	OF PUNTRE : P	PUNE I CDATE, MI	MO NSTI MO N. P MAD	2008 PAT.)(CTUTE OF COM OTHER, PERMA PASS MARKS,	COMPUTER) EXAMINATION MAY 2011 MPUTER TECHNOLOGY, PUNE. ANENT REG. NO., PREVIOUS SEAT NO. MARKS OBTAINED, P/F:PASS/FAIL, , 71132435C , S8054300	PA , COLLE C:PREVI , PIC	GE NO. GE, S OUS CAI	33 SEAT N RRY ON 	(3 NO . /ER 	97)
DATE: 18 AUG. 2011 NOTE: FIRST LINE: SEAT NO., NAME OF OTHER LINES: HEAD OF PASSING, S8054300 PATE MAYUR SATISH 01. DISCRETE STRUCTURES	CENT OF THE MAX	OF PUNTRE: POST CANDING MARKS	PUNE I DATE, MI	MO NSTI MO N. P MAD	2008 PAT.)(CTUTE OF COM OTHER, PERMA PASS MARKS, OHURI P C P C	COMPUTER) EXAMINATION MAY 2011 MPUTER TECHNOLOGY, PUNE. ANENT REG. NO., PREVIOUS SEAT NO. MARKS OBTAINED, P/F:PASS/FAIL, , 71132435C , \$8054300 11. ENGINEERING MATHEMATICS III	PA , COLLE C:PREVI , PIC	GE NO. GE, S OUS CAI T	33 SEAT M RRY ON , S80	(3 NO. /ER 	97) 0 P
DATE: 18 AUG. 2011 NOTE: FIRST LINE: SEAT NO., NAME OF OTHER LINES: HEAD OF PASSING, S8054300 PATE MAYUR SATISH O1. DISCRETE STRUCTURES O2. PROGRAMMING & PROBLEM SOLVING	CENT OF THE MAX	OF PUNTRE: POST PUNTAGE CANDING MARKS	PUNE I DATE, MI 40 40	MAD 46 53 66	2008 PAT.)(CTUTE OF COM OTHER, PERMA PASS MARKS, OHURI P C P C	COMPUTER) EXAMINATION MAY 2011 MPUTER TECHNOLOGY, PUNE. ANENT REG. NO., PREVIOUS SEAT NO. MARKS OBTAINED, P/F:PASS/FAIL, , 71132435C , \$8054300 11. ENGINEERING MATHEMATICS III 12. MICROPROC. & INTERFACING TEC	PA , COLLE C:PREVI , PIC PP HNIQ.PP	GE NO. GE, S OUS CAI T 100 100	33 SEAT M RRY ON , S80 40 40	(3 NO. /ER 05430 48 55	97) 0 P P
DATE: 18 AUG. 2011 NOTE: FIRST LINE: SEAT NO., NAME OF PASSING, OTHER LINES: HEAD OF PASSING, S8054300 PATE MAYUR SATISH 01. DISCRETE STRUCTURES 02. PROGRAMMING & PROBLEM SOLVING 03. DIGIT. ELECTRONICS & LOGIC DESIG	CENT CENT MAX. PP PP GNPP	OF PUNTRE: P	PUNE I DATE, MI 40 40 40	MAD 46 53 66 57	PASS MARKS, CHURI P C P C P C	COMPUTER) EXAMINATION MAY 2011 MPUTER TECHNOLOGY, PUNE. ANENT REG. NO., PREVIOUS SEAT NO. MARKS OBTAINED, P/F:PASS/FAIL, , 71132435C , S8054300 11. ENGINEERING MATHEMATICS III 12. MICROPROC. & INTERFACING TECH 13. DATA STRUCTURES	PA , COLLE C:PREVI , PIC PP HNIQ.PP PP	GE NO. GE, S OUS CAI T 100 100	33 SEAT N RRY ON , S80 40 40 40	(3 NO. /ER 05430 48 55 84	97) 0 P P P
DATE: 18 AUG. 2011 NOTE: FIRST LINE: SEAT NO., NAME OF PASSING, OTHER LINES: HEAD OF PASSING, S8054300 PATE MAYUR SATISH 01. DISCRETE STRUCTURES 02. PROGRAMMING & PROBLEM SOLVING 03. DIGIT. ELECTRONICS & LOGIC DESIGNORM.	RSITY CENT DF THE MAX PP PP GNPP PP	OF PUNTRE: POST PUNTAGE CANDING MARKS 100 100 100 100	PUNE I DATE, 5, MI 40 40 40 40 40	MAD 46 53 66 57	PASS MARKS, CHUTE OF COM CHURI P C P C P C	COMPUTER) EXAMINATION MAY 2011 MPUTER TECHNOLOGY, PUNE. ANENT REG. NO., PREVIOUS SEAT NO. MARKS OBTAINED, P/F:PASS/FAIL, , 71132435C , \$8054300 11. ENGINEERING MATHEMATICS III 12. MICROPROC. & INTERFACING TEC 13. DATA STRUCTURES 14. COMPUTER GRAPHICS	PA , COLLE C:PREVI , PIC PP HNIQ.PP PP PP	GE NO. GE, S OUS CAI T 100 100 100 100	33 SEAT N RRY ON , S80 40 40 40 40	(3 NO. /ER 05430 48 55 84 58	97) 0 P P P
DATE: 18 AUG. 2011 NOTE: FIRST LINE: SEAT NO., NAME OF OTHER LINES: HEAD OF PASSING, S8054300 PATE MAYUR SATISH O1. DISCRETE STRUCTURES O2. PROGRAMMING & PROBLEM SOLVING O3. DIGIT. ELECTRONICS & LOGIC DESIGNATION OF THE PROBLEM SOLVING O4. DATA STRUCTURES AND ALGORITHMS O5. HUMANITIES AND SOCIAL SCIENCE	RSITY CENT OF THE MAX PP PP PP GNPP PP PP	OF PUNTRE : POST POST	PUNE I DATE, 5, MI 40 40 40 40 40 40	MAD 46 53 66 57 21	PASS MARKS, CHUTE OF COM CHUTE OF COM CHUTE OF COM CHUTE OF COM P C P C P C P C P C	COMPUTER) EXAMINATION MAY 2011 MPUTER TECHNOLOGY, PUNE. ANENT REG. NO., PREVIOUS SEAT NO. MARKS OBTAINED, P/F:PASS/FAIL, , 71132435C , \$8054300 11. ENGINEERING MATHEMATICS III 12. MICROPROC. & INTERFACING TECH 13. DATA STRUCTURES 14. COMPUTER GRAPHICS 15. COMPUTER ORGANIZATION	PA , COLLE C:PREVI , PIC PP HNIQ.PP PP PP PP	GE NO. GE, S OUS CAI T 100 100 100 100 100	33 SEAT N RRY ON , S80 40 40 40 40 40 40	(3 NO. /ER 05430 48 55 84 58 52	97) 0 P P P P
DATE: 18 AUG. 2011 NOTE: FIRST LINE: SEAT NO., NAME OF OTHER LINES: HEAD OF PASSING, S8054300 PATE MAYUR SATISH O1. DISCRETE STRUCTURES O2. PROGRAMMING & PROBLEM SOLVING O3. DIGIT. ELECTRONICS & LOGIC DESIGNATION OF THE PROGRAMMING LABORATORY D1 DISCRETE STRUCTURES O4. DATA STRUCTURES AND ALGORITHMS O5. HUMANITIES AND SOCIAL SCIENCE O6. PROGRAMMING LABORATORY	RSITY CENT OF THE MAX PP PP PP PP TW	OF PUNTRE : POST PUNT PUNT PUNT PUNT PUNT PUNT PUNT PUN	PUNE I DATE, 5, MI 40 40 40 40 40 40	MAD 46 53 66 57 21 38	PASS MARKS, CONTINUE OF COM CONTINUE OF COM CONTINUE OF COM PC PC PC PC PC PC PC PC	COMPUTER) EXAMINATION MAY 2011 MPUTER TECHNOLOGY, PUNE. ANENT REG. NO., PREVIOUS SEAT NO. MARKS OBTAINED, P/F:PASS/FAIL, , 71132435C , \$8054300 11. ENGINEERING MATHEMATICS III 12. MICROPROC. & INTERFACING TECH 13. DATA STRUCTURES 14. COMPUTER GRAPHICS 15. COMPUTER ORGANIZATION 16. O. O. PROG. & COMP. GRAPH. L.	PA , COLLE C:PREVI , PIC PP HNIQ.PP PP PP PP AB TW AB PR	GE NO. GE, S OUS CAI T 100 100 100 100 50	33 SEAT N RRY ON , S80 40 40 40 40 40 40 20	(3 NO. /ER 05430 48 55 84 58 52 41	97) 0 P P P P
DATE: 18 AUG. 2011 NOTE: FIRST LINE: SEAT NO., NAME OF OTHER LINES: HEAD OF PASSING, S8054300 PATE MAYUR SATISH O1. DISCRETE STRUCTURES O2. PROGRAMMING & PROBLEM SOLVING O3. DIGIT. ELECTRONICS & LOGIC DESIGNATION OF THE PROGRAMMING LABORATORY O7. PROGRAMMING LABORATORY	RSITY CENT OF THE MAX PP PP PP TW PR	OF PUNTRE : POST	PUNE I DATE, 5, MI 40 40 40 40 40 20	MAD 46 53 66 57 21 38 20	PASS MARKS, PC PC PC PC PC PC PC PC PC	COMPUTER) EXAMINATION MAY 2011 MPUTER TECHNOLOGY, PUNE. ANENT REG. NO., PREVIOUS SEAT NO. MARKS OBTAINED, P/F:PASS/FAIL, , 71132435C , \$8054300 11. ENGINEERING MATHEMATICS III 12. MICROPROC. & INTERFACING TECH 13. DATA STRUCTURES 14. COMPUTER GRAPHICS 15. COMPUTER ORGANIZATION 16. O. O. PROG. & COMP. GRAPH. L. 17. O. O. PROG. & COMP. GRAPH. L.	PA , COLLE C:PREVI , PIC PP HNIQ.PP PP PP PP AB TW AB PR G LABTW	GE NO. GE, S OUS CAI T 100 100 100 100 50 50	33 SEAT N RRY ON , S80 40 40 40 40 40 20 20	(3 NO. /ER 05430 48 55 84 58 52 41 41	97) 0 P P P P

Cols05 10. SOFT SKILLS TW 50 20 40 P C 20. DATA STRUCTURES LABORATORY TW 50 20 37 P 21. DATA STRUCTURES LABORATORY PR 50 20 40 P

GRAND TOTAL = 957/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS:

S8054301 PATHAK AVINASH NARAYAN				NAL	INI	, 71132436M , S8	054301 , PIC	T	, s80	05430	1
01. DISCRETE STRUCTURES	PP	100	40	72	P C	11. ENGINEERING MATHEMATIC	S III PP	100	40	58	Р
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	55	P C	12. MICROPROC. & INTERFACI	NG TECHNIQ.PP	100	40	59	Р
03. DIGIT. ELECTRONICS & LOGIC DESIG	GNPP	100	40	72	P C	13. DATA STRUCTURES	PP	100	40	70	Р
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	60	P C	14. COMPUTER GRAPHICS	PP	100	40	76	Р
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	50	P C	15. COMPUTER ORGANIZATION	PP	100	40	60	Р
06. PROGRAMMING LABORATORY	TW	25	10	21	P C	16. O. O. PROG. & COMP. GR	APH. LAB TW	50	20	44	Р
07. PROGRAMMING LABORATORY	PR	50	20	43	P C	17. O. O. PROG. & COMP. GR	APH. LAB PR	50	20	41	Р
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	21	P C	18. MICROPROCESSORS & INTE	RFACING LABTW	50	20	38	Р
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	28	P C	19. MICROPROCESSORS & INTE	RFACING LABPR	50	20	34	Р
10. SOFT SKILLS	TW	50	20	39	P C	20. DATA STRUCTURES LABORA	TORY TW	50	20	40	Р
						21. DATA STRUCTURES LABORA	TORY PR	50	20	22	Р

GRAND TOTAL = 1003/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS :

S8054302 PATHAK VISHVAJIT HARISHCHANDRA					ATIBHA	, 71045561F , S8054302 , PICT , S805430	, s8054302			
01. DISCRETE STRUCTURES	PP	100	40	72	P C	11. ENGINEERING MATHEMATICS III PP 100 40 55	Р			
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	53	P C	12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 52	Р			
03. DIGIT. ELECTRONICS & LOGIC DESIGNATION OF THE PROPERTY OF	GNPP	100	40	55	P C	13. DATA STRUCTURES PP 100 40 44	Р			
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	56	P C	14. COMPUTER GRAPHICS PP 100 40 59	Р			
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	41	P C	15. COMPUTER ORGANIZATION PP 100 40 47	Р			
06. PROGRAMMING LABORATORY	TW	25	10	19	P C	16. O. O. PROG. & COMP. GRAPH. LAB TW 50 20 35 Page 133	Р			

PR 50 20 30 P C 17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 22 P

UT. PROGRAMMING LABORATORY	FIX	30	20	30	PC	•	1/.	U. U. PRUG. & COMP. GRAPH. LAB	FK	30	20	22	г
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	18	РC		18.	MICROPROCESSORS & INTERFACING LA	ABTW	50	20	39	Р
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	28	РC		19.	MICROPROCESSORS & INTERFACING LA	ABPR	50	20	38	Р
10. SOFT SKILLS	TW	50	20	34	РC		20.	DATA STRUCTURES LABORATORY	TW	50	20	37	Р
							21.	DATA STRUCTURES LABORATORY	PR	50	20	32	Р
GRAND TOTAL = 866/1500, RESULT: HIGHIORDN. 1 MARKS:	ER SEC	COND CI	LASS										
DATE: 18 AUG. 2011			-					JTER) EXAMINATION MAY 2011 R TECHNOLOGY, PUNE.		 E NO.	34		98)
NOTE: FIRST LINE : SEAT NO., NAME (OF THE	E CAND	IDATE,	, MO	THER	, PERMA	NENT	REG. NO., PREVIOUS SEAT NO., (COLLEG	iΕ, S	SEAT N	10.	
OTHER LINES: HEAD OF PASSING,	MAX.	. MARKS	S, MI	IN. P	PASS	MARKS,	MAR	KS OBTAINED, P/F:PASS/FAIL, C:F	PREVIO	US CAF	≀RY O\	/ER	
-0054202								74045500-			- 04		_
S8054303 PATIL AKSHAY JAYANT				P00)JA			, 71045562D , S8054303 ,	PICT		, S80	15430.	3
01. DISCRETE STRUCTURES	PP	100	40	73	РС		11.	ENGINEERING MATHEMATICS III	PP	100	40	59	Р
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	54	P C		12.	MICROPROC. & INTERFACING TECHNIC	Q.PP	100	40	53	Р
03. DIGIT. ELECTRONICS & LOGIC DESIGNATION OF THE PROPERTY OF	GNPP	100	40	75	P C		13.	DATA STRUCTURES	PP	100	40	63	Р
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	59	P C		14.	COMPUTER GRAPHICS	PP	100	40	59	Р
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	46	P C		15.	COMPUTER ORGANIZATION	PP	100	40	53	Р
06. PROGRAMMING LABORATORY	TW	25	10	18	P C		16.	O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	33	Р
07. PROGRAMMING LABORATORY	PR	50	20	35	Р		17.	O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	36	Р
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	18	P C		18.	MICROPROCESSORS & INTERFACING LA	ABTW	50	20	28	Р
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	37	Р		19.	MICROPROCESSORS & INTERFACING LA	ABPR	50	20	26	Р
10. SOFT SKILLS	TW	50	20	28	P C		20.	DATA STRUCTURES LABORATORY	TW	50	20	29	Р
							21.	DATA STRUCTURES LABORATORY	PR	50	20	12	F

GRAND TOTAL = 894/1500, RESULT: FAILS A.T.K.T.
ORDN. 1 MARKS :

07. PROGRAMMING LABORATORY

				• •			
S8054304 PATIL SAGAR RAJESH				USH	IA	, 71045567E , S8054304 , PICT , S8054304	
01. DISCRETE STRUCTURES	PP	100	40	73	P C	11. ENGINEERING MATHEMATICS III PP 100 40 59 P	
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	50	P C	12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 57 P	
03. DIGIT. ELECTRONICS & LOGIC DESI	GNPP	100	40	72	P C	13. DATA STRUCTURES PP 100 40 62 P	
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	62	P C	14. COMPUTER GRAPHICS PP 100 40 70 P	
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	49	P C	15. COMPUTER ORGANIZATION PP 100 40 63 P	
06. PROGRAMMING LABORATORY	TW	25	10	21	P C	16. O. O. PROG. & COMP. GRAPH. LAB TW 50 20 43 P	
07. PROGRAMMING LABORATORY	PR	50	20	42	P C	17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 43 P	
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	21	P C	18. MICROPROCESSORS & INTERFACING LABTW 50 20 46 P	
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	40	P C	19. MICROPROCESSORS & INTERFACING LABPR 50 20 45 P	
10. SOFT SKILLS	TW	50	20	41	P C	20. DATA STRUCTURES LABORATORY TW 50 20 42 P	
						21. DATA STRUCTURES LABORATORY PR 50 20 35 P	

GRAND TOTAL = 1036/1500, RESULT: FIRST CLASS WITH DISTINCTION
ORDN. 1 MARKS :

S8054305 PATIL SHALAKA VIJAY				SHA	ILAJA		, 71132437К	, s8054305 ,	PICT		, s8C)5430	5
01. DISCRETE STRUCTURES	PP	100	40	61	РС	11.	ENGINEERING MATH	EMATICS III	PP	100	40	78	Р
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	57	P C	12.	MICROPROC. & INT	ERFACING TECHNIQ	.PP	100	40	61	Р
03. DIGIT. ELECTRONICS & LOGIC DESIGNATION	GNPP	100	40	84	P C	13.	DATA STRUCTURES		PP	100	40	78	Р
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	68	P C	14.	COMPUTER GRAPHIC	S	PP	100	40	71	Р
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	48	P C	15.	COMPUTER ORGANIZA	ATION	PP	100	40	57	Р
06. PROGRAMMING LABORATORY	TW	25	10	20	P C	16.	O. O. PROG. & CO	MP. GRAPH. LAB	TW	50	20	40	Р
07. PROGRAMMING LABORATORY	PR	50	20	46	P C	17.	O. O. PROG. & CO	MP. GRAPH. LAB	PR	50	20	44	Р
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	22	P C	18.	MICROPROCESSORS (& INTERFACING LA	BTW	50	20	40	Р
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	36	P C	19.	MICROPROCESSORS (& INTERFACING LA	BPR	50	20	32	Р
10. SOFT SKILLS	TW	50	20	39	P C	20.	DATA STRUCTURES	LABORATORY	TW	50	20	40	Р
						21.	DATA STRUCTURES	LABORATORY age 135	PR	50	20	38	Р

GRAND TOTAL = 1060/1500, RESULT: FIRST CLASS WITH DISTINCTION
ORDN. 1 MARKS :

					••••							
DATE: 18 AUG. 2011								UTER) EXAMINATION MAY 2011 ER TECHNOLOGY, PUNE.	GE NO	35	(3	199)
DATE : 10 AUG. 2011	CLIVI		I ONE .	111311	. 101		n com o i i	The recinology, rowe.	IGE NO	. 33	()	(33)
NOTE: ETECT LINE: SEAT NO NAME (OE TUE			 MO		·	DEDMANENT	T DEC. NO. DREVIOUS SEAT NO. COLL	· · ·	CEAT !		
						-		REG. NO., PREVIOUS SEAT NO., COLL	-	SEAT I		
							•	RKS OBTAINED, P/F:PASS/FAIL, C:PREV				
						•						
S8054306 PAWAR AJINKYA NANDU				VID	YΑ			, 71045570E , S8054306 , PI	T	, s80	05430	06
01. DISCRETE STRUCTURES	PP	100	40	51	Р	С	11.	ENGINEERING MATHEMATICS III PP	100	40	40	Р
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	54	Р	C	12.	MICROPROC. & INTERFACING TECHNIQ.PP	100	40	40	Р
03. DIGIT. ELECTRONICS & LOGIC DESIGNATION OF THE PROPERTY OF	GNPP	100	40	54	Р	C	13.	DATA STRUCTURES PP	100	40	40	Р
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	55	Р	C	14.	COMPUTER GRAPHICS PP	100	40	60	Р
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	57	Р	С	15.	COMPUTER ORGANIZATION PP	100	40	50	Р
06. PROGRAMMING LABORATORY	TW	25	10	15	Р	С	16.	O. O. PROG. & COMP. GRAPH. LAB TW	50	20	22	Р
07. PROGRAMMING LABORATORY	PR	50	20	37	Р	C	17.	O. O. PROG. & COMP. GRAPH. LAB PR	50	20	32	Р
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	13	Р	C	18.	MICROPROCESSORS & INTERFACING LABTW	50	20	20	Р
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	28	Р		19.	MICROPROCESSORS & INTERFACING LABPR	50	20	24	Р
10. SOFT SKILLS	TW	50	20	36	Р	C	20.	DATA STRUCTURES LABORATORY TW	50	20	33	Р
							21.	DATA STRUCTURES LABORATORY PR	50	20	12	F
GRAND TOTAL = 773/1500, RESULT: FAIL:	S A.T.	.к.т.										
ORDN. 1 MARKS :												
S8054307 PAWAR HARISH MANGESH				REN	IUKA			, 71132438н , S8054307 , РІ	Т	, s80	05430)7
01. DISCRETE STRUCTURES	PP	100	40	63	Р	C	11.	ENGINEERING MATHEMATICS III PP	100	40	44	Р
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	59	Р	С	12.	MICROPROC. & INTERFACING TECHNIQ.PP Page 136	100	40	51	Р

03. DIGIT. ELECTRONICS & LOGIC DESIG	GNPP	100	40	57	PC	13. DATA STRUCTURES PP	100	40	55	Р
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	48	P C	14. COMPUTER GRAPHICS PP	100	40	54	Р
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	57	P C	15. COMPUTER ORGANIZATION PP	100	40	48	Р
06. PROGRAMMING LABORATORY	TW	25	10	21	P C	16. O. O. PROG. & COMP. GRAPH. LAB TW	50	20	42	Р
07. PROGRAMMING LABORATORY	PR	50	20	45	P C	17. O. O. PROG. & COMP. GRAPH. LAB PR	50	20	30	Р
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	21	P C	18. MICROPROCESSORS & INTERFACING LABTW	50	20	38	Р
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	37	P C	19. MICROPROCESSORS & INTERFACING LABPR	50	20	30	Р
10. SOFT SKILLS	TW	50	20	38	P C	20. DATA STRUCTURES LABORATORY TW	50	20	35	Р
						21. DATA STRUCTURES LABORATORY PR	50	20	11	F

GRAND TOTAL = 884/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS:

S8054308 PILAJI KEDARNATH BALAJI				ANU	JA		, 71045574н	, s8054308 ,	PICT		, s80	5430	8
01. DISCRETE STRUCTURES	PP	100	40	62	P C	11	ENGINEERING MA	THEMATICS III	PP	100	40	46	Р
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	63	P C	12	MICROPROC. & I	NTERFACING TECHNIQ	.PP	100	40	57	Р
03. DIGIT. ELECTRONICS & LOGIC DESIG	GNPP	100	40	78	P C	13	DATA STRUCTURE	S	PP	100	40	69	Р
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	48	P C	14	COMPUTER GRAPH	IICS	PP	100	40	61	Р
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	58	P C	15	COMPUTER ORGAN	IIZATION	PP	100	40	58	Р
06. PROGRAMMING LABORATORY	TW	25	10	16	P C	16	O. O. PROG. &	COMP. GRAPH. LAB	TW	50	20	34	Р
07. PROGRAMMING LABORATORY	PR	50	20	24	P C	17	O. O. PROG. &	COMP. GRAPH. LAB	PR	50	20	30	Р
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	16	P C	18	MICROPROCESSOR	S & INTERFACING LA	BTW	50	20	35	Р
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	30	P C	19	MICROPROCESSOR	S & INTERFACING LA	BPR	50	20	40	Р
10. SOFT SKILLS	TW	50	20	33	P C	20	DATA STRUCTURE	S LABORATORY	TW	50	20	35	Р
						21	DATA STRUCTURE	S LABORATORY	PR	50	20	30	Р

GRAND TOTAL = 923/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS:

UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(COMPUTER) EXAMINATION MAY 2011

16. O. O. PROG. & COMP. GRAPH. LAB TW

17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 44 P Page 138

50 20 46 P

						COISOS			
DATE : 18 AUG. 2011	CEN	TRE : I	PUNE]	INSTI	TUTE O	COMPUTER TECHNOLOGY, PUNE. PAGE	NO.	36	(400)
NOTE: FIRST LINE : SEAT NO., NAME (OF THI	E CAND	IDATE,	, МО	THER,	RMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE	, SE	EAT NO	0.
OTHER LINES: HEAD OF PASSING,	MAX	. MARKS	S, MI	IN. P	ASS MA	S, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOU	S CARF	RY OV	ER
S8054309 PORE DIVYA KISHOR				KAL	PANA	, 71132439F , S8054309 , PICT	:	, S80	54309
01. DISCRETE STRUCTURES	PP	100	40	62	P C	11. ENGINEERING MATHEMATICS III PP	100	40	49 P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	58	P C	12. MICROPROC. & INTERFACING TECHNIQ.PP	100	40	58 P
03. DIGIT. ELECTRONICS & LOGIC DESIG	SNPP	100	40	76	PC	13. DATA STRUCTURES PP	100	40	62 P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	67	P C	14. COMPUTER GRAPHICS PP	100	40	52 P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	69	PC	15. COMPUTER ORGANIZATION PP	100	40	59 P
06. PROGRAMMING LABORATORY	TW	25	10	20	P C	16. O. O. PROG. & COMP. GRAPH. LAB TW	50	20	43 P
07. PROGRAMMING LABORATORY	PR	50	20	35	P C	17. O. O. PROG. & COMP. GRAPH. LAB PR	50	20	28 P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	22	P C	18. MICROPROCESSORS & INTERFACING LABTW	50	20	43 P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	33	P C	19. MICROPROCESSORS & INTERFACING LABPR	50	20	38 P
10. SOFT SKILLS	TW	50	20	39	P C	20. DATA STRUCTURES LABORATORY TW	50	20	37 P
						21. DATA STRUCTURES LABORATORY PR	50	20	21 P
GRAND TOTAL = 971/1500, RESULT: FIRST	- CL A	S S							
ORDN. 1 MARKS :	CLA.	55							
CREAT I MARKS .									
S8054310 POTDAR SHANTANU RATNAKAR				ANI	TA	, 71132440к , s8054310 , ріст	,	, s80	54310
01. DISCRETE STRUCTURES	PP	100	40	77	РС	11. ENGINEERING MATHEMATICS III PP	100	40	78 P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	62	P C	12. MICROPROC. & INTERFACING TECHNIQ.PP	100	40	50 P
03. DIGIT. ELECTRONICS & LOGIC DESIG	SNPP	100	40	84	P C	13. DATA STRUCTURES PP	100	40	73 P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	74	P C	14. COMPUTER GRAPHICS PP	100	40	65 P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	59	P C	15. COMPUTER ORGANIZATION PP	100	40	63 P

25 10 22 P C

PR 50 20 42 P C

06. PROGRAMMING LABORATORY

07. PROGRAMMING LABORATORY

06. DIGITAL ELECTRONICS LABORATORY	I W	23	TO	22	PC	10.	MICROPROCESSORS & INTERFACING L	ADIW	30	20	43	Р
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	42	P C	19.	MICROPROCESSORS & INTERFACING L.	ABPR	50	20	44	Р
10. SOFT SKILLS	TW	50	20	38	PС	20.	DATA STRUCTURES LABORATORY	TW	50	20	43	Р
						21.	DATA STRUCTURES LABORATORY	PR	50	20	44	Р
GRAND TOTAL = 1117/1500, RESULT: FIRS	T CLA	SS WITT	u ntc	TTNCT	TON							
ORDN. 1 MARKS :	I CLA	33 WIII	п ртз	IINCI	ION							
ORDN. I MARKS .												
S8054311 PRANIDHYA KHANDELWAL				NIS	НА		, 71045580B , S8054311 ,	PICT	Ī	, s80	05431	.1
01. DISCRETE STRUCTURES	PP	100	40	62	РС	11.	ENGINEERING MATHEMATICS III	PP	100	40	50	Р
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	40	РС	12.	MICROPROC. & INTERFACING TECHNIC	Q.PP	100	40	43	Р
03. DIGIT. ELECTRONICS & LOGIC DESI	GNPP	100	40	53	РС	13.	DATA STRUCTURES	PP	100	40	52	Р
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	40	РС	14.	COMPUTER GRAPHICS	PP	100	40	64	Р
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	49	РС	15.	COMPUTER ORGANIZATION	PP	100	40	52	Р
06. PROGRAMMING LABORATORY	TW	25	10	17	PС	16.	O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	37	Р
07. PROGRAMMING LABORATORY	PR	50	20	39	Р	17.	O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	28	Р
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	17	P C	18.	MICROPROCESSORS & INTERFACING L	ABTW	50	20	38	Р
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	23	P C	19.	MICROPROCESSORS & INTERFACING L	ABPR	50	20	30	Р
10. SOFT SKILLS	TW	50	20	35	P C	20.	DATA STRUCTURES LABORATORY	TW	50	20	34	Р
						21.	DATA STRUCTURES LABORATORY	PR	50	20	32	Р
CRAND TOTAL 925 /1500 DECLUTA HTCH	ED 6E	COND C										
GRAND TOTAL = 835/1500, RESULT: HIGH	EK SE	COND C	LASS									
ORDN. 1 MARKS :												
UNIVE	RSITY	OF PU	NE ,S	.E.(2008 P	PAT.)(COMP	UTER) EXAMINATION MAY 2011					
DATE : 18 AUG. 2011	CEN.	TRE :	PUNE :	INSTI	TUTE (OF COMPUTE	R TECHNOLOGY, PUNE.	PAC	GE NO.	37	(4	01)
NOTE: FIRST LINE : SEAT NO., NAME	OF TH	E CAND	IDATE	, MC	THER,	PERMANENT	REG. NO., PREVIOUS SEAT NO.,	COLLEC	iΕ, 9	SEAT N	١٥.	
OTHER LINES: HEAD OF PASSING,	MAX	. MARK	S, M	IN. P	ASS M	ARKS, MAF	RKS OBTAINED, P/F:PASS/FAIL, C:	PREVIO)US CAI	RRY O	√ER	

08. DIGITAL ELECTRONICS LABORATORY TW 25 10 22 P C 18. MICROPROCESSORS & INTERFACING LABTW 50 20 45 P

S8054312 PRATIVINDHYA MISHRA				ANU	RAG	, 71045581L , S8054312 , PICT	, s80	054312	2
01. DISCRETE STRUCTURES	PP	100	40	69	P C	11. ENGINEERING MATHEMATICS III PP 100	40	61	Р
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	62	P C	12. MICROPROC. & INTERFACING TECHNIQ.PP 100	40	54	Р
03. DIGIT. ELECTRONICS & LOGIC DESIG	NPP	100	40	81	РС	13. DATA STRUCTURES PP 100	40	67	Р
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	65	РС	14. COMPUTER GRAPHICS PP 100	40	67	Р
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	65	P C	15. COMPUTER ORGANIZATION PP 100	40	70	Р
06. PROGRAMMING LABORATORY	TW	25	10	23	P C	16. O. O. PROG. & COMP. GRAPH. LAB TW 50	20	45	Р
07. PROGRAMMING LABORATORY	PR	50	20	43	P C	17. O. O. PROG. & COMP. GRAPH. LAB PR 50	20	41	Р
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	23	P C	18. MICROPROCESSORS & INTERFACING LABTW 50	20	43	Р
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	37	P C	19. MICROPROCESSORS & INTERFACING LABPR 50	20	45	Р
10. SOFT SKILLS	TW	50	20	46	P C	20. DATA STRUCTURES LABORATORY TW 50	20	45	Р
						21. DATA STRUCTURES LABORATORY PR 50	20	26	Р

GRAND TOTAL = 1078/1500, RESULT: FIRST CLASS WITH DISTINCTION
ORDN. 1 MARKS :

S8054313 RAHEJA ROHAN VINOD				P00	JA	, 71045585C , S8054313 , PICT , S8054313	
01. DISCRETE STRUCTURES	PP	100	40	67	P C	11. ENGINEERING MATHEMATICS III PP 100 40 52	Р
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	52	P C	12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 40	Р
03. DIGIT. ELECTRONICS & LOGIC DESIG	SNPP	100	40	67	P C	13. DATA STRUCTURES PP 100 40 66	Р
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	49	P C	14. COMPUTER GRAPHICS PP 100 40 48	Р
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	56	P C	15. COMPUTER ORGANIZATION PP 100 40 40	Р
06. PROGRAMMING LABORATORY	TW	25	10	22	P C	16. O. O. PROG. & COMP. GRAPH. LAB TW 50 20 36	Р
07. PROGRAMMING LABORATORY	PR	50	20	42	P C	17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 37	Р
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	21	P C	18. MICROPROCESSORS & INTERFACING LABTW 50 20 36	Р
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	41	P C	19. MICROPROCESSORS & INTERFACING LABPR 50 20 42	Р
10. SOFT SKILLS	TW	50	20	38	P C	20. DATA STRUCTURES LABORATORY TW 50 20 40	Р
						21. DATA STRUCTURES LABORATORY PR 50 20 38	Р

GRAND TOTAL = 930/1500, RESULT: FIRST CLASS

03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 40 54 P C

ORDN. 1 MARKS :

S8054314 RANE RAHUL DILIP				VID)YA	, 71045586M , S8054314 , PICT , S8054314
01. DISCRETE STRUCTURES	PP	100	40	54	РC	11. ENGINEERING MATHEMATICS III PP 100 40 67 P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	56	P C	12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 58 P
03. DIGIT. ELECTRONICS & LOGIC DESI	GNPP	100	40	79	P C	13. DATA STRUCTURES PP 100 40 71 P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	56	P C	14. COMPUTER GRAPHICS PP 100 40 52 P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	64	P C	15. COMPUTER ORGANIZATION PP 100 40 57 P
06. PROGRAMMING LABORATORY	TW	25	10	20	P C	16. O. O. PROG. & COMP. GRAPH. LAB TW 50 20 39 P
07. PROGRAMMING LABORATORY	PR	50	20	35	P C	17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 42 P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	20	P C	18. MICROPROCESSORS & INTERFACING LABTW 50 20 41 P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	35	P C	19. MICROPROCESSORS & INTERFACING LABPR 50 20 40 P
10. SOFT SKILLS	TW	50	20	38	P C	20. DATA STRUCTURES LABORATORY TW 50 20 37 P
						21. DATA STRUCTURES LABORATORY PR 50 20 30 P
GRAND TOTAL = 991/1500, RESULT: FIRS ORDN. 1 MARKS :	T CLAS	SS WIT	H DIST	ГІМСТ	TION	
	 ERSITY	 OF PU	 INE ,S	 .E.(2	 2008 ра	AT.)(COMPUTER) EXAMINATION MAY 2011
DATE : 18 AUG. 2011	CEN	TRE :	PUNE 3	INSTI	TUTE OF	F COMPUTER TECHNOLOGY, PUNE. PAGE NO. 38 (402)
						PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. RKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
S8054315 RAWALE SUHAS SUDAMRAO				RAT	NMALA	, 71045588н , S8054315 , РІСТ , S8054315
01. DISCRETE STRUCTURES	PP	100	40	49	P C	11. ENGINEERING MATHEMATICS III PP 100 40 22 F
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	52	P C	12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 24 F

13. DATA STRUCTURES

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PP 100 40 49 P

04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	53	P C	14.	COMPUTER GRAPHICS	PP	100	40	40	Р
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	40	P C	15.	COMPUTER ORGANIZATION	PP	100	40	44	Р
06. PROGRAMMING LABORATORY	TW	25	10	20	P C	16.	O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	32	Р
07. PROGRAMMING LABORATORY	PR	50	20	24	P C	17.	O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	29	Р
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	19	P C	18.	MICROPROCESSORS & INTERFACING LA	ABTW	50	20	32	Р
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	22	P C	19.	MICROPROCESSORS & INTERFACING L	ABPR	50	20	28	Р
10. SOFT SKILLS	TW	50	20	34	P C	20.	DATA STRUCTURES LABORATORY	TW	50	20	27	Р
						21.	DATA STRUCTURES LABORATORY	PR	50	20	08	F

GRAND TOTAL = 702/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS:

S8054316 SAKORE MITHILA RAMNATH				MADI	HURI		, 71045593D	, s8054316 ,	PICT		, s80	5431	6
01. DISCRETE STRUCTURES	PP	100	40	61	P C	11.	ENGINEERING MATHE	MATICS III	PP	100	40	40	Р
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	60	PС	12.	MICROPROC. & INTE	RFACING TECHNIQ	.PP	100	40	51	Р
03. DIGIT. ELECTRONICS & LOGIC DESIG	GNPP	100	40	71	P C	13.	DATA STRUCTURES		PP	100	40	80	Р
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	55	PС	14.	COMPUTER GRAPHICS		PP	100	40	71	Р
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	59	P C	15.	COMPUTER ORGANIZA	TION	PP	100	40	60	Р
06. PROGRAMMING LABORATORY	TW	25	10	22	P C	16.	O. O. PROG. & COM	IP. GRAPH. LAB	TW	50	20	39	Р
07. PROGRAMMING LABORATORY	PR	50	20	35	Р	17.	O. O. PROG. & COM	IP. GRAPH. LAB	PR	50	20	31	Р
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	20	P C	18.	MICROPROCESSORS &	INTERFACING LA	BTW	50	20	38	Р
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	30	P C	19.	MICROPROCESSORS &	INTERFACING LA	BPR	50	20	10	F
10. SOFT SKILLS	TW	50	20	36	PС	20.	DATA STRUCTURES L	ABORATORY	TW	50	20	41	Р
						21.	DATA STRUCTURES L	ABORATORY	PR	50	20	28	Р

GRAND TOTAL = 938/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

\$8054317 SANGHVI PALAK RAJESH NEHA , 71045595L , \$8054317 , PICT , \$8054317 Page 142

01. DISCRETE STRUCTURES	PP	100	40	69	P C	11. ENGINEERING MATHEMATICS III PP 100 40 47 F	Р
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	46	P C	12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 52 F	Р
03. DIGIT. ELECTRONICS & LOGIC DESI	GNPP	100	40	75	P C	13. DATA STRUCTURES PP 100 40 59 F	Р
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	61	P C	14. COMPUTER GRAPHICS PP 100 40 55 F	Р
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	48	P C	15. COMPUTER ORGANIZATION PP 100 40 55 F	Р
06. PROGRAMMING LABORATORY	TW	25	10	23	P C	16. O. O. PROG. & COMP. GRAPH. LAB TW 50 20 46 F	Р
07. PROGRAMMING LABORATORY	PR	50	20	38	P C	17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 37 F	Р
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	24	P C	18. MICROPROCESSORS & INTERFACING LABTW 50 20 46 F	Р
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	44	P C	19. MICROPROCESSORS & INTERFACING LABPR 50 20 40 F	Р
10. SOFT SKILLS	TW	50	20	40	P C	20. DATA STRUCTURES LABORATORY TW 50 20 45 F	Р
						21. DATA STRUCTURES LABORATORY PR 50 20 40 F	Р

GRAND TOTAL = 990/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS :

UNIVER	RSITY	OF PUN	E,S.	E.(2	008 PAT.)(COMPUTER) EXAMINATION MAY 2011					
DATE : 18 AUG. 2011	CENT	RE : P	UNE I	NSTI	TUTE OF COM	IPUTER TECHNOLOGY, PUNE.	PA	GE NO.	39	(4	03)
NOTE: FIRST LINE : SEAT NO., NAME O	F THE	CANDI	DATE,	MO	THER, PERMA	NENT REG. NO., PREVIOUS SEAT NO.,	COLLE	GE,	SEAT N	10.	
OTHER LINES: HEAD OF PASSING,	MAX.	MARKS	, MII	N. P.	ASS MARKS,	MARKS OBTAINED, P/F:PASS/FAIL, C	:PREVI	OUS CAI	RRY O	/ER	
S8054318 SANGLIKAR PRATIK SANTOSH				ANJ	ALI	, 71045597G , S8054318 ,	PIC	Т	, s80	05431	.8
01. DISCRETE STRUCTURES	PP	100	40	57	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	40	Р
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	53	P C	12. MICROPROC. & INTERFACING TECHN	IQ.PP	100	40	43	Р
03. DIGIT. ELECTRONICS & LOGIC DESIG	NPP	100	40	40	P C	13. DATA STRUCTURES	PP	100	40	58	Р
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	48	P C	14. COMPUTER GRAPHICS	PP	100	40	47	Р
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	53	P C	15. COMPUTER ORGANIZATION	PP	100	40	61	Р
06. PROGRAMMING LABORATORY	TW	25	10	20	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	30	Р
07. PROGRAMMING LABORATORY	PR	50	20	29	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	33	Р
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	20	P C	18. MICROPROCESSORS & INTERFACING Page 143	LABTW	50	20	37	Р

09.	DIGITAL ELECTRONICS LABORATORY	PR	50	20	23	P C	19.	MICROPROCESSORS & INTERFACING LAB	3PR	50	20	45	Р	
10.	SOFT SKILLS	TW	50	20	41	P C	20.	DATA STRUCTURES LABORATORY	TW	50	20	38	Р	
							21.	DATA STRUCTURES LABORATORY	PR	50	20	22	Р	
CDAND	TOTAL 929 /1500 PECULT, UTCHE	D CE/	SOND CI	۸۵۵										
	TOTAL = 838/1500, RESULT: HIGHE	K SEC	LOND CI	_ASS										
OKDN.	1 MARKS :													
					• •				• •					
S8054319 SAVLA SANKET HARISH				JYO	TI		, 71045602G , S8054319 ,			PICT , S8054319				
01.	DISCRETE STRUCTURES	PP	100	40	56	P C	11.	ENGINEERING MATHEMATICS III	PP	100	40	56	Р	
02.	PROGRAMMING & PROBLEM SOLVING	PP	100	40	57	РС	12.	MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	40	Р	
03.	DIGIT. ELECTRONICS & LOGIC DESIG	NPP	100	40	62	РС	13.	DATA STRUCTURES	PP	100	40	54	Р	
04.	DATA STRUCTURES AND ALGORITHMS	PP	100	40	49	РС	14.	COMPUTER GRAPHICS	PP	100	40	49	Р	
05.	HUMANITIES AND SOCIAL SCIENCE	PP	100	40	53	РС	15.	COMPUTER ORGANIZATION	PP	100	40	49	Р	
06.	PROGRAMMING LABORATORY	TW	25	10	20	P C	16.	O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	35	Р	
07.	PROGRAMMING LABORATORY	PR	50	20	41	P C	17.	O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	38	Р	
08.	DIGITAL ELECTRONICS LABORATORY	TW	25	10	17	P C	18.	MICROPROCESSORS & INTERFACING LAB	3TW	50	20	34	Р	
09.	DIGITAL ELECTRONICS LABORATORY	PR	50	20	41	P C	19.	MICROPROCESSORS & INTERFACING LAB	BPR	50	20	30	Р	
10.	SOFT SKILLS	TW	50	20	40	P C	20.	DATA STRUCTURES LABORATORY	TW	50	20	30	Р	
							21.	DATA STRUCTURES LABORATORY	PR	50	20	34	Р	
CDAND	TOTAL = 885/1500, RESULT: HIGHE	D SEA	בטאט כו	۸۵۵										
	1 MARKS :	K JLC	JOND CI	_A33										
ORDIN.	I MARKS .													
S8054320 SHAH DIMPLE PIYUSH			BINAL					, 71045606K , S8054320 ,			ICT , S8054320			
01.	DISCRETE STRUCTURES	PP	100	40	64	P C	11.	ENGINEERING MATHEMATICS III	PP	100	40	80	Р	
02.	PROGRAMMING & PROBLEM SOLVING	PP	100	40	43	P C	12.	MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	44	Р	
03.	DIGIT. ELECTRONICS & LOGIC DESIG	NPP	100	40	64	P C	13.	DATA STRUCTURES	PP	100	40	69	Р	
04.	DATA STRUCTURES AND ALGORITHMS	PP	100	40	54	P C	14.	COMPUTER GRAPHICS	PP	100	40	60	Р	
								Dama 144						

05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	52	P C	cols05 15. COMPUTER ORGANIZATION PP 100 40 60 P
06. PROGRAMMING LABORATORY	TW	25	10	19	P C	16. O. O. PROG. & COMP. GRAPH. LAB TW 50 20 40 P
07. PROGRAMMING LABORATORY	PR	50	20	29	P C	17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 36 P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	20	P C	18. MICROPROCESSORS & INTERFACING LABTW 50 20 43 P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	38	P C	19. MICROPROCESSORS & INTERFACING LABPR 50 20 42 P
10. SOFT SKILLS	TW	50	20	40	P C	20. DATA STRUCTURES LABORATORY TW 50 20 40 P
						21. DATA STRUCTURES LABORATORY PR 50 20 38 P
GRAND TOTAL = 975/1500, RESULT: FIRST ORDN. 1 MARKS :	CLAS	SS				
	 RSITY	 OF PU	 NE ,S	 .E.(2	 2008 f	AT.)(COMPUTER) EXAMINATION MAY 2011

DATE : 18 AUG. 2011 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 40 (404)

NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8054321 SHAH YASH MANESH RAJASHRI , 71045608F , s8054321 , PICT , s8054321 01. DISCRETE STRUCTURES 100 40 11. ENGINEERING MATHEMATICS III PP 100 02. PROGRAMMING & PROBLEM SOLVING 100 40 60 P C 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 52 P 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 40 86 P C 13. DATA STRUCTURES 100 67 P 04. DATA STRUCTURES AND ALGORITHMS 100 40 56 P C 14. COMPUTER GRAPHICS 100 70 P 05. HUMANITIES AND SOCIAL SCIENCE 100 40 52 P C 15. COMPUTER ORGANIZATION 100 06. PROGRAMMING LABORATORY 10 22 P C 16. O. O. PROG. & COMP. GRAPH. LAB TW 20 43 P C 07. PROGRAMMING LABORATORY 50 17. O. O. PROG. & COMP. GRAPH. LAB PR 50 45 P 08. DIGITAL ELECTRONICS LABORATORY 25 10 20 P C 18. MICROPROCESSORS & INTERFACING LABTW 20 09. DIGITAL ELECTRONICS LABORATORY 50 20 30 P.C 19. MICROPROCESSORS & INTERFACING LABPR 50 20 45 P 20 41 P C 10. SOFT SKILLS 20. DATA STRUCTURES LABORATORY TW 50 39 P 21. DATA STRUCTURES LABORATORY 50 20 25 P PR

GRAND TOTAL = 1028/1500, RESULT: FIRST CLASS WITH DISTINCTION

S8054322 SHEKHAR SAMIR SHAILESH K	KUMAR	GUNJAN		PUSI	HPA RANI	GUNJAN	, 71045609D , S8054322 ,	PICT		, s80)5432	2
01. DISCRETE STRUCTURES	PP	100	40	52	P C	11.	ENGINEERING MATHEMATICS III	PP	100	40	56	Р
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	47	P C	12.	MICROPROC. & INTERFACING TECHNIQ.	.PP	100	40	40	Р
03. DIGIT. ELECTRONICS & LOGIC DESI	GNPP	100	40	60	P C	13.	DATA STRUCTURES	PP	100	40	66	Р
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	46	P C	14.	COMPUTER GRAPHICS	PP	100	40	47	Р
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	53	P C	15.	COMPUTER ORGANIZATION	PP	100	40	56	Р
06. PROGRAMMING LABORATORY	TW	25	10	17	P C	16.	O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	34	Р
07. PROGRAMMING LABORATORY	PR	50	20	37	P C	17.	O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	41	Р
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	18	P C	18.	MICROPROCESSORS & INTERFACING LAB	3TW	50	20	35	Р
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	20	P C	19.	MICROPROCESSORS & INTERFACING LAB	BPR	50	20	45	Р
10. SOFT SKILLS	TW	50	20	36	P C	20.	DATA STRUCTURES LABORATORY	TW	50	20	37	Р
						21.	DATA STRUCTURES LABORATORY	PR	50	20	34	Р

GRAND TOTAL = 877/1500, RESULT: HIGHER SECOND CLASS ORDN. 1 MARKS:

S8054323 SHINDE AMIT KEDARNATH				SANGEETA	, 71045614L , S8054323 , PICT , S80543	23
01. DISCRETE STRUCTURES	PP	100	40	53 P C	11. ENGINEERING MATHEMATICS III PP 100 40 40) P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	40 P C	12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 AA	F
03. DIGIT. ELECTRONICS & LOGIC DESIG	GNPP	100	40	49 P C	13. DATA STRUCTURES PP 100 40 54	l P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	40 P C	14. COMPUTER GRAPHICS PP 100 40 46	6 P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	40 P C	15. COMPUTER ORGANIZATION PP 100 40 54	l P
06. PROGRAMMING LABORATORY	TW	25	10	17 P C	16. O. O. PROG. & COMP. GRAPH. LAB TW 50 20 32	2 P
07. PROGRAMMING LABORATORY	PR	50	20	35 P C	17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 32	2 P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	16 P C	18. MICROPROCESSORS & INTERFACING LABTW 50 20 36	6 P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	21 PC	19. MICROPROCESSORS & INTERFACING LABPR 50 20 38	3 P

10. SOFT SKILLS	TW	50	20	34	Р	C	20.	DATA	STRUCT	URES	cols05 LABORATORY	TW	1	50	20	30	Р
							21.	DATA	STRUCT	URES	LABORATORY	PR		50	20	05	F
GRAND TOTAL = 712/1500, RESULT: FAIL ORDN. 1 MARKS :	S A.T.	к.т.															
DATE : 18 AUG. 2011						E OF COM					MAY 2011 NE.	Р	AGE	NO.	41	(4	05)
NOTE: FIRST LINE : SEAT NO., NAME	OF THE	E CAND:	IDATE,	MC	THE	R, PERMA	NENT	REG.	NO.,	PREV:	IOUS SEAT NO.	, COLL	EGE,	S	EAT N	0.	
OTHER LINES: HEAD OF PASSING,	MAX.	MARK!	5, MI	IN. P	ASS	MARKS,	MAR	KS OB	TAINED,	P/I	F:PASS/FAIL,	C:PREV	IOUS	CAR	RY OV	ER	
S8054324 SHREYAS KUMAR PANIGRAHI				MIN	ATI			,	7104561	.8C	, s8054324	, PI	СТ		, s80	5432	4
01. DISCRETE STRUCTURES	PP	100	40	56	Р	С	11.	ENGI	NEERING	MATI	HEMATICS III	PP	1 1	L00	40	40	Р
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	52	Р	С	12.	MICR	OPROC.	& IN	TERFACING TEC	HNIQ.PP	· <u>1</u>	L00	40	49	Р
03. DIGIT. ELECTRONICS & LOGIC DESI	GNPP	100	40	56	Р	С	13.	DATA	STRUCT	URES		PP	. 1	L00	40	49	Р
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	49	Р	C	14.	COMP	UTER GR	APHI	CS	PP	1	L00	40	60	Р
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	62	Р	С	15.	COMP	UTER OR	GANI	ZATION	PP	· 1	L00	40	62	Р
06. PROGRAMMING LABORATORY	TW	25	10	15	Р	С	16.	0.0	. PROG.	& C	OMP. GRAPH. L	AB TW	l	50	20	28	Р
07. PROGRAMMING LABORATORY	PR	50	20	40	Р	C	17.	0.0	. PROG.	& C	OMP. GRAPH. L	AB PR		50	20	38	Р
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	16	Р	С	18.	MICR	OPROCES	SORS	& INTERFACIN	G LABTW	l	50	20	30	Р
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	30	Р	C	19.	MICR	OPROCES	SORS	& INTERFACIN	IG LABPR		50	20	28	Р
10. SOFT SKILLS	TW	50	20	35	Р	C	20.	DATA	STRUCT	URES	LABORATORY	TW	1	50	20	32	Р
							21.	DATA	STRUCT	URES	LABORATORY	PR	٠	50	20	42	Р
GRAND TOTAL = 869/1500, RESULT: HIGH	ER SEC	COND CI	_ASS														
ORDN. 1 MARKS :																	
S8054325 SHRUTI SWAGATIKA				SUJ	АТА			,	7104561	.9м	, s8054325	, PI	СТ		, s80	5432	5

						cols05
01. DISCRETE STRUCTURES	PP	100	40	70	РС	11. ENGINEERING MATHEMATICS III PP 100 40 40 P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	60	P C	12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 53 P
03. DIGIT. ELECTRONICS & LOGIC DESIGNATION OF THE PROPERTY OF	GNPP	100	40	65	P C	13. DATA STRUCTURES PP 100 40 59 P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	53	P C	14. COMPUTER GRAPHICS PP 100 40 64 P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	60	P C	15. COMPUTER ORGANIZATION PP 100 40 60 P
06. PROGRAMMING LABORATORY	TW	25	10	20	P C	16. O. O. PROG. & COMP. GRAPH. LAB TW 50 20 36 P
07. PROGRAMMING LABORATORY	PR	50	20	39	P C	17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 37 P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	20	P C	18. MICROPROCESSORS & INTERFACING LABTW 50 20 39 P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	25	P C	19. MICROPROCESSORS & INTERFACING LABPR 50 20 40 P
10. SOFT SKILLS	TW	50	20	38	P C	20. DATA STRUCTURES LABORATORY TW 50 20 37 P
						21. DATA STRUCTURES LABORATORY PR 50 20 28 P

GRAND TOTAL = 943/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS:

S8054326 SHUBHRANK GUPTA				INDIRA	, 71045621c	, s8054326 ,	PICT	, s8054326
01. DISCRETE STRUCTURES	PP	100	40	40 P				
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	40 P C				
03. DIGIT. ELECTRONICS & LOGIC DESIG	GNPP	100	40	AA F				
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	17 F				
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	40 P C				
06. PROGRAMMING LABORATORY	TW	25	10	13 P C				
07. PROGRAMMING LABORATORY	PR	50	20	AA F				
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	10 P C				
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	AA F				
10. SOFT SKILLS	TW	50	20	24 P C				

FIRST TERM TOTAL = 184/700.

ORDN. 1 MARKS:

	 ERSITY	 OF PU	 NE ,S	. E. (2	 2008 PAT.	COMPUTER) EXAMINATION MAY 2011
DATE : 18 AUG. 2011	CEN	TRE : I	PUNE I	INSTI	TUTE OF C	COMPUTER TECHNOLOGY, PUNE. PAGE NO. 42 (406)
NOTE: FIRST LINE : SEAT NO., NAME	OF THI	E CAND	[DATE	, MO	THER, PER	RMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
S8054327 SIDDHA MONIKA SUNIL				JAY	'ASHREE	, 71045623к , S8054327 , PICT , S8054327
01. DISCRETE STRUCTURES	PP	100	40	56	P C	11. ENGINEERING MATHEMATICS III PP 100 40 54 P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	63	P C	12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 44 P
03. DIGIT. ELECTRONICS & LOGIC DESI	GNPP	100	40	67	P C	13. DATA STRUCTURES PP 100 40 59 P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	47	P C	14. COMPUTER GRAPHICS PP 100 40 45 P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	59	P C	15. COMPUTER ORGANIZATION PP 100 40 55 P
06. PROGRAMMING LABORATORY	TW	25	10	19	P C	16. O. O. PROG. & COMP. GRAPH. LAB TW 50 20 37 P
07. PROGRAMMING LABORATORY	PR	50	20	39	P C	17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 37 P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	16	P C	18. MICROPROCESSORS & INTERFACING LABTW 50 20 41 P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	30	P C	19. MICROPROCESSORS & INTERFACING LABPR 50 20 40 P
10. SOFT SKILLS	TW	50	20	39	P C	20. DATA STRUCTURES LABORATORY TW 50 20 41 P
						21. DATA STRUCTURES LABORATORY PR 50 20 34 P
GRAND TOTAL = 922/1500, RESULT: FIRS ORDN. 1 MARKS :	T CLAS	SS				
S8054328 SIDDHARTH BATRA				SAN	IGEETA	, 71045624H , S8054328 , РІСТ , S8054328
01. DISCRETE STRUCTURES	PP	100	40	71	P C	
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	40	Р	
03. DIGIT. ELECTRONICS & LOGIC DESI	GNPP	100	40	40	P C	
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	AA	F	
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	43	P C	

OF PROCRAMMING LARGRATORY THE 2F 10 14 P.C.

06. PROGRAMMING LABORATORY	TW	25	10	14	РС	
07. PROGRAMMING LABORATORY	PR	50	20	24	P C	
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	10	P C	
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	20	Р	
10. SOFT SKILLS	TW	50	20	23	P C	

FIRST TERM TOTAL = 285/700.

ORDN. 1 MARKS:

S8054329 SINARE NUPOOR SANJAY				KAL	PANA	, 71045626D , S8054329 , PICT , S8054329	
01. DISCRETE STRUCTURES	PP	100	40	63	P C	11. ENGINEERING MATHEMATICS III PP 100 40 49	Р
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	68	P C	12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 46	Р
03. DIGIT. ELECTRONICS & LOGIC DESIGNATION OF THE PROPERTY OF	GNPP	100	40	82	P C	13. DATA STRUCTURES PP 100 40 71	Р
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	63	P C	14. COMPUTER GRAPHICS PP 100 40 66	Р
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	63	P C	15. COMPUTER ORGANIZATION PP 100 40 68	Р
06. PROGRAMMING LABORATORY	TW	25	10	22	P C	16. O. O. PROG. & COMP. GRAPH. LAB TW 50 20 38	Р
07. PROGRAMMING LABORATORY	PR	50	20	37	P C	17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 34	Р
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	22	P C	18. MICROPROCESSORS & INTERFACING LABTW 50 20 41	Р
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	38	P C	19. MICROPROCESSORS & INTERFACING LABPR 50 20 40	Р
10. SOFT SKILLS	TW	50	20	43	P C	20. DATA STRUCTURES LABORATORY TW 50 20 43	Р
						21. DATA STRUCTURES LABORATORY PR 50 20 42	Р

GRAND TOTAL = 1039/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS:

UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(COMPUTER) EXAMINATION MAY 2011

DATE: 18 AUG. 2011 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 43 (407)

NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

cols05

				cols05	
OTHER LINES: HEAD OF PASSING,	MAX. MARKS,	MIN. PASS MARKS,	MARKS OBTAINED,	P/F:PASS/FAIL,	C:PREVIOUS CARRY OVER

S8054330 SINGHAL PANKAJ SHRIBHAGW	AN			ANI	ТА		, 71045627в , ѕ8054330 ,	PICT		, s80)5433	0
01. DISCRETE STRUCTURES	PP	100	40	68	P C	11.	ENGINEERING MATHEMATICS III	PP	100	40	87	Р
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	52	P C	12	MICROPROC. & INTERFACING TECHNIQ.	. PP	100	40	43	Р
03. DIGIT. ELECTRONICS & LOGIC DESI	GNPP	100	40	83	P C	13	DATA STRUCTURES	PP	100	40	65	Р
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	50	P C	14	COMPUTER GRAPHICS	PP	100	40	65	Р
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	56	P C	15	COMPUTER ORGANIZATION	PP	100	40	64	Р
06. PROGRAMMING LABORATORY	TW	25	10	21	P C	16	O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	36	Р
07. PROGRAMMING LABORATORY	PR	50	20	40	P C	17	O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	25	Р
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	21	P C	18	MICROPROCESSORS & INTERFACING LAB	3TW	50	20	37	Р
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	30	P C	19	MICROPROCESSORS & INTERFACING LAB	BPR	50	20	40	Р
10. SOFT SKILLS	TW	50	20	39	P C	20	DATA STRUCTURES LABORATORY	TW	50	20	39	Р

GRAND TOTAL = 993/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS :

S8054331 SONAWANE KALINDI DATTATRA	AYA			MAY	Ά	, 71045631L , S8054331 , PICT , S8054331	
01. DISCRETE STRUCTURES	PP	100	40	56	РC	11. ENGINEERING MATHEMATICS III PP 100 40 43	Р
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	46	P C	12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 45	Р
03. DIGIT. ELECTRONICS & LOGIC DESIG	GNPP	100	40	63	P C	13. DATA STRUCTURES PP 100 40 63	Р
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	54	P C	14. COMPUTER GRAPHICS PP 100 40 55	Р
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	50	P C	15. COMPUTER ORGANIZATION PP 100 40 59	Р
06. PROGRAMMING LABORATORY	TW	25	10	20	P C	16. O. O. PROG. & COMP. GRAPH. LAB TW 50 20 39	Р
07. PROGRAMMING LABORATORY	PR	50	20	27	Р	17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 27	Р
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	20	P C	18. MICROPROCESSORS & INTERFACING LABTW 50 20 38	Р
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	30	P C	19. MICROPROCESSORS & INTERFACING LABPR 50 20 08	F
10. SOFT SKILLS	TW	50	20	36	P C	20. DATA STRUCTURES LABORATORY TW 50 20 36	Р

21. DATA STRUCTURES LABORATORY

PR 50 20 32 P

cols05 21. DATA STRUCTURES LABORATORY PR 50 20 25 P

GRAND TOTAL = 840/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS:

S8054332 SOUNDANKAR KOMAL RAJENDRA	Ą			KAL	PANA	, 71045634E , S8054332 , PICT , S8054332
01. DISCRETE STRUCTURES	PP	100	40	65	P C	11. ENGINEERING MATHEMATICS III PP 100 40 44 P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	45	P C	12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 32 F
03. DIGIT. ELECTRONICS & LOGIC DESIG	GNPP	100	40	41	P C	13. DATA STRUCTURES PP 100 40 46 P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	44	P C	14. COMPUTER GRAPHICS PP 100 40 40 P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	49	P C	15. COMPUTER ORGANIZATION PP 100 40 42 P
06. PROGRAMMING LABORATORY	TW	25	10	19	P C	16. O. O. PROG. & COMP. GRAPH. LAB TW 50 20 30 P
07. PROGRAMMING LABORATORY	PR	50	20	28	Р	17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 31 P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	19	P C	18. MICROPROCESSORS & INTERFACING LABTW 50 20 32 P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	28	P C	19. MICROPROCESSORS & INTERFACING LABPR 50 20 10 F
10. SOFT SKILLS	TW	50	20	37	P C	20. DATA STRUCTURES LABORATORY TW 50 20 35 P
						21. DATA STRUCTURES LABORATORY PR 50 20 10 F

GRAND TOTAL = 727/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS:

UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(COMPUTER) EXAMINATION MAY 2011

DATE : 18 AUG. 2011 CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 44 (408)

NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8054333 SUKHANI KUMAR DAYARAM ARTI , 71045636M , S8054333 , PICT , S8054333

PP 100 40 68 P C 11. ENGINEERING MATHEMATICS III PP 100 40 52 P 01. DISCRETE STRUCTURES

						cols05	
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	51	P C	12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 45 P	
03. DIGIT. ELECTRONICS & LOGIC DESIG	SNPP	100	40	70	P C	13. DATA STRUCTURES PP 100 40 64 P	
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	45	PС	14. COMPUTER GRAPHICS PP 100 40 59 P	
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	49	РС	15. COMPUTER ORGANIZATION PP 100 40 60 P	
06. PROGRAMMING LABORATORY	TW	25	10	18	РС	16. O. O. PROG. & COMP. GRAPH. LAB TW 50 20 30 P	
07. PROGRAMMING LABORATORY	PR	50	20	38	P C	17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 46 P	
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	17	P C	18. MICROPROCESSORS & INTERFACING LABTW 50 20 33 P	
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	42	РС	19. MICROPROCESSORS & INTERFACING LABPR 50 20 46 P	
10. SOFT SKILLS	TW	50	20	37	P C	20. DATA STRUCTURES LABORATORY TW 50 20 30 P	
						21. DATA STRUCTURES LABORATORY PR 50 20 31 P	

GRAND TOTAL = 931/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS:

S8054334 TAHAKIK AVINASH ABASAHEB				SHA	AKUNTALA	, 71045638н , s8054334 , ріст , s8054334
01. DISCRETE STRUCTURES	PP	100	40	66	P C	11. ENGINEERING MATHEMATICS III PP 100 40 81 P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	61	P C	12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 49 P
03. DIGIT. ELECTRONICS & LOGIC DESIG	GNPP	100	40	66	P C	13. DATA STRUCTURES PP 100 40 73 P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	51	P C	14. COMPUTER GRAPHICS PP 100 40 61 P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	48	P C	15. COMPUTER ORGANIZATION PP 100 40 70 P
06. PROGRAMMING LABORATORY	TW	25	10	18	P C	16. O. O. PROG. & COMP. GRAPH. LAB TW 50 20 25 P
07. PROGRAMMING LABORATORY	PR	50	20	35	P C	17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 43 P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	18	P C	18. MICROPROCESSORS & INTERFACING LABTW 50 20 28 P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	27	P C	19. MICROPROCESSORS & INTERFACING LABPR 50 20 42 P
10. SOFT SKILLS	TW	50	20	32	P C	20. DATA STRUCTURES LABORATORY TW 50 20 24 P
						21. DATA STRUCTURES LABORATORY PR 50 20 33 P

GRAND TOTAL = 951/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS:

S8054335 TANPURE AJAY PRAKASH				SUN	IITA		, 71045639F , S8054335 ,	PICT	Γ	, s80)54335	
01. DISCRETE STRUCTURES	PP	100	40	54	РC	11	. ENGINEERING MATHEMATICS III	PP	100	40	10 F	
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	43	P C	12	. MICROPROC. & INTERFACING TECHNI	Q.PP	100	40	30 F	
03. DIGIT. ELECTRONICS & LOGIC DESIG	GNPP	100	40	78	P C	13	. DATA STRUCTURES	PP	100	40	64 P	
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	60	РC	14	. COMPUTER GRAPHICS	PP	100	40	60 P	
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	50	РC	15	. COMPUTER ORGANIZATION	PP	100	40	51 P	
06. PROGRAMMING LABORATORY	TW	25	10	19	P C	16	. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	33 P	
07. PROGRAMMING LABORATORY	PR	50	20	37	РC	17	. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	35 P	
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	19	РC	18	. MICROPROCESSORS & INTERFACING L	ABTW	50	20	36 P	
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	31	РC	19	. MICROPROCESSORS & INTERFACING L	ABPR	50	20	32 P	
10. SOFT SKILLS	TW	50	20	38	РC	20	. DATA STRUCTURES LABORATORY	TW	50	20	40 P	
						21	. DATA STRUCTURES LABORATORY	PR	50	20	11 F	
ORDN. 1 MARKS :	 RSTTY	 OF PU	 INF .S	 . F. (2008 PAT	 T.)(COMI						
DATE : 18 AUG. 2011			-	_			ER TECHNOLOGY, PUNE.	PA(GE NO.	45	(409)	ı
NOTE: FIRST LINE : SEAT NO., NAME OF OTHER LINES: HEAD OF PASSING,	MAX	. MARKS	S, M:	IN. F	PASS MAR	RKS, MA	RKS OBTAINED, P/F:PASS/FAIL, C:	PREVIC	OUS CA		/ER	
S8054336 TARGE SOURABH SURESH				MAN	IISHA		, 71132441H , S8054336 ,	PICT	Г	, s80)54336	
01. DISCRETE STRUCTURES	PP	100	40	63	P C	11	. ENGINEERING MATHEMATICS III	PP	100	40	56 P	
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	56	P C	12	. MICROPROC. & INTERFACING TECHNI	Q.PP	100	40	45 P	
03. DIGIT. ELECTRONICS & LOGIC DESIG	GNPP	100	40	70	P C	13	. DATA STRUCTURES	PP	100	40	65 P	
04		100	40	40		4.4			100	40	60 -	

40 66 P C

25 10 21 P C

04. DATA STRUCTURES AND ALGORITHMS PP 100 40 49 P C

TW

05. HUMANITIES AND SOCIAL SCIENCE

06. PROGRAMMING LABORATORY

Page 154

16. O. O. PROG. & COMP. GRAPH. LAB TW

PP 100 40 62 P

PP 100 40 57 P

50 20 42 P

14. COMPUTER GRAPHICS

15. COMPUTER ORGANIZATION

cols05

cols05 07. PROGRAMMING LABORATORY 50 20 38 P C 17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 40 P 08. DIGITAL ELECTRONICS LABORATORY 25 10 22 P C 18. MICROPROCESSORS & INTERFACING LABTW 50 20 42 P TW 09. DIGITAL ELECTRONICS LABORATORY 20 40 P C 19. MICROPROCESSORS & INTERFACING LABPR 20 44 P 10. SOFT SKILLS TW 50 20 39 P C 20. DATA STRUCTURES LABORATORY 50 20 36 P TW 21. DATA STRUCTURES LABORATORY 50 20 32 P GRAND TOTAL = 985/1500, RESULT: FIRST CLASS ORDN. 1 MARKS:

S8054337 TATTI ADITI ANIL , 71045641H , s8054337 , ASHWINI PICT , s8054337 01. DISCRETE STRUCTURES 100 40 73 P C 11. ENGINEERING MATHEMATICS III PP 100 40 56 P 02. PROGRAMMING & PROBLEM SOLVING 100 40 61 P C 12. MICROPROC. & INTERFACING TECHNIQ.PP 42 P 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 40 91 P C 13. DATA STRUCTURES 100 40 57 P 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 67 P C 14. COMPUTER GRAPHICS 100 40 05. HUMANITIES AND SOCIAL SCIENCE 100 58 P C 15. COMPUTER ORGANIZATION 100 40 59 P 40 06. PROGRAMMING LABORATORY 25 10 19 P C 16. O. O. PROG. & COMP. GRAPH. LAB TW 50 20 38 P TW 07. PROGRAMMING LABORATORY 50 20 45 P C 17. O. O. PROG. & COMP. GRAPH. LAB 50 20 40 P 08. DIGITAL ELECTRONICS LABORATORY TW 25 10 20 P C 18. MICROPROCESSORS & INTERFACING LABTW 20 37 P 50 09. DIGITAL ELECTRONICS LABORATORY 19. MICROPROCESSORS & INTERFACING LABPR 50 20 37 P C 20 45 P 10. SOFT SKILLS 50 20 31 P C 20. DATA STRUCTURES LABORATORY TW TW 50 20 37 P

GRAND TOTAL = 1001/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS:

21. DATA STRUCTURES LABORATORY

50

20

41 P

S8054338 THAKARE DHANRAJSINGH SURES	БН			JAYA	, 71045646J , S8054338 , PICT ,	S8054338
01. DISCRETE STRUCTURES	PP	100	40	55 P C	11. ENGINEERING MATHEMATICS III PP 100 4	0 40 P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	52 P C	12. MICROPROC. & INTERFACING TECHNIQ.PP 100 4	0 40 P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	IPP	100	40	59 P C	13. DATA STRUCTURES PP 100 4 Page 155	0 59 р

PP 100 40 60 P

					-							-
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	40	P C	15.	COMPUTER ORGANIZATION	PP	100	40	49	Р
06. PROGRAMMING LABORATORY	TW	25	10	17	P C	16.	O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	34	Р
07. PROGRAMMING LABORATORY	PR	50	20	30	P C	17.	O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	24	Р
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	19	P C	18.	MICROPROCESSORS & INTERFACING LAB	ВТW	50	20	40	Р
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	20	P C	19.	MICROPROCESSORS & INTERFACING LAB	BPR	50	20	30	Р
10. SOFT SKILLS	TW	50	20	38	P C	20.	DATA STRUCTURES LABORATORY	TW	50	20	40	Р
						21.	DATA STRUCTURES LABORATORY	PR	50	20	32	Р
GRAND TOTAL = 831/1500, RESULT: HIGHE	ER SEC	COND CL	.ASS									
ORDN. 1 MARKS :												
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		TRE : P	ONE I	NSTI	TUTE OF COM	1PUTE	R TECHNOLOGY, PUNE.	PAG	E NO.	46	(4)	(0
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DATE : 18 AUG. 2011												
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DATE: 18 AUG. 2011	 OF THE	E CANDI					REG. NO., PREVIOUS SEAT NO., CO					
DATE: 18 AUG. 2011	 OF THE	E CANDI										
DATE: 18 AUG. 2011	 OF THE	E CANDI		N. P			KS OBTAINED, P/F:PASS/FAIL, C:PF	REVIO	US CAF		'ER 	
DATE: 18 AUG. 2011	 OF THE	E CANDI		N. P VAR	PASS MARKS,	MARI	KS OBTAINED, P/F:PASS/FAIL, C:PF	REVIO	US CAF	RRY OV	'ER 	
DATE: 18 AUG. 2011 NOTE: FIRST LINE: SEAT NO., NAME OF OTHER LINES: HEAD OF PASSING, S8054339 THAKKAR BHAVIN PRANAY	MAX.	CANDI MARKS	6, MI	N. P VAR 62	PASS MARKS,	MARK 	(S OBTAINED, P/F:PASS/FAIL, C:PF	REVIO PICT PP	US CAF	RRY OV , S80	'ER · · ·	Р
DATE: 18 AUG. 2011 NOTE: FIRST LINE: SEAT NO., NAME OF OTHER LINES: HEAD OF PASSING, S8054339 THAKKAR BHAVIN PRANAY 01. DISCRETE STRUCTURES	PP	CANDI MARKS	6, MI · ·	N. P VAR 62	PASS MARKS, RSHA P C P C	MARK 11. 12.	(S OBTAINED, P/F:PASS/FAIL, C:PF	REVIO PICT PP	US CAF	RRY OV , S80	′ER · · · · 954339 40	P P
DATE: 18 AUG. 2011 NOTE: FIRST LINE: SEAT NO., NAME OF OTHER LINES: HEAD OF PASSING, S8054339 THAKKAR BHAVIN PRANAY O1. DISCRETE STRUCTURES O2. PROGRAMMING & PROBLEM SOLVING	PP	CANDI MARKS 100 100	40 40	N. P VAR 62 47	PASS MARKS, RSHA P C P C	11. 12. 13.	(S OBTAINED, P/F:PASS/FAIL, C:PR, 71045647G , S8054339 , ENGINEERING MATHEMATICS III MICROPROC. & INTERFACING TECHNIQ.	REVIO PICT PP .PP	US CAF 100 100	RRY OV , S80 40 40	YER 954339 40 40	P P P
DATE: 18 AUG. 2011 NOTE: FIRST LINE: SEAT NO., NAME OF OTHER LINES: HEAD OF PASSING, S8054339 THAKKAR BHAVIN PRANAY O1. DISCRETE STRUCTURES O2. PROGRAMMING & PROBLEM SOLVING O3. DIGIT. ELECTRONICS & LOGIC DESIGNATION	PP PP GNPP	CANDI MARKS 100 100 100	40 40 40	N. P VAR 62 47 62	PASS MARKS, RSHA P C P C P C	11. 12. 13.	(S OBTAINED, P/F:PASS/FAIL, C:PR, 71045647G , S8054339 , ENGINEERING MATHEMATICS III MICROPROC. & INTERFACING TECHNIQ. DATA STRUCTURES	REVIO PICT PP PP	100 100 100	RRY OV , S80 40 40 40	YER	P P P
DATE: 18 AUG. 2011 NOTE: FIRST LINE: SEAT NO., NAME OF OTHER LINES: HEAD OF PASSING, S8054339 THAKKAR BHAVIN PRANAY O1. DISCRETE STRUCTURES O2. PROGRAMMING & PROBLEM SOLVING O3. DIGIT. ELECTRONICS & LOGIC DESIGNATION	PP PP PP	100 100 100 100	40 40 40 40 40	N. P VAR 62 47 62 49	PASS MARKS, RSHA PC PC PC PC PC	11. 12. 13. 14.	(S OBTAINED, P/F:PASS/FAIL, C:PR, 71045647G , S8054339 , ENGINEERING MATHEMATICS III MICROPROC. & INTERFACING TECHNIQ. DATA STRUCTURES COMPUTER GRAPHICS	PICT PP .PP PP	100 100 100 100	RRY OV , S80 40 40 40 40	YER	P P P P
DATE: 18 AUG. 2011 NOTE: FIRST LINE: SEAT NO., NAME OF OTHER LINES: HEAD OF PASSING, S8054339 THAKKAR BHAVIN PRANAY 01. DISCRETE STRUCTURES 02. PROGRAMMING & PROBLEM SOLVING 03. DIGIT. ELECTRONICS & LOGIC DESIGNATION OF THE PROBLEM SOLVING 04. DATA STRUCTURES AND ALGORITHMS 05. HUMANITIES AND SOCIAL SCIENCE	PPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPP	100 100 100 100 100	40 40 40 40 40 40	N. P VAR 62 47 62 49 63	PASS MARKS, RSHA PC PC PC PC PC	11. 12. 13. 14. 15.	(S OBTAINED, P/F:PASS/FAIL, C:PR, 71045647G , S8054339 , ENGINEERING MATHEMATICS III MICROPROC. & INTERFACING TECHNIQ. DATA STRUCTURES COMPUTER GRAPHICS COMPUTER ORGANIZATION	PICT PP PP PP	100 100 100 100 100	RRY OV , S80 40 40 40 40 40	YER	P P P P
DATE: 18 AUG. 2011 NOTE: FIRST LINE: SEAT NO., NAME OF OTHER LINES: HEAD OF PASSING, S8054339 THAKKAR BHAVIN PRANAY 01. DISCRETE STRUCTURES 02. PROGRAMMING & PROBLEM SOLVING 03. DIGIT. ELECTRONICS & LOGIC DESIGNATION OF THE PROBLEM SOLVING 04. DATA STRUCTURES AND ALGORITHMS 05. HUMANITIES AND SOCIAL SCIENCE 06. PROGRAMMING LABORATORY	PPPPPPTW	100 100 100 100 25	40 40 40 40 40 40	N. P VAR 62 47 62 49 63 17	PASS MARKS, RSHA PC PC PC PC PC PC	11. 12. 13. 14. 15. 16.	AS OBTAINED, P/F:PASS/FAIL, C:PR , 71045647G , \$8054339 , ENGINEERING MATHEMATICS III MICROPROC. & INTERFACING TECHNIQ. DATA STRUCTURES COMPUTER GRAPHICS COMPUTER ORGANIZATION O. O. PROG. & COMP. GRAPH. LAB	PICT PP PP PP PP TW PR	100 100 100 100 100 50	RRY OV , \$80 40 40 40 40 40 20	YER	P P P P P
DATE: 18 AUG. 2011	PPPPPTW	100 100 100 100 25 50	40 40 40 40 40 40 20	N. P VAR 62 47 62 49 63 17 35 18	PASS MARKS, RSHA PC PC PC PC PC PC PC	11. 12. 13. 14. 15. 16. 17.	AS OBTAINED, P/F:PASS/FAIL, C:PR, 71045647G , S8054339 , ENGINEERING MATHEMATICS III MICROPROC. & INTERFACING TECHNIQ. DATA STRUCTURES COMPUTER GRAPHICS COMPUTER ORGANIZATION O. O. PROG. & COMP. GRAPH. LAB O. O. PROG. & COMP. GRAPH. LAB	PICT PP PP PP TW PR BTW	100 100 100 100 100 50	RRY OV , \$80 40 40 40 40 40 20 20	40 40 69 64 67 34 45	P P P P P
DATE: 18 AUG. 2011	PPPPPPTWPRTTW	100 100 100 100 25 50 25	40 40 40 40 40 40 10 20	N. P VAR 62 47 62 49 63 17 35 18	PASS MARKS, CASS	11. 12. 13. 14. 15. 16. 17. 18.	AS OBTAINED, P/F:PASS/FAIL, C:PR, 71045647G , S8054339 , ENGINEERING MATHEMATICS III MICROPROC. & INTERFACING TECHNIQ. DATA STRUCTURES COMPUTER GRAPHICS COMPUTER ORGANIZATION O. O. PROG. & COMP. GRAPH. LAB O. O. PROG. & COMP. GRAPH. LAB MICROPROCESSORS & INTERFACING LAB	PICT PP PP PP TW PR BTW	100 100 100 100 100 50 50	RRY OV , \$80 40 40 40 40 20 20 20	40 40 69 64 67 34 45 32	P P P P P P
DATE: 18 AUG. 2011 NOTE: FIRST LINE: SEAT NO., NAME OF OTHER LINES: HEAD OF PASSING, S8054339 THAKKAR BHAVIN PRANAY 01. DISCRETE STRUCTURES 02. PROGRAMMING & PROBLEM SOLVING 03. DIGIT. ELECTRONICS & LOGIC DESIGNATION 04. DATA STRUCTURES AND ALGORITHMS 05. HUMANITIES AND SOCIAL SCIENCE 06. PROGRAMMING LABORATORY 07. PROGRAMMING LABORATORY 08. DIGITAL ELECTRONICS LABORATORY 09. DIGITAL ELECTRONICS LABORATORY	PP PP PP TW PR TW PR	100 100 100 100 25 50 25	40 40 40 40 40 10 20 10 20	N. P VAR 62 47 62 49 63 17 35 18 42	PASS MARKS, CASS	11. 12. 13. 14. 15. 16. 17. 18.	AS OBTAINED, P/F:PASS/FAIL, C:PR, 71045647G , \$8054339 , ENGINEERING MATHEMATICS III MICROPROC. & INTERFACING TECHNIQ. DATA STRUCTURES COMPUTER GRAPHICS COMPUTER ORGANIZATION O. O. PROG. & COMP. GRAPH. LAB O. O. PROG. & COMP. GRAPH. LAB MICROPROCESSORS & INTERFACING LAB MICROPROCESSORS & INTERFACING LAB	REVIO PICT PP PP PP TW PR BTW BPR	100 100 100 100 50 50 50	RRY OV , \$80 40 40 40 40 20 20 20 20	40 40 69 64 67 34 45 32 42	P P P P P P P

04. DATA STRUCTURES AND ALGORITHMS PP 100 40 53 P C 14. COMPUTER GRAPHICS

GRAND TOTAL = 918/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS:

S8054340	THAKUR DHIRAJSING UDAYSIN	G			SAR	OJADEVI		, 71045648E , S8054340 ,	PICT		, s80	54340	0
01. DISC	RETE STRUCTURES	PP	100	40	48	P C	11.	ENGINEERING MATHEMATICS III	PP	100	40	AA	F
02. PROG	RAMMING & PROBLEM SOLVING	PP	100	40	40	Р	12.	MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	09	F
03. DIGI	T. ELECTRONICS & LOGIC DESIG	NPP	100	40	40	P	13.	DATA STRUCTURES	PP	100	40	40	Р
04. DATA	STRUCTURES AND ALGORITHMS	PP	100	40	45	P	14.	COMPUTER GRAPHICS	PP	100	40	28	F
05. HUMA	NITIES AND SOCIAL SCIENCE	PP	100	40	40	P C	15.	COMPUTER ORGANIZATION	PP	100	40	40	Р
06. PROG	RAMMING LABORATORY	TW	25	10	13	P C	16.	O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	21	Р
07. PROG	RAMMING LABORATORY	PR	50	20	37	P	17.	O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	07	F
08. DIGI	TAL ELECTRONICS LABORATORY	TW	25	10	12	P C	18.	MICROPROCESSORS & INTERFACING LAB	BTW	50	20	25	Р
09. DIGI	TAL ELECTRONICS LABORATORY	PR	50	20	20	P C	19.	MICROPROCESSORS & INTERFACING LAB	BPR	50	20	AA	F
10. SOFT	SKILLS	TW	50	20	29	P C	20.	DATA STRUCTURES LABORATORY	TW	50	20	28	Р
							21.	DATA STRUCTURES LABORATORY	PR	50	20	13	F

GRAND TOTAL = 535/1500, RESULT: FAILS

ORDN. 1 MARKS:

S8054341 UTKARSH MISHRA				MAM	1TA	, 71045654к , S8054341 , PICT , S8054341
01. DISCRETE STRUCTURES	PP	100	40	71	P C	11. ENGINEERING MATHEMATICS III PP 100 40 54 P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	47	P C	12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 30 F
03. DIGIT. ELECTRONICS & LOGIC DESIG	SNPP	100	40	51	P C	13. DATA STRUCTURES PP 100 40 53 P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	40	P C	14. COMPUTER GRAPHICS PP 100 40 58 P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	53	P C	15. COMPUTER ORGANIZATION PP 100 40 48 P
06. PROGRAMMING LABORATORY	TW	25	10	15	P C	16. O. O. PROG. & COMP. GRAPH. LAB TW 50 20 24 P
07. PROGRAMMING LABORATORY	PR	50	20	10	F	17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 06 F
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	15	P C	18. MICROPROCESSORS & INTERFACING LABTW 50 20 25 P Page 157

10 COET CUTLIC		50	20	30	PC	19.	MICROPROCESSORS & INTERFACIN	IG LABPR	50	20	10	F
10. SOFT SKILLS	TW	50	20	22	P C	20.	DATA STRUCTURES LABORATORY	TW	50	20	28	Р
						21.	DATA STRUCTURES LABORATORY	PR	50	20	30	Р
RAND TOTAL = 720/1500, RESULT: FAIL	S A.T.	к.т.										
RDN. 1 MARKS :												
	FRSTTY	 OF PU	 NF S	 F (2008 PAT							
DATE : 18 AUG. 2011							R TECHNOLOGY, PUNE.	PAG	GE NO.	47	(4	 11)
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NOTE: FIRST LINE : SEAT NO., NAME												• •
OTHER LINES: HEAD OF PASSING,												
				• •							• •	
S8054342 VAYKOLE TEJAS ANIL				KAL	PANA		, 71045656F , S8054342	, PIC	Γ	, s80)5434	12
01. DISCRETE STRUCTURES	PP	100	40	51	P C	11.	ENGINEERING MATHEMATICS III	PP	100	40	40	Р
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	60	P C	12.	MICROPROC. & INTERFACING TEC	CHNIQ.PP	100	40	31*	· P
03. DIGIT. ELECTRONICS & LOGIC DESI	GNPP	100	40	40	P C	13.	DATA STRUCTURES	PP	100	40	50	Р
OA DATA STRUCTURES AND ALCORITUMS	PP	100	40	55	P C	14.	COMPUTER GRAPHICS	PP	100	40	48	Р
04. DATA STRUCTURES AND ALGORITHMS									100	_		
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	51	P C	15.	COMPUTER ORGANIZATION	PP	100	40	54	Р
		100 25	40 10		P C		COMPUTER ORGANIZATION O. O. PROG. & COMP. GRAPH. L	PP			54 32	
05. HUMANITIES AND SOCIAL SCIENCE	PP				PC	16.		PP .AB TW	100	40	_	Р
05. HUMANITIES AND SOCIAL SCIENCE06. PROGRAMMING LABORATORY	PP TW PR	25 50	10 20	15 37	PC	16. 17.	O. O. PROG. & COMP. GRAPH. L	PP .AB TW .AB PR	100 50 50	40 20	32	P P
05. HUMANITIES AND SOCIAL SCIENCE06. PROGRAMMING LABORATORY07. PROGRAMMING LABORATORY	PP TW PR	25 50	10 20	15 37 17	P C	16. 17. 18.	O. O. PROG. & COMP. GRAPH. L	PP AB TW AB PR IG LABTW	100 50 50	40 20 20	32 42	P P P
05. HUMANITIES AND SOCIAL SCIENCE06. PROGRAMMING LABORATORY07. PROGRAMMING LABORATORY08. DIGITAL ELECTRONICS LABORATORY	PP TW PR TW	25 50 25	10 20 10 20	15 37 17 25	P C P P C	16. 17. 18. 19.	O. O. PROG. & COMP. GRAPH. L O. O. PROG. & COMP. GRAPH. L MICROPROCESSORS & INTERFACIN	PP AB TW AB PR IG LABTW	100 50 50 50	40 20 20 20 20 20	32 42 30	P P P

S8054343 VEER ANJALI SUNDARDAS				URM	IILA	cols05 , 71132442F , S8054343 , PICT , S8054343
01. DISCRETE STRUCTURES	PP	100	40	53	РС	11. ENGINEERING MATHEMATICS III PP 100 40 60 P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	51	P C	12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 54 P
03. DIGIT. ELECTRONICS & LOGIC DESIG	GNPP	100	40	48	PС	13. DATA STRUCTURES PP 100 40 77 P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	73	PС	14. COMPUTER GRAPHICS PP 100 40 55 P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	72	P C	15. COMPUTER ORGANIZATION PP 100 40 61 P
06. PROGRAMMING LABORATORY	TW	25	10	21	P C	16. O. O. PROG. & COMP. GRAPH. LAB TW 50 20 46 P
07. PROGRAMMING LABORATORY	PR	50	20	38	P C	17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 36 P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	21	P C	18. MICROPROCESSORS & INTERFACING LABTW 50 20 36 P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	35	PС	19. MICROPROCESSORS & INTERFACING LABPR 50 20 36 P
10. SOFT SKILLS	TW	50	20	40	P C	20. DATA STRUCTURES LABORATORY TW 50 20 43 P
						21. DATA STRUCTURES LABORATORY PR 50 20 35 P

GRAND TOTAL = 991/1500, RESULT: FIRST CLASS WITH DISTINCTION
ORDN. 1 MARKS :

S8054344 VEERKAR PARTHA PRABODH				MED	НА	, 71045657D , S8054344 , PICT , S8054344	
01. DISCRETE STRUCTURES	PP	100	40	63	P C	11. ENGINEERING MATHEMATICS III PP 100 40 25	F
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	47	P C	12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 31	F
03. DIGIT. ELECTRONICS & LOGIC DESIGNATION OF THE PROPERTY OF	GNPP	100	40	58	P C	13. DATA STRUCTURES PP 100 40 42	Р
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	62	P C	14. COMPUTER GRAPHICS PP 100 40 56	Р
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	40	P C	15. COMPUTER ORGANIZATION PP 100 40 40	Р
06. PROGRAMMING LABORATORY	TW	25	10	22	P C	16. O. O. PROG. & COMP. GRAPH. LAB TW 50 20 42	Р
07. PROGRAMMING LABORATORY	PR	50	20	42	P C	17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 38	Р
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	19	P C	18. MICROPROCESSORS & INTERFACING LABTW 50 20 42	Р
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	27	P C	19. MICROPROCESSORS & INTERFACING LABPR 50 20 36	Р
10. SOFT SKILLS	TW	50	20	44	P C	20. DATA STRUCTURES LABORATORY TW 50 20 36	Р
						21. DATA STRUCTURES LABORATORY PR 50 20 42	Р

GRAND TOTAL = 854/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS:

UNIVE	 RSITY	OF PU	 INE ,S	.E.(2008	 PAT.)(COMP							
DATE : 18 AUG. 2011	CENT	ΓRE : I	PUNE :	INSTI	TUTE	OF COMPUTE	R TECHNOLOGY, PUNE		PAG	E NO.	48	(4	12)
NOTE: FIRST LINE : SEAT NO., NAME (OF THE	E CAND:	IDATE	, MC	THER	, PERMANENT	REG. NO., PREVIO	US SEAT NO., (COLLEG	iE, S	SEAT N	Ю.	
OTHER LINES: HEAD OF PASSING,	MAX.	. MARK	S, M	IN. F	PASS I	MARKS, MAF	KS OBTAINED, P/F:	PASS/FAIL, C:	PREVIO	US CAI	RRY OV	'ER	
S8054345 WAJE ROHIT GITARAM				PAR	RVATI		, 71045664G	, s8054345 ,	PICT	-	, s80	5434	5
01. DISCRETE STRUCTURES	PP	100	40	73	P C	11.	ENGINEERING MATHE	MATICS III	PP	100	40	59	Р
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	61	P C	12.	MICROPROC. & INTE	RFACING TECHNIC	Q.PP	100	40	40	Р
03. DIGIT. ELECTRONICS & LOGIC DESIGNATION OF THE PROPERTY OF	GNPP	100	40	73	P C	13.	DATA STRUCTURES		PP	100	40	60	Р
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	75	P C	14.	COMPUTER GRAPHICS		PP	100	40	49	Р
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	62	РС	15.	COMPUTER ORGANIZA	TION	PP	100	40	58	Р
06. PROGRAMMING LABORATORY	TW	25	10	20	РС	16.	O. O. PROG. & COM	P. GRAPH. LAB	TW	50	20	40	Р
07. PROGRAMMING LABORATORY	PR	50	20	41	РС	17.	O. O. PROG. & COM	P. GRAPH. LAB	PR	50	20	38	Р
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	22	РС	18.	MICROPROCESSORS &	INTERFACING LA	ABTW	50	20	45	Р
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	40	РС	19.	MICROPROCESSORS &	INTERFACING LA	ABPR	50	20	40	Р
10. SOFT SKILLS	TW	50	20	38	РС	20.	DATA STRUCTURES L	ABORATORY	TW	50	20	38	Р
						21.	DATA STRUCTURES L	ABORATORY	PR	50	20	35	Р
SPAND TOTAL 1007/1500 DECULT. ETDS	T (1.40	.c	u DTC	TTNCT	-TON								
GRAND TOTAL = 1007/1500, RESULT: FIRST	I CLAS	S MTII	H DI2	IINCI	TON								
ORDN. 1 MARKS :													
				• •									
S8054346 ABNAVE VINAY PANDURANG				RAN	ANACI		, 70925317G	, s8054346 ,	PICT	-	, s80	5434	6
01. DISCRETE STRUCTURES	PP	100	40	51	P C	11.	ENGINEERING MATHE	MATICS III	PP	100	40	40	P C
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	56	РС	12.	MICROPROC. & INTE	RFACING TECHNIC	Q.PP	100	40	40	Р
03. DIGIT. ELECTRONICS & LOGIC DESIG	GNPP	100	40	57	РС	13.	DATA STRUCTURES		PP	100	40	55	РС
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	47	РС	14.	COMPUTER GRAPHICS Pa	ge 160	PP	100	40	45	P C

05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	51	P C	15. COMPUTER ORGANIZATION PP 100 40 44 P	, C
06. PROGRAMMING LABORATORY	TW	25	10	23	P C	16. O. O. PROG. & COMP. GRAPH. LAB TW 50 20 42 P	C
07. PROGRAMMING LABORATORY	PR	50	20	39	P C	17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 44 P	, C
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	19	P C	18. MICROPROCESSORS & INTERFACING LABTW 50 20 40 P	, C
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	34	P C	19. MICROPROCESSORS & INTERFACING LABPR 50 20 36 P	, C
10. SOFT SKILLS	TW	50	20	40	P C	20. DATA STRUCTURES LABORATORY TW 50 20 41 P	C
						21. DATA STRUCTURES LABORATORY PR 50 20 37 P	C

GRAND TOTAL = 881/1500, RESULT: HIGHER SECOND CLASS ORDN. 1 MARKS:

S8054347 AMLER VIPUL DHARMA				MEE	ENA	, 71072123E , S8054347 , PICT , S8054347
01. DISCRETE STRUCTURES	PP	100	40	56	P C	11. ENGINEERING MATHEMATICS III PP 100 40 58 P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	67	PС	12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 52 P
03. DIGIT. ELECTRONICS & LOGIC DESIGNATION OF THE PROPERTY OF	GNPP	100	40	76	P C	13. DATA STRUCTURES PP 100 40 59 P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	62	PC	14. COMPUTER GRAPHICS PP 100 40 64 P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	58	PC	15. COMPUTER ORGANIZATION PP 100 40 47 P
06. PROGRAMMING LABORATORY	TW	25	10	21	PC	16. O. O. PROG. & COMP. GRAPH. LAB TW 50 20 43 P
07. PROGRAMMING LABORATORY	PR	50	20	40	PC	17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 43 P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	19	PC	18. MICROPROCESSORS & INTERFACING LABTW 50 20 43 P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	35	PC	19. MICROPROCESSORS & INTERFACING LABPR 50 20 40 P
10. SOFT SKILLS	TW	50	20	41	PC	20. DATA STRUCTURES LABORATORY TW 50 20 43 P
						21. DATA STRUCTURES LABORATORY PR 50 20 43 P

GRAND TOTAL = 1010/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS:

UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(COMPUTER) EXAMINATION MAY 2011

DATE : 18 AUG. 2011 PAGE NO. 49 (413) CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.

NOTE: FIRST	LINE : SEAT	NO., NAME	OF THE CANDIDA	TE, MOTHER, PERMA	ANENT REG. NO.,	PREVIOUS SEAT NO.	, COLLEGE,	SEAT NO.
OTHER	LINES: HEAD	OF PASSING	, MAX. MARKS,	MIN. PASS MARKS,	MARKS OBTAINED	, P/F:PASS/FAIL,	C:PREVIOUS	CARRY OVER

s8054349	ARASHDEEP SINGH HEIR				RUP	INDER		, 70925338К	, s8054349 ,	PICT		, s80	54349	9
01. DISCRE	TE STRUCTURES	PP	100	40	46	P C	11.	ENGINEERING MATHE	MATICS III	PP	100	40	40	РС
02. PROGRA	MMING & PROBLEM SOLVING	PP	100	40	44	РС	12.	MICROPROC. & INTE	RFACING TECHNIQ	.PP	100	40	18	F
03. DIGIT.	ELECTRONICS & LOGIC DESIG	NPP	100	40	40	P C	13.	DATA STRUCTURES		PP	100	40	40	P C
04. DATA S	TRUCTURES AND ALGORITHMS	PP	100	40	40	P C	14.	COMPUTER GRAPHICS		PP	100	40	43	P C
05. HUMANI	TIES AND SOCIAL SCIENCE	PP	100	40	40	P C	15.	COMPUTER ORGANIZA	TION	PP	100	40	40	Р
06. PROGRA	MMING LABORATORY	TW	25	10	17	P C	16.	O. O. PROG. & COM	P. GRAPH. LAB	TW	50	20	22	P C
07. PROGRA	MMING LABORATORY	PR	50	20	25	P C	17.	O. O. PROG. & COM	P. GRAPH. LAB	PR	50	20	35	P C
08. DIGITA	L ELECTRONICS LABORATORY	TW	25	10	14	P C	18.	MICROPROCESSORS &	INTERFACING LA	BTW	50	20	24	P C
09. DIGITA	L ELECTRONICS LABORATORY	PR	50	20	28	P C	19.	MICROPROCESSORS &	INTERFACING LA	BPR	50	20	30	P C
10. SOFT S	KILLS	TW	50	20	31	P C	20.	DATA STRUCTURES L	ABORATORY	TW	50	20	22	P C
							21.	DATA STRUCTURES L	ABORATORY	PR	50	20	28	РС

GRAND TOTAL = 667/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

S8054355 GADODIA ANUSHREE RAJENDR	A			GEE	TA	, 71072126K , S8054355 , PICT , S8054355	5
01. DISCRETE STRUCTURES	PP	100	40	40	P C	11. ENGINEERING MATHEMATICS III PP 100 40 27	F
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	40	РС	12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 42	РС
03. DIGIT. ELECTRONICS & LOGIC DESI	GNPP	100	40	49	P C	13. DATA STRUCTURES PP 100 40 49	РС
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	43	P C	14. COMPUTER GRAPHICS PP 100 40 49	РС
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	43	РС	15. COMPUTER ORGANIZATION PP 100 40 41	РС
06. PROGRAMMING LABORATORY	TW	25	10	19	РС	16. O. O. PROG. & COMP. GRAPH. LAB TW 50 20 36	РС
07. PROGRAMMING LABORATORY	PR	50	20	30	РС	17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 28	РС
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	19	РС	18. MICROPROCESSORS & INTERFACING LABTW 50 20 33	РС
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	29	P C	19. MICROPROCESSORS & INTERFACING LABPR 50 20 20 Page 162	P C

S8054361 JACHAK SURAJ GODHIRAM

lO. SOFT SKILLS	TW	50	20	42	P C	20.	DATA STRUCTURES LABORATORY	TW	50	20	32	Р (
						21.	DATA STRUCTURES LABORATORY	PR	50	20	10	F
AND TOTAL = 721/1500, RESULT: FAI	LS A.T.	к.т.										
DN. 1 MARKS :												
SOUE 4260 HARVARAN STAGU ANAND				HAD	DDEET		, 70925435M , S8054360	DICI	_	c 0 (05426	.0
68054360 HARKARAN SINGH ANAND				ПАК	PREET		, 70925435M , S8054360 ,	, PICT		, 300	05436	U
01. DISCRETE STRUCTURES	PP	100	40	43	P C	11.	ENGINEERING MATHEMATICS III	PP	100	40	AA	F
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	47	P C	12.	MICROPROC. & INTERFACING TECHN	NIQ.PP	100	40	40	Р (
3. DIGIT. ELECTRONICS & LOGIC DES	IGNPP	100	40	40	P C	13.	DATA STRUCTURES	PP	100	40	40	Р
04. DATA STRUCTURES AND ALGORITHMS	PP P	100	40	47	P C	14.	COMPUTER GRAPHICS	PP	100	40	57	Р
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	40	P C	15.	COMPUTER ORGANIZATION	PP	100	40	40	Р
06. PROGRAMMING LABORATORY	TW	25	10	16	P C	16.	O. O. PROG. & COMP. GRAPH. LAR	3 TW	50	20	28	Р
7. PROGRAMMING LABORATORY	PR	50	20	38	P C	17.	O. O. PROG. & COMP. GRAPH. LAR	3 PR	50	20	37	Р
08. DIGITAL ELECTRONICS LABORATORY	′ TW	25	10	13	P C	18.	MICROPROCESSORS & INTERFACING	LABTW	50	20	23	
9. DIGITAL ELECTRONICS LABORATORY	' PR	50	20	28	P C	19.	MICROPROCESSORS & INTERFACING	LABPR	50	20	25	Р
lO. SOFT SKILLS	TW	50	20	32	P C	20.	DATA STRUCTURES LABORATORY	TW	50	20	25	Р
						21.	DATA STRUCTURES LABORATORY	PR	50	20	AA	F
AND TOTAL = 659/1500, RESULT: FAI	LS A.T	к.т.										
DN. 1 MARKS :												

SHOBHA

, s8054361 ,

PICT

, s8054361

, 70925444L

01.	DISCRETE STRUCTURES	PP	100	40	46	P C	11	. ENGINEERING MATHEMATICS III	PP	100	40	56	P C
02.	PROGRAMMING & PROBLEM SOLVING	PP	100	40	40	P C	12	MICROPROC. & INTERFACING TECHNIC	.PP	100	40	46	P C
03.	DIGIT. ELECTRONICS & LOGIC DESIG	NPP	100	40	49	P C	13	. DATA STRUCTURES	PP	100	40	49	P C
04.	DATA STRUCTURES AND ALGORITHMS	PP	100	40	40	P C	14	COMPUTER GRAPHICS	PP	100	40	65	P C
05.	HUMANITIES AND SOCIAL SCIENCE	PP	100	40	50	P C	15	COMPUTER ORGANIZATION	PP	100	40	40	P C
06.	PROGRAMMING LABORATORY	TW	25	10	19	P C	16	O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	38	P C
07.	PROGRAMMING LABORATORY	PR	50	20	35	P C	17	O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	40	P C
08.	DIGITAL ELECTRONICS LABORATORY	TW	25	10	19	P C	18	. MICROPROCESSORS & INTERFACING LA	BTW	50	20	38	P C
09.	DIGITAL ELECTRONICS LABORATORY	PR	50	20	27	P C	19	. MICROPROCESSORS & INTERFACING LA	BPR	50	20	35	Р
10.	SOFT SKILLS	TW	50	20	38	P C	20	. DATA STRUCTURES LABORATORY	TW	50	20	40	P C
							21	DATA STRUCTURES LABORATORY	PR	50	20	33	РС

GRAND TOTAL = 843/1500, RESULT: HIGHER SECOND CLASS
ORDN. 1 MARKS :

S8054364 KADAM SHRINIWAS MUKUNDRA	0			URM	ILA		, 7092545	8L ,	, s8054364 ,	PICT	-	, s80	5436	4
01. DISCRETE STRUCTURES	PP	100	40	57	РС	11	. ENGINEERING	MATHEMA	ATICS III	PP	100	40	41	Р
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	52	P C	12	. MICROPROC.	& INTERF	FACING TECHNI	Q.PP	100	40	40	РС
03. DIGIT. ELECTRONICS & LOGIC DESI	GNPP	100	40	46	P C	13	. DATA STRUCT	URES		PP	100	40	52	РС
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	40	P C	14	. COMPUTER GR	APHICS		PP	100	40	40	P C
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	51	P C	15	. COMPUTER OR	GANIZAT:	ION	PP	100	40	47	Р
06. PROGRAMMING LABORATORY	TW	25	10	19	P C	16	. O. O. PROG.	& COMP	. GRAPH. LAB	TW	50	20	34	P C
07. PROGRAMMING LABORATORY	PR	50	20	27	P C	17	. O. O. PROG.	& COMP	. GRAPH. LAB	PR	50	20	35	P C
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	19	P C	18	. MICROPROCES	SORS & 1	INTERFACING L	ABTW	50	20	37	P C
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	20	P C	19	. MICROPROCES	SORS & 1	INTERFACING L	ABPR	50	20	21	P C
10. SOFT SKILLS	TW	50	20	39	PC	20	. DATA STRUCT	URES LA	BORATORY	TW	50	20	28	P C
						21	. DATA STRUCT	URES LA	BORATORY	PR	50	20	20	РС

GRAND TOTAL = 765/1500, RESULT: SECOND CLASS

ORDN. 1 MARKS:

S8054366 KANDEKAR AKSHAY POPATRAO				MAN	IGAL		, 71072129D , s8054366 ,	PIC	Г	, s80)5436	6
01. DISCRETE STRUCTURES	PP	100	40	40	P C	11.	ENGINEERING MATHEMATICS III	PP	100	40	12	F
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	52	P C	12.	MICROPROC. & INTERFACING TECHNI	Q.PP	100	40	52	P C
03. DIGIT. ELECTRONICS & LOGIC DESIG	SNPP	100	40	61	P C	13.	DATA STRUCTURES	PP	100	40	64	P C
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	64	P C	14.	COMPUTER GRAPHICS	PP	100	40	54	P C
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	57	P C	15.	COMPUTER ORGANIZATION	PP	100	40	50	P C
06. PROGRAMMING LABORATORY	TW	25	10	21	P C	16.	O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	40	РС
07. PROGRAMMING LABORATORY	PR	50	20	38	P C	17.	O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	42	РС
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	19	P C	18.	MICROPROCESSORS & INTERFACING L	ABTW	50	20	42	РС
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	36	P C	19.	MICROPROCESSORS & INTERFACING L	ABPR	50	20	35	РС
10. SOFT SKILLS	TW	50	20	41	P C	20.	DATA STRUCTURES LABORATORY	TW	50	20	39	РС
						21.	DATA STRUCTURES LABORATORY	PR	50	20	37	РС
IDDN 1 MADIC .		.K.T.										
	 RSITY		NE ,S	 .E.(2	 2008 PAT.)(COMPU						
		OF PU	-	-				PAG	 GE NO.	51		 15)
		OF PU	-	-				PAG	 GE NO.	 51 	(4	 15)
	CENT	 OF PU FRE : F	PUNE 1	INSTI	TUTE OF	COMPUTE	R TECHNOLOGY, PUNE.			 51 SEAT N		 15)
DATE: 18 AUG. 2011	CENT DF THE	OF PU ΓRE : F	PUNE I	INSTI	TUTE OF (COMPUTE RMANENT	R TECHNOLOGY, PUNE.	COLLEG	 GE, :	 SEAT N		 15)
DATE: 18 AUG. 2011 NOTE: FIRST LINE: SEAT NO., NAME OF COTHER LINES: HEAD OF PASSING,	CENT DF THE MAX.	OF PU FRE : F CAND MARKS	PUNE I	INSTI , MO	TUTE OF (OTHER, PEI	COMPUTE RMANENT MAR	R TECHNOLOGY, PUNE	COLLEC	GE,	 SEAT N	 NO. /ER	
DATE: 18 AUG. 2011 NOTE: FIRST LINE: SEAT NO., NAME OF COTHER LINES: HEAD OF PASSING,	CENT DF THE MAX.	OF PU FRE : F CAND MARKS	PUNE I	INSTI , MO	TUTE OF () THER, PEI PASS MARK!	COMPUTE RMANENT MAR	R TECHNOLOGY, PUNE	COLLEC	GE, S	 SEAT N	 NO. /ER	
DATE: 18 AUG. 2011 NOTE: FIRST LINE: SEAT NO., NAME O OTHER LINES: HEAD OF PASSING,	CENT OF THE MAX	OF PU FRE : F CAND MARKS	PUNE I	INSTI MO IN. P ALK	TUTE OF () THER, PEI PASS MARK!	COMPUTE RMANENT MAR	R TECHNOLOGY, PUNE.	COLLECTOR PREVIO	GE, S	 SEAT N RRY ON	 NO . /ER 	
UNIVE DATE: 18 AUG. 2011 NOTE: FIRST LINE: SEAT NO., NAME O OTHER LINES: HEAD OF PASSING, S8054367 KATDARE SANCHIT VINAYAK 01. DISCRETE STRUCTURES	CENT OF THE MAX	OF PU FRE : F CAND MARKS	PUNE I	INSTI MO IN. P ALK	TUTE OF () THER, PEI PASS MARK!	COMPUTE RMANENT MAR 11.	R TECHNOLOGY, PUNE. REG. NO., PREVIOUS SEAT NO., KS OBTAINED, P/F:PASS/FAIL, C: , 70925470K , S8054367 ,	COLLECT PREVIO	GE, : DUS CAI	 SEAT N RRY ON 	 NO . /ER 	 7

14. COMPUTER GRAPHICS

15. COMPUTER ORGANIZATION Page 165

PP 100 40 44 P C

04. DATA STRUCTURES AND ALGORITHMS PP 100 40 P C

05. HUMANITIES AND SOCIAL SCIENCE PP 100 40 47 P C

						cols05			
06. PROGRAMMING LABORATORY	TW	25	10	18 P	С	16. O. O. PROG. & COMP. GRAPH. LAB TW	50 20	43	P C
07. PROGRAMMING LABORATORY	PR	50	20	38 P	С	17. O. O. PROG. & COMP. GRAPH. LAB PR	50 20	38	P C
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	21 P	С	18. MICROPROCESSORS & INTERFACING LABTW	50 20	40	P C
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	40 P	С	19. MICROPROCESSORS & INTERFACING LABPR	50 20	40	P C
10. SOFT SKILLS	TW	50	20	42 P	С	20. DATA STRUCTURES LABORATORY TW	50 20	44	P C
						21. DATA STRUCTURES LABORATORY PR	50 20	37	P C
GRAND TOTAL = 901/1500, RESULT: FIRST	T CLAS	55							
ORDN. 1 MARKS :	. 0_, 10								
S8054368 KAVITKE INDRAJEET ARVIND				SUREKH	IΛ	, 70925471н , S8054368 , РІСТ	c	805436	Q
30034300 KAVIIKE INDKAJEEL AKVIND				JUNEKH	iA	, 70923471h , 30034300 , FICT	, 3	003430	O
01. DISCRETE STRUCTURES	PP	100	40	57 P	С	11. ENGINEERING MATHEMATICS III PP 10	00 40	20	F
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	40 P	С	12. MICROPROC. & INTERFACING TECHNIQ.PP 10	00 40	46	P C
03. DIGIT. ELECTRONICS & LOGIC DESIGNATION OF THE PROPERTY OF	GNPP	100	40	56 P	С	13. DATA STRUCTURES PP 10	00 40	52	P C
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	51 P	С	14. COMPUTER GRAPHICS PP 10	00 40	64	PC
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	66 P	С	15. COMPUTER ORGANIZATION PP 10	00 40	44	P C
06. PROGRAMMING LABORATORY	TW	25	10	16 P	С	16. O. O. PROG. & COMP. GRAPH. LAB TW	50 20	36	P C
07. PROGRAMMING LABORATORY	PR	50	20	39 P	С	17. O. O. PROG. & COMP. GRAPH. LAB PR	50 20	44	P C
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	21 P	С	18. MICROPROCESSORS & INTERFACING LABTW	50 20	38	P C
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	34 P	С	19. MICROPROCESSORS & INTERFACING LABPR	50 20	35	P C
10. SOFT SKILLS	TW	50	20	35 P	С	20. DATA STRUCTURES LABORATORY TW	50 20	36	P C
						21. DATA STRUCTURES LABORATORY PR	50 20	36	PC
GRAND TOTAL = 866/1500, RESULT: FAILS	5 A.T	.K.T.							
ORDN. 1 MARKS :									
S8054370 KHAIRNAR PANKAJ VASUDEO				PATNAM	ΙΔΙ Δ	, 70715931J , S8054370 , PICT	ς	805437	0
2000 1010 1011/2101010 17100000				. , (1 14/ 31*1		, , ,	, ,	200101	-

PP 100 40 AA F

01. DISCRETE STRUCTURES

Page 166

PP 100 40 55 P

11. ENGINEERING MATHEMATICS III

02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	AA	F	12.	cols05 MICROPROC. & INTERFACING TECHNI	Q.PP	100	40	40	РС
03. DIGIT. ELECTRONICS & LOGIC DESIGNATION	GNPP	100	40	AA	F	13.	DATA STRUCTURES	PP	100	40	53	РС
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	AA	F	14.	COMPUTER GRAPHICS	PP	100	40	48	РС
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	AA	F	15.	COMPUTER ORGANIZATION	PP	100	40	56	РС
06. PROGRAMMING LABORATORY	TW	25	10	AA	F	16.	O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	23	РС
07. PROGRAMMING LABORATORY	PR	50	20	AA	F	17.	O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	37	РС
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	AA	F	18.	MICROPROCESSORS & INTERFACING L	ABTW	50	20	25	P C
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	AA	F	19.	MICROPROCESSORS & INTERFACING L	ABPR	50	20	AA	F
10. SOFT SKILLS	TW	50	20	AA	F	20.	DATA STRUCTURES LABORATORY	TW	50	20	20	P C
						21.	DATA STRUCTURES LABORATORY	PR	50	20	AA	F
GRAND TOTAL = 357/1500, RESULT: FAIL ORDN. 1 MARKS :	5							RESI	ULT RE	SERVEI	O FOR	OTHR
	RSITY	OF PU	 NE ,S	(2008 PAT.)((COMPU						
DATE : 18 AUG. 2011	CENT	RE : F	PUNE :	INSTI	TTUTE OF CO	MPUTER	TECHNOLOGY, PUNE.	PAG	GE NO.	52	(4	16)
NOTE: FIRST LINE : SEAT NO., NAME	OF THE	CAND	DATE	, MC	OTHER, PERM	IANENT	REG. NO., PREVIOUS SEAT NO.,	COLLE	GE,	SEAT !	١0.	
OTHER LINES: HEAD OF PASSING,	MAX.											
		MARKS	5, M	IN. F	PASS MARKS,	MARK	S OBTAINED, P/F:PASS/FAIL, C:	PREVI	OUS CA	RRY O	/ER	
		MARKS	5, MI	IN. F	PASS MARKS,	MARK	S OBTAINED, P/F:PASS/FAIL, C:	PREVIO	OUS CA	RRY O	/ER 	
S8054371 KHALID RAZA KHAN		MARKS	5, M:		PASS MARKS,	MARK	S OBTAINED, P/F:PASS/FAIL, C:	PREVIO			VER 05437	
S8054371 KHALID RAZA KHAN 01. DISCRETE STRUCTURES	 PP	MARKS	5, M: 40			MARK						
					ESHA	MARK						
01. DISCRETE STRUCTURES	PP	100	40	AYE	ESHA P C F	MARK						
01. DISCRETE STRUCTURES02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	AYE 40 AA AA	ESHA P C F	MARK						
01. DISCRETE STRUCTURES02. PROGRAMMING & PROBLEM SOLVING03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP PP GNPP	100 100 100	40 40 40	AYE 40 AA AA	PC F F PC	MARK						
01. DISCRETE STRUCTURES02. PROGRAMMING & PROBLEM SOLVING03. DIGIT. ELECTRONICS & LOGIC DESIGN04. DATA STRUCTURES AND ALGORITHMS	PP PP GNPP PP	100 100 100 100	40 40 40 40	40 AA AA 40 AA	PC F F PC	MARK						
01. DISCRETE STRUCTURES02. PROGRAMMING & PROBLEM SOLVING03. DIGIT. ELECTRONICS & LOGIC DESIGN04. DATA STRUCTURES AND ALGORITHMS05. HUMANITIES AND SOCIAL SCIENCE	PP PP GNPP PP	100 100 100 100 100	40 40 40 40 40	40 AA AA 40 AA	ESHA P C F P C F P C	MARK						
 01. DISCRETE STRUCTURES 02. PROGRAMMING & PROBLEM SOLVING 03. DIGIT. ELECTRONICS & LOGIC DESIGN 04. DATA STRUCTURES AND ALGORITHMS 05. HUMANITIES AND SOCIAL SCIENCE 06. PROGRAMMING LABORATORY 	PP PP GNPP PP PP	100 100 100 100 100 25	40 40 40 40 40 10	AYE 40 AA 40 AA 10 25	ESHA P C F P C F P C	MARK						
<pre>01. DISCRETE STRUCTURES 02. PROGRAMMING & PROBLEM SOLVING 03. DIGIT. ELECTRONICS & LOGIC DESIGN 04. DATA STRUCTURES AND ALGORITHMS 05. HUMANITIES AND SOCIAL SCIENCE 06. PROGRAMMING LABORATORY 07. PROGRAMMING LABORATORY</pre>	PP PP PP TW PR	100 100 100 100 100 25 50	40 40 40 40 40 10 20	AYE 40 AA 40 AA 10 25	F C F C P C	MARK						
 01. DISCRETE STRUCTURES 02. PROGRAMMING & PROBLEM SOLVING 03. DIGIT. ELECTRONICS & LOGIC DESIGNATION 04. DATA STRUCTURES AND ALGORITHMS 05. HUMANITIES AND SOCIAL SCIENCE 06. PROGRAMMING LABORATORY 07. PROGRAMMING LABORATORY 08. DIGITAL ELECTRONICS LABORATORY 	PP PP PP TW PR TW	100 100 100 100 100 25 50 25	40 40 40 40 40 10 20	AYE 40 AA 40 AA 10 25 10 23	F C F C P C	MARK						

FIRST TERM TOTAL = 168/700.

ORDN. 1 MARKS:

S8054372 KHUPASE SANJANA UDAYRAO ALKA , 71072131F , S8054372 , PICT , S8054372

01. DISCRETE STRUCTURES	PP	100	40	40	P C	11. ENGINEERING MATHEMATICS III PP 100 40 40 P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	49	P C	12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 54 P C
03. DIGIT. ELECTRONICS & LOGIC DESIGNATION OF THE PROPERTY OF	GNPP	100	40	48	P C	13. DATA STRUCTURES PP 100 40 55 P C
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	62	P C	14. COMPUTER GRAPHICS PP 100 40 62 P C
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	58	P C	15. COMPUTER ORGANIZATION PP 100 40 44 P C
06. PROGRAMMING LABORATORY	TW	25	10	20	P C	16. O. O. PROG. & COMP. GRAPH. LAB TW 50 20 37 P C
07. PROGRAMMING LABORATORY	PR	50	20	33	P C	17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 38 P C
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	21	P C	18. MICROPROCESSORS & INTERFACING LABTW 50 20 40 P C
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	35	P C	19. MICROPROCESSORS & INTERFACING LABPR 50 20 30 P C
10. SOFT SKILLS	TW	50	20	38	P C	20. DATA STRUCTURES LABORATORY TW 50 20 39 P C
						21. DATA STRUCTURES LABORATORY PR 50 20 28 P C

GRAND TOTAL = 871/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS:

S8054374	KONDHAWALE PRASHANT MHATA	RABA			MANO	GAL		, 70925487D , S805437	4 ,	PICT		, s80	5437	4
01. DISCRE	TE STRUCTURES	PP	100	40	40	P C	11.	ENGINEERING MATHEMATICS III		PP	100	40	43	P C
02. PROGRA	MMING & PROBLEM SOLVING	PP	100	40	42	P C	12.	MICROPROC. & INTERFACING TE	CHNIQ	.PP	100	40	40	P C
03. DIGIT.	ELECTRONICS & LOGIC DESIG	NPP	100	40	40	P C	13.	DATA STRUCTURES		PP	100	40	40	P C
04. DATA S	TRUCTURES AND ALGORITHMS	PP	100	40	40	Р	14.	COMPUTER GRAPHICS		PP	100	40	40	P C
05. HUMANI	TIES AND SOCIAL SCIENCE	PP	100	40	54	P C	15.	COMPUTER ORGANIZATION		PP	100	40	46	P C
06. PROGRA	MMING LABORATORY	TW	25	10	19	P C	16.	O. O. PROG. & COMP. GRAPH.	LAB	TW	50	20	32	P C

						20.5								
07. PROGRAMMING LABORATORY	PR	50	20	26	P C	cols05 17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	32	P C			
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	19	P C	18. MICROPROCESSORS & INTERFACING L	ABTW	50	20	31	P C			
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	25	P C	19. MICROPROCESSORS & INTERFACING L	ABPR	50	20	38	Р			
10. SOFT SKILLS	TW	50	20	40	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	24	РС			
						21. DATA STRUCTURES LABORATORY	PR	50	20	34	Р			
GRAND TOTAL = 745+05/1500, RESULT: SE	COND	CLASS	Γo. 2	27										
ORDN. 1 MARKS :			-	-										
	RSITY	OF PU	NE ,S	.E.(2	(008 PAT.)	(COMPUTER) EXAMINATION MAY 2011								
DATE : 18 AUG. 2011	CENT	RE : F	PUNE]	INSTI	TUTE OF C	OMPUTER TECHNOLOGY, PUNE.	PAG	GE NO.	53	(4:	17)			
NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.														
OTHER LINES: HEAD OF PASSING,	MAX.	MARKS	5, M3	IN. P	ASS MARKS	, MARKS OBTAINED, P/F:PASS/FAIL, C:	PREVI	DUS CAI	RRY O\	/ER				
S8054376 LATIFI AAMASH IMTIYAZ AHM	IED			SHA	KERA	, 70925492L , S8054376 ,	PIC	Γ	, s80)5437	6			
01. DISCRETE STRUCTURES	PP	100	40	40	P C	11. ENGINEERING MATHEMATICS III		100	40	40				
02. PROGRAMMING & PROBLEM SOLVING	DD					TIT ENGINEERING THATTER THE TITLE	PP	100	40		PC			
	PP	100	40	43	P C	12. MICROPROC. & INTERFACING TECHNI		100	40	24				
03. DIGIT. ELECTRONICS & LOGIC DESIG		100 100	40 40	43 50	P C P C									
03. DIGIT. ELECTRONICS & LOGIC DESIGNATION OF THE PROPERTY OF						12. MICROPROC. & INTERFACING TECHNI	[Q.PP	100	40	24	F			
	INPP	100	40	50	P C	12. MICROPROC. & INTERFACING TECHNI	Q.PP PP	100 100	40 40	24 48	F P C			
04. DATA STRUCTURES AND ALGORITHMS	NPP PP	100 100	40 40	50 40 46	P C	12. MICROPROC. & INTERFACING TECHNI13. DATA STRUCTURES14. COMPUTER GRAPHICS	EQ.PP PP PP	100 100 100	40 40 40	24 48 54 44	F P C P C			
04. DATA STRUCTURES AND ALGORITHMS05. HUMANITIES AND SOCIAL SCIENCE	NPP PP PP	100 100 100	40 40 40	50 40 46 12	P C P C	12. MICROPROC. & INTERFACING TECHNI13. DATA STRUCTURES14. COMPUTER GRAPHICS15. COMPUTER ORGANIZATION	PPPPPTW	100 100 100 100	40 40 40 40	24 48 54 44 26	F P C P C			
04. DATA STRUCTURES AND ALGORITHMS05. HUMANITIES AND SOCIAL SCIENCE06. PROGRAMMING LABORATORY	NPP PP TW	100 100 100 25	40 40 40 10	50 40 46 12 35	P C P C P C	12. MICROPROC. & INTERFACING TECHNORS. 13. DATA STRUCTURES 14. COMPUTER GRAPHICS 15. COMPUTER ORGANIZATION 16. O. O. PROG. & COMP. GRAPH. LAB	PP PP TW PR	100 100 100 100 50	40 40 40 40 20	244854442635	F C P C			
04. DATA STRUCTURES AND ALGORITHMS05. HUMANITIES AND SOCIAL SCIENCE06. PROGRAMMING LABORATORY07. PROGRAMMING LABORATORY	PP PP TW PR	100 100 100 25 50	40 40 40 10 20	50 40 46 12 35 10	P C P C P C P C	12. MICROPROC. & INTERFACING TECHNORS. 13. DATA STRUCTURES 14. COMPUTER GRAPHICS 15. COMPUTER ORGANIZATION 16. O. O. PROG. & COMP. GRAPH. LAB 17. O. O. PROG. & COMP. GRAPH. LAB	PP PP TW PR	100 100 100 100 50 50	40 40 40 40 20 20	244854442635	F C P C C P C			
04. DATA STRUCTURES AND ALGORITHMS05. HUMANITIES AND SOCIAL SCIENCE06. PROGRAMMING LABORATORY07. PROGRAMMING LABORATORY08. DIGITAL ELECTRONICS LABORATORY	PP PP TW PR TW	100 100 100 25 50 25	40 40 40 10 20	50 40 46 12 35 10 20	P C P C P C P C P C	12. MICROPROC. & INTERFACING TECHNOL 13. DATA STRUCTURES 14. COMPUTER GRAPHICS 15. COMPUTER ORGANIZATION 16. O. O. PROG. & COMP. GRAPH. LAB 17. O. O. PROG. & COMP. GRAPH. LAB 18. MICROPROCESSORS & INTERFACING L	PP PP TW PR	100 100 100 100 50 50	40 40 40 40 20 20 20	2448544426352328	F C P C C P C			
 04. DATA STRUCTURES AND ALGORITHMS 05. HUMANITIES AND SOCIAL SCIENCE 06. PROGRAMMING LABORATORY 07. PROGRAMMING LABORATORY 08. DIGITAL ELECTRONICS LABORATORY 09. DIGITAL ELECTRONICS LABORATORY 	PP PP TW PR TW PR	100 100 100 25 50 25 50	40 40 40 10 20 10 20	50 40 46 12 35 10 20	P C P C P C P C P C	12. MICROPROC. & INTERFACING TECHNOL 13. DATA STRUCTURES 14. COMPUTER GRAPHICS 15. COMPUTER ORGANIZATION 16. O. O. PROG. & COMP. GRAPH. LAB 17. O. O. PROG. & COMP. GRAPH. LAB 18. MICROPROCESSORS & INTERFACING L 19. MICROPROCESSORS & INTERFACING L	PP PP TW PR -ABTW	100 100 100 100 50 50 50	40 40 40 40 20 20 20 20	 24 48 54 44 26 35 23 28 22 	F C P C P C P C			

GRAND TOTAL = 702/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

cols	s05
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S8054377 MAHAJAN VAIBHAV ANUP				ART]	Ι		, 71072133в	, s8054377 ,	PICT		, s80	5437	7
01. DISCRETE STRUCTURES	PP	100	40	40	P C	11.	ENGINEERING MA	ATHEMATICS III	PP	100	40	40	Р
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	42	РС	12.	MICROPROC. & 3	INTERFACING TECHNIQ	.PP	100	40	40	P C
03. DIGIT. ELECTRONICS & LOGIC DESIG	INPP	100	40	65	P C	13.	DATA STRUCTURI	ES	PP	100	40	60	P C
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	48	P C	14.	COMPUTER GRAPH	HICS	PP	100	40	58	P C
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	57	P C	15.	COMPUTER ORGAN	NIZATION	PP	100	40	40	P C
06. PROGRAMMING LABORATORY	TW	25	10	19	P C	16.	O. O. PROG. &	COMP. GRAPH. LAB	TW	50	20	36	P C
07. PROGRAMMING LABORATORY	PR	50	20	45	РС	17.	O. O. PROG. &	COMP. GRAPH. LAB	PR	50	20	38	P C
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	19	P C	18.	MICROPROCESSOR	RS & INTERFACING LA	BTW	50	20	40	P C
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	25	P C	19.	MICROPROCESSOR	RS & INTERFACING LAI	BPR	50	20	20	P C
10. SOFT SKILLS	TW	50	20	40	РС	20.	DATA STRUCTURI	ES LABORATORY	TW	50	20	34	P C
						21.	DATA STRUCTURI	ES LABORATORY	PR	50	20	21	РС

GRAND TOTAL = 827/1500, RESULT: HIGHER SECOND CLASS
ORDN. 1 MARKS :

S8054378 MANE OM ARUN				RAJ	ANI	, 70925499н , s	8054378 ,	PICT		, s80	5437	8
01. DISCRETE STRUCTURES	PP	100	40	40	P C	11. ENGINEERING MATHEMATI	CS III	PP	100	40	56	Р
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	40	P C	12. MICROPROC. & INTERFAC	ING TECHNIQ	. PP	100	40	26	F
03. DIGIT. ELECTRONICS & LOGIC DESIGNATION	GNPP	100	40	43	P C	13. DATA STRUCTURES		PP	100	40	40	P C
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	40	P C	14. COMPUTER GRAPHICS		PP	100	40	40	P C
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	50	P C	15. COMPUTER ORGANIZATION		PP	100	40	40	P C
06. PROGRAMMING LABORATORY	TW	25	10	14	P C	16. O. O. PROG. & COMP. G	RAPH. LAB	TW	50	20	20	P C
07. PROGRAMMING LABORATORY	PR	50	20	39	P C	17. O. O. PROG. & COMP. G	RAPH. LAB	PR	50	20	30	Р
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	12	P C	18. MICROPROCESSORS & INT	ERFACING LA	ВТW	50	20	20	P C
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	20	P C	19. MICROPROCESSORS & INT	ERFACING LA	BPR	50	20	32	Р
10. SOFT SKILLS	TW	50	20	29	P C	20. DATA STRUCTURES LABOR	ATORY	TW	50	20	20	P C
						21. DATA STRUCTURES LABOR	ATORY	PR	50	20	22	P C

GRAND TOTAL = 673/1500, RESULT: FAILS A.T.K.T.
ORDN. 1 MARKS :

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			•	-				JTER) EXAMINATION MAY 2011	DAG	SE NO	E 4	(1	10)
DATE : 18 AUG. 2011								R TECHNOLOGY, PUNE.					-
								DEC. NO DREVIOUS SEAT NO.					
NOTE: FIRST LINE : SEAT NO., NAME										-			
OTHER LINES. HEAD OF PASSING,	MAX	· MAKK.), IVI.	IIV. F	'A33	MARKS,	MAK	KS OBTAINED, P/F:PASS/FAIL, C:	PKEVI	703 CA	KKI UV	CK	
	• •			• •						• •			
S8054379 NANDKAR NEHA VIJAY				DIF	PALI			, 70925523D , S8054379 ,	PICT	Γ	, s80)5437	9
01. DISCRETE STRUCTURES	PP	100	40	48	Р	С	11.	ENGINEERING MATHEMATICS III	PP	100	40	42	P C
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	40	Р	С	12.	MICROPROC. & INTERFACING TECHNI	[Q.PP	100	40	48	P C
03. DIGIT. ELECTRONICS & LOGIC DESI	GNPP	100	40	58	Р	С	13.	DATA STRUCTURES	PP	100	40	61	P C
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	52	Р	С	14.	COMPUTER GRAPHICS	PP	100	40	65	P C
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	62	Р	С	15.	COMPUTER ORGANIZATION	PP	100	40	41	P C
06. PROGRAMMING LABORATORY	TW	25	10	17	Р	С	16.	O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	29	P C
07. PROGRAMMING LABORATORY	PR	50	20	25	Р	С	17.	O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	29	P C
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	17	Р	С	18.	MICROPROCESSORS & INTERFACING L	_ABTW	50	20	38	P C
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	30	Р	С	19.	MICROPROCESSORS & INTERFACING L	_ABPR	50	20	AA	F
10. SOFT SKILLS	TW	50	20	39	Р	С	20.	DATA STRUCTURES LABORATORY	TW	50	20	24	P C
							21.	DATA STRUCTURES LABORATORY	PR	50	20	30	P C
GRAND TOTAL = 795/1500, RESULT: FAIL	S A.T	.K.T.											
ORDN. 1 MARKS :													
S8054380 PATIL HARSHAL PRAVINSING				SAN	NDHY.	A		, 70925546C , S8054380 ,	PICT	Г	, s80)5438	0
01. DISCRETE STRUCTURES	PP	100	40	52	Р	С	11.	ENGINEERING MATHEMATICS III	PP	100	40	40	P C
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	40	Р	С	12.	MICROPROC. & INTERFACING TECHNI	[Q.PP	100	40	40	P C

								cols05					
03. DIGIT. ELECTRONICS	& LOGIC DESIG	GNPP	100	40	54	P C	13.	DATA STRUCTURES	PP	100	40	52	P C
04. DATA STRUCTURES AN	ND ALGORITHMS	PP	100	40	53	P C	14.	COMPUTER GRAPHICS	PP	100	40	52	P C
05. HUMANITIES AND SO	CIAL SCIENCE	PP	100	40	42	P C	15.	COMPUTER ORGANIZATION	PP	100	40	40	P C
06. PROGRAMMING LABORA	ATORY	TW	25	10	13	P C	16.	O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	24	P C
07. PROGRAMMING LABORA	ATORY	PR	50	20	29	P C	17.	O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	39	P C
08. DIGITAL ELECTRONIC	CS LABORATORY	TW	25	10	18	P C	18.	MICROPROCESSORS & INTERFACING LA	ABTW	50	20	28	P C
09. DIGITAL ELECTRONIC	CS LABORATORY	PR	50	20	24	P C	19.	MICROPROCESSORS & INTERFACING LA	ABPR	50	20	30	Р
10. SOFT SKILLS		TW	50	20	38	P C	20.	DATA STRUCTURES LABORATORY	TW	50	20	25	P C
							21.	DATA STRUCTURES LABORATORY	PR	50	20	20	РС

GRAND TOTAL = 753/1500, RESULT: SECOND CLASS

ORDN. 1 MARKS:

S8054381 PATIL VINEET JAYANT				PRAJAKTA	, 70601438D , S8054381 , PICT , S8054381
01. DISCRETE STRUCTURES	PP	100	40	AA F	11. ENGINEERING MATHEMATICS III PP 100 40 27 F
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	AA F	12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 40 P C
03. DIGIT. ELECTRONICS & LOGIC DESIG	GNPP	100	40	AA F	13. DATA STRUCTURES PP 100 40 51 P C
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	AA F	14. COMPUTER GRAPHICS PP 100 40 59 P C
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	AA F	15. COMPUTER ORGANIZATION PP 100 40 48 P
06. PROGRAMMING LABORATORY	TW	25	10	AA F	16. O. O. PROG. & COMP. GRAPH. LAB TW 50 20 30 P C
07. PROGRAMMING LABORATORY	PR	50	20	AA F	17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 34 P C
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	AA F	18. MICROPROCESSORS & INTERFACING LABTW 50 20 30 P C
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	AA F	19. MICROPROCESSORS & INTERFACING LABPR 50 20 40 P C
10. SOFT SKILLS	TW	50	20	AA F	20. DATA STRUCTURES LABORATORY TW 50 20 29 P C
					21. DATA STRUCTURES LABORATORY PR 50 20 P C

GRAND TOTAL = 408/1500, RESULT: FAILS RESULT RESERVED FOR OTHR

ORDN. 1 MARKS:

UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(COMPUTER) EXAMINATION MAY 2011

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_			 				

DATE : 18 AUG. 2011	CENT	RE : P	UNE I	INSTIT	UTE OF CO	MPUTE	R TECHNOLOGY, PUNE.	PAG	E NO.	55	(419)	
NOTE: FIRST LINE : SEAT NO., NAME (OF THE	CANDI	DATE,	, MOT	HER, PERM	IANENT	REG. NO., PREVIOUS SEAT NO., C	OLLEG	Ε,	SEAT N	10.	
OTHER LINES: HEAD OF PASSING,	MAX.	MARKS	, MI	IN. PA	SS MARKS,	MAR	KS OBTAINED, P/F:PASS/FAIL, C:P	REVIO	US CAI	RRY OV	'ER	
S8054382 RATHOD NIKHIL PRAVEEN				ASHA			, 70925576E , S8054382 ,	PICT		, s80	54382	
01. DISCRETE STRUCTURES	PP	100	40	78	P C	11.	ENGINEERING MATHEMATICS III	PP	100	40	43 P C	
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	40	P C	12.	MICROPROC. & INTERFACING TECHNIQ	.PP	100	40	40 P C	
03. DIGIT. ELECTRONICS & LOGIC DESIGNATION	GNPP	100	40	52	P C	13.	DATA STRUCTURES	PP	100	40	51 P C	
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	40	P C	14.	COMPUTER GRAPHICS	PP	100	40	43 P C	
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	62	P C	15.	COMPUTER ORGANIZATION	PP	100	40	40 P C	
06. PROGRAMMING LABORATORY	TW	25	10	15	P C	16.	O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	38 P C	
07. PROGRAMMING LABORATORY	PR	50	20	25	P C	17.	O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	35 P C	
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	21	P C	18.	MICROPROCESSORS & INTERFACING LA	BTW	50	20	35 P C	
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	27	P C	19.	MICROPROCESSORS & INTERFACING LA	BPR	50	20	36 P C	
10. SOFT SKILLS	TW	50	20	36	P C	20.	DATA STRUCTURES LABORATORY	TW	50	20	37 P C	
						21.	DATA STRUCTURES LABORATORY	PR	50	20	32 P	
GRAND TOTAL = 826/1500, RESULT: HIGH	ED SEC	טאט כו	۸۶۶									
ORDN. 1 MARKS :	EK SEC	OND CL	.A33									
ORDN. I MARKS .												
S8054383 ROKADE VISHALSAGAR DEVIDA	AS			VAND	ANA		, 70701610L , S8054383 ,	PICT		, s80	54383	
01. DISCRETE STRUCTURES	PP	100	40	AA	F	11.	ENGINEERING MATHEMATICS III	PP	100	40	51 P C	
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	AA	F	12.	MICROPROC. & INTERFACING TECHNIQ	.PP	100	40	53 P C	
03. DIGIT. ELECTRONICS & LOGIC DESIGNATION OF THE PROPERTY OF	GNPP	100	40	AA	F	13.	DATA STRUCTURES	PP	100	40	52 P C	
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	AA	F	14.	COMPUTER GRAPHICS	PP	100	40	51 P C	
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	AA	F	15.	COMPUTER ORGANIZATION	PP	100	40	45 P C	
06. PROGRAMMING LABORATORY	TW	25	10	AA	F	16.	O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	23 P C	
07. PROGRAMMING LABORATORY	PR	50	20	AA	F	17.	O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	20 P	

						cols05
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	AA	F	18. MICROPROCESSORS & INTERFACING LABTW 50 20 28 P C
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	AA	F	19. MICROPROCESSORS & INTERFACING LABPR 50 20 30 P C
10. SOFT SKILLS	TW	50	20	AA	F	20. DATA STRUCTURES LABORATORY TW 50 20 22 P C
						21. DATA STRUCTURES LABORATORY PR 50 20 30 P
GRAND TOTAL = 405/1500, RESULT: FAIL	S					RESULT RESERVED FOR OTHE
ORDN. 1 MARKS :						
S8054384 SARNAIK ABHISHEK MANOJ				KAV	'ITA	, 70925592G , S8054384 , PICT , S8054384
01. DISCRETE STRUCTURES	PP	100	40	52	P C	11. ENGINEERING MATHEMATICS III PP 100 40 26 F
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	40	РC	12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 32 F
03. DIGIT. ELECTRONICS & LOGIC DESI	GNPP	100	40	40	P C	13. DATA STRUCTURES PP 100 40 40 P C
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	41	P C	14. COMPUTER GRAPHICS PP 100 40 41 P C
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	48	P C	15. COMPUTER ORGANIZATION PP 100 40 40 P C
06. PROGRAMMING LABORATORY	TW	25	10	17	P C	16. O. O. PROG. & COMP. GRAPH. LAB TW 50 20 26 P C
07. PROGRAMMING LABORATORY	PR	50	20	26	P C	17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 43 P C
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	18	P C	18. MICROPROCESSORS & INTERFACING LABTW 50 20 28 P C
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	30	PC	19. MICROPROCESSORS & INTERFACING LABPR 50 20 22 P C
10. SOFT SKILLS	TW	50	20	34	P C	20. DATA STRUCTURES LABORATORY TW 50 20 25 P C
						21. DATA STRUCTURES LABORATORY PR 50 20 28 P C
GRAND TOTAL = 697/1500, RESULT: FAIL	S A.T	.K.T.				
ORDN. 1 MARKS :						
	 ERSITY	 OF PL	 JNE .S	 S.E.(2	 2008 PAT	
DATE : 18 AUG. 2011			•	-		COMPUTER TECHNOLOGY, PUNE. PAGE NO. 56 (420)
NOTE: FIRST LINE : SEAT NO., NAME	OF TH	E CAND	IDATE	, MC	THER, P	ERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
						(S, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8054385	SAURAV JAUHARI				POON	NAM		, 70925593E	, s8054385 ,	PICT		, S80	5438	5
01. DISCRET	E STRUCTURES	PP	100	40	AA	F	11.	ENGINEERING MATHE	EMATICS III	PP	100	40	AA	F
02. PROGRAM	MING & PROBLEM SOLVING	PP	100	40	AA	F	12.	MICROPROC. & INTE	ERFACING TECHNIQ	.PP	100	40	AA	F
03. DIGIT.	ELECTRONICS & LOGIC DESIG	NPP	100	40	AA	F	13.	DATA STRUCTURES		PP	100	40	AA	F
04. DATA ST	RUCTURES AND ALGORITHMS	PP	100	40	AA	F	14.	COMPUTER GRAPHICS	5	PP	100	40	AA	F
05. HUMANIT	IES AND SOCIAL SCIENCE	PP	100	40	AA	F	15.	COMPUTER ORGANIZA	ATION	PP	100	40	AA	F
06. PROGRAM	MING LABORATORY	TW	25	10	13	P C	16.	O. O. PROG. & COM	MP. GRAPH. LAB	TW	50	20	20	P C
07. PROGRAM	MING LABORATORY	PR	50	20	05	F	17.	O. O. PROG. & COM	MP. GRAPH. LAB	PR	50	20	20	Р
08. DIGITAL	ELECTRONICS LABORATORY	TW	25	10	10	P C	18.	MICROPROCESSORS &	& INTERFACING LA	BTW	50	20	20	P C
09. DIGITAL	ELECTRONICS LABORATORY	PR	50	20	AA	F	19.	MICROPROCESSORS &	& INTERFACING LA	BPR	50	20	00	F
10. SOFT SK	ILLS	TW	50	20	26	P C	20.	DATA STRUCTURES I	_ABORATORY	TW	50	20	20	P C
							21.	DATA STRUCTURES I	_ABORATORY	PR	50	20	00	F

GRAND TOTAL = 134/1500, RESULT: FAILS
ORDN. 1 MARKS :

S8054386 SHANTANU SINGHAL SHIKHA				SHII	КНА		, 70504046в	, s8054386 ,	PIC	Г	, s80	05438	6
01. DISCRETE STRUCTURES	PP	100	40	AA	F	11.	ENGINEERING MATH	EMATICS III	PP	100	40	40	Р
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	AA	F	12.	MICROPROC. & INT	ERFACING TECHNIC	Q.PP	100	40	40	РС
03. DIGIT. ELECTRONICS & LOGIC DESIGNATION	GNPP	100	40	AA	F	13.	DATA STRUCTURES		PP	100	40	51	P C
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	AA	F	14.	COMPUTER GRAPHIC	5	PP	100	40	48	P C
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	AA	F	15.	COMPUTER ORGANIZA	ATION	PP	100	40	40	P C
06. PROGRAMMING LABORATORY	TW	25	10	AA	F	16.	O. O. PROG. & CO	MP. GRAPH. LAB	TW	50	20	20	P C
07. PROGRAMMING LABORATORY	PR	50	20	AA	F	17.	O. O. PROG. & CO	MP. GRAPH. LAB	PR	50	20	36	P C
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	AA	F	18.	MICROPROCESSORS (& INTERFACING LA	ABTW	50	20	20	P C
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	AA	F	19.	MICROPROCESSORS (& INTERFACING LA	ABPR	50	20	30	P C
10. SOFT SKILLS	TW	50	20	AA	F	20.	DATA STRUCTURES	_ABORATORY	TW	50	20	20	P C
						21.	DATA STRUCTURES	_ABORATORY	PR	50	20	36	P C

GRAND TOTAL = 381/1500, RESULT: FAILS

RESULT RESERVED FOR OTHR

ORDN. 1 MARKS:

S8054387 SHASHANK KUMAR GUPTA	S8054387 SHASHANK KUMAR GUPTA						, 7092559	98F , S805	4387 ,	PICT		, s80	5438	7
01. DISCRETE STRUCTURES	PP	100	40	47	P C	11	ENGINEERIN	G MATHEMATICS	III	PP	100	40	42	P C
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	40	P C	12	MICROPROC.	& INTERFACING	TECHNIQ	.PP	100	40	AA	F
03. DIGIT. ELECTRONICS & LOGIC DESIGNATION OF THE PROPERTY OF	GNPP	100	40	AA	F	13	DATA STRUC	TURES		PP	100	40	47	Р
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	40	P C	14	COMPUTER GI	RAPHICS		PP	100	40	48	P C
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	62	P C	15	COMPUTER O	RGANIZATION		PP	100	40	49	Р
06. PROGRAMMING LABORATORY	TW	25	10	12	P C	16	O. O. PROG	. & COMP. GRAP	H. LAB	TW	50	20	23	РС
07. PROGRAMMING LABORATORY	PR	50	20	32	P C	17	O. O. PROG	. & COMP. GRAP	H. LAB	PR	50	20	35	Р
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	10	P C	18	MICROPROCE:	SSORS & INTERF	ACING LA	BTW	50	20	20	P C
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	22	P C	19	MICROPROCES	SSORS & INTERF	ACING LA	BPR	50	20	34	P C
10. SOFT SKILLS	TW	50	20	21	P C	20	DATA STRUC	TURES LABORATO	RY	TW	50	20	20	P C
						21	DATA STRUC	TURES LABORATO	RY	PR	50	20	09	F
GRAND TOTAL = 613/1500, RESULT: FAILS ORDN. 1 MARKS :	S A.T.	K.T.												
	 RSITY	OF PU	 NE ,S	 .E.(2	 2008 F	 PAT.)(COMP	 UTER) EXAMIN							
DATE : 18 AUG. 2011	CENT	TRE : F	PUNE :	INSTI	TUTE	OF COMPUTE	R TECHNOLOG	Y, PUNE.		PAG	E NO.	57	(4	21)
NOTE: FIRST LINE : SEAT NO., NAME (OF THE	E CAND	IDATE	, MO	THER,	PERMANEN	REG. NO.,	PREVIOUS SEAT	NO., C	OLLEG	Ε, 9	SEAT N	Ю.	
OTHER LINES: HEAD OF PASSING,	MAX.	MARKS	5, M	IN. P	PASS M	ARKS, MAI	KS OBTAINED	, P/F:PASS/FA	IL, C:P	REVIO	US CAI	RRY OV	'ER	
S8054388 SHERKAR ANIRUDHA UMAKANT				SMI	TA		, 7092560	О1K , S805	4388 ,	PICT		, s80	5438	8
01. DISCRETE STRUCTURES	PP	100	40	40	P C	11	ENGINEERIN	G MATHEMATICS	III	PP	100	40	19	F
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	40	P C	12	MICROPROC.	& INTERFACING	TECHNIQ	.PP	100	40	26	F
03. DIGIT. ELECTRONICS & LOGIC DESIG	GNPP	100	40	49	P C	13	DATA STRUC	TURES		PP	100	40	40	P C

						cols05	
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	40	P C	14. COMPUTER GRAPHICS PP 100 40 40 P	, C
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	50	P C	15. COMPUTER ORGANIZATION PP 100 40 26 F	:
06. PROGRAMMING LABORATORY	TW	25	10	16	P C	16. O. O. PROG. & COMP. GRAPH. LAB TW 50 20 39 P	C
07. PROGRAMMING LABORATORY	PR	50	20	41	P C	17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 36 P	, C
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	18	P C	18. MICROPROCESSORS & INTERFACING LABTW 50 20 39 P	, C
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	27	P C	19. MICROPROCESSORS & INTERFACING LABPR 50 20 20 P	, C
10. SOFT SKILLS	TW	50	20	38	P C	20. DATA STRUCTURES LABORATORY TW 50 20 36 P	, C
						21. DATA STRUCTURES LABORATORY PR 50 20 30 P	, C
7 40 (4 7 00							

GRAND TOTAL = 710/1500, RESULT: FAILS A.T.K.T.
ORDN. 1 MARKS :

S8054394	THAKARE SAGAR SATISH				JAYA	ASHRI		, 70925632к	, s8054394 ,	PICT		, s80	54394	4
01. DISCRE	TE STRUCTURES	PP	100	40	40	P C	11.	ENGINEERING MATHE	EMATICS III	PP	100	40	20	F
02. PROGRA	MMING & PROBLEM SOLVING	PP	100	40	40	P C	12.	MICROPROC. & INTE	ERFACING TECHNIQ	.PP	100	40	40	P C
03. DIGIT.	ELECTRONICS & LOGIC DESIG	NPP	100	40	26	F	13.	DATA STRUCTURES		PP	100	40	40	P C
04. DATA S	TRUCTURES AND ALGORITHMS	PP	100	40	40	P C	14.	COMPUTER GRAPHICS	5	PP	100	40	40	P C
05. HUMANI	TIES AND SOCIAL SCIENCE	PP	100	40	48	P C	15.	COMPUTER ORGANIZA	ATION	PP	100	40	40	Р
06. PROGRAM	MMING LABORATORY	TW	25	10	17	P C	16.	O. O. PROG. & COM	MP. GRAPH. LAB	TW	50	20	32	P C
07. PROGRA	MMING LABORATORY	PR	50	20	20	P C	17.	O. O. PROG. & COM	MP. GRAPH. LAB	PR	50	20	28	P C
08. DIGITA	L ELECTRONICS LABORATORY	TW	25	10	19	P C	18.	MICROPROCESSORS &	WINTERFACING LA	BTW	50	20	33	P C
09. DIGITA	L ELECTRONICS LABORATORY	PR	50	20	21	P C	19.	MICROPROCESSORS &	WINTERFACING LA	BPR	50	20	25	P C
10. SOFT SI	KILLS	TW	50	20	25	P C	20.	DATA STRUCTURES L	_ABORATORY	TW	50	20	28	P C
							21.	DATA STRUCTURES L	_ABORATORY	PR	50	20	04	F

GRAND TOTAL = 626/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

\$8054395 UIKEY ANURAG VINAYAK JYOTI , 70925640L , \$8054395 , PICT , \$8054395

01. DISCRETE STRUCTURES PP 100 40 40 P C 11. ENGINEERING MATHEMATICS III PP 100 40 AA F

UI. DISCRETE STRUCTURES		100									
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	AA	F	12. MICROPROC. & INTERFACING TECHN	NIQ.PP	100	40	AA	F
03. DIGIT. ELECTRONICS & LOGIC DESIGNATION	GNPP	100	40	47	P C	13. DATA STRUCTURES	PP	100	40	AA	F
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	40	P C	14. COMPUTER GRAPHICS	PP	100	40	AA	F
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	62	P C	15. COMPUTER ORGANIZATION	PP	100	40	AA	F
06. PROGRAMMING LABORATORY	TW	25	10	15	P C	16. O. O. PROG. & COMP. GRAPH. LAE	3 TW	50	20	24	Р
07. PROGRAMMING LABORATORY	PR	50	20	15	F	17. O. O. PROG. & COMP. GRAPH. LAE	3 PR	50	20	06	F
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	11	P C	18. MICROPROCESSORS & INTERFACING	LABTW	50	20	30	Р
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	24	Р	19. MICROPROCESSORS & INTERFACING	LABPR	50	20	30	Р
10. SOFT SKILLS	TW	50	20	38	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	20	Р
						21. DATA STRUCTURES LABORATORY	PR	50	20	10	F
RAND TOTAL = 412/1500, RESULT: FAIL:	S					RESI	JLT RESI	ERVED	FOR BK	(LG +	OTHR
ORDN. 1 MARKS :	_					KESC	KLJI		. 51. 51	. _ 0	O I I III
			-			COMPUTER) EXAMINATION MAY 2011					
DATE : 18 AUG. 2011			-			COMPUTER) EXAMINATION MAY 2011 PUTER TECHNOLOGY, PUNE.					
DATE : 18 AUG. 2011	CENT	ΓRE : F	PUNE 1	INSTI	TUTE OF COM	COMPUTER) EXAMINATION MAY 2011 IPUTER TECHNOLOGY, PUNE.	PA(GE NO.	58	(4 	22)
DATE: 18 AUG. 2011	CENT · · ·	ΓRE : F	PUNE]	INSTI	TUTE OF COM	COMPUTER) EXAMINATION MAY 2011 IPUTER TECHNOLOGY, PUNE.	PAG 	GE NO. GE,	58 SEAT N	(4 	22)
DATE: 18 AUG. 2011	CENT · · ·	ΓRE : F	PUNE]	INSTI	TUTE OF COM	COMPUTER) EXAMINATION MAY 2011 IPUTER TECHNOLOGY, PUNE.	PAG 	GE NO. GE,	58 SEAT N	(4 	22)
DATE: 18 AUG. 2011	CENT OF THE MAX.	TRE : F	PUNE 3	ENSTI , MO	TUTE OF COM THER, PERMA	COMPUTER) EXAMINATION MAY 2011 IPUTER TECHNOLOGY, PUNE.	PAG COLLEG	GE NO. GE, OUS CA	58 · · · SEAT N RRY ON	(4 NO. /ER	22)
DATE: 18 AUG. 2011	CENT OF THE MAX.	TRE : F	PUNE 3	INSTI , MO IN. P	TUTE OF COM THER, PERMA	COMPUTER) EXAMINATION MAY 2011 IPUTER TECHNOLOGY, PUNE.	PAG COLLEG	GE NO. GE, DUS CA	58 · · · SEAT N RRY ON	(4 NO. /ER 	22)
DATE: 18 AUG. 2011	CENT OF THE MAX.	TRE : F	PUNE 3	INSTI , MO IN. P	TUTE OF COM THER, PERMA ASS MARKS,	COMPUTER) EXAMINATION MAY 2011 IPUTER TECHNOLOGY, PUNE.	PAG COLLEG	GE NO. GE, DUS CA	58 SEAT N RRY ON	(4 NO. /ER 	22)
DATE: 18 AUG. 2011	CENT OF THE MAX.	TRE : F	PUNE 1	INSTI , MO IN. P	TUTE OF COM THER, PERMA ASS MARKS, ASHRI P C	COMPUTER) EXAMINATION MAY 2011 IPUTER TECHNOLOGY, PUNE.	PAG COLLEG C:PREVIG 	GE NO. GE, OUS CA	58 SEAT N RRY ON	(4 NO. /ER 	22) 7
DATE: 18 AUG. 2011	CENT OF THE MAX	TRE : F	PUNE 1 IDATE, S, M1	INSTI , MO IN. P RAJ 51 40	TUTE OF COM THER, PERMA ASS MARKS, ASHRI P C	COMPUTER) EXAMINATION MAY 2011 IPUTER TECHNOLOGY, PUNE.	PAG COLLEG C:PREVIG 	GE NO. GE, DUS CA	58 SEAT N RRY ON , S80	(4 NO . /ER D5439 AA AA	22) 7
DATE: 18 AUG. 2011	CENT OF THE MAX	TRE : F	PUNE 1 IDATE, 40 40	INSTI MO IN . P RAJ 51 40 48	TUTE OF COM THER, PERMA ASS MARKS, ASHRI P C P C	COMPUTER) EXAMINATION MAY 2011 IPUTER TECHNOLOGY, PUNE.	PAG COLLEG C:PREVIG 	GE NO. GE, DUS CA T 100 100	58 SEAT N RRY ON , S80 40 40	(4 /ER 05439 AA AA 41	22) 7 F
DATE: 18 AUG. 2011	CENT OF THE MAX. PP PP GNPP	TRE : F	PUNE 1 IDATE, 40 40 40 40	INSTI MO IN . P RAJ 51 40 48 40	TUTE OF COM THER, PERMA ASS MARKS, ASHRI P C P C P C	COMPUTER) EXAMINATION MAY 2011 IPUTER TECHNOLOGY, PUNE.	PAG COLLEG C:PREVIG 	GE NO. GE, DUS CA T 100 100	58 SEAT N RRY ON , \$80 40 40 40	(4 /ER 05439 AA AA 41	22) 7 F P C P C
DATE: 18 AUG. 2011	CENT CENT MAX PP PP GNPP PP	100 100 100 100	PUNE 3 IDATE, 40 40 40 40 40	INSTI MO IN . P RAJ 51 40 48 40 55	TUTE OF COM THER, PERMA ASS MARKS, ASHRI P C P C P C P C	COMPUTER) EXAMINATION MAY 2011 IPUTER TECHNOLOGY, PUNE.	PAG COLLEG C: PREVIO PICT PP PP PP	GE NO. GE, DUS CA T 100 100 100	58 SEAT N RRY ON , S80 40 40 40 40 40	(4 NO. /ER 05439 AA AA 41 46 AA	22) 7 F P C P C
DATE: 18 AUG. 2011	CENT OF THE MAX PP PP GNPP PP	100 100 100 100 100	PUNE 1 IDATE, 40 40 40 40 40 40	INSTI MO IN . P RAJ 51 40 48 40 55 16	TUTE OF COM THER, PERMA ASS MARKS, ASHRI P C P C P C P C P C	COMPUTER) EXAMINATION MAY 2011 IPUTER TECHNOLOGY, PUNE.	PAG COLLEGE PREVIO PP PP PP PP PP PP TW	GE NO. GE, DUS CA T 100 100 100 100 100	58 SEAT N RRY ON , S80 40 40 40 40 40 40	(4 NO. /ER 05439 AA AA 41 46 AA 34	22) 7 F P C P C F
DATE: 18 AUG. 2011	CENT CENT MAX PP PP PP PP TW	100 100 100 100 25	PUNE 1 IDATE, 5, M 40 40 40 40 40 10	INSTI MO IN . P RAJ 51 40 48 40 55 16 24	TUTE OF COM THER, PERMA ASS MARKS, ASHRI P C P C P C P C P C P C P C	COMPUTER) EXAMINATION MAY 2011 IPUTER TECHNOLOGY, PUNE.	PAGE COLLEGE C: PREVIO PP	GE NO. GE, OUS CA T 100 100 100 100 50	58 SEAT N RRY ON , S80 40 40 40 40 40 40 20	(4	22) 7 F P C P C F P C

09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	31	P C	19.	MICROPROCESSORS	cols05 & INTERFACING	LABPR	50	20	AA	F
10. SOFT SKILLS	TW	50	20	40	P C	20.	DATA STRUCTURES	LABORATORY	TW	50	20	20	P C
						21.	DATA STRUCTURES	LABORATORY	PR	50	20	AA	F

GRAND TOTAL = 542/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS:

S8054399 SANDEEP AGARWAL				RIT	'A	, 70925587L , S8054315 , PICT , S8054399	
01. DISCRETE STRUCTURES	PP	100	40	59	P C	11. ENGINEERING MATHEMATICS III PP 100 40 53 F	РС
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	40	P C	12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 12	F
03. DIGIT. ELECTRONICS & LOGIC DESIGN	NPP	100	40	47	P C	13. DATA STRUCTURES PP 100 40 43 F	РС
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	40	P C	14. COMPUTER GRAPHICS PP 100 40 01 F	F
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	52	P C	15. COMPUTER ORGANIZATION PP 100 40 15 F	F
06. PROGRAMMING LABORATORY	TW	25	10	20	P C	16. O. O. PROG. & COMP. GRAPH. LAB TW 50 20 30 F	РС
07. PROGRAMMING LABORATORY	PR	50	20	30	P C	17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 24 F	РС
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	17	P C	18. MICROPROCESSORS & INTERFACING LABTW 50 20 32 F	РС
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	25	P C	19. MICROPROCESSORS & INTERFACING LABPR 50 20 20 F	Р
10. SOFT SKILLS	TW	50	20	40	P C	20. DATA STRUCTURES LABORATORY TW 50 20 22 F	РС
						21. DATA STRUCTURES LABORATORY PR 50 20 12 F	F

GRAND TOTAL = 634/1500, RESULT: FAILS A.T.K.T.
ORDN. 1 MARKS :

S	8054400 POORVI ARVIND DHARWAD	O POORVI ARVIND DHARWAD				INI		, 70925566н	, s8054306 ,	PICT		, s8054400			
0	1. DISCRETE STRUCTURES	PP	100	40	40	Р	11.	ENGINEERING MATH	EMATICS III	PP	100	40	11	F	
0	2. PROGRAMMING & PROBLEM SOLVING	PP	100	40	40	P C	12.	MICROPROC. & INTI	ERFACING TECHNIQ	.PP	100	40	31	F	
0	3. DIGIT. ELECTRONICS & LOGIC DESIG	NPP	100	40	40	P C	13.	DATA STRUCTURES		PP	100	40	40	P C	
0	4. DATA STRUCTURES AND ALGORITHMS	PP	100	40	31	F	14.	COMPUTER GRAPHICS	5	PP	100	40	40	P C	
0	5. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	40	P C	15.	COMPUTER ORGANIZA	ATION age 179	PP	100	40	41	Р	

TW 25 10 14 P C 16. O. O. PROG. & COMP. GRAPH. LAB TW 50 20 23 P C

07. PROGRAMMING LABORATORY	PR	50	20	28	РС	17.	O. O. PROG.	& COMP. GRAPH. LAB	PR	50	20	24	P C
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	19	РС	18.	MICROPROCESS	ORS & INTERFACING L	ABTW	50	20	25	P C
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	26	РС	19.	MICROPROCESS	ORS & INTERFACING L	ABPR	50	20	30	Р
10. SOFT SKILLS	TW	50	20	32	РС	20.	DATA STRUCTU	RES LABORATORY	TW	50	20	22	P C
						21.	DATA STRUCTU	RES LABORATORY	PR	50	20	11	F
GRAND TOTAL = 608/1500, RESULT: FAILS	- A T	и т											
, ,	5 A.I.	. K. I .											
ORDN. 1 MARKS :													
UNIVERSITY OF	PUNE	,S.E.	(2008	PAT	.)(IN	FORMATION T	ECHNOLOGY)				• •		
DATE : 18 AUG. 2011	CENT	ΓRE : F	PUNE I	NSTI	TUTE	OF COMPUTE	R TECHNOLOGY,	PUNE.	PAG	E NO.	01	(4	23)
NOTE: FIRST LINE : SEAT NO., NAME (OF THE	E CAND	DATE,	MO	THER	, PERMANENT	REG. NO., P	REVIOUS SEAT NO.,	COLLEG	iE, S	SEAT N	10.	
OTHER LINES: HEAD OF PASSING,	MAX.	. MARKS	5, MI	IN. P	ASS N	MARKS, MAR	KS OBTAINED,	P/F:PASS/FAIL, C:	PREVIO	US CAF	≀RY OV	/ER	
S8058501 AGHAV ISHWARI JAGANNATH				VAT	SHAL	Г	71045363	K , S8058501 ,	PICT	-	, s80)5850°	1
7.6.m.v 15m.n.2 37.6.m.v.11				•,,,		-	, 720,3303.	, 55555551 ,	. 10.		, 500	3030.	_
01. DISCRETE STRUCTURES	PP	100	40	40	РС	11.	ENG MATHS II	I	PP	100	40	40	Р
02. COMPUTER ORGANIZATION	PP	100	40	54	РС	12.	COMPUTER GRA	PHICS	PP	100	40	63	Р
03. DIGITAL ELECTRONICS & LOGIC DESI	[GPP	100	40	54	РС	13.	PROCESSOR AR	CHITECTURE & INTER.	PP	100	40	55	Р
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	59	РС	14.	DATA STRUCTU	RES AND FILES	PP	100	40	64	Р
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	61	P C	15.	DATA COMMUNIO	CATION	PP	100	40	66	Р
06. DIGITAL LABORATORY	TW	50	20	33	РС	16.	PROCESSOR IN	TERFACING LABORATOR	RY TW	25	10	20	Р
07. DIGITAL LABORATORY	PR	50	20	35	РС	17.	PROCESSOR IN	TERFACING LABORATOR	RY OR	50	20	35	Р
08. PROGRAMMING LABORATORY	TW	50	20	38	РС	18.	DATA STRUCTU	RES AND FILES LAB	TW	25	10	16	Р
09. PROGRAMMING LABORATORY	PR	50	20	39	РС	19.	DATA STRUCTU	RES AND FILES LAB	PR	50	20	35	Р
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	36	РС	20.	OBJECT ORIEN	TED PROGRAMMING LAE	3 TW	50	20	42	Р
						21.	OBJECT ORIEN	TED PROGRAMMING LAE	B PR	50	20	20	Р

GRAND TOTAL = 905+05/1500, RESULT: FIRST CLASS[0.163]
ORDN. 1 MARKS :

06. PROGRAMMING LABORATORY

S8058502 AGRAWAL NILIMA OMPRAKASH				LAT	Ā	, 71134941L , S8058502 , PICT , S8058502
01. DISCRETE STRUCTURES	PP	100	40	46	P C	11. ENG MATHS III PP 100 40 40 P
02. COMPUTER ORGANIZATION	PP	100	40	40	P C	12. COMPUTER GRAPHICS PP 100 40 56 P
03. DIGITAL ELECTRONICS & LOGIC DESI	GPP	100	40	43	P C	13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 54 P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	52	P C	14. DATA STRUCTURES AND FILES PP 100 40 51 P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	51	P C	15. DATA COMMUNICATION PP 100 40 55 P
06. DIGITAL LABORATORY	TW	50	20	30	P C	16. PROCESSOR INTERFACING LABORATORY TW 25 10 21 P
07. DIGITAL LABORATORY	PR	50	20	25	P C	17. PROCESSOR INTERFACING LABORATORY OR 50 20 38 P
08. PROGRAMMING LABORATORY	TW	50	20	41	P C	18. DATA STRUCTURES AND FILES LAB TW 25 10 20 P
09. PROGRAMMING LABORATORY	PR	50	20	27	P C	19. DATA STRUCTURES AND FILES LAB PR 50 20 15* P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	40	P C	20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 44 P
						21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 32 P

GRAND TOTAL = 821/1500, RESULT: SECOND CLASS * [0.4]
ORDN. 1 MARKS:

S8058503 AHER SHUBHAM SURESH				LAT	A		, 71134942〕	, s8058503 ,	PICT		, s80	5850	3
01. DISCRETE STRUCTURES	PP	100	40	52	Р	11.	ENG MATHS III		PP	100	40	40	Р
02. COMPUTER ORGANIZATION	PP	100	40	50	P C	12.	COMPUTER GRAPHIC	CS	PP	100	40	60	Р
03. DIGITAL ELECTRONICS & LOGIC DES	[GPP	100	40	65	P C	13.	PROCESSOR ARCHIT	TECTURE & INTER.	PP	100	40	61	Р
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	53	P C	14.	DATA STRUCTURES	AND FILES	PP	100	40	63	Р
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	58	P C	15.	DATA COMMUNICATI	ION	PP	100	40	72	Р
06. DIGITAL LABORATORY	TW	50	20	37	P C	16.	PROCESSOR INTERF	ACING LABORATORY	TW	25	10	23	Р
07. DIGITAL LABORATORY	PR	50	20	27	P C	17.	PROCESSOR INTERF	ACING LABORATORY	OR	50	20	40	Р
08. PROGRAMMING LABORATORY	TW	50	20	39	P C	18.	DATA STRUCTURES	AND FILES LAB	TW	25	10	20	Р
09. PROGRAMMING LABORATORY	PR	50	20	38	P C	19.	DATA STRUCTURES	AND FILES LAB	PR	50	20	40	Р
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	41	P C	20.	OBJECT ORIENTED	PROGRAMMING LAB Page 181	TW	50	20	45	Р

21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 42 P

GRAND TOTAL = 966/1500, RESULT: FIRST CLASS ORDN. 1 MARKS: UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(INFORMATION TECHNOLOGY) DATE: 18 AUG. 2011 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 02 (424) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER S8058504 ALWAL ANNAPURNA RAMESH RENUKA , 71134943G , S8058504 , PICT , S8058504 01. DISCRETE STRUCTURES PP 100 40 40 P C PP 100 40 40 P 11. ENG MATHS III 02. COMPUTER ORGANIZATION PP 100 40 54 P C 12. COMPUTER GRAPHICS PP 100 40 61 P 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 40 60 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 51 P 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 46 P C PP 100 40 55 P 14. DATA STRUCTURES AND FILES 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 59 P C 15. DATA COMMUNICATION PP 100 40 77 P 06. DIGITAL LABORATORY 50 20 28 P C TW 16. PROCESSOR INTERFACING LABORATORY TW 25 10 22 P 07. DIGITAL LABORATORY 50 20 30 P C 17. PROCESSOR INTERFACING LABORATORY OR 50 20 35 P PR 08. PROGRAMMING LABORATORY TW 50 20 36 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 17 P 09. PROGRAMMING LABORATORY 50 20 40 P C 19. DATA STRUCTURES AND FILES LAB PR 50 20 39 P 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 33 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 43 P 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 40 P GRAND TOTAL = 906/1500, RESULT: FIRST CLASS ORDN. 1 MARKS: , 71045372J , S8058505 , PICT , S8058505 S8058505 ANKIT SHARMA ANITA

PP 100 40 60 P C

01. DISCRETE STRUCTURES

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11. ENG MATHS III

PP 100 40 40 P

02.	COMPUTER ORGANIZATION	PP	100	40	40	P C	12.	COMPUTER GRAPHICS	PP	100	40	45	Р
03.	DIGITAL ELECTRONICS & LOGIC DESIGNATION	GPP	100	40	52	P C	13.	PROCESSOR ARCHITECTURE & INTER.	PP	100	40	40	Р
04.	FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	40	P C	14.	DATA STRUCTURES AND FILES	PP	100	40	56	Р
05.	HUMANITIES AND SOCIAL SCIENCES	PP	100	40	40	P C	15.	DATA COMMUNICATION	PP	100	40	41	Р
06.	DIGITAL LABORATORY	TW	50	20	39	P C	16.	PROCESSOR INTERFACING LABORATORY	TW	25	10	15	Р
07.	DIGITAL LABORATORY	PR	50	20	42	P C	17.	PROCESSOR INTERFACING LABORATORY	OR	50	20	34	Р
08.	PROGRAMMING LABORATORY	TW	50	20	41	P C	18.	DATA STRUCTURES AND FILES LAB	TW	25	10	14	Р
09.	PROGRAMMING LABORATORY	PR	50	20	37	P C	19.	DATA STRUCTURES AND FILES LAB	PR	50	20	35	Р
10.	COMMUNICATION AND LANGUAGE LAB.	TW	50	20	42	P C	20.	OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	34	Р
							21.	OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	25	Р

GRAND TOTAL = 812/1500, RESULT: SECOND CLASS
ORDN. 1 MARKS :

S805	8506 ANUJA WANGNOO				AMB:	ICA			, 71045373G	, s8058506 ,	PICT		, s80!	58500	ò
01.	DISCRETE STRUCTURES	PP	100	40	60	P C	13	1.	ENG MATHS III		PP	100	40	27	F
02.	COMPUTER ORGANIZATION	PP	100	40	48	P C	12	2.	COMPUTER GRAPHICS		PP	100	40	49	Р
03.	DIGITAL ELECTRONICS & LOGIC DESI	GPP	100	40	55	P C	13	3.	PROCESSOR ARCHITEC	TURE & INTER.	PP	100	40	32	F
04.	FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	59	P C	14	4.	DATA STRUCTURES AN	ID FILES	PP	100	40	45	Р
05.	HUMANITIES AND SOCIAL SCIENCES	PP	100	40	41	P C	1!	5.	DATA COMMUNICATION	ı	PP	100	40	32	F
06.	DIGITAL LABORATORY	TW	50	20	32	P C	10	6.	PROCESSOR INTERFAC	ING LABORATORY	TW	25	10	12	Р
07.	DIGITAL LABORATORY	PR	50	20	42	P C	17	7.	PROCESSOR INTERFAC	ING LABORATORY	OR	50	20	32	Р
08.	PROGRAMMING LABORATORY	TW	50	20	33	P C	18	8.	DATA STRUCTURES AN	ID FILES LAB	TW	25	10	15	Р
09.	PROGRAMMING LABORATORY	PR	50	20	44	P C	19	9.	DATA STRUCTURES AN	ID FILES LAB	PR	50	20	25	Р
10.	COMMUNICATION AND LANGUAGE LAB.	TW	50	20	34	P C	20	0.	OBJECT ORIENTED PR	OGRAMMING LAB	TW	50	20	36	Р
							21	1.	OBJECT ORIENTED PR	OGRAMMING LAB	PR	50	20	30	Р

GRAND TOTAL = 783/1500, RESULT: FAILS A.T.K.T.

UNIVERSITY OF	 PUNE	 ,S.E.	(2008	 PAT.)(INI	FORMATION	TECHNOLOGY)						
DATE : 18 AUG. 2011	CENT	RE : F	PUNE I	NSTI	TUTE	OF COMPUTE	R TECHNOLOGY, P	JNE.	PAG	E NO.	03	(4	25)
NOTE: FIRST LINE : SEAT NO., NAME (F THE	CANDI	DATE,	MO	THER,	PERMANENT	REG. NO., PRE	/IOUS SEAT NO., C	OLLEG	Ε, S	EAT N	Ο.	
OTHER LINES: HEAD OF PASSING,	MAX.	MARKS	S, MI	N. P.	ASS M	MARKS, MAR	RKS OBTAINED, P	F:PASS/FAIL, C:P	REVIO	US CAR	RY OV	ER	
S8058507 ASHISH SANADHYA				MAD	HU		, 71045374E	, s8058507 ,	PICT		, s80	5850	7
01. DISCRETE STRUCTURES	PP	100	40	62	P C	11.	ENG MATHS III		PP	100	40	76	Р
02. COMPUTER ORGANIZATION	PP	100	40	40	РС	12.	COMPUTER GRAPH	ics	PP	100	40	61	Р
03. DIGITAL ELECTRONICS & LOGIC DESI	GPP	100	40	65	P C	13.	PROCESSOR ARCH	TTECTURE & INTER.	PP	100	40	68	Р
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	68	P C	14.	DATA STRUCTURES	S AND FILES	PP	100	40	63	Р
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	50	P C	15.	DATA COMMUNICA	TION	PP	100	40	63	Р
06. DIGITAL LABORATORY	TW	50	20	40	P C	16.	PROCESSOR INTER	RFACING LABORATORY	TW	25	10	20	Р
07. DIGITAL LABORATORY	PR	50	20	35	P C	17.	PROCESSOR INTER	RFACING LABORATORY	OR	50	20	44	Р
08. PROGRAMMING LABORATORY	TW	50	20	43	P C	18.	DATA STRUCTURES	S AND FILES LAB	TW	25	10	19	Р
09. PROGRAMMING LABORATORY	PR	50	20	39	P C	19.	DATA STRUCTURES	S AND FILES LAB	PR	50	20	35	Р
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	44	P C	20.	OBJECT ORIENTE	PROGRAMMING LAB	TW	50	20	45	Р
						21.	OBJECT ORIENTE	PROGRAMMING LAB	PR	50	20	44	Р
GRAND TOTAL = 1024/1500, RESULT: FIRST	CLAS	S WITH	I DIST	INCT	ION								
ORDN. 1 MARKS :	0_/ 10												
S8058508 ASHTEKAR KRUTTIKA CHANDRA	AK ANT			SIIR	ЕКНА		, 71045375C	, s8058508 ,	PICT		, s80	5850	S
Joogeon Asim Elville (III)				3010			, 720133730	, 30030300 ,			, 500	3030	J
01. DISCRETE STRUCTURES	PP	100	40	55	P C	11.	ENG MATHS III		PP	100	40	26	F
02. COMPUTER ORGANIZATION	PP	100	40	40	РС	12.	COMPUTER GRAPH	ICS	PP	100	40	40	Р
03. DIGITAL ELECTRONICS & LOGIC DESI	GPP	100	40	56	РС	13.	PROCESSOR ARCH	ITECTURE & INTER.	PP	100	40	43	Р
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	41	P C	14.	DATA STRUCTURES	S AND FILES	PP	100	40	47	Р
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	53	P C	15.	DATA COMMUNICA	ΓΙΟΝ	PP	100	40	59	Р
06. DIGITAL LABORATORY	TW	50	20	38	P C	16.	PROCESSOR INTER	RFACING LABORATORY Page 184	TW	25	10	19	Р

07. DIGITAL LA	BORATORY	PR	50	20	36	P C	17.	PROCESSOR INTERF	FACING LABORATORY	OR	50	20	32	Р
08. PROGRAMMIN	G LABORATORY	TW	50	20	38	P C	18.	DATA STRUCTURES	AND FILES LAB	TW	25	10	15	Р
09. PROGRAMMIN	G LABORATORY	PR	50	20	34	P C	19.	DATA STRUCTURES	AND FILES LAB	PR	50	20	37	Р
10. COMMUNICAT	TION AND LANGUAGE LAB.	TW	50	20	38	P C	20.	OBJECT ORIENTED	PROGRAMMING LAB	TW	50	20	43	Р
							21.	OBJECT ORIENTED	PROGRAMMING LAB	PR	50	20	32	Р

GRAND TOTAL = 822/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS:

S8058509	AUTADE SAGAR ASHOK				INDI	[RA		, 71045376M , S8058509 ,	PICT		, s80!	58509	9
01. DISCR	ETE STRUCTURES	PP	100	40	40	P C	11.	ENG MATHS III	PP	100	40	23	F
02. COMPU	TER ORGANIZATION	PP	100	40	26	F	12.	COMPUTER GRAPHICS	PP	100	40	22	F
03. DIGIT	AL ELECTRONICS & LOGIC DESI	GPP	100	40	44	P C	13.	PROCESSOR ARCHITECTURE & INTER.	PP	100	40	16	F
04. FUNDA	MENTAL OF DATA STRUCTURES	PP	100	40	40#	Р	14.	DATA STRUCTURES AND FILES	PP	100	40	40	Р
05. HUMAN	ITIES AND SOCIAL SCIENCES	PP	100	40	28	F	15.	DATA COMMUNICATION	PP	100	40	22	F
06. DIGIT	AL LABORATORY	TW	50	20	24	P C	16.	PROCESSOR INTERFACING LABORATORY	TW	25	10	13	Р
07. DIGIT	AL LABORATORY	PR	50	20	22	P C	17.	PROCESSOR INTERFACING LABORATORY	OR	50	20	29	Р
08. PROGR	AMMING LABORATORY	TW	50	20	26	P C	18.	DATA STRUCTURES AND FILES LAB	TW	25	10	12	Р
09. PROGR	AMMING LABORATORY	PR	50	20	27	P C	19.	DATA STRUCTURES AND FILES LAB	PR	50	20	36	Р
10. COMMU	NICATION AND LANGUAGE LAB.	TW	50	20	30	P C	20.	OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	23	Р
							21.	OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	28	Р

GRAND TOTAL = 571/1500, RESULT: FAILS #[0.163+0.1]

ORDN. 1 MARKS: (04)3,

UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(INFORMATION TECHNOLOGY)

DATE: 18 AUG. 2011 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 04 (426)

NOTE: FIRST LINE: SEAT NO... NAME OF THE CANDIDATE. MOTHER, PERMANENT REG. NO... PREVIOUS SEAT NO... COLLEGE. SEAT NO.

NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER Page 185

S8	058511 BADGUJAR PRATIKSHA BHASKA	R			ANI [.]	ТА		, 71045380к	, s8058511 ,	PICT		, s80)5851	.1
01	. DISCRETE STRUCTURES	PP	100	40	45	P C	11.	ENG MATHS III		PP	100	40	40	Р
02	. COMPUTER ORGANIZATION	PP	100	40	46	Р	12.	COMPUTER GRAPHIC	:S	PP	100	40	57	Р
03	. DIGITAL ELECTRONICS & LOGIC DESI	GPP	100	40	50	P C	13.	PROCESSOR ARCHIT	ECTURE & INTER.	PP	100	40	47	Р
04	. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	40	P C	14.	DATA STRUCTURES	AND FILES	PP	100	40	62	Р
05	. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	48	P C	15.	DATA COMMUNICATI	ON	PP	100	40	45	Р
06	. DIGITAL LABORATORY	TW	50	20	32	P C	16.	PROCESSOR INTERF	ACING LABORATORY	TW	25	10	19	Р
07	. DIGITAL LABORATORY	PR	50	20	31	P C	17.	PROCESSOR INTERF	ACING LABORATORY	OR	50	20	23	Р
08	. PROGRAMMING LABORATORY	TW	50	20	29	P C	18.	DATA STRUCTURES	AND FILES LAB	TW	25	10	16	Р
09	. PROGRAMMING LABORATORY	PR	50	20	37	P C	19.	DATA STRUCTURES	AND FILES LAB	PR	50	20	24	Р
10	. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	29	P C	20.	OBJECT ORIENTED	PROGRAMMING LAB	TW	50	20	38	Р
							21.	OBJECT ORIENTED	PROGRAMMING LAB	PR	50	20	30	Р

GRAND TOTAL = 788/1500, RESULT: SECOND CLASS
ORDN. 1 MARKS :

S80	58512 BHARTENDU BHARTI				MAN	ISHA DEVI	, 71045387G , S8058512 , PICT , S8058512	
01.	DISCRETE STRUCTURES	PP	100	40	46	P C	11. ENG MATHS III PP 100 40 51 P	,
02.	COMPUTER ORGANIZATION	PP	100	40	43	P C	12. COMPUTER GRAPHICS PP 100 40 48 P)
03.	DIGITAL ELECTRONICS & LOGIC DESI	GPP	100	40	64	P C	13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 49 P)
04.	FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	41	P C	14. DATA STRUCTURES AND FILES PP 100 40 63 P)
05.	HUMANITIES AND SOCIAL SCIENCES	PP	100	40	43	P C	15. DATA COMMUNICATION PP 100 40 58 P)
06.	DIGITAL LABORATORY	TW	50	20	20	P C	16. PROCESSOR INTERFACING LABORATORY TW 25 10 14 P)
07.	DIGITAL LABORATORY	PR	50	20	24	P C	17. PROCESSOR INTERFACING LABORATORY OR 50 20 34 P)
08.	PROGRAMMING LABORATORY	TW	50	20	28	P C	18. DATA STRUCTURES AND FILES LAB TW 25 10 15 P)
09.	PROGRAMMING LABORATORY	PR	50	20	27	P C	19. DATA STRUCTURES AND FILES LAB PR 50 20 39 P)
10.	COMMUNICATION AND LANGUAGE LAB.	TW	50	20	20	P C	20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 34 P)
							21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 30 P Page 186	,

GRAND TOTAL = 791/1500, RESULT: SECOND CLASS

ORDN. 1 MARKS:

ORDN. 1 MARKS:

, 71134944E , S8058513 , PICT S8058513 BHINTADE VIVEK ASHOK SANGEETA , s8058513 01. DISCRETE STRUCTURES PP 100 40 61 P C 11. ENG MATHS III PP 100 47 P 02. COMPUTER ORGANIZATION 100 53 P C 12. COMPUTER GRAPHICS 100 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 40 68 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 76 P 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 71 P C 14. DATA STRUCTURES AND FILES 100 60 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 51 P C 100 65 P 15. DATA COMMUNICATION 40 06. DIGITAL LABORATORY 50 20 38 P C 16. PROCESSOR INTERFACING LABORATORY TW 25 10 17 P TW 07. DIGITAL LABORATORY 50 20 31 P C 17. PROCESSOR INTERFACING LABORATORY OR 50 20 30 P 08. PROGRAMMING LABORATORY 50 20 34 P C TW 18. DATA STRUCTURES AND FILES LAB TW 25 10 17 P 09. PROGRAMMING LABORATORY 50 20 45 P C 19. DATA STRUCTURES AND FILES LAB PR 50 20 36 P

50 20 28 P C

GRAND TOTAL = 970/1500, RESULT: FIRST CLASS

10. COMMUNICATION AND LANGUAGE LAB. TW

UNIVERSIT	Y OF PUNE ,S.E.(2008 PAT.)(INFORMATION TECHNOLOGY)	
DATE : 18 AUG. 2011	CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.	PAGE NO. 05 (427)

NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8058514 BHOSALE SHREEYA DEEPAKRA	0			JYOTSNA	, 71045394К	, s8058514 ,	PIC	Т	, 580	05851	4
01. DISCRETE STRUCTURES	PP	100	40	59 P C	11. ENG MATHS III		PP	100	40	54	Р
02. COMPUTER ORGANIZATION	PP	100	40	54 P C	12. COMPUTER GRAPHI	CS 107	PP	100	40	68	Р

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20. OBJECT ORIENTED PROGRAMMING LAB TW

21. OBJECT ORIENTED PROGRAMMING LAB PR

50 20

50 20 37 P

42 P

03. DIGITAL ELECTRONICS & LOGIC DES	IGPP	100	40	65	PC	13	. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	64	Р
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	60	PC	14	. DATA STRUCTURES AND FILES	PP	100	40	54	Р
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	53	PC	15	. DATA COMMUNICATION	PP	100	40	70	Р
06. DIGITAL LABORATORY	TW	50	20	37	PC	16	. PROCESSOR INTERFACING LABORATORY	TW	25	10	19	Р
07. DIGITAL LABORATORY	PR	50	20	33	P C	17	. PROCESSOR INTERFACING LABORATORY	OR	50	20	35	Р
08. PROGRAMMING LABORATORY	TW	50	20	38	PC	18	. DATA STRUCTURES AND FILES LAB	TW	25	10	14	Р
09. PROGRAMMING LABORATORY	PR	50	20	42	P C	19	. DATA STRUCTURES AND FILES LAB	PR	50	20	12	F
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	41	P C	20	. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	33	Р
						21	. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	35	Р

GRAND TOTAL = 940/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS:

S8058515 BORDE ROHAN SATISHRAO				UJWA	ALA		, 71045398в	, s8058515 ,	PICT		, s80	5851	5
01. DISCRETE STRUCTURES	PP	100	40	63	P C	11.	ENG MATHS III		PP	100	40	52	Р
02. COMPUTER ORGANIZATION	PP	100	40	49	P C	12.	COMPUTER GRAPHICS	5	PP	100	40	58	Р
03. DIGITAL ELECTRONICS & LOGIC DES	IGPP	100	40	52	P C	13.	PROCESSOR ARCHITE	ECTURE & INTER.	PP	100	40	56	Р
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	64	P C	14.	DATA STRUCTURES A	AND FILES	PP	100	40	73	Р
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	49	P C	15.	DATA COMMUNICATIO	ON	PP	100	40	60	Р
06. DIGITAL LABORATORY	TW	50	20	20	P C	16.	PROCESSOR INTERFA	ACING LABORATORY	TW	25	10	17	Р
07. DIGITAL LABORATORY	PR	50	20	30	P C	17.	PROCESSOR INTERFA	ACING LABORATORY	OR	50	20	28	Р
08. PROGRAMMING LABORATORY	TW	50	20	33	P C	18.	DATA STRUCTURES A	AND FILES LAB	TW	25	10	16	Р
09. PROGRAMMING LABORATORY	PR	50	20	30	P C	19.	DATA STRUCTURES A	AND FILES LAB	PR	50	20	31	Р
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	27	P C	20.	OBJECT ORIENTED F	PROGRAMMING LAB	TW	50	20	42	Р
						21.	OBJECT ORIENTED F	PROGRAMMING LAB	PR	50	20	34	Р

GRAND TOTAL = 884/1500, RESULT: HIGHER SECOND CLASS

S8058516 BRAHME NACHIKET SHRIKANT				VID	ULA		, 71134945C	, s8058516 ,	PICT	-	, s80)5851	6
01. DISCRETE STRUCTURES	PP	100	40	50	РС	11.	ENG MATHS III		PP	100	40	47	Р
02. COMPUTER ORGANIZATION	PP	100	40	48	РС	12.	COMPUTER GRAPHIC	:S	PP	100	40	52	Р
03. DIGITAL ELECTRONICS & LOGIC DESI	GPP	100	40	51	P C	13.	PROCESSOR ARCHIT	ECTURE & INTER.	PP	100	40	59	Р
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	48	РС	14.	DATA STRUCTURES	AND FILES	PP	100	40	56	Р
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	45	P C	15.	DATA COMMUNICATI	ON	PP	100	40	58	Р
06. DIGITAL LABORATORY	TW	50	20	33	P C	16.	PROCESSOR INTERF	ACING LABORATORY	TW	25	10	18	Р
07. DIGITAL LABORATORY	PR	50	20	32	P C	17.	PROCESSOR INTERF	ACING LABORATORY	OR	50	20	25	Р
08. PROGRAMMING LABORATORY	TW	50	20	42	РС	18.	DATA STRUCTURES	AND FILES LAB	TW	25	10	15	Р
09. PROGRAMMING LABORATORY	PR	50	20	37	РС	19.	DATA STRUCTURES	AND FILES LAB	PR	50	20	43	Р
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	38	P C	20.	OBJECT ORIENTED	PROGRAMMING LAB	TW	50	20	39	Р
						21.	OBJECT ORIENTED	PROGRAMMING LAB	PR	50	20	38	Р
ORDN. 1 MARKS :													
UNIVERSITY OF							ECHNOLOGY)						
UNIVERSITY OF DATE : 18 AUG. 2011									PAG	 iE NO.	06		28)
DATE : 18 AUG. 2011	CENT	RE : P	PUNE I	NSTI	TUTE	OF COMPUTE	TECHNOLOGY) R TECHNOLOGY, PUN	E.					28)
DATE: 18 AUG. 2011	CENT OF THE	RE : P	DUNE I	NSTI	TUTE THER	OF COMPUTE	R TECHNOLOGY) R TECHNOLOGY, PUN REG. NO., PREVI	OUS SEAT NO., C	 OLLEG	 iE, S	 SEAT N		28)
DATE : 18 AUG. 2011	CENT OF THE	RE : P	DUNE I	NSTI	TUTE THER	OF COMPUTE	R TECHNOLOGY) R TECHNOLOGY, PUN REG. NO., PREVI	OUS SEAT NO., C	 OLLEG	 iE, S	 SEAT N		28)
DATE: 18 AUG. 2011	CENT OF THE	RE : P	DUNE I	NSTI	TUTE THER	OF COMPUTE	R TECHNOLOGY) R TECHNOLOGY, PUN REG. NO., PREVI	OUS SEAT NO., C	 OLLEG	 iE, S	 SEAT N		28)
DATE: 18 AUG. 2011	CENT OF THE	RE : P	DUNE I	NSTI	TUTE THER ASS	OF COMPUTE	R TECHNOLOGY) R TECHNOLOGY, PUN REG. NO., PREVI	OUS SEAT NO., C	 OLLEG	 iE, S OUS CAF	 SEAT N	 IO. /ER	
DATE: 18 AUG. 2011 NOTE: FIRST LINE: SEAT NO., NAME OF OTHER LINES: HEAD OF PASSING,	CENT OF THE	RE : P	DUNE I	MOTAL MONTA	TUTE THER ASS	OF COMPUTE PERMANENT IARKS, MAR	R TECHNOLOGY) R TECHNOLOGY, PUN REG. NO., PREVI KS OBTAINED, P/F	E. OUS SEAT NO., C PASS/FAIL, C:P	· · · OLLEG REVIO	 iE, S OUS CAF	EAT NRY OV	 IO. /ER	
DATE: 18 AUG. 2011 NOTE: FIRST LINE: SEAT NO., NAME OF OTHER LINES: HEAD OF PASSING, S8058517 CHAKRAVARTHY ROHAN KUMAR	CENT THE MAX.	RE : P CANDI MARKS	DATE,	MOTAL MONTA	TUTE THER ASS	OF COMPUTE PERMANENT IARKS, MAR	R TECHNOLOGY) R TECHNOLOGY, PUN REG. NO., PREVI KS OBTAINED, P/F , 71045400H	E. OUS SEAT NO., C PASS/FAIL, C:P	OLLEGREVIO	 GE, S DUS CAF	EAT NRRY OV	 /ER 	 7
DATE: 18 AUG. 2011 NOTE: FIRST LINE: SEAT NO., NAME OF OTHER LINES: HEAD OF PASSING, S8058517 CHAKRAVARTHY ROHAN KUMAR 01. DISCRETE STRUCTURES	CENT OF THE MAX.	RE : P CANDI MARKS	DATE, MI	MOTAL MONE	TUTE THER ASS ICA P C	OF COMPUTE PERMANENT IARKS, MAR	R TECHNOLOGY) R TECHNOLOGY, PUN REG. NO., PREVI KS OBTAINED, P/F , 71045400H ENG MATHS III	E. OUS SEAT NO., C PASS/FAIL, C:P , S8058517,	OLLEGREVIO	E, SOUS CAF		 NO. /ER 05851	
DATE: 18 AUG. 2011 NOTE: FIRST LINE: SEAT NO., NAME OF OTHER LINES: HEAD OF PASSING, S8058517 CHAKRAVARTHY ROHAN KUMAR 01. DISCRETE STRUCTURES 02. COMPUTER ORGANIZATION	CENT OF THE MAX.	RE : P CANDI MARKS	DATE, MI	MOTAL MONE	TUTE THER ASS ICA P C	OF COMPUTE PERMANENT IARKS, MAR 11. 12. 13.	R TECHNOLOGY) R TECHNOLOGY, PUN REG. NO., PREVI KS OBTAINED, P/F , 71045400H ENG MATHS III COMPUTER GRAPHIC	COUS SEAT NO., CE:PASS/FAIL, C:P, S8058517, CS ECTURE & INTER.	OLLEGREVIO			 NO. /ER 05851 49 51	
DATE: 18 AUG. 2011 NOTE: FIRST LINE: SEAT NO., NAME OF OTHER LINES: HEAD OF PASSING, S8058517 CHAKRAVARTHY ROHAN KUMAR O1. DISCRETE STRUCTURES O2. COMPUTER ORGANIZATION O3. DIGITAL ELECTRONICS & LOGIC DESI	CENT MAX. PP PP	RE : P CANDI MARKS 100 100 100	DATE, MI	MOTAN PARAMETERS NO.	TUTE THER ASS ICA P C P C	OF COMPUTE PERMANENT HARKS, MAR 11. 12. 13. 14.	R TECHNOLOGY) R TECHNOLOGY, PUN REG. NO., PREVI KS OBTAINED, P/F , 71045400H ENG MATHS III COMPUTER GRAPHIC PROCESSOR ARCHIT	E. OUS SEAT NO., C PASS/FAIL, C:P , S8058517, SS ECTURE & INTER. AND FILES	OLLEGREVIO				
DATE: 18 AUG. 2011 NOTE: FIRST LINE: SEAT NO., NAME OF OTHER LINES: HEAD OF PASSING, S8058517 CHAKRAVARTHY ROHAN KUMAR O1. DISCRETE STRUCTURES O2. COMPUTER ORGANIZATION O3. DIGITAL ELECTRONICS & LOGIC DESI O4. FUNDAMENTAL OF DATA STRUCTURES	CENT OF THE MAX. PP PP CGPP PP	RE : P CANDI MARKS 100 100 100 100	DATE, ADATE, ADATE,	MON: MON: 53 58 55 73	TUTE THER ASS ICA P C P C P C	OF COMPUTE PERMANENT IARKS, MAR 11. 12. 13. 14. 15.	R TECHNOLOGY) R TECHNOLOGY, PUN REG. NO., PREVI KS OBTAINED, P/F , 71045400H ENG MATHS III COMPUTER GRAPHIC PROCESSOR ARCHIT DATA STRUCTURES	COUS SEAT NO., CEPASS/FAIL, C:PASS/FAIL, C:P	OLLEG REVIO PICT PP PP PP	100 100 100	SEAT N RRY OV , S80 40 40 40 40		

									cols05					
08.	PROGRAMMING LABORATORY	TW	50	20	32	РС	18.	DATA STRUCTURES	AND FILES LAB	TW	25	10	14	Р
09.	PROGRAMMING LABORATORY	PR	50	20	48	РС	19.	DATA STRUCTURES	AND FILES LAB	PR	50	20	41	Р
10.	COMMUNICATION AND LANGUAGE LAB.	TW	50	20	27	P C	20.	OBJECT ORIENTED	PROGRAMMING LAB	TW	50	20	34	Р
							21.	OBJECT ORIENTED	PROGRAMMING LAB	PR	50	20	43	Р
GRAND	TOTAL = 868/1500, RESULT: HIGHE	R SEC	OND CL	ASS										
ORDN.	1 MARKS :													
S80!	58518 CHANDAK RASHMI VIJAYKUMAR.	JI			SHY	АМА		, 71134946M	, s8058518 ,	PICT		, s80	58518	8
01.	DISCRETE STRUCTURES	PP	100	40	50	P C	11.	ENG MATHS III		PP	100	40	44	Р
02.	COMPUTER ORGANIZATION	PP	100	40	46	РС	12.	COMPUTER GRAPHI	cs	PP	100	40	58	Р
03.	DIGITAL ELECTRONICS & LOGIC DESIGNATION	GPP	100	40	66	P C	13.	PROCESSOR ARCHI	TECTURE & INTER.	PP	100	40	61	Р
04.	FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	69	P C	14.	DATA STRUCTURES	AND FILES	PP	100	40	73	Р
05.	HUMANITIES AND SOCIAL SCIENCES	PP	100	40	40	P C	15.	DATA COMMUNICAT	ION	PP	100	40	49	Р
06.	DIGITAL LABORATORY	TW	50	20	38	P C	16.	PROCESSOR INTER	FACING LABORATORY	TW	25	10	20	Р
07.	DIGITAL LABORATORY	PR	50	20	28	P C	17.	PROCESSOR INTER	FACING LABORATORY	OR	50	20	44	Р
08.	PROGRAMMING LABORATORY	TW	50	20	40	P C	18.	DATA STRUCTURES	AND FILES LAB	TW	25	10	20	Р
09.	PROGRAMMING LABORATORY	PR	50	20	40	P C	19.	DATA STRUCTURES	AND FILES LAB	PR	50	20	38	Р
10.	COMMUNICATION AND LANGUAGE LAB.	TW	50	20	46	P C	20.	OBJECT ORIENTED	PROGRAMMING LAB	TW	50	20	42	Р
							21.	OBJECT ORIENTED	PROGRAMMING LAB	PR	50	20	30	Р
GRAND	TOTAL = 942/1500, RESULT: FIRST	CLAS	S											
ORDN.	1 MARKS :													
S80!	58519 CHANGEDIYA SUNNY SANJAY				ASH	A		, 71058517К	, s8058519 ,	PICT		, s80	58519	9
01.	DISCRETE STRUCTURES	PP	100	40	43	P C	11.	ENG MATHS III		PP	100	40	40	Р
02.	COMPUTER ORGANIZATION	PP	100	40	41	P C	12.	COMPUTER GRAPHI	CS	PP	100	40	63	Р

03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 40 47 P C

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13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 52 P

								7 4-					
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	49	P C	14.	DATA STRUCTURES	cols05 AND FILES	PP	100	40	52	Р
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	42	P C	15.	DATA COMMUNICATI	ION	PP	100	40	53	Р
06. DIGITAL LABORATORY	TW	50	20	30	P C	16.	PROCESSOR INTERF	ACING LABORATORY	TW	25	10	19	Р
07. DIGITAL LABORATORY	PR	50	20	22	Р	17.	PROCESSOR INTERF	ACING LABORATORY	OR	50	20	22	Р
08. PROGRAMMING LABORATORY	TW	50	20	40	P C	18.	DATA STRUCTURES	AND FILES LAB	TW	25	10	17	Р
09. PROGRAMMING LABORATORY	PR	50	20	47	P C	19.	DATA STRUCTURES	AND FILES LAB	PR	50	20	38	Р
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	41	P C	20.	OBJECT ORIENTED	PROGRAMMING LAB	TW	50	20	43	Р
						21.	OBJECT ORIENTED	PROGRAMMING LAB	PR	50	20	25	Р
GRAND TOTAL = 826+05/1500, RESULT: HI	CHER	SECOND	ا دا ۵۸	ςΓn	1631								
ORDN. 1 MARKS :	GIILK	SECOND	CLAS	3[0.	103]								
ORDIN. 1 PARKS .													
UNIVERSITY OF	PUNE	,S.E.	(2008	PAT.	.)(INFORMA	TION T							
DATE : 18 AUG. 2011	CENT	RE : P	PUNE I	NSTI	TUTE OF CO	OMPUTEI	R TECHNOLOGY, PUN	NE.	PAG	E NO.	07	(4	29)
				• •									
NOTE: FIRST LINE : SEAT NO., NAME O	· · · F THE	 E CANDI	 DATE,	MO	THER, PERM	 MANENT	REG. NO., PREVI	IOUS SEAT NO., C	olleg	 E, S	EAT N	10.	
NOTE: FIRST LINE : SEAT NO., NAME OF OTHER LINES: HEAD OF PASSING,													
				N. P.				F:PASS/FAIL, C:P		US CAR		/ER	
OTHER LINES: HEAD OF PASSING,				N. P.	ASS MARKS,		KS OBTAINED, P/F		REVIO	US CAR	RY OV	/ER	
OTHER LINES: HEAD OF PASSING,				N. P.	ASS MARKS,	, MARI	KS OBTAINED, P/F	F:PASS/FAIL, C:P	REVIO	US CAR	RY OV	/ER	
OTHER LINES: HEAD OF PASSING, S8058520 CHAUDHAR SATISH RAOSAHEB	MAX.	MARKS	6, MI	N. P.	ASS MARKS,	, MARI	KS OBTAINED, P/F	F:PASS/FAIL, C:P	REVIO	US CAR	RY OV 	/ER)5852	
OTHER LINES: HEAD OF PASSING, S8058520 CHAUDHAR SATISH RAOSAHEB 01. DISCRETE STRUCTURES	MAX.	MARKS	6, MI · ·	N. P SAN	ASS MARKS, GITA P C	, MARI	KS OBTAINED, P/F	F:PASS/FAIL, C:P	REVIO PICT PP PP	US CAR 	 , s80	/ER · · 05852 74	P P
OTHER LINES: HEAD OF PASSING, S8058520 CHAUDHAR SATISH RAOSAHEB O1. DISCRETE STRUCTURES O2. COMPUTER ORGANIZATION	MAX.	MARKS	40 40	N. P. SAN 63	ASS MARKS, GITA P C P C P C	, MARI 11. 12. 13.	KS OBTAINED, P/F	F:PASS/FAIL, C:PASS/FAIL, C:PAS	REVIO PICT PP PP	US CAR 100 100	RY OV , \$80 40 40	/ER)5852 74 57	P P P
OTHER LINES: HEAD OF PASSING, S8058520 CHAUDHAR SATISH RAOSAHEB O1. DISCRETE STRUCTURES O2. COMPUTER ORGANIZATION O3. DIGITAL ELECTRONICS & LOGIC DESI	PP PP CGPP	100 100 100	40 40 40	N. P. SAN 63 41 56	ASS MARKS, GITA P C P C P C P C	, MARI 11. 12. 13.	KS OBTAINED, P/F, 71045402D ENG MATHS III COMPUTER GRAPHIC	F:PASS/FAIL, C:PASS/FAIL, C:PAS	PICT PP PP	100 100 100	40 40 40	/ER 05852 74 57 65	P P P
OTHER LINES: HEAD OF PASSING, S8058520 CHAUDHAR SATISH RAOSAHEB 01. DISCRETE STRUCTURES 02. COMPUTER ORGANIZATION 03. DIGITAL ELECTRONICS & LOGIC DESI 04. FUNDAMENTAL OF DATA STRUCTURES	PP PP PP	100 100 100 100	40 40 40 40	N. P. SAN 63 41 56 59	ASS MARKS, GITA P C P C P C P C P C	, MARI 11. 12. 13. 14.	KS OBTAINED, P/F, 71045402D ENG MATHS III COMPUTER GRAPHIC PROCESSOR ARCHIT DATA STRUCTURES DATA COMMUNICATION	F:PASS/FAIL, C:PASS/FAIL, C:PAS	PICT PP PP PP PP	100 100 100 100	40 40 40 40	74 57 65 66	P P P P
OTHER LINES: HEAD OF PASSING, S8058520 CHAUDHAR SATISH RAOSAHEB 01. DISCRETE STRUCTURES 02. COMPUTER ORGANIZATION 03. DIGITAL ELECTRONICS & LOGIC DESI 04. FUNDAMENTAL OF DATA STRUCTURES 05. HUMANITIES AND SOCIAL SCIENCES	PP PP PP PP	100 100 100 100 100	40 40 40 40 40	N. P. SAN 63 41 56 59 42 33	ASS MARKS, GITA P C P C P C P C P C	11. 12. 13. 14. 15.	RS OBTAINED, P/F, 71045402D ENG MATHS III COMPUTER GRAPHIC PROCESSOR ARCHIT DATA STRUCTURES DATA COMMUNICATION PROCESSOR INTERF	F:PASS/FAIL, C:PASS/FAIL, C:PAS	PICT PP PP PP PP TW	100 100 100 100 100	40 40 40 40 40	74 57 65 66 72	P P P P
OTHER LINES: HEAD OF PASSING, S8058520 CHAUDHAR SATISH RAOSAHEB 01. DISCRETE STRUCTURES 02. COMPUTER ORGANIZATION 03. DIGITAL ELECTRONICS & LOGIC DEST 04. FUNDAMENTAL OF DATA STRUCTURES 05. HUMANITIES AND SOCIAL SCIENCES 06. DIGITAL LABORATORY	PP PP GGPP PP TW	100 100 100 100 100 50	40 40 40 40 40 40 20	N. P. SAN 63 41 56 59 42 33 29	ASS MARKS, GITA P C P C P C P C P C P C	11. 12. 13. 14. 15. 16.	RS OBTAINED, P/F, 71045402D ENG MATHS III COMPUTER GRAPHIC PROCESSOR ARCHIT DATA STRUCTURES DATA COMMUNICATION PROCESSOR INTERF	F:PASS/FAIL, C:PASS/FAIL, C:PAS	PICT PP PP PP PP TW	100 100 100 100 100 25	40 40 40 40 40 10	74 57 65 66 72 18	P P P P P
OTHER LINES: HEAD OF PASSING, S8058520 CHAUDHAR SATISH RAOSAHEB 01. DISCRETE STRUCTURES 02. COMPUTER ORGANIZATION 03. DIGITAL ELECTRONICS & LOGIC DEST 04. FUNDAMENTAL OF DATA STRUCTURES 05. HUMANITIES AND SOCIAL SCIENCES 06. DIGITAL LABORATORY 07. DIGITAL LABORATORY	PP PP PP TW PR	100 100 100 100 100 50	40 40 40 40 40 20 20	N. P. SAN 63 41 56 59 42 33 29 37	ASS MARKS, GITA P C P C P C P C P C P C P C	, MARI 11. 12. 13. 14. 15. 16. 17.	KS OBTAINED, P/F, 71045402D ENG MATHS III COMPUTER GRAPHIC PROCESSOR ARCHIT DATA STRUCTURES DATA COMMUNICATI PROCESSOR INTERF	F:PASS/FAIL, C:PASS/FAIL, C:PAS	PICT PP PP PP PP TW OR	100 100 100 100 100 25 50	40 40 40 40 40 20	74 57 65 66 72 18 38	P P P P P P
OTHER LINES: HEAD OF PASSING, S8058520 CHAUDHAR SATISH RAOSAHEB 01. DISCRETE STRUCTURES 02. COMPUTER ORGANIZATION 03. DIGITAL ELECTRONICS & LOGIC DEST 04. FUNDAMENTAL OF DATA STRUCTURES 05. HUMANITIES AND SOCIAL SCIENCES 06. DIGITAL LABORATORY 07. DIGITAL LABORATORY 08. PROGRAMMING LABORATORY	PP PP PP TW PR TW PR	100 100 100 100 100 50 50	40 40 40 40 40 20 20	N. P. SAN 63 41 56 59 42 33 29 37 41	ASS MARKS, GITA P C P C P C P C P C P C P C	, MARI	RS OBTAINED, P/F, 71045402D ENG MATHS III COMPUTER GRAPHIC PROCESSOR ARCHIT DATA STRUCTURES DATA COMMUNICATI PROCESSOR INTERF PROCESSOR INTERF DATA STRUCTURES DATA STRUCTURES	F:PASS/FAIL, C:PASS/FAIL, C:PAS	PICT PP PP PP PP TW OR TW PR	100 100 100 100 100 25 50 25	40 40 40 40 40 10 20	74 57 65 66 72 18 38 15	P P P P P P P

GRAND TOTAL = 925/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS:

S8058521 CHAUDHARI AMAR SHRIKANT				SHU	JBHANGI	, 71045403B , S8058521 , PICT , S805	8521
01. DISCRETE STRUCTURES	PP	100	40	52	P C	1. ENG MATHS III PP 100 40	40 P
02. COMPUTER ORGANIZATION	PP	100	40	44	P C	2. COMPUTER GRAPHICS PP 100 40	51 P
03. DIGITAL ELECTRONICS & LOGIC DESI	GPP	100	40	58	P C	3. PROCESSOR ARCHITECTURE & INTER. PP 100 40	54 P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	59	P C	4. DATA STRUCTURES AND FILES PP 100 40	64 P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	49	P C	.5. DATA COMMUNICATION PP 100 40	61 P
06. DIGITAL LABORATORY	TW	50	20	36	P C	.6. PROCESSOR INTERFACING LABORATORY TW 25 10	21 P
07. DIGITAL LABORATORY	PR	50	20	35	P C	7. PROCESSOR INTERFACING LABORATORY OR 50 20	44 P
08. PROGRAMMING LABORATORY	TW	50	20	38	P C	.8. DATA STRUCTURES AND FILES LAB TW 25 10	15 P
09. PROGRAMMING LABORATORY	PR	50	20	45	P C	9. DATA STRUCTURES AND FILES LAB PR 50 20	39 P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	38	P C	0. OBJECT ORIENTED PROGRAMMING LAB TW 50 20	38 P
						1. OBJECT ORIENTED PROGRAMMING LAB PR 50 20	32 P

GRAND TOTAL = 913/1500, RESULT: FIRST CLASS
ORDN. 1 MARKS :

S8058522 CHAVAN PUSHPAK VILAS				SUR	EKHA	,	71134947к	, s8058522 ,	PICT		, s80	58522	2
01. DISCRETE STRUCTURES	PP	100	40	58	P C	11. ENG	MATHS III		PP	100	40	40	Р
02. COMPUTER ORGANIZATION	PP	100	40	62	P C	12. COMP	UTER GRAPHICS		PP	100	40	58	Р
03. DIGITAL ELECTRONICS & LOGIC DES	IGPP	100	40	57	P C	13. PROC	ESSOR ARCHITE	CTURE & INTER.	PP	100	40	54	Р
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	52	P C	14. DATA	STRUCTURES A	ND FILES	PP	100	40	56	Р
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	56	P C	15. DATA	COMMUNICATION	N	PP	100	40	66	Р
06. DIGITAL LABORATORY	TW	50	20	36	P C	16. PROC	ESSOR INTERFA	CING LABORATORY	TW	25	10	16	Р
07. DIGITAL LABORATORY	PR	50	20	23	P C	17. PROC	ESSOR INTERFA	CING LABORATORY	OR	50	20	35	Р
08. PROGRAMMING LABORATORY	TW	50	20	38	P C	18. DATA	STRUCTURES A	ND FILES LAB	TW	25	10	18	Р

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09. PROGRAMMING LABORATORY	PR	50	20	33	P C	19. DATA STRUC	cols05 CTURES AND FILES L	.AB PR	50	20	12	F
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	31	P C	20. OBJECT ORI	ENTED PROGRAMMING	LAB TW	50	20	43	Р
						21. OBJECT ORI	ENTED PROGRAMMING	LAB PR	50	20	20	Р
GRAND TOTAL = 864/1500, RESULT: FAILS	6 A.T.	к.т.										
ORDN. 1 MARKS :												
UNIVERSITY OF	PUNE	,S.E.	(2008	PAT	.)(INFO	MATION TECHNOLOGY)						•
DATE : 18 AUG. 2011	CENT	RE : F	PUNE]	INSTI	TUTE O	COMPUTER TECHNOLOG	SY, PUNE.	P	AGE NO.	08	(4	30)
								COLL	ECE.	CEAT	NO	
NOTE: FIRST LINE : SEAT NO., NAME O	OF THE	CAND:	IDATE,	, MO	THER,	ERMANENT REG. NO.,	PREVIOUS SEAT NO	o., COLL	EGE,	SEAT		
NOTE: FIRST LINE : SEAT NO., NAME OF OTHER LINES: HEAD OF PASSING,												
				[N. P), P/F:PASS/FAIL,	C:PREV	IOUS CA	RRY O		
OTHER LINES: HEAD OF PASSING,				[N. P	PASS MAI	KS, MARKS OBTAINED	0, P/F:PASS/FAIL, 	C:PREV	IOUS CA	RRY O	VER	
OTHER LINES: HEAD OF PASSING, S8058524 DARP YOGESH RAMCHANDRA	MAX.	MARKS	S, MI	IN. P	PASS MAI	(S, MARKS OBTAINED	0, P/F:PASS/FAIL, 948H , S805852	C:PREV	IOUS CA	RRY 0	VER 05852	F
OTHER LINES: HEAD OF PASSING, S8058524 DARP YOGESH RAMCHANDRA 01. DISCRETE STRUCTURES	MAX.	MARKS	5, M3	IN. P SAN 48	PASS MAI IDHYA P C	(S, MARKS OBTAINED , 711349 11. ENG MATHS 12. COMPUTER G	0, P/F:PASS/FAIL, 948H , S805852	C:PREV	IOUS CA	RRY O	VER 05852 20	F P
OTHER LINES: HEAD OF PASSING, S8058524 DARP YOGESH RAMCHANDRA O1. DISCRETE STRUCTURES O2. COMPUTER ORGANIZATION	MAX.	100 100	5, M ³ 40 40	SAN 48 46	PASS MAI IDHYA PC PC	, 711349 11. ENG MATHS 12. COMPUTER G 13. PROCESSOR	0, P/F:PASS/FAIL, 	C:PREV	OUS CA CT 100 100	RRY 0' , S8 40 40	VER 05852 20 46	F P P
OTHER LINES: HEAD OF PASSING, S8058524 DARP YOGESH RAMCHANDRA O1. DISCRETE STRUCTURES O2. COMPUTER ORGANIZATION O3. DIGITAL ELECTRONICS & LOGIC DESI	MAX. PP PP	100 100 100	40 40 40 40	SAN 48 46 43	PASS MAI IDHYA PC PC PC	, 711349 11. ENG MATHS 12. COMPUTER G 13. PROCESSOR	P/F:PASS/FAIL, 948H , S805852 III GRAPHICS ARCHITECTURE & IN	C:PREV	10US CA TT 100 100 100	RRY 0' , \$8 40 40 40	VER 05852 20 46 56	F P P
OTHER LINES: HEAD OF PASSING, S8058524 DARP YOGESH RAMCHANDRA O1. DISCRETE STRUCTURES O2. COMPUTER ORGANIZATION O3. DIGITAL ELECTRONICS & LOGIC DESI O4. FUNDAMENTAL OF DATA STRUCTURES	MAX. PP PP CGPP PP	100 100 100 100	40 40 40 40 40	SAN 48 46 43 48	PASS MAI DHYA PC PC PC PC	, 711349 11. ENG MATHS 12. COMPUTER G 13. PROCESSOR 14. DATA STRUC	P/F:PASS/FAIL, 948H , S805852 III GRAPHICS ARCHITECTURE & IN	C:PREV	100S CA 100 100 100 100	RRY 0' , S8 40 40 40 40	VER 05852 20 46 56 51	F P P
OTHER LINES: HEAD OF PASSING, S8058524 DARP YOGESH RAMCHANDRA O1. DISCRETE STRUCTURES O2. COMPUTER ORGANIZATION O3. DIGITAL ELECTRONICS & LOGIC DESI O4. FUNDAMENTAL OF DATA STRUCTURES O5. HUMANITIES AND SOCIAL SCIENCES	PP PP PP PP	100 100 100 100 100	40 40 40 40 40 40	SAN 48 46 43 48 45 32	PASS MAI DHYA PC PC PC PC PC	, 711349 11. ENG MATHS 12. COMPUTER G 13. PROCESSOR 14. DATA STRUC 15. DATA COMMU 16. PROCESSOR	P/F:PASS/FAIL, 948H , S805852 III GRAPHICS ARCHITECTURE & IN CTURES AND FILES UNICATION	C:PREV	10US CA 100 100 100 100 100	RRY O' , S8 40 40 40 40 40	VER 05852 20 46 56 51 53	F P P P
OTHER LINES: HEAD OF PASSING, S8058524 DARP YOGESH RAMCHANDRA O1. DISCRETE STRUCTURES O2. COMPUTER ORGANIZATION O3. DIGITAL ELECTRONICS & LOGIC DESI O4. FUNDAMENTAL OF DATA STRUCTURES O5. HUMANITIES AND SOCIAL SCIENCES O6. DIGITAL LABORATORY	PP PP PP TW	100 100 100 100 100 50	40 40 40 40 40 40 20	SAN 48 46 43 48 45 32	PASS MAI IDHYA PC PC PC PC PC PC	, 711349 11. ENG MATHS 12. COMPUTER G 13. PROCESSOR 14. DATA STRUC 15. DATA COMMU 16. PROCESSOR 17. PROCESSOR	P/F:PASS/FAIL, O, P/F:PASS/FAIL, O48H , S805852 III GRAPHICS ARCHITECTURE & INCITURES AND FILES UNICATION INTERFACING LABOR	C:PREVENTED COMMENT OF	10US CA TT 100 100 100 100 25	RRY O' , S8 40 40 40 40 40 10	VER 05852 20 46 56 51 53 17	F P P P
OTHER LINES: HEAD OF PASSING, S8058524 DARP YOGESH RAMCHANDRA 01. DISCRETE STRUCTURES 02. COMPUTER ORGANIZATION 03. DIGITAL ELECTRONICS & LOGIC DESI 04. FUNDAMENTAL OF DATA STRUCTURES 05. HUMANITIES AND SOCIAL SCIENCES 06. DIGITAL LABORATORY 07. DIGITAL LABORATORY	PP PP PP TW PR	100 100 100 100 100 50	40 40 40 40 40 20 20	SAN 48 46 43 48 45 32 24	PASS MAI IDHYA PC PC PC PC PC PC PC	, 711349 11. ENG MATHS 12. COMPUTER G 13. PROCESSOR 14. DATA STRUC 15. DATA COMMU 16. PROCESSOR 17. PROCESSOR 18. DATA STRUC	P/F:PASS/FAIL, OF P/F:PASS/FAIL	C:PREVENTED COMMENT OF	100S CA CT 100 100 100 100 25 50	RRY O , \$8 40 40 40 40 10 20	VER 05852 20 46 56 51 53 17 35	F P P P P
OTHER LINES: HEAD OF PASSING, S8058524 DARP YOGESH RAMCHANDRA O1. DISCRETE STRUCTURES O2. COMPUTER ORGANIZATION O3. DIGITAL ELECTRONICS & LOGIC DESI O4. FUNDAMENTAL OF DATA STRUCTURES O5. HUMANITIES AND SOCIAL SCIENCES O6. DIGITAL LABORATORY O7. DIGITAL LABORATORY O8. PROGRAMMING LABORATORY	MAX PP PP CGPP PP TW PR TW	100 100 100 100 100 50 50	40 40 40 40 40 20 20 20	SAN 48 46 43 48 45 32 24 39	PASS MAI IDHYA PC PC PC PC PC PC PC PC	, 711349 11. ENG MATHS 12. COMPUTER G 13. PROCESSOR 14. DATA STRUC 15. DATA COMMU 16. PROCESSOR 17. PROCESSOR 18. DATA STRUC 19. DATA STRUC	P/F:PASS/FAIL, OF P/F:PASS/FAIL	C:PREVENTED COMMENT OF	10US CA CT 100 100 100 100 25 50 25	RRY O , \$8 40 40 40 40 10 20 10	VER 05852 20 46 56 51 53 17 35 15	F P P P P

GRAND TOTAL = 778/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

, 71134949F , S8058525 , PICT Page 193 , s8058525 S8058525 DAWANGE VISHAL SANJAY KALPANA

01. DI	ISCRETE STRUCTURES	PP	100	40	49	P C	11.	ENG MATHS III	PP	100	40	42	Р
02. CC	OMPUTER ORGANIZATION	PP	100	40	53	P C	12.	COMPUTER GRAPHICS	PP	100	40	57	Р
03. DI	GITAL ELECTRONICS & LOGIC DESIG	GPP	100	40	49	P C	13.	PROCESSOR ARCHITECTURE & INTER.	PP	100	40	63	Р
04. FU	INDAMENTAL OF DATA STRUCTURES	PP	100	40	65	P C	14.	DATA STRUCTURES AND FILES	PP	100	40	73	Р
05. н	JMANITIES AND SOCIAL SCIENCES	PP	100	40	52	P C	15.	DATA COMMUNICATION	PP	100	40	68	Р
06. DI	IGITAL LABORATORY	TW	50	20	37	P C	16.	PROCESSOR INTERFACING LABORATORY	TW	25	10	19	Р
07. DI	IGITAL LABORATORY	PR	50	20	27	P C	17.	PROCESSOR INTERFACING LABORATORY	OR	50	20	30	Р
08. PF	ROGRAMMING LABORATORY	TW	50	20	39	P C	18.	DATA STRUCTURES AND FILES LAB	TW	25	10	18	Р
09. PF	ROGRAMMING LABORATORY	PR	50	20	44	P C	19.	DATA STRUCTURES AND FILES LAB	PR	50	20	37	Р
10. cc	DMMUNICATION AND LANGUAGE LAB.	TW	50	20	29	P C	20.	OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	41	Р
							21.	OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	20	Р

GRAND TOTAL = 912/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS:

S8058526 DESHPANDE KIRTI VINOD				SHA	LINI		, 71134950к , s8058526 ,	PICT		, s80	58526	6
01. DISCRETE STRUCTURES	PP	100	40	44	РС	11	. ENG MATHS III	PP	100	40	23	F
02. COMPUTER ORGANIZATION	PP	100	40	49	P C	12	. COMPUTER GRAPHICS	PP	100	40	58	Р
03. DIGITAL ELECTRONICS & LOGIC DESI	GPP	100	40	60	P C	13	PROCESSOR ARCHITECTURE & INTER.	PP	100	40	53	Р
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	55	P C	14	DATA STRUCTURES AND FILES	PP	100	40	54	Р
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	51	P C	15	. DATA COMMUNICATION	PP	100	40	69	Р
06. DIGITAL LABORATORY	TW	50	20	29	P C	16	PROCESSOR INTERFACING LABORATORY	TW	25	10	19	Р
07. DIGITAL LABORATORY	PR	50	20	22	P C	17	PROCESSOR INTERFACING LABORATORY	OR	50	20	27	Р
08. PROGRAMMING LABORATORY	TW	50	20	37	P C	18	DATA STRUCTURES AND FILES LAB	TW	25	10	16	Р
09. PROGRAMMING LABORATORY	PR	50	20	27	P C	19	DATA STRUCTURES AND FILES LAB	PR	50	20	10	F
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	30	P C	20	OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	41	Р
						21	OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	31	Р

GRAND TOTAL = 805+05/1500, RESULT: FAILS A.T.K.T.[0.163]

UNIVERSITY OF	PUNE	 ,S.E.	(2008	PAT.)(INI	FORMATION	TECHNOLOGY)					
DATE : 18 AUG. 2011	CENT	RE : I	PUNE I	NSTI	ГИТЕ	OF COMPUT	ER TECHNOLOGY, PUNE.	PAG	E NO.	09	(43	31)
NOTE: FIRST LINE : SEAT NO., NAME O	F THE	CAND	IDATE,	MO	ΓHER,	, PERMANEN	REG. NO., PREVIOUS SEAT NO., C	OLLEG	Ε, 9	SEAT N	ΙΟ.	
OTHER LINES: HEAD OF PASSING,	MAX.	MARKS	S, MI	N. PA	ASS M	MARKS, MA	RKS OBTAINED, P/F:PASS/FAIL, C:P	REVIO	US CAI	RY OV	ER	
S8058527 DESHPANDE NACHIKET CHANDE	RAKANT	-		KIS	HORI		, 71134951н , ѕ8058527 ,	PICT		, s80	58527	7
01. DISCRETE STRUCTURES	PP	100	40	43	P C	11	ENG MATHS III	PP	100	40	12	F
02. COMPUTER ORGANIZATION	PP	100	40	41	P C	12	COMPUTER GRAPHICS	PP	100	40	51	Р
03. DIGITAL ELECTRONICS & LOGIC DESI	GPP	100	40	51	P C	13	PROCESSOR ARCHITECTURE & INTER.	PP	100	40	61	Р
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	53	P C	14	DATA STRUCTURES AND FILES	PP	100	40	61	Р
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	53	P C	15	DATA COMMUNICATION	PP	100	40	59	Р
06. DIGITAL LABORATORY	TW	50	20	34	P C	16	PROCESSOR INTERFACING LABORATORY	TW	25	10	18	Р
07. DIGITAL LABORATORY	PR	50	20	35	P C	17	PROCESSOR INTERFACING LABORATORY	OR	50	20	29	Р
08. PROGRAMMING LABORATORY	TW	50	20	39	P C	18	DATA STRUCTURES AND FILES LAB	TW	25	10	19	Р
09. PROGRAMMING LABORATORY	PR	50	20	36	P C	19	DATA STRUCTURES AND FILES LAB	PR	50	20	30	Р
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	38	P C	20	OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	43	Р
						21	OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	30	Р
GRAND TOTAL = 836/1500, RESULT: FAILS	6 A.T.	к.т.										
ORDN. 1 MARKS :												
S8058528 DHADIWAL KOMAL AJIT				ASHA	A		, 71134952F , S8058528 ,	PICT		, s80	58528	3
01. DISCRETE STRUCTURES	PP	100	40	41	P C	11	ENG MATHS III	PP	100	40	40	Р
02. COMPUTER ORGANIZATION	PP	100	40	48	P C	12	COMPUTER GRAPHICS	PP	100	40	55	Р
03. DIGITAL ELECTRONICS & LOGIC DESI	GPP	100	40	49	P C	13	PROCESSOR ARCHITECTURE & INTER.	PP	100	40	44	Р
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	59	P C	14	DATA STRUCTURES AND FILES	PP	100	40	62	Р

05	HUMANITIES AND SOCIAL SCIENCES	PP	100	40	54	P C	15	cols05 DATA COMMUNICATION	PP	100	40	54	Р
06	DIGITAL LABORATORY	TW	50	20	37	P C	16	. PROCESSOR INTERFACING LABORA	TORY TW	25	10	19	Р
07	DIGITAL LABORATORY	PR	50	20	22	P C	17	. PROCESSOR INTERFACING LABORA	TORY OR	50	20	28	Р
08	PROGRAMMING LABORATORY	TW	50	20	43	P C	18	. DATA STRUCTURES AND FILES LA	AB TW	25	10	19	Р
09	PROGRAMMING LABORATORY	PR	50	20	37	P C	19	. DATA STRUCTURES AND FILES LA	AB PR	50	20	43	Р
10	COMMUNICATION AND LANGUAGE LAB	. TW	50	20	38	P C	20	. OBJECT ORIENTED PROGRAMMING	LAB TW	50	20	45	Р
							21	. OBJECT ORIENTED PROGRAMMING	LAB PR	50	20	39	Р
	TOTAL = 876/1500, RESULT: HIGH	HER SEC	COND CL	-ASS									
JKUN	1 MARKS :												
S8(058529 DHULE AKSHAYKUMAR MAHADI	EVAPPA			SHI	VANI		, 71045416D , S8058529), PI(т	, s80)58529	9

S8058529 DHULE AKSHAYKUMAR MA	HADEVAPPA			SHIV	'ANI		, 71045416D	, s8058529 ,	PICT		, s80	58529	}
01. DISCRETE STRUCTURES	PP	100	40	75	РС	11	ENG MATHS III		PP	100	40	51	Р
02. COMPUTER ORGANIZATION	PP	100	40	50	РС	12	COMPUTER GRAPHIC	S	PP	100	40	56	Р
03. DIGITAL ELECTRONICS & LOGIC	DESIGPP	100	40	40	РС	13	PROCESSOR ARCHIT	ECTURE & INTER.	PP	100	40	58	Р
04. FUNDAMENTAL OF DATA STRUCTU	RES PP	100	40	43	РС	14	DATA STRUCTURES	AND FILES	PP	100	40	64	Р
05. HUMANITIES AND SOCIAL SCIEN	CES PP	100	40	50	РС	15	DATA COMMUNICATI	ON	PP	100	40	69	Р
06. DIGITAL LABORATORY	TW	50	20	33	РС	16	PROCESSOR INTERF	ACING LABORATORY	TW	25	10	16	Р
07. DIGITAL LABORATORY	PR	50	20	24	Р	17	PROCESSOR INTERF	ACING LABORATORY	OR	50	20	12	F
08. PROGRAMMING LABORATORY	TW	50	20	40	РС	18	DATA STRUCTURES	AND FILES LAB	TW	25	10	14	Р
09. PROGRAMMING LABORATORY	PR	50	20	34	РС	19	DATA STRUCTURES	AND FILES LAB	PR	50	20	30	Р
10. COMMUNICATION AND LANGUAGE	LAB. TW	50	20	36	РС	20	OBJECT ORIENTED	PROGRAMMING LAB	TW	50	20	33	Р
						21	OBJECT ORIENTED	PROGRAMMING LAB	PR	50	20	10	F

GRAND TOTAL = 838/1500, RESULT: FAILS A.T.K.T.
ORDN. 1 MARKS :

UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(INFORMATION TECHNOLOGY)

DATE: 18 AUG. 2011 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 10 (432)

NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8058530 DIKE ASHISH VIJAY				RAJ	ANI		, 71045418L	, s8058530 ,	PICT		, s80	5853	0
01. DISCRETE STRUCTURES	PP	100	40	51	P C	11.	ENG MATHS III		PP	100	40	10	F
02. COMPUTER ORGANIZATION	PP	100	40	40	P C	12.	COMPUTER GRAPH	ICS	PP	100	40	41	Р
03. DIGITAL ELECTRONICS & LOGIC DES	EGPP	100	40	43	P C	13.	PROCESSOR ARCH	ITECTURE & INTER.	PP	100	40	13	F
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	40	P C	14.	DATA STRUCTURE	S AND FILES	PP	100	40	45	Р
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	50	P C	15.	DATA COMMUNICA	TION	PP	100	40	29	F
06. DIGITAL LABORATORY	TW	50	20	38	P C	16.	PROCESSOR INTE	RFACING LABORATORY	TW	25	10	16	Р
07. DIGITAL LABORATORY	PR	50	20	36	P C	17.	PROCESSOR INTE	RFACING LABORATORY	OR	50	20	24	Р
08. PROGRAMMING LABORATORY	TW	50	20	35	P C	18.	DATA STRUCTURE	S AND FILES LAB	TW	25	10	12	Р
09. PROGRAMMING LABORATORY	PR	50	20	27	Р	19.	DATA STRUCTURE	S AND FILES LAB	PR	50	20	AA	F
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	38	P C	20.	OBJECT ORIENTE	D PROGRAMMING LAB	TW	50	20	34	Р
						21.	OBJECT ORIENTE	D PROGRAMMING LAB	PR	50	20	30	Р

GRAND TOTAL = 652/1500, RESULT: FAILS A.T.K.T.
ORDN. 1 MARKS :

S8058531 DIVEKAR SANDIP LAXMAN				SHO	ОВНА	, 71134953D , S8058531 , PICT , S8058	531
01. DISCRETE STRUCTURES	PP	100	40	69	P C	11. ENG MATHS III PP 100 40 4	6 P
02. COMPUTER ORGANIZATION	PP	100	40	44	P C	12. COMPUTER GRAPHICS PP 100 40 6	2 P
03. DIGITAL ELECTRONICS & LOGIC DES	IGPP	100	40	67	P C	13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 6	4 P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	67	PС	14. DATA STRUCTURES AND FILES PP 100 40 7	0 Р
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	51	P C	15. DATA COMMUNICATION PP 100 40 6	4 P
06. DIGITAL LABORATORY	TW	50	20	38	P C	16. PROCESSOR INTERFACING LABORATORY TW 25 10 2	1 P
07. DIGITAL LABORATORY	PR	50	20	22	P C	17. PROCESSOR INTERFACING LABORATORY OR 50 20 3	9 P
08. PROGRAMMING LABORATORY	TW	50	20	43	PС	18. DATA STRUCTURES AND FILES LAB TW 25 10 2	1 P
09. PROGRAMMING LABORATORY	PR	50	20	43	P C	19. DATA STRUCTURES AND FILES LAB PR 50 20 3	5 P

10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	39	РC	20.	OBJECT ORIENTED	cols05 PROGRAMMING LAB	TW	50	20	42	Р
						21.	OBJECT ORIENTED	PROGRAMMING LAB	PR	50	20	44	Р
GRAND TOTAL = 991/1500, RESULT: FIRST	· CLAS	S WITH	DIST	INCT	ION								
ORDN. 1 MARKS :													
-005050							74124054-	-0050500			- 00		•
S8058532 DUMAVAT KUNAL BHAVARLAL				VID	ΥA		, 71134954в	, s8058532 ,	PICT		, s80	58534	2
01. DISCRETE STRUCTURES	PP	100	40	40	P C	11.	ENG MATHS III		PP	100	40	40	Р
02. COMPUTER ORGANIZATION	PP	100	40	43	P C	12.	COMPUTER GRAPHI	CS	PP	100	40	57	Р
03. DIGITAL ELECTRONICS & LOGIC DESI	.GPP	100	40	62	P C	13.	PROCESSOR ARCHI	TECTURE & INTER.	PP	100	40	59	Р
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	46	P C	14.	DATA STRUCTURES	AND FILES	PP	100	40	41	Р
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	45	P C	15.	DATA COMMUNICAT	TION	PP	100	40	58	Р
06. DIGITAL LABORATORY	TW	50	20	36	P C	16.	PROCESSOR INTER	FACING LABORATORY	TW	25	10	17	Р
07. DIGITAL LABORATORY	PR	50	20	32	P C	17.	PROCESSOR INTER	FACING LABORATORY	OR	50	20	38	Р
08. PROGRAMMING LABORATORY	TW	50	20	41	P C	18.	DATA STRUCTURES	AND FILES LAB	TW	25	10	21	Р
09. PROGRAMMING LABORATORY	PR	50	20	28	P C	19.	DATA STRUCTURES	AND FILES LAB	PR	50	20	25	Р
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	35	P C	20.	OBJECT ORIENTED	PROGRAMMING LAB	TW	50	20	43	Р
						21.	OBJECT ORIENTED	PROGRAMMING LAB	PR	50	20	27	Р
GRAND TOTAL = 834/1500, RESULT: HIGHE	D SEC	OND CI	۸۵۵										
ORDN. 1 MARKS :	K SEC	OND CL	A33										
ORDN. I MARKS .													
UNIVERSITY OF	PUNE	,S.E.(2008	PAT	.)(INFOR	DRMATION -	ECHNOLOGY)						
DATE : 18 AUG. 2011	CENT	RE : P	UNE I	NSTI	TUTE OF	F COMPUTE	R TECHNOLOGY, PU	NE.	PAGI	E NO.	11	(43	33)
NOTE: FIRST LINE : SEAT NO., NAME O	F THE	CANDI	DATE,	МО	THER, P	PERMANENT	REG. NO., PREV	TIOUS SEAT NO., C	OLLEGI	Ε, S	EAT N	0.	
OTHER LINES: HEAD OF PASSING,	MAX.	MARKS	, MI	N. P	ASS MAR	RKS, MAR	KS OBTAINED, P/	F:PASS/FAIL, C:P	REVIO	JS CAR	RY OV	ER	
S8058533 EKHANDE DNYANESHWAR KACHA	.RU			VIT	HABAI		, 71134955L	, s8058533 ,	PICT		, s80	5853	3

						cols05	
01. DISCRETE STRUCTURES	PP	100	40	52	P C	11. ENG MATHS III PP 100 40 43	Р
02. COMPUTER ORGANIZATION	PP	100	40	68	P C	12. COMPUTER GRAPHICS PP 100 40 65	Р
03. DIGITAL ELECTRONICS & LOGIC DESI	IGPP	100	40	69	P C	13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 67	Р
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	74	P C	14. DATA STRUCTURES AND FILES PP 100 40 45	Р
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	57	P C	15. DATA COMMUNICATION PP 100 40 65	Р
06. DIGITAL LABORATORY	TW	50	20	39	P C	16. PROCESSOR INTERFACING LABORATORY TW 25 10 20	Р
07. DIGITAL LABORATORY	PR	50	20	32	P C	17. PROCESSOR INTERFACING LABORATORY OR 50 20 38	Р
08. PROGRAMMING LABORATORY	TW	50	20	41	P C	18. DATA STRUCTURES AND FILES LAB TW 25 10 17	Р
09. PROGRAMMING LABORATORY	PR	50	20	45	P C	19. DATA STRUCTURES AND FILES LAB PR 50 20 40	Р
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	29	P C	20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 41	. Р
						21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 38	Р

GRAND TOTAL = 985/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS:

S8058534 GAD	KARI ASHISH VASANTRAO				SANG	GITA		, 71134956〕	, s8058534 ,	PICT		, s80	58534	4
01. DISCRETE ST	TRUCTURES	PP	100	40	61	РС	11.	ENG MATHS III		PP	100	40	42	Р
02. COMPUTER OF	RGANIZATION	PP	100	40	52	P C	12.	COMPUTER GRAP	HICS	PP	100	40	63	Р
03. DIGITAL ELE	ECTRONICS & LOGIC DESI	GPP	100	40	60	P C	13.	PROCESSOR ARC	HITECTURE & INTER.	PP	100	40	67	Р
04. FUNDAMENTAL	L OF DATA STRUCTURES	PP	100	40	63	P C	14.	DATA STRUCTUR	ES AND FILES	PP	100	40	63	Р
05. HUMANITIES	AND SOCIAL SCIENCES	PP	100	40	45	P C	15.	DATA COMMUNIC	ATION	PP	100	40	60	Р
06. DIGITAL LA	BORATORY	TW	50	20	38	P C	16.	PROCESSOR INT	ERFACING LABORATORY	TW	25	10	18	Р
07. DIGITAL LA	BORATORY	PR	50	20	33	P C	17.	PROCESSOR INT	ERFACING LABORATORY	OR	50	20	29	Р
08. PROGRAMMING	G LABORATORY	TW	50	20	44	P C	18.	DATA STRUCTUR	ES AND FILES LAB	TW	25	10	20	Р
09. PROGRAMMING	G LABORATORY	PR	50	20	38	P C	19.	DATA STRUCTUR	ES AND FILES LAB	PR	50	20	35	Р
10. COMMUNICATI	ION AND LANGUAGE LAB.	TW	50	20	39	P C	20.	OBJECT ORIENT	ED PROGRAMMING LAB	TW	50	20	40	Р
							21.	OBJECT ORIENT	ED PROGRAMMING LAB	PR	50	20	43	Р

GRAND TOTAL = 953/1500, RESULT: FIRST CLASS

S8058535 GAIKWAD AKSHAY SHAHAJI				MAH	ANANDA		, 71045430K , S8058535 ,	PICT		, s80	5853	5
01. DISCRETE STRUCTURES	PP	100	40	60	P C	11.	ENG MATHS III	PP	100	40	71	Р
02. COMPUTER ORGANIZATION	PP	100	40	56	P C	12.	COMPUTER GRAPHICS	PP	100	40	52	Р
03. DIGITAL ELECTRONICS & LOGIC DESI	GPP	100	40	77	P C	13.	PROCESSOR ARCHITECTURE & INTER.	PP	100	40	47	Р
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	54	P C	14.	DATA STRUCTURES AND FILES	PP	100	40	52	Р
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	40	P C	15.	DATA COMMUNICATION	PP	100	40	59	Р
06. DIGITAL LABORATORY	TW	50	20	22	P C	16.	PROCESSOR INTERFACING LABORATORY	TW	25	10	10	Р
07. DIGITAL LABORATORY	PR	50	20	40	P C	17.	PROCESSOR INTERFACING LABORATORY	OR	50	20	13	F
08. PROGRAMMING LABORATORY	TW	50	20	21	P C	18.	DATA STRUCTURES AND FILES LAB	TW	25	10	12	Р
09. PROGRAMMING LABORATORY	PR	50	20	40	P C	19.	DATA STRUCTURES AND FILES LAB	PR	50	20	40	Р
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	27	P C	20.	OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	20	Р
						21.	OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	22	Р
GRAND TOTAL = 835/1500, RESULT: FAILS ORDN. 1 MARKS:			(2008	 PAT.)(INFORMA	 TION T	 ECHNOLOGY)					
DATE : 18 AUG. 2011	CENT	RE : F	UNE I	NSTI ⁻	TUTE OF CO	OMPUTEI	R TECHNOLOGY, PUNE.	PAG	E NO.	12	(4	34)
,	MAX.	MARKS	, MI	N. P	ASS MARKS,	, MARI	REG. NO., PREVIOUS SEAT NO., CO	REVIO	US CAR		ER	
S8058536 GAIKWAD SAGAR ANAND					EKHA		, 71045773в , ѕ8058536 ,	PICT		, s80		
01. DISCRETE STRUCTURES	PP	100	40	51	РС	11.	ENG MATHS III	PP	100	40	44	Р
02. COMPUTER ORGANIZATION	PP	100	40		PC		COMPUTER GRAPHICS	PP	100	40	52	
03. DIGITAL ELECTRONICS & LOGIC DESI		100	40		PC		PROCESSOR ARCHITECTURE & INTER.		100	40	69	
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40		PC		DATA STRUCTURES AND FILES	PP	100	40	66	
05. HUMANITIES AND SOCIAL SCIENCES		100	40		P C		DATA COMMUNICATION	PP	100	40	55	
The state of the s			. •	. •				• •		. •		•

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cols05 06. DIGITAL LABORATORY 50 20 25 P C 16. PROCESSOR INTERFACING LABORATORY TW 25 10 17 P 07. DIGITAL LABORATORY PR 50 20 27 P C 17. PROCESSOR INTERFACING LABORATORY OR 50 20 24 P 08. PROGRAMMING LABORATORY 50 20 39 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 15 P 09. PROGRAMMING LABORATORY 50 20 45 P C 19. DATA STRUCTURES AND FILES LAB PR 50 20 42 P 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 39 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 39 P 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 28 P

GRAND TOTAL = 884/1500, RESULT: HIGHER SECOND CLASS ORDN. 1 MARKS:

S8058537 GEHANI ASHISH MOHAN				ВНА	RTI		, 71134957G	, s8058537 ,	PICT		, s80	5853	7
01. DISCRETE STRUCTURES	PP	100	40	72	P C	11. ENG	G MATHS III		PP	100	40	73	Р
02. COMPUTER ORGANIZATION	PP	100	40	73	P C	12. COM	MPUTER GRAPHICS	S	PP	100	40	62	Р
03. DIGITAL ELECTRONICS & LOGIC DESI	GPP	100	40	73	P C	13. PRO	OCESSOR ARCHIT	ECTURE & INTER.	PP	100	40	70	Р
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	79	P C	14. DA	TA STRUCTURES	AND FILES	PP	100	40	76	Р
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	50	P C	15. DA	TA COMMUNICATION	ON	PP	100	40	76	Р
06. DIGITAL LABORATORY	TW	50	20	40	P C	16. PR	OCESSOR INTERF	ACING LABORATORY	TW	25	10	24	Р
07. DIGITAL LABORATORY	PR	50	20	35	P C	17. PR	OCESSOR INTERF	ACING LABORATORY	OR	50	20	47	Р
08. PROGRAMMING LABORATORY	TW	50	20	43	P C	18. DA	TA STRUCTURES	AND FILES LAB	TW	25	10	24	Р
09. PROGRAMMING LABORATORY	PR	50	20	47	P C	19. DA	TA STRUCTURES	AND FILES LAB	PR	50	20	45	Р
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	42	P C	20. OB:	JECT ORIENTED	PROGRAMMING LAB	TW	50	20	43	Р
						21. OB:	JECT ORIENTED	PROGRAMMING LAB	PR	50	20	46	Р

GRAND TOTAL = 1140/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS :

S8058538 GHADGE TEJAL RAJENDRA				ROHINI	, 71134958E , S8058538 ,	PIC	Т	, s80	05853	8
01. DISCRETE STRUCTURES	PP	100	40	59 P C	11. ENG MATHS III	PP	100	40	40	Р
02. COMPUTER ORGANIZATION	PP	100	40	63 P C	12. COMPUTER GRAPHICS Page 201	PP	100	40	59	Р

03. DIGITAL ELECTRONICS & LOGIC DESI	GPP	100	40	60	P C	13.	PROCESSOR ARCHITECTURE & INTER.	PP	100	40	58	Р
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	58	P C	14.	DATA STRUCTURES AND FILES	PP	100	40	73	Р
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	53	P C	15.	DATA COMMUNICATION	PP	100	40	66	Р
06. DIGITAL LABORATORY	TW	50	20	39	P C	16.	PROCESSOR INTERFACING LABORATORY	TW	25	10	20	Р
07. DIGITAL LABORATORY	PR	50	20	36	P C	17.	PROCESSOR INTERFACING LABORATORY	OR	50	20	42	Р
08. PROGRAMMING LABORATORY	TW	50	20	43	P C	18.	DATA STRUCTURES AND FILES LAB	TW	25	10	22	Р
09. PROGRAMMING LABORATORY	PR	50	20	41	P C	19.	DATA STRUCTURES AND FILES LAB	PR	50	20	38	Р
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	38	P C	20.	OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	44	Р
						21.	OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	29	Р
GRAND TOTAL = 981/1500, RESULT: FIRST ORDN. 1 MARKS:	CLAS	SS										
			(2008	 DAT) (TNEODN	 MATION T	ECHNOLOGY)					
UNIVERSITY OF	PUNE	,S.E.	(2008	FAI.	.)(INFORM		LCINOLOGI)					
UNIVERSITY OF							R TECHNOLOGY, PUNE.	PAG	E NO.	13	(43	35)
DATE : 18 AUG. 2011	CENT	RE : P	PUNE I	NSTI	TUTE OF	COMPUTE					`	35)
DATE : 18 AUG. 2011	CENT	RE : P	PUNE I	NSTI	TUTE OF	COMPUTE:	R TECHNOLOGY, PUNE.					35)
DATE: 18 AUG. 2011	CENT 	RE : F	PUNE I	NSTI MO	TUTE OF	COMPUTER	R TECHNOLOGY, PUNE.	 OLLEG	E, S	 SEAT N		35)
DATE: 18 AUG. 2011	CENT 	RE : F	PUNE I	NSTI MO	TUTE OF	COMPUTER	R TECHNOLOGY, PUNE	 OLLEG	E, S	 SEAT N		35)
DATE: 18 AUG. 2011	CENT 	RE : F	PUNE I	NSTI MO 	TUTE OF	COMPUTER	R TECHNOLOGY, PUNE	 OLLEG	E, S	 SEAT N RRY O\		
DATE: 18 AUG. 2011	CENT 	RE : F	PUNE I	MO N. P	TUTE OF THER, PE ASS MARK	COMPUTER RMANENT S, MARR	R TECHNOLOGY, PUNE.	 OLLEG REVIO	E, S	 SEAT N RRY O\	 NO. /ER 	
DATE: 18 AUG. 2011 NOTE: FIRST LINE: SEAT NO., NAME OF OTHER LINES: HEAD OF PASSING, S8058539 GHODE AMAR PRADEEPKUMAR	CENT F THE MAX.	CANDI	PUNE I IDATE, S, MI	MO N. P SIN	TUTE OF THER, PE ASS MARK DHU	COMPUTER RMANENT S, MARR	R TECHNOLOGY, PUNE. REG. NO., PREVIOUS SEAT NO., CONTAINED, P/F:PASS/FAIL, C:PONTAINED, P/F:PASS/FAIL, P/F	OLLEGREVIO	E, S	 SEAT N RRY ON 	NO. /ER) F
DATE: 18 AUG. 2011 NOTE: FIRST LINE: SEAT NO., NAME OF OTHER LINES: HEAD OF PASSING, S8058539 GHODE AMAR PRADEEPKUMAR 01. DISCRETE STRUCTURES	CENT F THE MAX. PP	CANDI MARKS	PUNE I IDATE, S, MI	MO N. P SIN 52 42	TUTE OF THER, PE ASS MARK DHU P C	COMPUTER RMANENT S, MARI 11.	R TECHNOLOGY, PUNE. REG. NO., PREVIOUS SEAT NO., CONTAINED, P/F:PASS/FAIL, C:PONTAINED, P/F:PASS/FAIL, P/F:PAS	OLLEGREVIO	E, S	 SEAT N RRY ON , S80	NO. /ER) F
DATE: 18 AUG. 2011 NOTE: FIRST LINE: SEAT NO., NAME OF OTHER LINES: HEAD OF PASSING, S8058539 GHODE AMAR PRADEEPKUMAR 01. DISCRETE STRUCTURES 02. COMPUTER ORGANIZATION	CENT F THE MAX. PP	CANDI MARKS	PUNE I IDATE, S, MI 40 40	MO N. P SIN 52 42 44	TUTE OF THER, PE ASS MARK DHU P C P C	COMPUTER RMANENT S, MARK 11. 12. 13.	R TECHNOLOGY, PUNE. REG. NO., PREVIOUS SEAT NO., CONTAINED, P/F:PASS/FAIL, C:PONTAINED, P/F:PASS/FAIL,	OLLEGREVIO	100	SEAT N RRY ON , S80 40 40	NO. /ER 058539 15 42	
DATE: 18 AUG. 2011 NOTE: FIRST LINE: SEAT NO., NAME OF OTHER LINES: HEAD OF PASSING, S8058539 GHODE AMAR PRADEEPKUMAR O1. DISCRETE STRUCTURES O2. COMPUTER ORGANIZATION O3. DIGITAL ELECTRONICS & LOGIC DESI	CENT OF THE MAX. PP PP PP	CANDI MARKS 100 100 100	PUNE I IDATE, 5, MI 40 40 40 40	NSTI MO SIN 52 42 44 40	TUTE OF THER, PE ASS MARK DHU P C P C P C	COMPUTER COMPUTER RMANENT S, MARK 11. 12. 13. 14.	R TECHNOLOGY, PUNE. REG. NO., PREVIOUS SEAT NO., CONTROL OF CONTROL OF PASS/FAIL, C:PONTROL OF PASS/FAIL, SEAT NO., TOO PASS/FAIL, C:PONTROL OF PASS/	OLLEG REVIO PICT PP PP	100 100	SEAT N RRY ON , S80 40 40 40	NO. /ER D58539 15 42 22	
DATE: 18 AUG. 2011 NOTE: FIRST LINE: SEAT NO., NAME OF OTHER LINES: HEAD OF PASSING, S8058539 GHODE AMAR PRADEEPKUMAR O1. DISCRETE STRUCTURES O2. COMPUTER ORGANIZATION O3. DIGITAL ELECTRONICS & LOGIC DESIGNATION O4. FUNDAMENTAL OF DATA STRUCTURES	CENT OF THE MAX. PP PP PP PP	TRE : F	PUNE I IDATE, 5, MI 40 40 40 40 40	NSTI MO SIN 52 42 44 40 41	TUTE OF THER, PE ASS MARK DHU P C P C P C P C	COMPUTER RMANENT S, MARK 11. 12. 13. 14. 15.	R TECHNOLOGY, PUNE. REG. NO., PREVIOUS SEAT NO., CONTROL OF PROCESSOR ARCHITECTURE & INTER. REG. NO., PREVIOUS SEAT NO., CONTROL OF PROCESSOR AND FILES	OLLEGREVIO PICT PP PP PP PP	100 100 100 100	SEAT N RRY ON , S80 40 40 40 40	NO. /ER D58539 15 42 22 49	
DATE: 18 AUG. 2011 NOTE: FIRST LINE: SEAT NO., NAME OF OTHER LINES: HEAD OF PASSING, S8058539 GHODE AMAR PRADEEPKUMAR 01. DISCRETE STRUCTURES 02. COMPUTER ORGANIZATION 03. DIGITAL ELECTRONICS & LOGIC DESITOR OF DATA STRUCTURES 04. FUNDAMENTAL OF DATA STRUCTURES 05. HUMANITIES AND SOCIAL SCIENCES	CENT CENT PF THE MAX. PP PP PP PP PP	100 100 100 100 100	PUNE I IDATE, S, MI 40 40 40 40 40 40	NSTI MO SIN 52 42 44 40 41 20	TUTE OF THER, PE ASS MARK DHU P C P C P C P C P C	COMPUTER RMANENT S, MARK 11. 12. 13. 14. 15. 16.	R TECHNOLOGY, PUNE. REG. NO., PREVIOUS SEAT NO., CONTROL OF PROCESSOR ARCHITECTURE & INTER. DATA STRUCTURES AND FILES PROCESSOR AND FILES DATA COMMUNICATION	OLLEGREVIO	100 100 100 100	SEAT N RRY ON , S80 40 40 40 40 40	NO. VER D58539 15 42 22 49 45	F P P P
DATE: 18 AUG. 2011	CENT CENT PF THE MAX. PP PP PP PP TW	100 100 100 100 100 50	PUNE I IDATE, S, MI 40 40 40 40 40 40 20	NSTI MO N. P SIN 52 42 44 40 41 20 33	TUTE OF THER, PE ASS MARK DHU P C P C P C P C P C P C	COMPUTER RMANENT S, MARK 11. 12. 13. 14. 15. 16. 17.	R TECHNOLOGY, PUNE. REG. NO., PREVIOUS SEAT NO., CONTROL OF PROCESSOR ARCHITECTURE & INTER. DATA STRUCTURES AND FILES DATA COMMUNICATION PROCESSOR INTERFACING LABORATORY	OLLEGREVIO	100 100 100 100 25	SEAT N RRY ON , S80 40 40 40 40 40		
DATE: 18 AUG. 2011	CENT CENT PF THE MAX. PP PP PP PP PP PP PP PP PP	100 100 100 100 50 50	PUNE I IDATE, S, MI 40 40 40 40 40 20 20	NSTI MO N. P SIN 52 42 44 40 41 20 33 30	TUTE OF THER, PE ASS MARK DHU P C P C P C P C P C P C P C P	COMPUTER RMANENT S, MARI 11. 12. 13. 14. 15. 16. 17. 18.	R TECHNOLOGY, PUNE. REG. NO., PREVIOUS SEAT NO., CONTROL OF PROCESSOR ARCHITECTURE & INTER. DATA STRUCTURES AND FILES DATA COMMUNICATION PROCESSOR INTERFACING LABORATORY PROCESSOR INTERFACING LABORATORY	OLLEG REVIO PICT PP PP PP PP TW OR	100 100 100 100 25 50	SEAT N RRY ON , S80 40 40 40 40 40 40 20		

cols05 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 20 P

GRAND TOTAL = 635/1500, RESULT: FAILS A.T.K.T. RESULT RESERVED FOR BKLG

ORDN. 1 MARKS:

S8058540 GUNDECHA ANIKET PRAKASH				UJW	ALA		, 71134959C	, s8058540 ,	PICT		, s80	58540	0
01. DISCRETE STRUCTURES	PP	100	40	45	РС	11.	ENG MATHS III		PP	100	40	10	F
02. COMPUTER ORGANIZATION	PP	100	40	52	P C	12.	COMPUTER GRAPHIC	S	PP	100	40	49	Р
03. DIGITAL ELECTRONICS & LOGIC DES	[GPP	100	40	40	P C	13.	PROCESSOR ARCHIT	ECTURE & INTER.	PP	100	40	40	Р
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	53	P C	14.	DATA STRUCTURES	AND FILES	PP	100	40	41	Р
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	57	P C	15.	DATA COMMUNICATION	ON	PP	100	40	69	Р
06. DIGITAL LABORATORY	TW	50	20	35	P C	16.	PROCESSOR INTERF	ACING LABORATORY	TW	25	10	10	Р
07. DIGITAL LABORATORY	PR	50	20	21	P C	17.	PROCESSOR INTERF	ACING LABORATORY	OR	50	20	24	Р
08. PROGRAMMING LABORATORY	TW	50	20	33	РС	18.	DATA STRUCTURES	AND FILES LAB	TW	25	10	11	Р
09. PROGRAMMING LABORATORY	PR	50	20	37	РС	19.	DATA STRUCTURES	AND FILES LAB	PR	50	20	35	Р
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	36	РС	20.	OBJECT ORIENTED	PROGRAMMING LAB	TW	50	20	27	Р
						21.	OBJECT ORIENTED	PROGRAMMING LAB	PR	50	20	20	Р

GRAND TOTAL = 745/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

S8058541 GUPTA VANI JITENDRA				RIT	ГИ	, 71045443M , S8058541 , PICT ,	S8058541
01. DISCRETE STRUCTURES	PP	100	40	56	РС	11. ENG MATHS III PP 100	40 72 P
02. COMPUTER ORGANIZATION	PP	100	40	60	P C	12. COMPUTER GRAPHICS PP 100	40 61 P
03. DIGITAL ELECTRONICS & LOGIC DES	IGPP	100	40	79	P C	13. PROCESSOR ARCHITECTURE & INTER. PP 100	40 59 P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	72	P C	14. DATA STRUCTURES AND FILES PP 100	40 74 P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	44	P C	15. DATA COMMUNICATION PP 100	40 65 P
06. DIGITAL LABORATORY	TW	50	20	41	P C	16. PROCESSOR INTERFACING LABORATORY TW 25	10 23 P
07. DIGITAL LABORATORY	PR	50	20	41	P C	17. PROCESSOR INTERFACING LABORATORY OR 50 Page 203	20 42 P

TW 50 20 43 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 19 P

		50	20	44	РС	19.						4.3	Р
09. PROGRAMMING LABORATORY	PR	30					DATA STRUCTURES	AND FILES LAB	PR	50	20	43	•
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	45	P C	20.	OBJECT ORIENTED	PROGRAMMING LAB	TW	50	20	45	Р
						21.	OBJECT ORIENTED	PROGRAMMING LAB	PR	50	20	40	Р
RAND TOTAL = 1068/1500, RESULT: FIRS	T CLAS	S WITH	H DIST	INCT	TION								
	 F PUNE	, ,S.E.	 (2008	 PAT	 .)(INFORM	 MATION TE							
DATE : 18 AUG. 2011	CENT	RE : F	PUNE I	NSTI	TUTE OF (COMPUTER	TECHNOLOGY, PUR	NE.	PAC	E NO.	14	(4	36)
NOTE: FIRST LINE : SEAT NO., NAME	OF THE	CAND:	IDATE,	МО	THER, PER	RMANENT	REG. NO., PREV	IOUS SEAT NO., C	COLLEC	iΕ, S	SEAT N	١٥.	
OTHER LINES: HEAD OF PASSING,	MAX.	MARKS	S, MI	N. P	ASS MARKS	S, MARK	S OBTAINED, P/I	F:PASS/FAIL, C:F	PREVIO	US CAI	RRY O	/ER	
S8058543 HIRAN PRANAV PRAMODKUMAR				REK			, 71045447D	, s8058543 ,	PICT	-	, s80)5854	3
	PP	100	40	REK					PICT PP	100	, s80)5854 74	
S8058543 HIRAN PRANAV PRAMODKUMAR				REK	НА	11.	, 71045447D	, S8058543 ,			,		Ρ
S8058543 HIRAN PRANAV PRAMODKUMAR 01. DISCRETE STRUCTURES	PP PP	100	40	REK 65 56	HA P C	11. 12.	, 71045447D ENG MATHS III COMPUTER GRAPHIO	, S8058543 ,	PP PP	100	40	74	P P
S8058543 HIRAN PRANAV PRAMODKUMAR 01. DISCRETE STRUCTURES 02. COMPUTER ORGANIZATION	PP PP	100 100	40 40	REK 65 56 59	HA P C P C	11. 12. 13.	, 71045447D ENG MATHS III COMPUTER GRAPHIO	, S8058543 , CS TECTURE & INTER.	PP PP	100 100	40 40	74 52	P P
S8058543 HIRAN PRANAV PRAMODKUMAR 01. DISCRETE STRUCTURES 02. COMPUTER ORGANIZATION 03. DIGITAL ELECTRONICS & LOGIC DES	PP PP SIGPP	100 100 100	40 40 40	REK 65 56 59	HA PC PC PC	11. 12. 13. 14.	, 71045447D ENG MATHS III COMPUTER GRAPHIC PROCESSOR ARCHIT	, S8058543 , CS TECTURE & INTER. AND FILES	PP PP	100 100 100	40 40 40	74 52 47	P P P
S8058543 HIRAN PRANAV PRAMODKUMAR 01. DISCRETE STRUCTURES 02. COMPUTER ORGANIZATION 03. DIGITAL ELECTRONICS & LOGIC DES 04. FUNDAMENTAL OF DATA STRUCTURES	PP PP IGPP PP	100 100 100 100	40 40 40 40	65 56 59 56 40	HA PC PC PC	11. 12. 13. 14.	, 71045447D ENG MATHS III COMPUTER GRAPHIC PROCESSOR ARCHIT DATA STRUCTURES DATA COMMUNICATE	, S8058543 , CS TECTURE & INTER. AND FILES	PP PP PP	100 100 100 100	40 40 40 40	74 52 47 51	P P P
S8058543 HIRAN PRANAV PRAMODKUMAR 01. DISCRETE STRUCTURES 02. COMPUTER ORGANIZATION 03. DIGITAL ELECTRONICS & LOGIC DES 04. FUNDAMENTAL OF DATA STRUCTURES 05. HUMANITIES AND SOCIAL SCIENCES	PP PP IGPP PP	100 100 100 100 100	40 40 40 40 40	REK 65 56 59 56 40 36	HA PC PC PC PC	11. 12. 13. 14. 15.	, 71045447D ENG MATHS III COMPUTER GRAPHIC PROCESSOR ARCHIT DATA STRUCTURES DATA COMMUNICATE PROCESSOR INTERI	, S8058543 , CS TECTURE & INTER. AND FILES ION	PP PP PP TW	100 100 100 100 100	40 40 40 40 40	74 52 47 51 57	P P P
S8058543 HIRAN PRANAV PRAMODKUMAR 01. DISCRETE STRUCTURES 02. COMPUTER ORGANIZATION 03. DIGITAL ELECTRONICS & LOGIC DES 04. FUNDAMENTAL OF DATA STRUCTURES 05. HUMANITIES AND SOCIAL SCIENCES 06. DIGITAL LABORATORY	PP PP PP TW	100 100 100 100 100 50	40 40 40 40 40 20	REK 65 56 59 56 40 36	HA PC PC PC PC	11. 12. 13. 14. 15. 16.	, 71045447D ENG MATHS III COMPUTER GRAPHIC PROCESSOR ARCHIT DATA STRUCTURES DATA COMMUNICATE PROCESSOR INTERI	, S8058543 , CS TECTURE & INTER. AND FILES ION FACING LABORATORY FACING LABORATORY	PP PP PP TW	100 100 100 100 100 25	40 40 40 40 40 10	74 52 47 51 57 19 35	P P P P
S8058543 HIRAN PRANAV PRAMODKUMAR 01. DISCRETE STRUCTURES 02. COMPUTER ORGANIZATION 03. DIGITAL ELECTRONICS & LOGIC DES 04. FUNDAMENTAL OF DATA STRUCTURES 05. HUMANITIES AND SOCIAL SCIENCES 06. DIGITAL LABORATORY 07. DIGITAL LABORATORY	PP PP PP TW PR	100 100 100 100 100 50 50	40 40 40 40 40 20 20	REK 65 56 59 56 40 36 32	HA PC PC PC PC PC	11. 12. 13. 14. 15. 16. 17.	, 71045447D ENG MATHS III COMPUTER GRAPHIC PROCESSOR ARCHIT DATA STRUCTURES DATA COMMUNICATE PROCESSOR INTERI	, S8058543 , CS TECTURE & INTER. AND FILES ION FACING LABORATORY AND FILES LAB	PP PP PP TW OR	100 100 100 100 100 25 50	40 40 40 40 40 10 20	74 52 47 51 57 19 35	P P P P
S8058543 HIRAN PRANAV PRAMODKUMAR 01. DISCRETE STRUCTURES 02. COMPUTER ORGANIZATION 03. DIGITAL ELECTRONICS & LOGIC DES 04. FUNDAMENTAL OF DATA STRUCTURES 05. HUMANITIES AND SOCIAL SCIENCES 06. DIGITAL LABORATORY 07. DIGITAL LABORATORY 08. PROGRAMMING LABORATORY	PP PP PP TW PR TW PR	100 100 100 100 50 50	40 40 40 40 40 20 20 20	REK 65 56 59 56 40 36 32 36 34	HA PC PC PC PC PC	11. 12. 13. 14. 15. 16. 17. 18.	, 71045447D ENG MATHS III COMPUTER GRAPHIC PROCESSOR ARCHIT DATA STRUCTURES DATA COMMUNICATE PROCESSOR INTERI PROCESSOR INTERI DATA STRUCTURES DATA STRUCTURES	, S8058543 , CS TECTURE & INTER. AND FILES ION FACING LABORATORY AND FILES LAB	PP PP PP TW OR TW PR	100 100 100 100 25 50 25	40 40 40 40 40 10 20	74 52 47 51 57 19 35 18	P P P P P

08. PROGRAMMING LABORATORY

S8058544 HIRE PRATIK PARASHURAM				MAN	IGALA	, 71045448B , S8058544 , PICT , S8058544	
01. DISCRETE STRUCTURES	PP	100	40	63	P C	11. ENG MATHS III PP 100 40 65 F	>
02. COMPUTER ORGANIZATION	PP	100	40	49	P C	12. COMPUTER GRAPHICS PP 100 40 51 F	>
03. DIGITAL ELECTRONICS & LOGIC DES	IGPP	100	40	57	P C	13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 32* F	>
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	76	P C	14. DATA STRUCTURES AND FILES PP 100 40 71 F	>
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	45	P C	15. DATA COMMUNICATION PP 100 40 55 F	>
06. DIGITAL LABORATORY	TW	50	20	37	P C	16. PROCESSOR INTERFACING LABORATORY TW 25 10 17 F	>
07. DIGITAL LABORATORY	PR	50	20	29	P C	17. PROCESSOR INTERFACING LABORATORY OR 50 20 22 F	>
08. PROGRAMMING LABORATORY	TW	50	20	37	P C	18. DATA STRUCTURES AND FILES LAB TW 25 10 18 F	>
09. PROGRAMMING LABORATORY	PR	50	20	32	P C	19. DATA STRUCTURES AND FILES LAB PR 50 20 23 F	>
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	30	P C	20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 38 F	>
						21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 44 F	>

GRAND TOTAL = 891/1500, RESULT: HIGHER SECOND CLASS * [0.4]
ORDN. 1 MARKS:

S8058545 INAMDAR MOHSIN IMTIYAZ				SHAI	HENAZ	, 71045449L , S8058545 , PICT , S8058545	
01. DISCRETE STRUCTURES	PP	100	40	44	P C	11. ENG MATHS III PP 100 40 48 P	,
02. COMPUTER ORGANIZATION	PP	100	40	27	F	12. COMPUTER GRAPHICS PP 100 40 40 P	ı
03. DIGITAL ELECTRONICS & LOGIC DESIG	GPP	100	40	53	P C	13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 27 F	
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	46	P C	14. DATA STRUCTURES AND FILES PP 100 40 59 P	ı
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	40	P C	15. DATA COMMUNICATION PP 100 40 40 P	1
06. DIGITAL LABORATORY	TW	50	20	28	P C	16. PROCESSOR INTERFACING LABORATORY TW 25 10 19 P	1
07. DIGITAL LABORATORY	PR	50	20	22	P C	17. PROCESSOR INTERFACING LABORATORY OR 50 20 27 P	1
08. PROGRAMMING LABORATORY	TW	50	20	37	P C	18. DATA STRUCTURES AND FILES LAB TW 25 10 17 P	ı
09. PROGRAMMING LABORATORY	PR	50	20	37	P C	19. DATA STRUCTURES AND FILES LAB PR 50 20 40 P	1
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	37	P C	20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 41 P	1
						21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 32 P)

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GRAND TOTAL = 761/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

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UNIVERSITY OF		-											
DATE : 18 AUG. 2011	CENT	RE : F	PUNE I	NSTI	TUTE	OF COMPUTE	R TECHNOLOGY, PU	NE.	PAG	E NO.	15	(4)	37)
				• •									
NOTE: FIRST LINE : SEAT NO., NAME O	F THE	CAND]	DATE,	МО	THER,	PERMANENT	REG. NO., PREV	IOUS SEAT NO., C	OLLEG	E, S	EAT N	0.	
OTHER LINES: HEAD OF PASSING,	MAX.	MARKS	S, MI	N. P	ASS M	MARKS, MAR	KS OBTAINED, P/	F:PASS/FAIL, C:P	REVIO	US CAR	RY OV	'ER	
S8058546 INGALE KOMAL RAJENDRA				USH	Α		, 71045450D	, s8058546 ,	PICT		, s80	5854	6
01. DISCRETE STRUCTURES	PP	100	40	54	РС	11.	ENG MATHS III		PP	100	40	65	Р
02. COMPUTER ORGANIZATION	PP	100	40	43	РС	12.	COMPUTER GRAPHI	cs	PP	100	40	46	Р
03. DIGITAL ELECTRONICS & LOGIC DESI	GPP	100	40	55	P C	13.	PROCESSOR ARCHI	TECTURE & INTER.	PP	100	40	30*	Р
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	46	P C	14.	DATA STRUCTURES	AND FILES	PP	100	40	68	Р
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	46	P C	15.	DATA COMMUNICAT	ION	PP	100	40	52	Р
06. DIGITAL LABORATORY	TW	50	20	37	P C	16.	PROCESSOR INTER	FACING LABORATORY	TW	25	10	21	Р
07. DIGITAL LABORATORY	PR	50	20	34	P C	17.	PROCESSOR INTER	FACING LABORATORY	OR	50	20	37	Р
08. PROGRAMMING LABORATORY	TW	50	20	42	P C	18.	DATA STRUCTURES	AND FILES LAB	TW	25	10	19	Р
09. PROGRAMMING LABORATORY	PR	50	20	42	P C	19.	DATA STRUCTURES	AND FILES LAB	PR	50	20	32	Р
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	39	P C	20.	OBJECT ORIENTED	PROGRAMMING LAB	TW	50	20	43	Р
						21.	OBJECT ORIENTED	PROGRAMMING LAB	PR	50	20	28	Р
GRAND TOTAL = 879/1500, RESULT: HIGHE ORDN. 1 MARKS:	R SEC	COND CL	-ASS	* [0.4]								
S8058547 INGOLE PRACHI VASANT				NIL	IMA		, 71045451в	, s8058547 ,	PICT		, s80	5854	7
01. DISCRETE STRUCTURES	PP	100	40	59	P C	11.	ENG MATHS III		PP	100	40	68	Р
02. COMPUTER ORGANIZATION	PP	100	40	40	P C	12.	COMPUTER GRAPHI	cs	PP	100	40	67	Р
03. DIGITAL ELECTRONICS & LOGIC DESI	GPP	100	40	45	РС	13.		TECTURE & INTER.	PP	100	40	44	Р

04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	42	P C	14.	DATA STRUCTURES	AND FILES	PP	100	40	70	Р
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	40	P C	15.	DATA COMMUNICAT	ION	PP	100	40	63	Р
06. DIGITAL LABORATORY	TW	50	20	20	P C	16.	PROCESSOR INTER	FACING LABORATORY	TW	25	10	20	Р
07. DIGITAL LABORATORY	PR	50	20	34	P C	17.	PROCESSOR INTER	FACING LABORATORY	OR	50	20	40	Р
08. PROGRAMMING LABORATORY	TW	50	20	29	P C	18.	DATA STRUCTURES	AND FILES LAB	TW	25	10	19	Р
09. PROGRAMMING LABORATORY	PR	50	20	38	P C	19.	DATA STRUCTURES	AND FILES LAB	PR	50	20	37	Р
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	33	P C	20.	OBJECT ORIENTED	PROGRAMMING LAB	TW	50	20	44	Р
						21.	OBJECT ORIENTED	PROGRAMMING LAB	PR	50	20	20#	Р

GRAND TOTAL = 872+02/1500, RESULT: HIGHER SECOND CLASS #[0.163+0.1][0.163]

ORDN. 1 MARKS : (21)2,

S8058548	ISHAN AGARWAL				MAM	ГΑ		, 71045452L , S80	58548 ,	PICT		, s805	8548	3
01. DISCRE	TE STRUCTURES	PP	100	40	67	P C	11.	ENG MATHS III		PP	100	40	54	Р
02. COMPUT	ER ORGANIZATION	PP	100	40	45	P C	12.	COMPUTER GRAPHICS		PP	100	40	50	Р
03. DIGITA	L ELECTRONICS & LOGIC DESIG	GPP	100	40	45	P C	13.	PROCESSOR ARCHITECTURE	& INTER.	PP	100	40	44	Р
04. FUNDAM	ENTAL OF DATA STRUCTURES	PP	100	40	64	P C	14.	DATA STRUCTURES AND FIL	ES	PP	100	40	59	Р
05. HUMANI	TIES AND SOCIAL SCIENCES	PP	100	40	44	P C	15.	DATA COMMUNICATION		PP	100	40	40	Р
06. DIGITA	L LABORATORY	TW	50	20	30	P C	16.	PROCESSOR INTERFACING L	ABORATORY	TW	25	10	18	Р
07. DIGITA	L LABORATORY	PR	50	20	33	P C	17.	PROCESSOR INTERFACING L	ABORATORY	OR	50	20	38	Р
08. PROGRA	MMING LABORATORY	TW	50	20	33	P C	18.	DATA STRUCTURES AND FIL	ES LAB	TW	25	10	13	Р
09. PROGRA	MMING LABORATORY	PR	50	20	40	P C	19.	DATA STRUCTURES AND FIL	ES LAB	PR	50	20	30	Р
10. COMMUN	ICATION AND LANGUAGE LAB.	TW	50	20	30	P C	20.	OBJECT ORIENTED PROGRAM	MING LAB	TW	50	20	31	Р
							21.	OBJECT ORIENTED PROGRAM	MING LAB	PR	50	20	22	Р

GRAND TOTAL = 830/1500, RESULT: HIGHER SECOND CLASS ORDN. 1 MARKS:

UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(INFORMATION TECHNOLOGY)

DATE : 18 AUG. 2011 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 16 (438)

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

s8058549	JADHAV DHANRAJ KHANDERAO				СНН	AYA		, 71134960	G,	s8058549 ,	PICT		, s80!	58549)
01. DISCRE	TE STRUCTURES	PP	100	40	49	P C	11	ENG MATHS II	ΙΙ		PP	100	40	18	F
02. COMPUT	ER ORGANIZATION	PP	100	40	65	РС	12	COMPUTER GRA	APHICS		PP	100	40	53	Р
03. DIGITA	AL ELECTRONICS & LOGIC DESI	GPP	100	40	59	РС	13	PROCESSOR AR	RCHITECT	TURE & INTER.	PP	100	40	53	Р
04. FUNDAM	MENTAL OF DATA STRUCTURES	PP	100	40	75	P C	14	DATA STRUCTU	JRES AND	FILES	PP	100	40	71	Р
05. HUMAN	TIES AND SOCIAL SCIENCES	PP	100	40	53	P C	15	DATA COMMUNI	CATION		PP	100	40	68	Р
06. DIGITA	AL LABORATORY	TW	50	20	39	P C	16	PROCESSOR IN	ITERFAC:	ING LABORATORY	TW	25	10	20	Р
07. DIGITA	AL LABORATORY	PR	50	20	30	P C	17	PROCESSOR IN	ITERFAC:	ING LABORATORY	OR	50	20	43	Р
08. PROGRA	MMMING LABORATORY	TW	50	20	42	P C	18	DATA STRUCTU	JRES AND	FILES LAB	TW	25	10	23	Р
09. PROGRA	MMMING LABORATORY	PR	50	20	46	P C	19	DATA STRUCTU	JRES AND	FILES LAB	PR	50	20	35	Р
10. COMMUN	NICATION AND LANGUAGE LAB.	TW	50	20	38	P C	20	OBJECT ORIEN	TED PRO	OGRAMMING LAB	TW	50	20	46	Р
							21	OBJECT ORIEN	TED PRO	GRAMMING LAB	PR	50	20	32	Р

GRAND TOTAL = 958/1500, RESULT: FAILS A.T.K.T.

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	S8058550 JADHAV MAYUR JAGAHNATH				JAN	ABAI		, 71134961E , S8058550 ,	PICT		, s80	58550	0
	01. DISCRETE STRUCTURES	PP	100	40	65	P C	11.	ENG MATHS III	PP	100	40	42	Р
	02. COMPUTER ORGANIZATION	PP	100	40	65	P C	12.	COMPUTER GRAPHICS	PP	100	40	62	Р
	03. DIGITAL ELECTRONICS & LOGIC DESI	GPP	100	40	81	P C	13.	PROCESSOR ARCHITECTURE & INTER.	PP	100	40	46	Р
	04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	74	P C	14.	DATA STRUCTURES AND FILES	PP	100	40	76	Р
	05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	45	P C	15.	DATA COMMUNICATION	PP	100	40	58	Р
	06. DIGITAL LABORATORY	TW	50	20	36	P C	16.	PROCESSOR INTERFACING LABORATORY	TW	25	10	21	Р
	07. DIGITAL LABORATORY	PR	50	20	30	P C	17.	PROCESSOR INTERFACING LABORATORY	OR	50	20	42	Р
	08. PROGRAMMING LABORATORY	TW	50	20	42	P C	18.	DATA STRUCTURES AND FILES LAB	TW	25	10	20	Р

09.	PROGRAMMING LABORATORY	PR	50	20	46	P C	19	. DATA STRUCTURES AND FILES LAB	PR	50	20	39	Р
10.	COMMUNICATION AND LANGUAGE LAB.	TW	50	20	40	P C	20	. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	38	Р
							21	. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	46	Р
CDAND	TOTAL 1014/1500 RESULT. FIRST		C WITI	LDICI	TNCT	TON							
	TOTAL = 1014/1500, RESULT: FIRST	CLAS	S WIIF	ו מדמו	INCI	ION							
ORDN.	1 MARKS :												
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S80	58551 JODHWANI SAHIL PRADEEP				BAB	ITA		, 71045462н , ѕ8058551 ,	PICT		, s80)5855	1
01.	DISCRETE STRUCTURES	PP	100	40	47	P C	11	. ENG MATHS III	PP	100	40	85	Р
02.	COMPUTER ORGANIZATION	PP	100	40	40	P C	12	. COMPUTER GRAPHICS	PP	100	40	48	Р
03.	DIGITAL ELECTRONICS & LOGIC DESI	GPP	100	40	58	P C	13	. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	40	Р
04.	FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	59	P C	14	. DATA STRUCTURES AND FILES	PP	100	40	63	Р
05.	HUMANITIES AND SOCIAL SCIENCES	PP	100	40	40	P C	15	. DATA COMMUNICATION	PP	100	40	48	Р
06.	DIGITAL LABORATORY	TW	50	20	34	P C	16	. PROCESSOR INTERFACING LABORATORY	TW	25	10	19	Р
07.	DIGITAL LABORATORY	PR	50	20	39	P C	17	. PROCESSOR INTERFACING LABORATORY	OR	50	20	40	Р
08.	PROGRAMMING LABORATORY	TW	50	20	41	P C	18	. DATA STRUCTURES AND FILES LAB	TW	25	10	18	Р
09.	PROGRAMMING LABORATORY	PR	50	20	48	P C	19	. DATA STRUCTURES AND FILES LAB	PR	50	20	43	Р
10.	COMMUNICATION AND LANGUAGE LAB.	TW	50	20	36	P C	20	. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	42	Р
							21	. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	35	Р
GRAND	TOTAL = 923/1500, RESULT: FIRST	CLAS	SS										
ORDN.	1 MARKS :												
			 S F		 PAT								
u			-					ER TECHNOLOGY, PUNE.	PΛC	E NO.	17	<i>(</i> 4	39)
•									1 70	L NO.	Τ,	(-	33)
NOT								F PEC NO PREVIOUS SEAT NO CO			SEAT N		
NOT								Γ REG. NO., PREVIOUS SEAT NO., CO RKS OBTAINED, P/F:PASS/FAIL, C:PI					
	OTHER LINES. HEAD OF FASSING,	MAA.	MANNS	, 1413	.iv. P	A33 №	MAINIO, MAI	C.FI	KLVIO	UJ CAR	aci Ov	LIX	

s80!	58552 JUVVADI MANOBHIRAM				J V	IJAYASHRI		, 71057070յ	cols05 , s8058552 ,	PICT		, s80	58552	2
01.	DISCRETE STRUCTURES	PP	100	40	58	P C	11.	ENG MATHS III		PP	100	40	50	Р
02.	COMPUTER ORGANIZATION	PP	100	40	62	P C	12.	COMPUTER GRAPHI	cs	PP	100	40	49	Р
03.	DIGITAL ELECTRONICS & LOGIC DESIGNATION	GPP	100	40	40	P C	13.	PROCESSOR ARCHI	TECTURE & INTER.	PP	100	40	50	Р
04.	FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	48	P C	14. 1	DATA STRUCTURES	AND FILES	PP	100	40	72	Р
05.	HUMANITIES AND SOCIAL SCIENCES	PP	100	40	48	P C	15. 1	DATA COMMUNICAT	ION	PP	100	40	63	Р
06.	DIGITAL LABORATORY	TW	50	20	26	P C	16.	PROCESSOR INTER	FACING LABORATORY	TW	25	10	18	Р
07.	DIGITAL LABORATORY	PR	50	20	21	P C	17.	PROCESSOR INTER	FACING LABORATORY	OR	50	20	15*	Р
08.	PROGRAMMING LABORATORY	TW	50	20	32	P C	18. 1	DATA STRUCTURES	AND FILES LAB	TW	25	10	20	Р
09.	PROGRAMMING LABORATORY	PR	50	20	33	P C	19. 1	DATA STRUCTURES	AND FILES LAB	PR	50	20	37	Р
10.	COMMUNICATION AND LANGUAGE LAB.	TW	50	20	22	P C	20.	OBJECT ORIENTED	PROGRAMMING LAB	TW	50	20	42	Р
							21. (OBJECT ORIENTED	PROGRAMMING LAB	PR	50	20	25	Р
GRAND	TOTAL = 831/1500, RESULT: HIGHE	R SEC	OND CL	ASS	*[o	.163+0.4]	[4	& o.16						

S8058553 KADAM AMIT ASHOKRAO				MAN	IDAKINI	, 71134962C , S8058553 , PICT	, s80	58553
01. DISCRETE STRUCTURES	PP	100	40	40	P C	11. ENG MATHS III PP 1	.00 40	40 P
02. COMPUTER ORGANIZATION	PP	100	40	51	P C	12. COMPUTER GRAPHICS PP 1	.00 40	47 P
03. DIGITAL ELECTRONICS & LOGIC DES	IGPP	100	40	64	P C	13. PROCESSOR ARCHITECTURE & INTER. PP 1	.00 40	43 P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	50	P C	14. DATA STRUCTURES AND FILES PP 1	.00 40	60 P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	45	P C	15. DATA COMMUNICATION PP 1	.00 40	54 P
06. DIGITAL LABORATORY	TW	50	20	38	P C	16. PROCESSOR INTERFACING LABORATORY TW	25 10	22 P
07. DIGITAL LABORATORY	PR	50	20	22	P C	17. PROCESSOR INTERFACING LABORATORY OR	50 20	39 P
08. PROGRAMMING LABORATORY	TW	50	20	43	P C	18. DATA STRUCTURES AND FILES LAB TW	25 10	22 P
09. PROGRAMMING LABORATORY	PR	50	20	30	P C	19. DATA STRUCTURES AND FILES LAB PR	50 20	39 P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	39	P C	20. OBJECT ORIENTED PROGRAMMING LAB TW	50 20	44 P
						21. OBJECT ORIENTED PROGRAMMING LAB PR	50 20	38 P

GRAND TOTAL = 870/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

S8058554 KADAM PRADNYA PRADEEP				PRA	NITA		, 71134963M , S8058554 ,	PIC	Γ	, s8	05855	54
01. DISCRETE STRUCTURES	PP	100	40	54	Р	11.	ENG MATHS III	PP	100	40	40	Р
02. COMPUTER ORGANIZATION	PP	100	40	40	P C	12.	COMPUTER GRAPHICS	PP	100	40	41	Р
03. DIGITAL ELECTRONICS & LOGIC DES	IGPP	100	40	46	P C	13.	PROCESSOR ARCHITECTURE & INTER.	PP	100	40	40	Р
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	49	P C	14.	DATA STRUCTURES AND FILES	PP	100	40	66	Р
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	46	P C	15.	DATA COMMUNICATION	PP	100	40	67	Р
06. DIGITAL LABORATORY	TW	50	20	36	P C	16.	PROCESSOR INTERFACING LABORATORY	′ TW	25	10	18	Р
07. DIGITAL LABORATORY	PR	50	20	34	P C	17.	PROCESSOR INTERFACING LABORATORY	OR	50	20	24	Р
08. PROGRAMMING LABORATORY	TW	50	20	37	P C	18.	DATA STRUCTURES AND FILES LAB	TW	25	10	15	Р
09. PROGRAMMING LABORATORY	PR	50	20	27	P C	19.	DATA STRUCTURES AND FILES LAB	PR	50	20	20	Р
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	33	P C	20.	OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	32	Р
						21.	OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	37	Р

UNIVERSITY OF	 PUNE	 ,S.E.((2008	 PAT.)(INFO	ORMATION	TECHNOLOGY)						
DATE : 18 AUG. 2011	CENT	RE : P	UNE I	NSTI	TUTE O	OF COMPUT	ER TECHNOLOGY, P	UNE.	PAG	E NO.	18	(4	40)
NOTE: FIRST LINE : SEAT NO., NAME OF OTHER LINES: HEAD OF PASSING,	OF THE	CANDI	DATE,	МО	THER,	PERMANEN	Γ REG. NO., PRE	•	OLLEG	Ε, S	SEAT N	0.	
S8058555 KADAM RAHUL DNYANESHWAR					 UBAI		, 71134964к	, s8058555 ,	PICT		, s80	5855	
01. DISCRETE STRUCTURES	PP	100	40	59	P C	11	. ENG MATHS III		PP	100	40	54	Р
02. COMPUTER ORGANIZATION	PP	100	40	68	P C	12	. COMPUTER GRAPH	ICS	PP	100	40	62	Р
03. DIGITAL ELECTRONICS & LOGIC DESI	[GPP	100	40	62	P C	13	. PROCESSOR ARCH	ITECTURE & INTER.	PP	100	40	51	Р
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	66	P C	14	. DATA STRUCTURE	S AND FILES Page 211	PP	100	40	74	Р

								cols05					
05.	HUMANITIES AND SOCIAL SCIENCES	PP	100	40	56	P C	15.	DATA COMMUNICATION	PP	100	40	60	Р
06.	DIGITAL LABORATORY	TW	50	20	37	P C	16.	PROCESSOR INTERFACING LABORATORY	TW	25	10	21	Р
07.	DIGITAL LABORATORY	PR	50	20	36	P C	17.	PROCESSOR INTERFACING LABORATORY	OR	50	20	32	Р
08.	PROGRAMMING LABORATORY	TW	50	20	42	P C	18.	DATA STRUCTURES AND FILES LAB	TW	25	10	22	Р
09.	PROGRAMMING LABORATORY	PR	50	20	41	P C	19.	DATA STRUCTURES AND FILES LAB	PR	50	20	39	Р
10.	COMMUNICATION AND LANGUAGE LAB.	TW	50	20	37	P C	20.	OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	43	Р
							21.	OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	40	Р
					SUNI	 TTA			 PICT		 , s80		
300	NANADE 1003A DIEITRA				3011	LIA		, 1213 130311 , 30030330 ,	1101		, 500	30330	J
01.	DISCRETE STRUCTURES	PP	100	40	41	P C	11.	ENG MATHS III	PP	100	40	52	Р
02.	COMPUTER ORGANIZATION	PP	100	40	52	P C	12.	COMPUTER GRAPHICS	PP	100	40	59	Р
03.	DIGITAL ELECTRONICS & LOGIC DESIGNATION	GPP	100	40	76	P C	13.	PROCESSOR ARCHITECTURE & INTER.	PP	100	40	43	Р
04.	FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	58	P C	14.	DATA STRUCTURES AND FILES	PP	100	40	71	Р
05.	HUMANITIES AND SOCIAL SCIENCES	PP	100	40	46	P C	15.	DATA COMMUNICATION	PP	100	40	63	Р
06.	DIGITAL LABORATORY	TW	50	20	37	P C	16.	PROCESSOR INTERFACING LABORATORY	TW	25	10	21	Р
07.	DIGITAL LABORATORY	PR	50	20	33	P C	17.	PROCESSOR INTERFACING LABORATORY	OR	50	20	38	Р

GRAND TOTAL = 941/1500, RESULT: FIRST CLASS ORDN. 1 MARKS :

10. COMMUNICATION AND LANGUAGE LAB. TW

08. PROGRAMMING LABORATORY

09. PROGRAMMING LABORATORY

S8058557 KALBANDE GAURI RAJENDRA PRABHA , 71134966F , S8058557 , PICT , S8058557

50 20 39 P C

50 20 35 P C

20 42 P C

18. DATA STRUCTURES AND FILES LAB

19. DATA STRUCTURES AND FILES LAB

20. OBJECT ORIENTED PROGRAMMING LAB TW

21. OBJECT ORIENTED PROGRAMMING LAB PR

25 10 21 P

50 20 40 P

50 20 47 P

50 20 27 P

s(

01.	DISCRETE STRUCTURES	PP	100	40	47	P C	11.	ENG MATHS III	PP	100	40	41	Р
02.	COMPUTER ORGANIZATION	PP	100	40	58	P C	12.	COMPUTER GRAPHICS	PP	100	40	51	Р
03.	DIGITAL ELECTRONICS & LOGIC DESI	GPP	100	40	62	P C	13.	PROCESSOR ARCHITECTURE & INTER.	PP	100	40	50	Р
04.	FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	79	P C	14.	DATA STRUCTURES AND FILES	PP	100	40	78	Р
05.	HUMANITIES AND SOCIAL SCIENCES	PP	100	40	40	P C	15.	DATA COMMUNICATION	PP	100	40	61	Р
06.	DIGITAL LABORATORY	TW	50	20	39	P C	16.	PROCESSOR INTERFACING LABORATORY	TW	25	10	21	Р
07.	DIGITAL LABORATORY	PR	50	20	32	P C	17.	PROCESSOR INTERFACING LABORATORY	OR	50	20	39	Р
08.	PROGRAMMING LABORATORY	TW	50	20	40	P C	18.	DATA STRUCTURES AND FILES LAB	TW	25	10	22	Р
09.	PROGRAMMING LABORATORY	PR	50	20	42	P C	19.	DATA STRUCTURES AND FILES LAB	PR	50	20	36	Р
10.	COMMUNICATION AND LANGUAGE LAB.	TW	50	20	35	P C	20.	OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	43	Р
							21.	OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	44	Р

GRAND TOTAL = 960/1500, RESULT: FIRST CLASS ORDN. 1 MARKS:

UNIV	ERSITY OF PUNE ,S.E.(2008 PAT.)(INF	FORMATION TECHNOLOGY)	

DATE : 18 AUG. 2011	CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.	PAGE NO.	19	(441)

NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8058558 KALRA JASBIRKAUR AMARJIT	SING	4		MEE	NA		, 71134967D , S8058558 ,	PICT		, s80)5855	8
01. DISCRETE STRUCTURES	PP	100	40	46	РС	11.	ENG MATHS III	PP	100	40	40	Р
02. COMPUTER ORGANIZATION	PP	100	40	48	P C	12.	COMPUTER GRAPHICS	PP	100	40	64	Р
03. DIGITAL ELECTRONICS & LOGIC DES	IGPP	100	40	47	P C	13.	PROCESSOR ARCHITECTURE & INTER.	PP	100	40	40	Р
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	58	P C	14.	DATA STRUCTURES AND FILES	PP	100	40	57	Р
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	40	P C	15.	DATA COMMUNICATION	PP	100	40	59	Р
06. DIGITAL LABORATORY	TW	50	20	35	P C	16.	PROCESSOR INTERFACING LABORATORY	TW	25	10	22	Р
07. DIGITAL LABORATORY	PR	50	20	20	P C	17.	PROCESSOR INTERFACING LABORATORY	OR	50	20	44	Р
08. PROGRAMMING LABORATORY	TW	50	20	39	P C	18.	DATA STRUCTURES AND FILES LAB	TW	25	10	21	Р
09. PROGRAMMING LABORATORY	PR	50	20	44	P C	19.	DATA STRUCTURES AND FILES LAB Page 213	PR	50	20	39	Р

						21.	OBJECT ORIENTED	PROGRAMMING LAB	PR	50	20	28	Р
GRAND TOTAL = 878/1500, RESULT: HIGH	ER SE	COND CI	LASS										
DRDN. 1 MARKS :													
S8058559 KAMBLE JITESH SHAMRAO				PUS	БНРА		, 71045466L	, s8058559 ,	PICT	Г	, s8	05855	59
01. DISCRETE STRUCTURES	PP	100	40	69	P C	11.	ENG MATHS III		PP	100	40	45	Р
02. COMPUTER ORGANIZATION	PP	100	40	42	P C	12.	COMPUTER GRAPHI	CS	PP	100	40	52	Р
03. DIGITAL ELECTRONICS & LOGIC DES	IGPP	100	40	56	P C	13.	PROCESSOR ARCHI	TECTURE & INTER.	PP	100	40	45	Р
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	67	P C	14.	DATA STRUCTURES	AND FILES	PP	100	40	64	Р
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	40	P C	15.	DATA COMMUNICAT	ION	PP	100	40	57	Р
06. DIGITAL LABORATORY	TW	50	20	38	P C	16.	PROCESSOR INTER	FACING LABORATORY	TW	25	10	24	Р
07. DIGITAL LABORATORY	PR	50	20	35	P C	17.	PROCESSOR INTER	FACING LABORATORY	OR	50	20	44	Р
08. PROGRAMMING LABORATORY	TW	50	20	43	P C	18.	DATA STRUCTURES	AND FILES LAB	TW	25	10	20	Р
09. PROGRAMMING LABORATORY	PR	50	20	45	P C	19.	DATA STRUCTURES	AND FILES LAB	PR	50	20	43	Р
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	39	P C	20.	OBJECT ORIENTED	PROGRAMMING LAB	TW	50	20	40	Р
						21.	OBJECT ORIENTED	PROGRAMMING LAB	PR	50	20	45	Р
RAND TOTAL = 953/1500, RESULT: FIRS	T CLA	SS											
RDN. 1 MARKS :													
S8058560 KARACHIWALA HAMZA SHABBI	R			DUE	RRIYA		, 71045470յ	, s8058560 ,	PICT	-	58(05856	50
TO THE CONTRACT OF THE STATE OF				501			, 710131703	, 30030300 ,			, 50	,,,,,,	,,
01. DISCRETE STRUCTURES	PP	100	40	52	P C	11.	ENG MATHS III		PP	100	40	22	F
02. COMPUTER ORGANIZATION	PP	100	40	50	Р	12.	COMPUTER GRAPHI	CS	PP	100	40	46	Р
03. DIGITAL ELECTRONICS & LOGIC DES	IGPP	100	40	52	Р	13.	PROCESSOR ARCHI	TECTURE & INTER.	PP	100	40	47	Р
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	51	P C	14.	DATA STRUCTURES	AND FILES	PP	100	40	69	Р
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	45	P C	15.	DATA COMMUNICAT	ION	PP	100	40	45	Р
								Page 214					

10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 40 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 47 P

06. DIGITAL LABORATORY	TW	50	20	30	PC	16.	PROCESSOR		cols05 ACING LABORATORY	TW	25	10	19	Р
07. DIGITAL LABORATORY	PR	50	20	34	PC	17.	PROCESSOR	INTERF	ACING LABORATORY	OR	50	20	39	Р
08. PROGRAMMING LABORATORY	TW	50	20	36	PC	18.	DATA STRU	CTURES	AND FILES LAB	TW	25	10	15	Р
09. PROGRAMMING LABORATORY	PR	50	20	45	PC	19.	DATA STRU	CTURES	AND FILES LAB	PR	50	20	38	Р
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	37	P C	20.	OBJECT OR	IENTED	PROGRAMMING LAB	TW	50	20	39	Р
						21.	OBJECT OR	IENTED	PROGRAMMING LAB	PR	50	20	44	Р
GRAND TOTAL = 855/1500, RESULT: FAILS	· ^ T I	v T												
	A.I.I	N. I.												
ORDN. 1 MARKS :														
UNIVERSITY OF	PUNE	,S.E.((2008	PAT.)(II	NFORMATION T	ECHNOLOGY)						• •	
DATE : 18 AUG. 2011	CENTI	RE : P	UNE I	NSTI ⁻	TUTE	E OF COMPUTE	R TECHNOLO	GY, PUN	E.	PAGE	NO.	20	(4	42)
NOTE: FIRST LINE : SEAT NO., NAME O	F THE	CANDI	DATE,	MO	THER	R, PERMANENT	REG. NO.,	PREVI	OUS SEAT NO., C	OLLEGE	., s	EAT N	0.	
OTHER LINES: HEAD OF PASSING,	MAX.	MARKS	, MII	N. P	ASS	MARKS, MAR	KS OBTAINE	D, P/F	:PASS/FAIL, C:P	REVIOU	S CAR	RY OV	ER	
S8058561 KARISHMA GARG				KAV:	TTA		, 71045	171M	, s8058561 ,	PICT		, s80	5856	1
30030301 KAKISIIMA GARG				NAV.	114		, 71043	7/ 711	, 30030301 ,	FICT		, 300	3030.	L
01. DISCRETE STRUCTURES	PP	100	40	53	PC	11.	ENG MATHS	III		PP	100	40	59	Р
02. COMPUTER ORGANIZATION	PP	100	40	56	PC	12.	COMPUTER	GRAPHIC	S	PP	100	40	62	Р
03. DIGITAL ELECTRONICS & LOGIC DESI	GPP	100	40	63	PC	13.	PROCESSOR	ARCHIT	ECTURE & INTER.	PP	100	40	40	Р
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	76	PC	14.	DATA STRU	CTURES	AND FILES	PP	100	40	69	Р
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	44	PC	15.	DATA COMM	UNICATI	ON	PP	100	40	48	Р
06. DIGITAL LABORATORY	TW	50	20	38	PC	16.	PROCESSOR	INTERF	ACING LABORATORY	TW	25	10	17	Р
07. DIGITAL LABORATORY	PR	50	20	33	P C	17.	PROCESSOR	INTERF	ACING LABORATORY	OR	50	20	37	Р
08. PROGRAMMING LABORATORY	TW	50	20	39	PC	18.	DATA STRU	CTURES	AND FILES LAB	TW	25	10	13	Р
09. PROGRAMMING LABORATORY	PR	50	20	35	PC	19.	DATA STRU	CTURES	AND FILES LAB	PR	50	20	28	Р
10. COMMUNICATION AND LANGUAGE LAB.														
	TW	50	20	40	PC	20.	OBJECT OR	IENTED	PROGRAMMING LAB	TW	50	20	32	Р
	TW	50	20	40	PC					TW PR	50 50	20 20	32 42	

GRAND TOTAL = 924/1500, RESULT: FIRST CLASS
ORDN. 1 MARKS :

21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 40 P

S	8058562 KAZI SANA NAFIS				FAR	JANA		, 71045478D , S8058562 ,	PICT		, s80	5856	2
C	1. DISCRETE STRUCTURES	PP	100	40	65	P C	11.	ENG MATHS III	PP	100	40	65	Р
C	2. COMPUTER ORGANIZATION	PP	100	40	49	P C	12.	COMPUTER GRAPHICS	PP	100	40	64	Р
C	3. DIGITAL ELECTRONICS & LOGIC DESI	GPP	100	40	75	P C	13.	PROCESSOR ARCHITECTURE & INTER.	PP	100	40	45	Р
C	4. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	75	P C	14.	DATA STRUCTURES AND FILES	PP	100	40	64	Р
C	5. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	40	P C	15.	DATA COMMUNICATION	PP	100	40	61	Р
C	6. DIGITAL LABORATORY	TW	50	20	38	P C	16.	PROCESSOR INTERFACING LABORATORY	TW	25	10	19	Р
C	7. DIGITAL LABORATORY	PR	50	20	32	P C	17.	PROCESSOR INTERFACING LABORATORY	OR	50	20	45	Р
C	8. PROGRAMMING LABORATORY	TW	50	20	43	P C	18.	DATA STRUCTURES AND FILES LAB	TW	25	10	20	Р
C	9. PROGRAMMING LABORATORY	PR	50	20	39	P C	19.	DATA STRUCTURES AND FILES LAB	PR	50	20	39	Р
1	0. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	40	P C	20.	OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	46	Р

GRAND TOTAL = 1004/1500, RESULT: FIRST CLASS WITH DISTINCTION
ORDN. 1 MARKS :

S8058563 KELKAR ANUJA MILIND				SUP	RIYA	, 71045480F , S8058563 , PICT , S80	58563
01. DISCRETE STRUCTURES	PP	100	40	82	P C	11. ENG MATHS III PP 100 40	75 P
02. COMPUTER ORGANIZATION	PP	100	40	40	PС	12. COMPUTER GRAPHICS PP 100 40	58 P
03. DIGITAL ELECTRONICS & LOGIC DES	IGPP	100	40	56	P C	13. PROCESSOR ARCHITECTURE & INTER. PP 100 40	47 P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	76	P C	14. DATA STRUCTURES AND FILES PP 100 40	75 P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	49	P C	15. DATA COMMUNICATION PP 100 40	72 P
06. DIGITAL LABORATORY	TW	50	20	45	P C	16. PROCESSOR INTERFACING LABORATORY TW 25 10	24 P
07. DIGITAL LABORATORY	PR	50	20	38	P C	17. PROCESSOR INTERFACING LABORATORY OR 50 20	45 P
08. PROGRAMMING LABORATORY	TW	50	20	46	P C	18. DATA STRUCTURES AND FILES LAB TW 25 10	20 P
09. PROGRAMMING LABORATORY	PR	50	20	47	P C	19. DATA STRUCTURES AND FILES LAB PR 50 20	32 P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	46	P C	20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20	46 P

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cols05 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 40 P

GRAND TOTAL = 1059+05/1500, RESULT: FIRST CLASS WITH DISTINCTION[0.163]
ORDN. 1 MARKS :

F THE	CANDI	DATE	, MO	THER, PE	ERMANENT		COLLE	GE,	SEAT N	10.	
				-		·					
MAX.	MARKS	, M	IN. P	ASS MARK	KS, MAR	RKS OBTAINED, P/F:PASS/FAIL, C:	PREVI	OUS CA	RRY O	/ER	
							_				
			ANI	TA		, 71045486E , S8058564 ,	PIC	Т	, 580)58564	4
PP	100	40	58	P C	11.	. ENG MATHS III	PP	100	40	40	Р
PP	100	40	29	F	12.	. COMPUTER GRAPHICS	PP	100	40	50	Р
GPP	100	40	74	P C	13.	. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	31	F
PP	100	40	55	P C	14.	. DATA STRUCTURES AND FILES	PP	100	40	66	Р
PP	100	40	40	P C	15.	. DATA COMMUNICATION	PP	100	40	41	Р
TW	50	20	37	P C	16.	. PROCESSOR INTERFACING LABORATOR	XY TW	25	10	23	Р
PR	50	20	36	P C	17.	. PROCESSOR INTERFACING LABORATOR	Y OR	50	20	37	Р
TW	50	20	40	P C	18.	. DATA STRUCTURES AND FILES LAB	TW	25	10	17	Р
PR	50	20	27	P C	19.	. DATA STRUCTURES AND FILES LAB	PR	50	20	40	Р
TW	50	20	40	P C	20.	. OBJECT ORIENTED PROGRAMMING LAE	3 TW	50	20	38	Р
					21.	. OBJECT ORIENTED PROGRAMMING LAE	B PR	50	20	38	Р
: A T	κт										
, д	К. Г.										
			GEE	TA		, 71045491m , S8058565 ,	PIC	Т	, s80)58565	5
	PP PP TW PR TW PR TW PR	PP 100 PP 100 TW 50 PR 50 TW 50 PR 50 TW 50 A.T.K.T.	GPP 100 40 PP 100 40 PP 100 40 TW 50 20 PR 50 20 TW 50 20 TW 50 20 TW 50 20 TW 50 20	GPP 100 40 74 PP 100 40 55 PP 100 40 40 TW 50 20 37 PR 50 20 36 TW 50 20 40 PR 50 20 27 TW 50 20 40 A.T.K.T.	GPP 100 40 74 P C PP 100 40 55 P C PP 100 40 40 P C TW 50 20 37 P C PR 50 20 36 P C TW 50 20 40 P C TW 50 20 40 P C TW 50 20 40 P C A.T.K.T.	GPP 100 40 74 P C 13 PP 100 40 55 P C 14 PP 100 40 40 P C 15 TW 50 20 37 P C 16 PR 50 20 36 P C 17 TW 50 20 40 P C 18 PR 50 20 27 P C 19 TW 50 20 40 P C 20 21 A.T.K.T.	GPP 100 40 74 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 55 P C 14. DATA STRUCTURES AND FILES PP 100 40 40 P C 15. DATA COMMUNICATION TW 50 20 37 P C 16. PROCESSOR INTERFACING LABORATOR PR 50 20 36 P C 17. PROCESSOR INTERFACING LABORATOR TW 50 20 40 P C 18. DATA STRUCTURES AND FILES LAB PR 50 20 27 P C 19. DATA STRUCTURES AND FILES LAB TW 50 20 40 P C 20. OBJECT ORIENTED PROGRAMMING LAB A.T.K.T. GEETA , 71045491M , S8058565 ,	GPP 100 40 74 P C 13. PROCESSOR ARCHITECTURE & INTER. PP PP 100 40 55 P C 14. DATA STRUCTURES AND FILES PP PP 100 40 40 P C 15. DATA COMMUNICATION PP TW 50 20 37 P C 16. PROCESSOR INTERFACING LABORATORY TW PR 50 20 40 P C 17. PROCESSOR INTERFACING LABORATORY OR TW 50 20 40 P C 18. DATA STRUCTURES AND FILES LAB TW PR 50 20 27 P C 19. DATA STRUCTURES AND FILES LAB PR TW 50 20 40 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 21. OBJECT ORIENTED PROGRAMMING LAB PR A.T.K.T.	GPP 100 40 74 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 PP 100 40 55 P C 14. DATA STRUCTURES AND FILES PP 100 PP 100 40 40 P C 15. DATA COMMUNICATION PP 100 TW 50 20 37 P C 16. PROCESSOR INTERFACING LABORATORY TW 25 PR 50 20 36 P C 17. PROCESSOR INTERFACING LABORATORY OR 50 TW 50 20 40 P C 18. DATA STRUCTURES AND FILES LAB TW 25 PR 50 20 27 P C 19. DATA STRUCTURES AND FILES LAB PR 50 TW 50 20 40 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 A.T.K.T.	GPP 100 40 74 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 PP 100 40 55 P C 14. DATA STRUCTURES AND FILES PP 100 40 PC 15. DATA COMMUNICATION PP 100 40 TW 50 20 37 P C 16. PROCESSOR INTERFACING LABORATORY TW 25 10 PR 50 20 36 P C 17. PROCESSOR INTERFACING LABORATORY OR 50 20 TW 50 20 40 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 PR 50 20 27 P C 19. DATA STRUCTURES AND FILES LAB PR 50 20 TW 50 20 40 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 A.T.K.T.	GPP 100 40 74 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 31 PP 100 40 55 P C 14. DATA STRUCTURES AND FILES PP 100 40 66 PP 100 40 40 P C 15. DATA COMMUNICATION PP 100 40 41 TW 50 20 37 P C 16. PROCESSOR INTERFACING LABORATORY TW 25 10 23 PR 50 20 36 P C 17. PROCESSOR INTERFACING LABORATORY OR 50 20 37 TW 50 20 40 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 17 PR 50 20 27 P C 19. DATA STRUCTURES AND FILES LAB PR 50 20 40 TW 50 20 40 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 38 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 38 A.T.K.T.

02. COMPUTER ORGANIZATION	PP	100	40	03	F	cols05 12. COMPUTER GRAPHICS PP 100 40 05 F
03. DIGITAL ELECTRONICS & LOGIC DESI	GPP	100	40	19	F	13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 02 F
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	45	Р	14. DATA STRUCTURES AND FILES PP 100 40 07 F
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	40	P C	15. DATA COMMUNICATION PP 100 40 04 F
06. DIGITAL LABORATORY	TW	50	20	22	РС	16. PROCESSOR INTERFACING LABORATORY TW 25 10 15 P
07. DIGITAL LABORATORY	PR	50	20	33	РС	17. PROCESSOR INTERFACING LABORATORY OR 50 20 13 F
08. PROGRAMMING LABORATORY	TW	50	20	35	P C	18. DATA STRUCTURES AND FILES LAB TW 25 10 15 P
09. PROGRAMMING LABORATORY	PR	50	20	35	P C	19. DATA STRUCTURES AND FILES LAB PR 50 20 29 P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	37	РС	20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 35 P
						21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 40 P

GRAND TOTAL = 434/1500, RESULT: FAILS RESULT RESERVED FOR BKLG

ORDN. 1 MARKS:

S8058566 KULKARNI AMITA DILIP				MEG	НА		, 71045492K , S8058566	, PIC	Т	, s80	05856	6
01. DISCRETE STRUCTURES	PP	100	40	52	P C	11	ENG MATHS III	PP	100	40	56	Р
02. COMPUTER ORGANIZATION	PP	100	40	49	P C	12	COMPUTER GRAPHICS	PP	100	40	55	Р
03. DIGITAL ELECTRONICS & LOGIC DESI	GPP	100	40	56	P C	13	PROCESSOR ARCHITECTURE & INTER	R. PP	100	40	40	Р
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	71	P C	14	DATA STRUCTURES AND FILES	PP	100	40	68	Р
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	40	P C	15	DATA COMMUNICATION	PP	100	40	47	Р
06. DIGITAL LABORATORY	TW	50	20	39	P C	16	PROCESSOR INTERFACING LABORATO	ORY TW	25	10	20	Р
07. DIGITAL LABORATORY	PR	50	20	42	P C	17	PROCESSOR INTERFACING LABORATO	ORY OR	50	20	38	Р
08. PROGRAMMING LABORATORY	TW	50	20	42	P C	18	DATA STRUCTURES AND FILES LAB	TW	25	10	20	Р
09. PROGRAMMING LABORATORY	PR	50	20	32	P C	19	DATA STRUCTURES AND FILES LAB	PR	50	20	39	Р
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	42	P C	20	OBJECT ORIENTED PROGRAMMING LA	AB TW	50	20	46	Р
						21	OBJECT ORIENTED PROGRAMMING LA	AB PR	50	20	35	Р

GRAND TOTAL = 929/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS:

UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(INFORMATION TECHNOLOGY)

Ш		UNIVERSITY OF	FUNL	, 3. ∟.	(2000	rai.) (INI OK	MATION	LCHNOLOG1)						
	DATE : 18	AUG. 2011	CENT	RE :	PUNE IN	NSTI	TUTE OF	COMPUTE	R TECHNOLOGY, PUNE.		PAG	E NO.	22	(4	44)
	NOTE: FIRST L	INE : SEAT NO., NAME (OF THE	CAND	IDATE,	MO	THER, PE	ERMANENT	REG. NO., PREVIOUS SEAT NO	., c	DLLEG	E, S	EAT N	10.	
	OTHER L	INES: HEAD OF PASSING,	MAX.	MARK	S, MIN	N. P	ASS MARK	KS, MARI	(S OBTAINED, P/F:PASS/FAIL,	C:PF	REVIO	US CAR	RY OV	/ER	
	S8058567 KU	LKARNI PRAMOD SHIVAJIR	40			SUM	ATI		, 71045672н , ѕ805856	7,	PICT		, s80)5856	7
	01. DISCRETE	STRUCTURES	PP	100	40	40	P C	11.	ENG MATHS III		PP	100	40	40	Р
	02. COMPUTER	ORGANIZATION	PP	100	40	52	P C	12.	COMPUTER GRAPHICS		PP	100	40	43	Р
	03. DIGITAL E	LECTRONICS & LOGIC DES	IGPP	100	40	40	P C	13.	PROCESSOR ARCHITECTURE & IN	TER.	PP	100	40	22	F
	04. FUNDAMENT	AL OF DATA STRUCTURES	PP	100	40	42	P C	14.	DATA STRUCTURES AND FILES		PP	100	40	50	Р
	05. HUMANITIE	S AND SOCIAL SCIENCES	PP	100	40	43	P C	15.	DATA COMMUNICATION		PP	100	40	40	Р
	06. DIGITAL L	ABORATORY	TW	50	20	20	P C	16.	PROCESSOR INTERFACING LABOR	ATORY	TW	25	10	19	Р
	07. DIGITAL L	ABORATORY	PR	50	20	28	P C	17.	PROCESSOR INTERFACING LABOR	ATORY	OR	50	20	10	F
	08. PROGRAMMI	NG LABORATORY	TW	50	20	27	P C	18.	DATA STRUCTURES AND FILES LA	AB	TW	25	10	12	Р
	09. PROGRAMMI	NG LABORATORY	PR	50	20	27	P C	19.	DATA STRUCTURES AND FILES LA	AB	PR	50	20	10	F
	10. COMMUNICA	TION AND LANGUAGE LAB.	TW	50	20	23	P C	20.	OBJECT ORIENTED PROGRAMMING	LAB	TW	50	20	32	Р
								21.	OBJECT ORIENTED PROGRAMMING	LAB	PR	50	20	10	F

GRAND TOTAL = 630/1500, RESULT: FAILS A.T.K.T.
ORDN. 1 MARKS :

S8058568 LAKHEPATIL AJINKYA RAMRAJ	ΙE			HEM	LATA		, 71045502L , S8058568 ,	PICT		, \$80)5856	8
01. DISCRETE STRUCTURES	PP	100	40	54	P C	11	ENG MATHS III	PP	100	40	40	Р
02. COMPUTER ORGANIZATION	PP	100	40	46	P C	12	COMPUTER GRAPHICS	PP	100	40	55	Р
03. DIGITAL ELECTRONICS & LOGIC DESI	GPP	100	40	67	P C	13	PROCESSOR ARCHITECTURE & INTER.	PP	100	40	40	Р
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	49	P C	14	DATA STRUCTURES AND FILES	PP	100	40	73	Р
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	44	P C	15	DATA COMMUNICATION	PP	100	40	50	Р
06. DIGITAL LABORATORY	TW	50	20	35	P C	16	PROCESSOR INTERFACING LABORATORY	TW	25	10	17	Р

cols05 07. DIGITAL LABORATORY 50 20 27 P C 17. PROCESSOR INTERFACING LABORATORY OR 50 20 42 P 08. PROGRAMMING LABORATORY 20 38 P C 18. DATA STRUCTURES AND FILES LAB TW TW 50 25 10 19 P 09. PROGRAMMING LABORATORY 50 20 35 P C 19. DATA STRUCTURES AND FILES LAB 50 20 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 35 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 43 P 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 GRAND TOTAL = 893+07/1500, RESULT: FIRST CLASS [0.2] ORDN. 1 MARKS: , 71134968в S8058569 LONKAR GORAKH SUBHASH SHOBHA , s8058569 , PICT , S8058569 01. DISCRETE STRUCTURES PP 100 40 59 P C 11. ENG MATHS III 100 40 43 P 02. COMPUTER ORGANIZATION 100 40 71 P C 12. COMPUTER GRAPHICS 100

13. PROCESSOR ARCHITECTURE & INTER. PP 40 57 P C 100 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 70 P C 14. DATA STRUCTURES AND FILES 100 75 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 41 P C 100 15. DATA COMMUNICATION 68 P 20 38 P C 06. DIGITAL LABORATORY 25 23 P TW 50 16. PROCESSOR INTERFACING LABORATORY TW 10 07. DIGITAL LABORATORY 50 20 38 P C 17. PROCESSOR INTERFACING LABORATORY OR 50 20 39 P 08. PROGRAMMING LABORATORY 18. DATA STRUCTURES AND FILES LAB TW TW 50 20 44 P C 25 10 23 P 09. PROGRAMMING LABORATORY 50 20 44 P C 19. DATA STRUCTURES AND FILES LAB PR 50 20 39 P 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 38 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 46 P 50 20 38 P 21. OBJECT ORIENTED PROGRAMMING LAB PR

GRAND TOTAL = 992/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS:

UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(INFORMATION TECHNOLOGY)

DATE: 18 AUG. 2011 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 23 (445)

NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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S8058570 MAHAJAN AKSHATA DEVENDRA				JAIS	SHREE	, 71045507м , S8058570 , РІСТ , S80585	570
01. DISCRETE STRUCTURES	PP	100	40	74	P C	1. ENG MATHS III PP 100 40 71	L P
02. COMPUTER ORGANIZATION	PP	100	40	59	P C	2. COMPUTER GRAPHICS PP 100 40 60) P
03. DIGITAL ELECTRONICS & LOGIC DESI	GPP	100	40	73	P C	3. PROCESSOR ARCHITECTURE & INTER. PP 100 40 52	2 P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	74	P C	4. DATA STRUCTURES AND FILES PP 100 40 86	5 P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	40	P C	5. DATA COMMUNICATION PP 100 40 53	3 P
06. DIGITAL LABORATORY	TW	50	20	35	P C	6. PROCESSOR INTERFACING LABORATORY TW 25 10 24	ł P
07. DIGITAL LABORATORY	PR	50	20	42	P C	7. PROCESSOR INTERFACING LABORATORY OR 50 20 47	7 P
08. PROGRAMMING LABORATORY	TW	50	20	41	P C	8. DATA STRUCTURES AND FILES LAB TW 25 10 20) P
09. PROGRAMMING LABORATORY	PR	50	20	44	P C	9. DATA STRUCTURES AND FILES LAB PR 50 20 42	2 P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	38	P C	O. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 45	5 P
						1. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 41	L P

GRAND TOTAL = 1061/1500, RESULT: FIRST CLASS WITH DISTINCTION
ORDN. 1 MARKS :

S8058571 MAHALPURE SHRUTI SATISH				SUSI	НМА		, 71045509н	, s8058571 ,	PICT		, s80	5857	1
01. DISCRETE STRUCTURES	PP	100	40	53	P C	11	ENG MATHS III		PP	100	40	76	Р
02. COMPUTER ORGANIZATION	PP	100	40	46	P C	12	COMPUTER GRAPHICS	5	PP	100	40	55	Р
03. DIGITAL ELECTRONICS & LOGIC DES	IGPP	100	40	52	P C	13	PROCESSOR ARCHITE	ECTURE & INTER.	PP	100	40	41	Р
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	69	P C	14	DATA STRUCTURES A	AND FILES	PP	100	40	66	Р
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	49	P C	15	DATA COMMUNICATIO	ON	PP	100	40	57	Р
06. DIGITAL LABORATORY	TW	50	20	41	P C	16	PROCESSOR INTERFA	ACING LABORATORY	TW	25	10	18	Р
07. DIGITAL LABORATORY	PR	50	20	38	P C	17	PROCESSOR INTERFA	ACING LABORATORY	OR	50	20	44	Р
08. PROGRAMMING LABORATORY	TW	50	20	43	P C	18	DATA STRUCTURES A	AND FILES LAB	TW	25	10	20	Р
09. PROGRAMMING LABORATORY	PR	50	20	28	P C	19	DATA STRUCTURES A	AND FILES LAB	PR	50	20	25	Р
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	44	P C	20	OBJECT ORIENTED	PROGRAMMING LAB	TW	50	20	45	Р
						21	OBJECT ORIENTED F	PROGRAMMING LAB	PR	50	20	32	Р

GRAND TOTAL = 942/1500, RESULT: FIRST CLASS

02. COMPUTER ORGANIZATION

ORDN. 1 MARKS:

S8058572 MARDA SAKSHI BRIJMOHAN				JY0	TI		, 71045521G , S8058572 ,	PICT	-	, s80)5857	'2
01. DISCRETE STRUCTURES	PP	100	40	66	P C	11.	ENG MATHS III	PP	100	40	54	Р
02. COMPUTER ORGANIZATION	PP	100	40	46	P C	12.	COMPUTER GRAPHICS	PP	100	40	56	Р
03. DIGITAL ELECTRONICS & LOGIC DES	IGPP	100	40	59	P C	13.	PROCESSOR ARCHITECTURE & INTER.	PP	100	40	43	Р
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	59	P C	14.	DATA STRUCTURES AND FILES	PP	100	40	65	Р
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	45	P C	15.	DATA COMMUNICATION	PP	100	40	47	Р
06. DIGITAL LABORATORY	TW	50	20	38	P C	16.	PROCESSOR INTERFACING LABORATORY	′ TW	25	10	19	Р
07. DIGITAL LABORATORY	PR	50	20	37	P C	17.	PROCESSOR INTERFACING LABORATORY	OR	50	20	30	Р
08. PROGRAMMING LABORATORY	TW	50	20	36	P C	18.	DATA STRUCTURES AND FILES LAB	TW	25	10	16	Р
09. PROGRAMMING LABORATORY	PR	50	20	30	РС	19.	DATA STRUCTURES AND FILES LAB	PR	50	20	25	Р
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	34	P C	20.	OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	33	Р
						21.	OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	38	Р

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DATE: 18 AUG. 2011	CENTRE : PUNE INSTITUTE OF CO		PAGE NO. 24 (446)
NOTE: FIRST LINE : SEAT NO., NAME	OF THE CANDIDATE, MOTHER, PERM	•	., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING	, MAX. MARKS, MIN. PASS MARKS,	MARKS OBTAINED, P/F:PASS/FAIL,	C:PREVIOUS CARRY OVER
S8058573 MARWA MAYUR RAJESH	ASHA	, 71045522E , S8058573	3 , PICT , S8058573
01. DISCRETE STRUCTURES	PP 100 40 65 P C	11. ENG MATHS III	PP 100 40 43 P

PP 100 40 40 P C 12. COMPUTER GRAPHICS

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PP 100 40 42 P

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03. DIGITAL ELECTRONICS & LOGIC DESI	GPP	100	40	48	P C	cols05 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 40	Р
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	48	PC	14. DATA STRUCTURES AND FILES PP 100 40 59	Р
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	54	P C	15. DATA COMMUNICATION PP 100 40 42	Р
06. DIGITAL LABORATORY	TW	50	20	20	PC	16. PROCESSOR INTERFACING LABORATORY TW 25 10 17	Р
07. DIGITAL LABORATORY	PR	50	20	25	PC	17. PROCESSOR INTERFACING LABORATORY OR 50 20 29	Р
08. PROGRAMMING LABORATORY	TW	50	20	30	PC	18. DATA STRUCTURES AND FILES LAB TW 25 10 13	Р
09. PROGRAMMING LABORATORY	PR	50	20	40	PC	19. DATA STRUCTURES AND FILES LAB PR 50 20 39	Р
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	29	P C	20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 33	Р
						21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 20	Р

GRAND TOTAL = 776/1500, RESULT: SECOND CLASS

ORDN. 1 MARKS:

S8058574 MAYANK SINGH				ARCI	HANA		, 71045524M	, s8058574 ,	PICT		, s80	58574	4
01. DISCRETE STRUCTURES	PP	100	40	52	P C	11.	ENG MATHS III		PP	100	40	44	Р
02. COMPUTER ORGANIZATION	PP	100	40	56	P C	12.	COMPUTER GRAPHICS		PP	100	40	40	Р
03. DIGITAL ELECTRONICS & LOGIC DESI	GPP	100	40	56	P C	13.	PROCESSOR ARCHITE	CTURE & INTER.	PP	100	40	55	Р
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	55	P C	14.	DATA STRUCTURES A	ND FILES	PP	100	40	56	Р
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	50	P C	15.	DATA COMMUNICATIO	N	PP	100	40	53	Р
06. DIGITAL LABORATORY	TW	50	20	26	P C	16.	PROCESSOR INTERFA	CING LABORATORY	TW	25	10	17	Р
07. DIGITAL LABORATORY	PR	50	20	27	P C	17.	PROCESSOR INTERFA	CING LABORATORY	OR	50	20	40	Р
08. PROGRAMMING LABORATORY	TW	50	20	32	P C	18.	DATA STRUCTURES A	ND FILES LAB	TW	25	10	20	Р
09. PROGRAMMING LABORATORY	PR	50	20	44	P C	19.	DATA STRUCTURES A	ND FILES LAB	PR	50	20	36	Р
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	26	P C	20.	OBJECT ORIENTED P	ROGRAMMING LAB	TW	50	20	37	Р
						21.	OBJECT ORIENTED P	ROGRAMMING LAB	PR	50	20	43	Р

GRAND TOTAL = 865/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS:

S8058575 MOHAMMED AAQUIB ANSARI MO	HAMME	D YUSU	F	SUF	IYA		, 71045526н	cols05 , S8058575 ,	PICT		, s80	5857	5
01. DISCRETE STRUCTURES	PP	100	40	43	P	11.	ENG MATHS III		PP	100	40	14	F
02. COMPUTER ORGANIZATION	PP	100	40	41	P C	12.	COMPUTER GRAPHIC	CS	PP	100	40	50	Р
03. DIGITAL ELECTRONICS & LOGIC DESI	GPP	100	40	45	P C	13.	PROCESSOR ARCHI	TECTURE & INTER.	PP	100	40	30	F
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	40	P C	14.	DATA STRUCTURES	AND FILES	PP	100	40	73	Р
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	40	P C	15.	DATA COMMUNICAT:	ION	PP	100	40	45	Р
06. DIGITAL LABORATORY	TW	50	20	20	P C	16.	PROCESSOR INTER	FACING LABORATORY	TW	25	10	10	Р
07. DIGITAL LABORATORY	PR	50	20	06	F	17.	PROCESSOR INTER	FACING LABORATORY	OR	50	20	08	F
08. PROGRAMMING LABORATORY	TW	50	20	20	P C	18.	DATA STRUCTURES	AND FILES LAB	TW	25	10	13	Р
09. PROGRAMMING LABORATORY	PR	50	20	35	P C	19.	DATA STRUCTURES	AND FILES LAB	PR	50	20	10	F
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	20	P C	20.	OBJECT ORIENTED	PROGRAMMING LAB	TW	50	20	20	Р
						21.	OBJECT ORIENTED	PROGRAMMING LAB	PR	50	20	20	Р
GRAND TOTAL = 603/1500, RESULT: FAILS ORDN. 1 MARKS:			· · ·	·									
UNIVERSITY OF	PUNE	,S.E.	(2008	PAT.)(INFORMA	TION T	ECHNOLOGY)						
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DATE : 18 AUG. 2011	CENT	RE : P	UNE I	NSTI	TUTE OF CO	MPUTEI	R TECHNOLOGY, PUI	NE.	PAG	E NO.	25	(4	47)
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	 F THE		 DATE,	 МО	· · · · · · THER, PERM	 MANENT	REG. NO., PREV		 DLLEG	 E, S	 EAT N		47)
NOTE: FIRST LINE : SEAT NO., NAME O	 F THE		 DATE,	 МО	 THER, PERM ASS MARKS,	 MANENT	REG. NO., PREV		 DLLEG	 E, S US CAR	 EAT N	 IO . 'ER 	
NOTE: FIRST LINE : SEAT NO., NAME O OTHER LINES: HEAD OF PASSING,	 F THE		 DATE,	MO'N. PA	 THER, PERM ASS MARKS,	 IANENT MARI	REG. NO., PREVI		 OLLEG REVIO	 E, S US CAR	 EAT N RY OV	 IO . 'ER 	
NOTE: FIRST LINE: SEAT NO., NAME O OTHER LINES: HEAD OF PASSING,	 F THE MAX.	CANDI MARKS	DATE,	 MO ¹ N. P. SNE	 THER, PERM ASS MARKS, 	MARI	REG. NO., PREVI KS OBTAINED, P/I		OLLEGREVIO	 E, S US CAR	EAT NRY OV	ER	 6 F
NOTE: FIRST LINE : SEAT NO., NAME O OTHER LINES: HEAD OF PASSING, S8058576 MULAY AMIT YESHWANT 01. DISCRETE STRUCTURES	F THE MAX	CANDI MARKS	DATE, , MI	 MO ¹ N. P ² SNE ¹ 61 44	THER, PERMASS MARKS, HAL	 MARI 11. 12.	REG. NO., PREVI KS OBTAINED, P/I 	COUS SEAT NO., CO F:PASS/FAIL, C:PI 	OLLEGREVIO	E, S US CAR			 6 F
NOTE: FIRST LINE: SEAT NO., NAME OF OTHER LINES: HEAD OF PASSING, S8058576 MULAY AMIT YESHWANT O1. DISCRETE STRUCTURES O2. COMPUTER ORGANIZATION	F THE MAX	CANDI MARKS	DATE, MI	MO [*] N. P. SNE 61 44 44	THER, PERMASS MARKS, HAL PC	MARI	REG. NO., PREVI KS OBTAINED, P/I 	COUS SEAT NO., COE:PASS/FAIL, C:PI , S8058576 , CS FECTURE & INTER.	DLLEG REVIO PICT PP PP	E, S US CAR	EAT NRY OV		6 F F
NOTE: FIRST LINE: SEAT NO., NAME OF OTHER LINES: HEAD OF PASSING, S8058576 MULAY AMIT YESHWANT O1. DISCRETE STRUCTURES O2. COMPUTER ORGANIZATION O3. DIGITAL ELECTRONICS & LOGIC DESI	F THE MAX	CANDI MARKS 	DATE, MI	MO' N. P. SNE	THER, PERMASS MARKS, HAL PC PC PC	MARI 11. 12. 13. 14.	REG. NO., PREVIOUS PREVIOUS PREVIOUS PREVIOUS PREVIOUS PREVIOUS PROCESSOR ARCHITECTURE PROCESSOR	COUS SEAT NO., CONTROL COUS SEAT NO., CONTROL COURS SEAT NO., CONTROL COURS SEAT NO., CONTROL COURS SEAT SECTURE & INTER.	DLLEG REVIO PICT PP PP	E, S US CAR	EAT N RY OV , S80 40 40 40		6 F P
NOTE: FIRST LINE: SEAT NO., NAME OF OTHER LINES: HEAD OF PASSING, S8058576 MULAY AMIT YESHWANT O1. DISCRETE STRUCTURES O2. COMPUTER ORGANIZATION O3. DIGITAL ELECTRONICS & LOGIC DESIGNATION O4. FUNDAMENTAL OF DATA STRUCTURES	F THE MAX. PP PP GPP	CANDI MARKS 	DATE, MI 40 40 40 40 40	MO' N. P. SNE 61 44 44 41 53	THER, PERMASS MARKS, HAL PC PC PC PC	MARI 11. 12. 13. 14. 15.	REG. NO., PREVIOUS PREVIOUS PREVIOUS PREVIOUS PREVIOUS PREVIOUS PROCESSOR ARCHIDATA STRUCTURES DATA COMMUNICATION	COUS SEAT NO., CONTROL COUS SEAT NO., CONTROL COURS SEAT NO., CONTROL COURS SEAT NO., CONTROL COURS SEAT SECTURE & INTER.	DLLEGREVIO PICT PP PP PP PP	E, S US CAR 100 100 100 100	EAT N RY OV , S80 40 40 40 40		6 F P P
NOTE: FIRST LINE: SEAT NO., NAME OF OTHER LINES: HEAD OF PASSING, S8058576 MULAY AMIT YESHWANT O1. DISCRETE STRUCTURES O2. COMPUTER ORGANIZATION O3. DIGITAL ELECTRONICS & LOGIC DESITOR O4. FUNDAMENTAL OF DATA STRUCTURES O5. HUMANITIES AND SOCIAL SCIENCES	F THE MAX. PP PP PP	CANDI MARKS 	DATE, MI 40 40 40 40 40 40	MO' N. P. SNE 61 44 41 53 20	THER, PERMASS MARKS, HAL PC PC PC PC PC	 MANENT MARI 11. 12. 13. 14. 15. 16.	REG. NO., PREVIOUS PROCESSOR INTERIOR	COUS SEAT NO., CONTROL COUS SEAT NO., CONTROL COURS SEAT NO., CONTROL COURS SEAT NO., CONTROL COURS SEAT NO., S8058576 , CONTROL COURS SEAT NO.	DLLEGREVIO PICT PP PP PP PP TW	E, S US CAR 100 100 100 100 100	EAT N RY OV , \$80 40 40 40 40 40		6 F P P

						cols05	
08. PROGRAMMING LABORATORY	TW	50	20	25	PC	18. DATA STRUCTURES AND FILES LAB TW 25 10 12	Р
09. PROGRAMMING LABORATORY	PR	50	20	29	Р	19. DATA STRUCTURES AND FILES LAB PR 50 20 AA	F
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	25	P C	20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 23	Р
						21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 25	Р

GRAND TOTAL = 646/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS:

S8058	3577 MULEY PRASAD MUKUNDRAO				SUSI	HMA		, 71134969L	, s8058577 ,	PICT		, s80	58577	7
01. [DISCRETE STRUCTURES	PP	100	40	44	РС	11.	ENG MATHS III		PP	100	40	26	F
02. 0	COMPUTER ORGANIZATION	PP	100	40	52	P C	12	COMPUTER GRAPHICS	5	PP	100	40	48	Р
03. [DIGITAL ELECTRONICS & LOGIC DESI	GPP	100	40	58	P C	13	PROCESSOR ARCHITE	ECTURE & INTER.	PP	100	40	54	Р
04. F	FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	52	P C	14	DATA STRUCTURES A	AND FILES	PP	100	40	63	Р
05. H	HUMANITIES AND SOCIAL SCIENCES	PP	100	40	47	P C	15	DATA COMMUNICATIO	ON	PP	100	40	56	Р
06. [DIGITAL LABORATORY	TW	50	20	38	P C	16	PROCESSOR INTERFA	ACING LABORATORY	TW	25	10	18	Р
07. [DIGITAL LABORATORY	PR	50	20	27	P C	17	PROCESSOR INTERFA	ACING LABORATORY	OR	50	20	34	Р
08. F	PROGRAMMING LABORATORY	TW	50	20	43	P C	18	DATA STRUCTURES A	AND FILES LAB	TW	25	10	19	Р
09. F	PROGRAMMING LABORATORY	PR	50	20	38	P C	19	DATA STRUCTURES A	AND FILES LAB	PR	50	20	41	Р
10. 0	COMMUNICATION AND LANGUAGE LAB.	TW	50	20	34	P C	20	OBJECT ORIENTED F	PROGRAMMING LAB	TW	50	20	42	Р
							21	OBJECT ORIENTED F	PROGRAMMING LAB	PR	50	20	28	Р

GRAND TOTAL = 862/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

S8058578 MULLA ASMA MUSA				RAB	IYA		, 71134970D , s805	8578 ,	PICT		, s80)58578	8
01. DISCRETE STRUCTURES	PP	100	40	49	Р	11	ENG MATHS III		PP	100	40	24	F
02. COMPUTER ORGANIZATION	PP	100	40	40	P C	12	COMPUTER GRAPHICS		PP	100	40	32	F
03. DIGITAL ELECTRONICS & LOGIC DESIG	GPP	100	40	18	F	13	PROCESSOR ARCHITECTURE &	W INTER.	PP	100	40	30	F
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	48	Р	14	DATA STRUCTURES AND FILE Page 225	ES	PP	100	40	54	Р

05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	44	P C	15.	DATA COMMUNICATION	PP	100	40	43	Р
06. DIGITAL LABORATORY	TW	50	20	38	P C	16.	PROCESSOR INTERFACING LABORATORY	TW	25	10	21	Р
07. DIGITAL LABORATORY	PR	50	20	22	P C	17.	PROCESSOR INTERFACING LABORATORY	OR	50	20	29	Р
08. PROGRAMMING LABORATORY	TW	50	20	42	P C	18.	DATA STRUCTURES AND FILES LAB	TW	25	10	22	Р
09. PROGRAMMING LABORATORY	PR	50	20	33	P C	19.	DATA STRUCTURES AND FILES LAB	PR	50	20	22	Р
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	37	P C	20.	OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	43	Р
						21.	OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	20	Р
GRAND TOTAL = 711/1500, RESULT: FAILS												
ORDN. 1 MARKS :												
UNIVERSITY OF	 PUNE	 ,S.E.(2008	 PAT.		 ION T	ECHNOLOGY)					
	CENT	RE : P	UNE I	NSTI	TUTE OF COM	1PUTE	R TECHNOLOGY, PUNE.	PAG	E NO.	26	(4	48)
DATE : 18 AUG. 2011												
										 SEAT N		
NOTE: FIRST LINE : SEAT NO., NAME O	F THE	CANDI	DATE,	МО	THER, PERMA	ANENT		OLLEG	E, S	EAT N	NO.	
NOTE: FIRST LINE : SEAT NO., NAME O OTHER LINES: HEAD OF PASSING,	F THE	CANDI MARKS	DATE,	MO N. P	THER, PERMA	ANENT MAR	REG. NO., PREVIOUS SEAT NO., CO	OLLEG REVIO	E, S US CAR	EAT N	NO. /ER	
NOTE: FIRST LINE : SEAT NO., NAME O OTHER LINES: HEAD OF PASSING,	F THE	CANDI MARKS	DATE,	MO N. P.	THER, PERMA	ANENT MAR	REG. NO., PREVIOUS SEAT NO., CO	OLLEG REVIO	E, S US CAR	EAT N	NO. /ER	
NOTE: FIRST LINE : SEAT NO., NAME O OTHER LINES: HEAD OF PASSING,	F THE	CANDI MARKS	DATE, , MII	MO N. P KAL	THER, PERMAASS MARKS,	MAR	REG. NO., PREVIOUS SEAT NO., COKS OBTAINED, P/F:PASS/FAIL, C:PI	OLLEG REVIO	E, S US CAR 	SEAT N	NO. /ER 	9
NOTE: FIRST LINE : SEAT NO., NAME O OTHER LINES: HEAD OF PASSING, S8058579 MURKUTE DEVYANI BALASAHEB 01. DISCRETE STRUCTURES	F THE	CANDI MARKS	DATE, , MII	MO N. P. KAL	THER, PERMAASS MARKS, PANA P C	MAR	REG. NO., PREVIOUS SEAT NO., COKS OBTAINED, P/F:PASS/FAIL, C:PI, 71134971B , S8058579 , ENG MATHS III	OLLEG REVIO PICT PP	E, S US CAR 	SEAT N RRY OV , S80	NO. VER D58579	 9
NOTE: FIRST LINE: SEAT NO., NAME O OTHER LINES: HEAD OF PASSING, S8058579 MURKUTE DEVYANI BALASAHEB 01. DISCRETE STRUCTURES 02. COMPUTER ORGANIZATION	F THE MAX	MARKS 100 100	DATE, , MII 40 40	MO N. P. KAL 45 47	THER, PERMAASS MARKS, PANA P C P C	MAR	REG. NO., PREVIOUS SEAT NO., COKS OBTAINED, P/F:PASS/FAIL, C:PI, 71134971B , S8058579 , ENG MATHS III COMPUTER GRAPHICS	OLLEG REVIO PICT PP	E, S US CAR 100 100	RRY OV , S80 40 40	NO. VER D58579 49 60	 9
NOTE: FIRST LINE: SEAT NO., NAME O OTHER LINES: HEAD OF PASSING, S8058579 MURKUTE DEVYANI BALASAHEB 01. DISCRETE STRUCTURES 02. COMPUTER ORGANIZATION 03. DIGITAL ELECTRONICS & LOGIC DESI	F THE MAX PP PP GPP	100 100 100	DATE, , MII 40 40 40	MO N. P. KAL 45 47 40	THER, PERMA ASS MARKS, PANA P C P C P C	MAR	REG. NO., PREVIOUS SEAT NO., COKS OBTAINED, P/F:PASS/FAIL, C:PI, 71134971B , S8058579 , ENG MATHS III COMPUTER GRAPHICS PROCESSOR ARCHITECTURE & INTER.	OLLEG REVIO PICT PP	E, S US CAR 100 100 100	RRY OV , S80 40 40 40	NO. VER 058579 49 60 49	9 P P
NOTE: FIRST LINE: SEAT NO., NAME O OTHER LINES: HEAD OF PASSING, S8058579 MURKUTE DEVYANI BALASAHEB 01. DISCRETE STRUCTURES 02. COMPUTER ORGANIZATION 03. DIGITAL ELECTRONICS & LOGIC DESI 04. FUNDAMENTAL OF DATA STRUCTURES	F THE MAX. PP PP GPP	100 100 100 100	DATE, , MII 40 40	MO N. P. KAL 45 47 40 48	THER, PERMA ASS MARKS, PANA P C P C P C P C	MAR 11. 12. 13.	REG. NO., PREVIOUS SEAT NO., COKS OBTAINED, P/F:PASS/FAIL, C:PI, 71134971B , S8058579 , ENG MATHS III COMPUTER GRAPHICS PROCESSOR ARCHITECTURE & INTER. DATA STRUCTURES AND FILES	OLLEG REVIO PICT PP	E, S US CAR 100 100	RRY OV , S80 40 40	NO. VER D58579 49 60	9 P P
NOTE: FIRST LINE : SEAT NO., NAME O OTHER LINES: HEAD OF PASSING, S8058579 MURKUTE DEVYANI BALASAHEB 01. DISCRETE STRUCTURES 02. COMPUTER ORGANIZATION 03. DIGITAL ELECTRONICS & LOGIC DESI 04. FUNDAMENTAL OF DATA STRUCTURES 05. HUMANITIES AND SOCIAL SCIENCES	F THE MAX. PP PP GPP	100 100 100 100 100	DATE, , MII 40 40 40 40 40 40	MO N. P. KAL 45 47 40 48 40	THER, PERMA ASS MARKS, PANA PC PC PC PC	MAR 11. 12. 13. 14.	REG. NO., PREVIOUS SEAT NO., COKS OBTAINED, P/F:PASS/FAIL, C:PI, 71134971B , S8058579 , ENG MATHS III COMPUTER GRAPHICS PROCESSOR ARCHITECTURE & INTER. DATA STRUCTURES AND FILES DATA COMMUNICATION	PICT PP PP PP PP	E, S US CAR 100 100 100	RRY OV , S80 40 40 40 40 40	NO. VER 058579 49 60 49 76 65	9 P P P
NOTE: FIRST LINE: SEAT NO., NAME O OTHER LINES: HEAD OF PASSING, S8058579 MURKUTE DEVYANI BALASAHEB 01. DISCRETE STRUCTURES 02. COMPUTER ORGANIZATION 03. DIGITAL ELECTRONICS & LOGIC DESI 04. FUNDAMENTAL OF DATA STRUCTURES	F THE MAX. PP PP GPP	100 100 100 100	DATE, , MII 40 40 40 40	MO N. P KAL 45 47 40 48 40 36	THER, PERMA ASS MARKS, PANA PC PC PC PC PC PC	MAR 11. 12. 13. 14.	REG. NO., PREVIOUS SEAT NO., COKS OBTAINED, P/F:PASS/FAIL, C:PI, 71134971B , S8058579 , ENG MATHS III COMPUTER GRAPHICS PROCESSOR ARCHITECTURE & INTER. DATA STRUCTURES AND FILES	PICT PP PP PP PP	E, S US CAR 100 100 100	RY OV , \$80 40 40 40 40 40	NO. VER 058579 49 60 49 76	9 P P P
NOTE: FIRST LINE : SEAT NO., NAME O OTHER LINES: HEAD OF PASSING, S8058579 MURKUTE DEVYANI BALASAHEB 01. DISCRETE STRUCTURES 02. COMPUTER ORGANIZATION 03. DIGITAL ELECTRONICS & LOGIC DESI 04. FUNDAMENTAL OF DATA STRUCTURES 05. HUMANITIES AND SOCIAL SCIENCES	F THE MAX PP PP GPP PP	100 100 100 100 100	DATE, , MII 40 40 40 40 40 40	MO N. P KAL 45 47 40 48 40 36	THER, PERMA ASS MARKS, PANA PC PC PC PC	11. 12. 13. 14. 15. 16.	REG. NO., PREVIOUS SEAT NO., COKS OBTAINED, P/F:PASS/FAIL, C:PROCESSOR ARCHITECTURE & INTER. DATA STRUCTURES AND FILES DATA COMMUNICATION PROCESSOR INTERFACING LABORATORY PROCESSOR INTERFACING LABORATORY	PICT PP PP PP PP TW	E, S US CAR 100 100 100 100	RY OV , \$80 40 40 40 40 40 20	NO. VER 058579 49 60 49 76 65	
NOTE: FIRST LINE : SEAT NO., NAME O OTHER LINES: HEAD OF PASSING, S8058579 MURKUTE DEVYANI BALASAHEB 01. DISCRETE STRUCTURES 02. COMPUTER ORGANIZATION 03. DIGITAL ELECTRONICS & LOGIC DESI 04. FUNDAMENTAL OF DATA STRUCTURES 05. HUMANITIES AND SOCIAL SCIENCES 06. DIGITAL LABORATORY 07. DIGITAL LABORATORY 08. PROGRAMMING LABORATORY	F THE MAX PP PP PP TW	100 100 100 100 50 50	DATE, , MIN 40 40 40 40 40 20 20 20	MO N. P. KAL 45 47 40 48 40 36 28 37	THER, PERMA ASS MARKS, PANA P C P C P C P C P C P C P C P C P C	11. 12. 13. 14. 15. 16. 17.	REG. NO., PREVIOUS SEAT NO., COKS OBTAINED, P/F:PASS/FAIL, C:PROCESSOR ARCHITECTURE & INTER. DATA STRUCTURES AND FILES DATA COMMUNICATION PROCESSOR INTERFACING LABORATORY PROCESSOR INTERFACING LABORATORY DATA STRUCTURES AND FILES LAB	PICT PP PP PP PP TW	E, S US CAR 100 100 100 100 25	RY OV , \$80 40 40 40 40 10 20	NO. VER 0588579 49 60 49 76 65 19 37 20	
NOTE: FIRST LINE : SEAT NO., NAME O OTHER LINES: HEAD OF PASSING, S8058579 MURKUTE DEVYANI BALASAHEB 01. DISCRETE STRUCTURES 02. COMPUTER ORGANIZATION 03. DIGITAL ELECTRONICS & LOGIC DESI 04. FUNDAMENTAL OF DATA STRUCTURES 05. HUMANITIES AND SOCIAL SCIENCES 06. DIGITAL LABORATORY 07. DIGITAL LABORATORY	F THE MAX PP PP PP PP TW PR	100 100 100 100 50	DATE, , MIN 40 40 40 40 40 20 20	MO N. P. KAL 45 47 40 48 40 36 28 37 31	THER, PERMA ASS MARKS, PANA PC PC PC PC PC PC PC PC PC	11. 12. 13. 14. 15. 16. 17. 18.	REG. NO., PREVIOUS SEAT NO., COKS OBTAINED, P/F:PASS/FAIL, C:PROCESSOR ARCHITECTURE & INTER. DATA STRUCTURES AND FILES DATA COMMUNICATION PROCESSOR INTERFACING LABORATORY PROCESSOR INTERFACING LABORATORY DATA STRUCTURES AND FILES LAB DATA STRUCTURES AND FILES LAB	PICT PP PP PP PP TW OR TW PR	E, S US CAR 100 100 100 100 25 50	RY OV , \$80 40 40 40 40 40 20	NO. VER 0588579 49 60 49 76 65 19 37 20 30	
NOTE: FIRST LINE : SEAT NO., NAME O OTHER LINES: HEAD OF PASSING, S8058579 MURKUTE DEVYANI BALASAHEB 01. DISCRETE STRUCTURES 02. COMPUTER ORGANIZATION 03. DIGITAL ELECTRONICS & LOGIC DESI 04. FUNDAMENTAL OF DATA STRUCTURES 05. HUMANITIES AND SOCIAL SCIENCES 06. DIGITAL LABORATORY 07. DIGITAL LABORATORY 08. PROGRAMMING LABORATORY	F THE MAX PP PP GPP PP TW PR TW PR	100 100 100 100 50 50	DATE, , MIN 40 40 40 40 40 20 20 20	MO N. P. KAL 45 47 40 48 40 36 28 37 31	THER, PERMA ASS MARKS, PANA P C P C P C P C P C P C P C P C P C	11. 12. 13. 14. 15. 16. 17. 18.	REG. NO., PREVIOUS SEAT NO., COKS OBTAINED, P/F:PASS/FAIL, C:PROCESSOR ARCHITECTURE & INTER. DATA STRUCTURES AND FILES DATA COMMUNICATION PROCESSOR INTERFACING LABORATORY PROCESSOR INTERFACING LABORATORY DATA STRUCTURES AND FILES LAB	PICT PP PP PP PP TW OR TW PR	E, S US CAR 100 100 100 100 25 50 25	RY OV , \$80 40 40 40 40 10 20	NO. VER 0588579 49 60 49 76 65 19 37 20	
NOTE: FIRST LINE : SEAT NO., NAME O OTHER LINES: HEAD OF PASSING, S8058579 MURKUTE DEVYANI BALASAHEB 01. DISCRETE STRUCTURES 02. COMPUTER ORGANIZATION 03. DIGITAL ELECTRONICS & LOGIC DESI 04. FUNDAMENTAL OF DATA STRUCTURES 05. HUMANITIES AND SOCIAL SCIENCES 06. DIGITAL LABORATORY 07. DIGITAL LABORATORY 08. PROGRAMMING LABORATORY	F THE MAX PP PP GPP PP TW PR TW PR	100 100 100 100 50 50 50	DATE, , MIN 40 40 40 40 20 20 20 20	MO N. P. KAL 45 47 40 48 40 36 28 37 31	THER, PERMA ASS MARKS, PANA PC PC PC PC PC PC PC PC PC	11. 12. 13. 14. 15. 16. 17. 18.	REG. NO., PREVIOUS SEAT NO., COKS OBTAINED, P/F:PASS/FAIL, C:PROCESSOR ARCHITECTURE & INTER. DATA STRUCTURES AND FILES DATA COMMUNICATION PROCESSOR INTERFACING LABORATORY PROCESSOR INTERFACING LABORATORY DATA STRUCTURES AND FILES LAB DATA STRUCTURES AND FILES LAB	PICT PP PP PP PP TW OR TW PR TW	E, S US CAR 100 100 100 100 25 50 25 50	40 40 40 40 40 20 10	NO. VER 0588579 49 60 49 76 65 19 37 20 30	

GRAND TOTAL = 868/1500, RESULT: HIGHER SECOND CLASS

21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 42 P

ORDN. 1 MARKS:

S8058580 NAIKNAWARE UTKARSH MARUT	Ι			MAHANANDA	, 71045538M	, s8058580 ,	PICT	, s8058580
01 DISCRETE STRUCTURES	PP	100	40	54 P.C	11 FNG MATHS TTT		PP 100	40 45 P

01.	DISCRETE STRUCTURES	PP	100	40	54	PC	1	L1.	ENG MATHS III	PP	100	40	45	Р
02.	COMPUTER ORGANIZATION	PP	100	40	46	P C	1	L2.	COMPUTER GRAPHICS	PP	100	40	51	Р
03.	DIGITAL ELECTRONICS & LOGIC DESI	GPP	100	40	45	P C	1	L3.	PROCESSOR ARCHITECTURE & INTER.	PP	100	40	26	F
04.	FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	40	P C	1	L4.	DATA STRUCTURES AND FILES	PP	100	40	66	Р
05.	HUMANITIES AND SOCIAL SCIENCES	PP	100	40	52	P C	1	L5.	DATA COMMUNICATION	PP	100	40	40	Р
06.	DIGITAL LABORATORY	TW	50	20	42	P C	1	L6.	PROCESSOR INTERFACING LABORATORY	TW	25	10	21	Р
07.	DIGITAL LABORATORY	PR	50	20	32	P C	1	L7.	PROCESSOR INTERFACING LABORATORY	OR	50	20	25	Р
08.	PROGRAMMING LABORATORY	TW	50	20	42	P C	1	L8.	DATA STRUCTURES AND FILES LAB	TW	25	10	20	Р
09.	PROGRAMMING LABORATORY	PR	50	20	37	P C	1	L9.	DATA STRUCTURES AND FILES LAB	PR	50	20	32	Р
10.	COMMUNICATION AND LANGUAGE LAB.	TW	50	20	40	РС	2	20.	OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	43	Р

GRAND TOTAL = 841/1500, RESULT: FAILS A.T.K.T.
ORDN. 1 MARKS :

S8058581 NIKAM SONALI JIVAN				ARL	JNA		, 71134972L	, s8058581 ,	PICT		, s80)5858	1
01. DISCRETE STRUCTURES	PP	100	40	41	Р	11.	ENG MATHS III		PP	100	40	40	Р
02. COMPUTER ORGANIZATION	PP	100	40	53	РС	12.	COMPUTER GRAPHICS	5	PP	100	40	64	Р
03. DIGITAL ELECTRONICS & LOGIC DES	IGPP	100	40	53	РС	13.	PROCESSOR ARCHITI	ECTURE & INTER.	PP	100	40	44	Р
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	54	P C	14.	DATA STRUCTURES A	AND FILES	PP	100	40	64	Р
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	49	РС	15.	DATA COMMUNICATION	ON	PP	100	40	62	Р
06. DIGITAL LABORATORY	TW	50	20	38	РС	16.	PROCESSOR INTERFA	ACING LABORATORY	TW	25	10	20	Р
07. DIGITAL LABORATORY	PR	50	20	25	РС	17.	PROCESSOR INTERFA	ACING LABORATORY	OR	50	20	28	Р
08. PROGRAMMING LABORATORY	TW	50	20	41	P C	18.	DATA STRUCTURES A	AND FILES LAB	TW	25	10	21	Р
09. PROGRAMMING LABORATORY	PR	50	20	42	P C	19.	DATA STRUCTURES A	AND FILES LAB age 227	PR	50	20	20	Р

10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	39	РС	20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 41 P 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 22 P
GRAND TOTAL = 861/1500, RESULT: HIGHE	ER SE	COND C	LASS			
ORDN. 1 MARKS :						
UNIVERSITY OF	 PUNE	 .,S.E.	(2008	 PAT	 .)(INFOF	MATION TECHNOLOGY)
DATE : 18 AUG. 2011	CEN	TRE :	PUNE I	INSTI	TUTE OF	COMPUTER TECHNOLOGY, PUNE. PAGE NO. 27 (449)
NOTE: FIRST LINE : SEAT NO., NAME (F THI	E CAND	IDATE,	, MC	THER, P	ERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING,	MAX	. MARK	S, MI	IN. P	ASS MAR	(S, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
S8058582 NILESH D PHADTARE				KUN	IDA	, 70925526J , S8058582 , PICT , S8058582
01. DISCRETE STRUCTURES	PP	100	40	43	РС	11. ENG MATHS III PP 100 40 40 P
02. COMPUTER ORGANIZATION	PP	100	40	50	Р	12. COMPUTER GRAPHICS PP 100 40 43 P
03. DIGITAL ELECTRONICS & LOGIC DESI	GPP	100	40	50	PC	13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 18 F
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	40	PC	14. DATA STRUCTURES AND FILES PP 100 40 72 P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	40	PC	15. DATA COMMUNICATION PP 100 40 43 P
06. DIGITAL LABORATORY	TW	50	20	35	P C	16. PROCESSOR INTERFACING LABORATORY TW 25 10 20 P
07. DIGITAL LABORATORY	PR	50	20	31	PC	17. PROCESSOR INTERFACING LABORATORY OR 50 20 12 F
08. PROGRAMMING LABORATORY	TW	50	20	43	PC	18. DATA STRUCTURES AND FILES LAB TW 25 10 23 P
09. PROGRAMMING LABORATORY	PR	50	20	32	P C	19. DATA STRUCTURES AND FILES LAB PR 50 20 10 F
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	36	PC	20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 44 P
						21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 16 F
CRAND TOTAL - 741/1500 PECHAT, FATIS		νт				
GRAND TOTAL = 741/1500, RESULT: FAILS	A.I					
ORDN. 1 MARKS :						
S8058583 NIMBALKAR ABHIJIT SHIRISH	1			SUR	EKHA	, 71057179」 , S8058583 , PICT , S8058583

01. DISCRETE STRUCTURES	PP	100	40	58	PC	11.	ENG MATHS III	PP	100	40	83	Р
02. COMPUTER ORGANIZATION	PP	100	40	48	P C	12.	COMPUTER GRAPHICS	PP	100	40	57	Р
03. DIGITAL ELECTRONICS & LOGIC DE	SIGPP	100	40	62	P C	13.	PROCESSOR ARCHITECTURE & INTER.	PP	100	40	49	Р
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	65	P C	14.	DATA STRUCTURES AND FILES	PP	100	40	74	Р
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	46	P C	15.	DATA COMMUNICATION	PP	100	40	58	Р
06. DIGITAL LABORATORY	TW	50	20	28	P C	16.	PROCESSOR INTERFACING LABORATORY	TW	25	10	21	Р
07. DIGITAL LABORATORY	PR	50	20	36	P C	17.	PROCESSOR INTERFACING LABORATORY	OR	50	20	40	Р
08. PROGRAMMING LABORATORY	TW	50	20	36	P C	18.	DATA STRUCTURES AND FILES LAB	TW	25	10	22	Р
09. PROGRAMMING LABORATORY	PR	50	20	42	P C	19.	DATA STRUCTURES AND FILES LAB	PR	50	20	43	Р
10. COMMUNICATION AND LANGUAGE LAB	. TW	50	20	28	P C	20.	OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	46	Р
						21.	OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	38	Р

GRAND TOTAL = 980/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

S8058584	PANDIT DIVYA SYAMANTAKMAN	I			VEE	NA			, 71045552G , S8058584 ,	PICT		, s805	58584	4
01. DISC	RETE STRUCTURES	PP	100	40	63	РС	1	1.	ENG MATHS III	PP	100	40	46	Р
02. COMP	UTER ORGANIZATION	PP	100	40	47	P C	1	2.	COMPUTER GRAPHICS	PP	100	40	45	Р
03. DIGI	TAL ELECTRONICS & LOGIC DESI	GPP	100	40	61	P C	1	3.	PROCESSOR ARCHITECTURE & INTER.	PP	100	40	46	Р
04. FUND	AMENTAL OF DATA STRUCTURES	PP	100	40	59	P C	1	4.	DATA STRUCTURES AND FILES	PP	100	40	60	Р
05. HUMA	NITIES AND SOCIAL SCIENCES	PP	100	40	45	P C	1	5.	DATA COMMUNICATION	PP	100	40	53	Р
06. DIGI	TAL LABORATORY	TW	50	20	27	P C	1	6.	PROCESSOR INTERFACING LABORATORY	TW	25	10	18	Р
07. DIGI	TAL LABORATORY	PR	50	20	34	P C	1	7.	PROCESSOR INTERFACING LABORATORY	OR	50	20	38	Р
08. PROG	RAMMING LABORATORY	TW	50	20	38	P C	1	8.	DATA STRUCTURES AND FILES LAB	TW	25	10	15	Р
09. PROG	RAMMING LABORATORY	PR	50	20	45	P C	1	9.	DATA STRUCTURES AND FILES LAB	PR	50	20	37	Р
10. COMM	UNICATION AND LANGUAGE LAB.	TW	50	20	35	P C	2	0.	OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	39	Р
							2	1.	OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	32	Р

GRAND TOTAL = 883+05/1500, RESULT: HIGHER SECOND CLASS[0.163]
ORDN. 1 MARKS :

UNIVERSITY OF	 PUNE	 ,S.E.	(2008	PAT.)(INF	
DATE : 18 AUG. 2011	CENT	ΓRE :	PUNE :	INSTIT	TUTE (OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 28 (450)
NOTE: FIRST LINE : SEAT NO., NAME OF OTHER LINES: HEAD OF PASSING,	F THE	E CAND	IDATE	, MOT	THER,	PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. ARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
S8058585 PANSARE ROHAN SAMPAT				MANO		, 71045554C , S8058585 , PICT , S8058585
01. DISCRETE STRUCTURES	PP	100	40	59	P C	11. ENG MATHS III PP 100 40 45 P
02. COMPUTER ORGANIZATION	PP	100	40	53	РС	12. COMPUTER GRAPHICS PP 100 40 58 P
03. DIGITAL ELECTRONICS & LOGIC DESIGNATION OF THE PROPERTY OF	GPP	100	40	56	P C	13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 60 P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	55	P C	14. DATA STRUCTURES AND FILES PP 100 40 73 P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	52	РС	15. DATA COMMUNICATION PP 100 40 58 P
06. DIGITAL LABORATORY	TW	50	20	31	P C	16. PROCESSOR INTERFACING LABORATORY TW 25 10 15 P
07. DIGITAL LABORATORY	PR	50	20	34	P C	17. PROCESSOR INTERFACING LABORATORY OR 50 20 25 P
08. PROGRAMMING LABORATORY	TW	50	20	37	P C	18. DATA STRUCTURES AND FILES LAB TW 25 10 19 P
09. PROGRAMMING LABORATORY	PR	50	20	35	P C	19. DATA STRUCTURES AND FILES LAB PR 50 20 30 P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	35	P C	20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 39 P
						21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 36 P
GRAND TOTAL = 905/1500, RESULT: FIRST	CLAS	SS				
ORDN. 1 MARKS :						
S8058586 PARADKAR SUPRIYA SUNIL				CHIT	ΓRA	, 71045555M , S8058586 , PICT , S8058586
01. DISCRETE STRUCTURES	PP	100	40	57	P C	11. ENG MATHS III PP 100 40 44 P
02. COMPUTER ORGANIZATION	PP	100	40	45	РС	12. COMPUTER GRAPHICS PP 100 40 55 P
03. DIGITAL ELECTRONICS & LOGIC DESIG	GPP	100	40	51	РС	13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 45 P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	49	РС	14. DATA STRUCTURES AND FILES PP 100 40 77 P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	40	РС	15. DATA COMMUNICATION PP 100 40 49 P Page 230

06.	DIGITAL LABORATORY	TW	50	20	38	P C	16.	PROCESSOR INTERF	FACING LABORATORY	TW	25	10	20	Р
07.	DIGITAL LABORATORY	PR	50	20	30	P C	17.	PROCESSOR INTERF	ACING LABORATORY	OR	50	20	39	Р
08.	PROGRAMMING LABORATORY	TW	50	20	44	P C	18.	DATA STRUCTURES	AND FILES LAB	TW	25	10	19	Р
09.	PROGRAMMING LABORATORY	PR	50	20	40	P C	19.	DATA STRUCTURES	AND FILES LAB	PR	50	20	28	Р
10.	COMMUNICATION AND LANGUAGE LAB.	TW	50	20	40	P C	20.	OBJECT ORIENTED	PROGRAMMING LAB	TW	50	20	43	Р
							21.	OBJECT ORIENTED	PROGRAMMING LAB	PR	50	20	20#	Р

GRAND TOTAL = 873+04/1500, RESULT: HIGHER SECOND CLASS #[0.163+0.1][0.163]

ORDN. 1 MARKS : (21)2,

S8058587 PARAKH KUNAL KAILAS				MEE	ENA	, 71045556к , S8058587 , РІС	Т	, s80	58587	7
01. DISCRETE STRUCTURES	PP	100	40	58	РС	11. ENG MATHS III PP	100	40	43	Р
02. COMPUTER ORGANIZATION	PP	100	40	67	P C	12. COMPUTER GRAPHICS PP	100	40	69	Р
03. DIGITAL ELECTRONICS & LOGIC DESI	[GPP	100	40	65	P C	13. PROCESSOR ARCHITECTURE & INTER. PP	100	40	52	Р
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	62	P C	14. DATA STRUCTURES AND FILES PP	100	40	76	Р
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	57	P C	15. DATA COMMUNICATION PP	100	40	63	Р
06. DIGITAL LABORATORY	TW	50	20	38	P C	16. PROCESSOR INTERFACING LABORATORY TW	25	10	19	Р
07. DIGITAL LABORATORY	PR	50	20	42	P C	17. PROCESSOR INTERFACING LABORATORY OR	50	20	39	Р
08. PROGRAMMING LABORATORY	TW	50	20	41	P C	18. DATA STRUCTURES AND FILES LAB TW	25	10	18	Р
09. PROGRAMMING LABORATORY	PR	50	20	45	P C	19. DATA STRUCTURES AND FILES LAB PR	50	20	22	Р
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	30	P C	20. OBJECT ORIENTED PROGRAMMING LAB TW	50	20	35	Р
						21. OBJECT ORIENTED PROGRAMMING LAB PR	50	20	40	Р

GRAND TOTAL = 981/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS:

UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(INFORMATION TECHNOLOGY)

DATE: 18 AUG. 2011 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 29 (451)

NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

cols05

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS (OBTAINED, P/F:PASS/FA	AIL, C:PREVIOUS CARRY OVER
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S8058588 PAREKH SONALI SUNIL				REKI	НА		, 71045558F	, s8058588 ,	PICT		, s80!	58588	3
01. DISCRETE STRUCTURES	PP	100	40	52	P C	11.	ENG MATHS III		PP	100	40	82	Р
02. COMPUTER ORGANIZATION	PP	100	40	62	P C	12.	COMPUTER GRAPHIC	:S	PP	100	40	53	Р
03. DIGITAL ELECTRONICS & LOGIC DESI	GPP	100	40	51	P C	13.	PROCESSOR ARCHIT	ECTURE & INTER.	PP	100	40	40	Р
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	68	P C	14.	DATA STRUCTURES	AND FILES	PP	100	40	77	Р
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	56	P C	15.	DATA COMMUNICATI	ON	PP	100	40	56	Р
06. DIGITAL LABORATORY	TW	50	20	33	P C	16.	PROCESSOR INTERF	ACING LABORATORY	TW	25	10	20	Р
07. DIGITAL LABORATORY	PR	50	20	39	P C	17.	PROCESSOR INTERF	ACING LABORATORY	OR	50	20	44	Р
08. PROGRAMMING LABORATORY	TW	50	20	37	P C	18.	DATA STRUCTURES	AND FILES LAB	TW	25	10	18	Р
09. PROGRAMMING LABORATORY	PR	50	20	41	P C	19.	DATA STRUCTURES	AND FILES LAB	PR	50	20	40	Р
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	38	P C	20.	OBJECT ORIENTED	PROGRAMMING LAB	TW	50	20	42	Р
						21.	OBJECT ORIENTED	PROGRAMMING LAB	PR	50	20	42	Р

GRAND TOTAL = 991+05/1500, RESULT: FIRST CLASS WITH DISTINCTION[0.163]
ORDN. 1 MARKS :

S8058589 PARTH SARTHI PIPLANI				SURI	EKHA		, 71045560н	, s8058589 ,	PICT		, s80)58589	9
01. DISCRETE STRUCTURES	PP	100	40	56	P C	11.	ENG MATHS III		PP	100	40	40	Р
02. COMPUTER ORGANIZATION	PP	100	40	43	P C	12.	COMPUTER GRAPHIC	S	PP	100	40	54	Р
03. DIGITAL ELECTRONICS & LOGIC DES	IGPP	100	40	40	P C	13.	PROCESSOR ARCHIT	ECTURE & INTER.	PP	100	40	16	F
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	45	P C	14.	DATA STRUCTURES	AND FILES	PP	100	40	72	Р
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	44	P C	15.	DATA COMMUNICATI	ON	PP	100	40	40	Р
06. DIGITAL LABORATORY	TW	50	20	22	P C	16.	PROCESSOR INTERF	ACING LABORATORY	TW	25	10	10	Р
07. DIGITAL LABORATORY	PR	50	20	23	P C	17.	PROCESSOR INTERF	ACING LABORATORY	OR	50	20	05	F
08. PROGRAMMING LABORATORY	TW	50	20	23	P C	18.	DATA STRUCTURES	AND FILES LAB	TW	25	10	19	Р
09. PROGRAMMING LABORATORY	PR	50	20	25	P C	19.	DATA STRUCTURES	AND FILES LAB	PR	50	20	41	Р
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	35	P C	20.	OBJECT ORIENTED P	PROGRAMMING LAB	TW	50	20	37	Р

21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 22 P

GRAND TOTAL = 712/1500, RESULT: FAILS A.T.K.T.
ORDN. 1 MARKS :

S8058590 PATEL NAZNIN JAVED				MAH	ERUNISSA	, 71134973」 , S8058590 , PICT , S8	058590
01. DISCRETE STRUCTURES	PP	100	40	40	P C	. ENG MATHS III PP 100 40	24 F
02. COMPUTER ORGANIZATION	PP	100	40	40	P C	. COMPUTER GRAPHICS PP 100 40	48 P
03. DIGITAL ELECTRONICS & LOGIC DES	[GPP	100	40	45	P C	. PROCESSOR ARCHITECTURE & INTER. PP 100 40	31 F
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	44	P C	. DATA STRUCTURES AND FILES PP 100 40	64 P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	46	P C	. DATA COMMUNICATION PP 100 40	53 P
06. DIGITAL LABORATORY	TW	50	20	35	P C	. PROCESSOR INTERFACING LABORATORY TW 25 10	14 P
07. DIGITAL LABORATORY	PR	50	20	40	P C	. PROCESSOR INTERFACING LABORATORY OR 50 20	06 F
08. PROGRAMMING LABORATORY	TW	50	20	42	P C	. DATA STRUCTURES AND FILES LAB TW 25 10	19 P
09. PROGRAMMING LABORATORY	PR	50	20	22	P C	. DATA STRUCTURES AND FILES LAB PR 50 20	35 P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	43	P C	. OBJECT ORIENTED PROGRAMMING LAB TW 50 20	41 P
						. OBJECT ORIENTED PROGRAMMING LAB PR 50 20	35 P

GRAND TOTAL = 767/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS :

UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(INFORMATION TECHNOLOGY)

DATE: 18 AUG. 2011 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 30 (452)

NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8058591 PATIL DNYANESHWARI PRAKASH SANGITA , 71045563B , S8058591 , PICT , S8058591

01. DISCRETE STRUCTURES PP 100 40 56 P C 11. ENG MATHS III PP 100 40 57 P

02.	COMPUTER ORGANIZATION	PP	100	40	40	PC	12.	COMPUTER GRAPHICS	PP	100	40	52	Р
03.	DIGITAL ELECTRONICS & LOGIC DESIGNATION	GPP	100	40	63	P C	13.	PROCESSOR ARCHITECTURE & INTER.	PP	100	40	64	Р
04.	FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	64	P C	14.	DATA STRUCTURES AND FILES	PP	100	40	79	Р
05.	HUMANITIES AND SOCIAL SCIENCES	PP	100	40	44	P C	15.	DATA COMMUNICATION	PP	100	40	59	Р
06.	DIGITAL LABORATORY	TW	50	20	40	P C	16.	PROCESSOR INTERFACING LABORATORY	TW	25	10	22	Р
07.	DIGITAL LABORATORY	PR	50	20	40	P C	17.	PROCESSOR INTERFACING LABORATORY	OR	50	20	44	Р
08.	PROGRAMMING LABORATORY	TW	50	20	41	P C	18.	DATA STRUCTURES AND FILES LAB	TW	25	10	22	Р
09.	PROGRAMMING LABORATORY	PR	50	20	43	P C	19.	DATA STRUCTURES AND FILES LAB	PR	50	20	42	Р
10.	COMMUNICATION AND LANGUAGE LAB.	TW	50	20	37	P C	20.	OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	44	Р
							21.	OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	32	Р

GRAND TOTAL = 985/1500, RESULT: FIRST CLASS
ORDN. 1 MARKS :

S805	8592 PATIL KHUSHBU PRAKASH				CHHA	AYA		, 71045565)	, s8058592 ,	PICT		, S80	58592	2
01.	DISCRETE STRUCTURES	PP	100	40	55	P C	11.	ENG MATHS III		PP	100	40	55	Р
02.	COMPUTER ORGANIZATION	PP	100	40	61	P C	12.	COMPUTER GRAPHIC	S	PP	100	40	59	Р
03.	DIGITAL ELECTRONICS & LOGIC DESI	GPP	100	40	59	P C	13.	PROCESSOR ARCHIT	ECTURE & INTER.	PP	100	40	51	Р
04.	FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	64	P C	14.	DATA STRUCTURES	AND FILES	PP	100	40	76	Р
05.	HUMANITIES AND SOCIAL SCIENCES	PP	100	40	48	P C	15.	DATA COMMUNICATI	ON	PP	100	40	57	Р
06.	DIGITAL LABORATORY	TW	50	20	33	P C	16.	PROCESSOR INTERF	ACING LABORATORY	TW	25	10	19	Р
07.	DIGITAL LABORATORY	PR	50	20	34	P C	17.	PROCESSOR INTERF	ACING LABORATORY	OR	50	20	37	Р
08.	PROGRAMMING LABORATORY	TW	50	20	39	P C	18.	DATA STRUCTURES	AND FILES LAB	TW	25	10	15	Р
09.	PROGRAMMING LABORATORY	PR	50	20	41	P C	19.	DATA STRUCTURES	AND FILES LAB	PR	50	20	35	Р
10.	COMMUNICATION AND LANGUAGE LAB.	TW	50	20	39	P C	20.	OBJECT ORIENTED	PROGRAMMING LAB	TW	50	20	37	Р
							21.	OBJECT ORIENTED	PROGRAMMING LAB	PR	50	20	36	Р

GRAND TOTAL = 950+05/1500, RESULT: FIRST CLASS[0.163]

ORDN. 1 MARKS :

									cols05					
9	5805	8593 PAWAR BHANUPRIYA VITTHAL				SHAI	KUNTALA		, 71045571C , S8058593 ,	PICT		, s80	5859:	3
(01.	DISCRETE STRUCTURES	PP	100	40	47	P C	11.	ENG MATHS III	PP	100	40	52	Р
(02.	COMPUTER ORGANIZATION	PP	100	40	51	P C	12.	COMPUTER GRAPHICS	PP	100	40	43	Р
(03.	DIGITAL ELECTRONICS & LOGIC DESIGNATION	GPP	100	40	54	P C	13.	PROCESSOR ARCHITECTURE & INTER.	PP	100	40	40	Р
(04.	FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	40	P C	14.	DATA STRUCTURES AND FILES	PP	100	40	86	Р
(05.	HUMANITIES AND SOCIAL SCIENCES	PP	100	40	43	P C	15.	DATA COMMUNICATION	PP	100	40	52	Р
(06.	DIGITAL LABORATORY	TW	50	20	33	P C	16.	PROCESSOR INTERFACING LABORATORY	TW	25	10	21	Р
(07.	DIGITAL LABORATORY	PR	50	20	34	P C	17.	PROCESSOR INTERFACING LABORATORY	OR	50	20	20	Р
(08.	PROGRAMMING LABORATORY	TW	50	20	35	P C	18.	DATA STRUCTURES AND FILES LAB	TW	25	10	16	Р
(09.	PROGRAMMING LABORATORY	PR	50	20	42	P C	19.	DATA STRUCTURES AND FILES LAB	PR	50	20	37	Р
_	10.	COMMUNICATION AND LANGUAGE LAB.	TW	50	20	33	P C	20.	OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	41	Р
								21.	OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	40	Р
GRA	AND	TOTAL = 860/1500, RESULT: HIGHE	R SEC	OND CL	ASS									
ORI	ON.	1 MARKS :												
		UNIVERSITY OF	 PUNE	 ,S.E.((2008	 PAT.)(INFOR	 MATION T	ECHNOLOGY)					

UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(INFORMATION TECHNOLOGY)														
DATE : 18 AUG. 2011	CENT	RE : Pl	JNE IN	STI	TUTE OF COM	PUTE	R TECHNOLOGY, PUNE.	PAGI	E NO.	31	(4!	53)		
				-										
NOTE: FIRST LINE : SEAT NO., NAME O	F THE	CANDI	DATE,	MO	THER, PERMAN	NENT	REG. NO., PREVIOUS SEAT NO., CO	OLLEGI	Ξ, S	EAT NO	Э.			
OTHER LINES: HEAD OF PASSING,	MAX.	MARKS	, MIN	. PA	ASS MARKS,	MARI	(S OBTAINED, P/F:PASS/FAIL, C:P	REVIO	JS CAR	RY OVE	ER			
				-										
S8058594 PHUTANE AMEY UDAY				SAR	ITA		, 71054577M , S8058594 ,	PICT		, s80	58594	4		
01. DISCRETE STRUCTURES	PP	100	40	67	P C	11.	ENG MATHS III	PP	100	40	50	Р		
02. COMPUTER ORGANIZATION	PP	100	40	55	P C	12.	COMPUTER GRAPHICS	PP	100	40	61	Р		
03. DIGITAL ELECTRONICS & LOGIC DESI	GPP	100	40	60	P C	13.	PROCESSOR ARCHITECTURE & INTER.	PP	100	40	60	Р		
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	56	P C	14.	DATA STRUCTURES AND FILES	PP	100	40	78	Р		
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	56	P C	15.	DATA COMMUNICATION	PP	100	40	66	Р		
06. DIGITAL LABORATORY	TW	50	20	28	P C	16.	PROCESSOR INTERFACING LABORATORY Page 235	TW	25	10	21	Р		

							cols05					
07. DIGITAL LABORATORY	PR	50	20	38	P C	17.	PROCESSOR INTERFACING LABORATOR	Y OR	50	20	39	Р
08. PROGRAMMING LABORATORY	TW	50	20	37	P C	18.	DATA STRUCTURES AND FILES LAB	TW	25	10	15	Р
09. PROGRAMMING LABORATORY	PR	50	20	48	P C	19.	DATA STRUCTURES AND FILES LAB	PR	50	20	44	Р
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	35	P C	20.	OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	37	Р
						21.	OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	40	Р
GRAND TOTAL = 991/1500, RESULT: FIRST	CL AS:	c wttu	DTST	TNCT	TON							
ORDN. 1 MARKS :	CLAS	2 MIIU	וכנט	INCI	TON							
ONDIN. I MARKS .												
S8058595 PINGLE ADITYA ULHAS				ARC	HANA		, 71134974G , S8058595 ,	PICT		, s80	5859	5
01. DISCRETE STRUCTURES	PP	100	40	59	P C	11.	ENG MATHS III	PP	100	40	40	Р
02. COMPUTER ORGANIZATION	PP	100	40	40	P C	12.	COMPUTER GRAPHICS	PP	100	40	55	Р
03. DIGITAL ELECTRONICS & LOGIC DESI	GPP	100	40	40	P C	13.	PROCESSOR ARCHITECTURE & INTER.	PP	100	40	40	Р
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	49	P C	14.	DATA STRUCTURES AND FILES	PP	100	40	61	Р
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	53	P C	15.	DATA COMMUNICATION	PP	100	40	54	Р
06. DIGITAL LABORATORY	TW	50	20	39	P C	16.	PROCESSOR INTERFACING LABORATOR	Y TW	25	10	19	Р
07. DIGITAL LABORATORY	PR	50	20	34	P C	17.	PROCESSOR INTERFACING LABORATOR	Y OR	50	20	40	Р
08. PROGRAMMING LABORATORY	TW	50	20	42	P C	18.	DATA STRUCTURES AND FILES LAB	TW	25	10	20	Р
09. PROGRAMMING LABORATORY	PR	50	20	37	P C	19.	DATA STRUCTURES AND FILES LAB	PR	50	20	38	Р
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	38	P C	20.	OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	41	Р
						21.	OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	42	Р
GRAND TOTAL = 881/1500, RESULT: HIGHE	R SEC	OND CL	Δςς									
ORDN. 1 MARKS :	K SEC	OND CE	-133									
S8058596 POLE AKSHAYKUMAR RAHUL				SHE	ELA		, 71045576D , S8058596 ,	PICT		, S80	5859	6
01. DISCRETE STRUCTURES	PP	100	40	43	Р	11.	ENG MATHS III	PP	100	40	40	Р
02. COMPUTER ORGANIZATION	PP	100	40	41	Р	12.	COMPUTER GRAPHICS	PP	100	40	44	Р

						cols05	
03. DIGITAL ELECTRONICS & LOGIC DESI	GPP	100	40	40	PC	13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 40 P	
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	49	PC	14. DATA STRUCTURES AND FILES PP 100 40 46 P	
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	47	P C	15. DATA COMMUNICATION PP 100 40 26 F	
06. DIGITAL LABORATORY	TW	50	20	30	P C	16. PROCESSOR INTERFACING LABORATORY TW 25 10 12 P	
07. DIGITAL LABORATORY	PR	50	20	20	Р	17. PROCESSOR INTERFACING LABORATORY OR 50 20 AA F	
08. PROGRAMMING LABORATORY	TW	50	20	35	P C	18. DATA STRUCTURES AND FILES LAB TW 25 10 14 P	
09. PROGRAMMING LABORATORY	PR	50	20	29	PC	19. DATA STRUCTURES AND FILES LAB PR 50 20 AA F	
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	38	P C	20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 27 P	
						21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 32 P	

GRAND TOTAL = 653+05/1500, RESULT: FAILS A.T.K.T.[0.163] ORDN. 1 MARKS:

UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(INFORMATION TECHNOLOGY)

DATE: 18 AUG. 2011 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 32 (454)

NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8058597 POTE SMITA RAGHUNATH				VIJ	AYA		, 71045577в	, s8058597 ,	PICT		, s80	5859	7
01. DISCRETE STRUCTURES	PP	100	40	49	P C	11.	ENG MATHS III		PP	100	40	60	Р
02. COMPUTER ORGANIZATION	PP	100	40	50	P C	12.	COMPUTER GRAPHICS	5	PP	100	40	54	Р
03. DIGITAL ELECTRONICS & LOGIC D	ESIGPP	100	40	44	P C	13.	PROCESSOR ARCHITE	ECTURE & INTER.	PP	100	40	26	F
04. FUNDAMENTAL OF DATA STRUCTURE	S PP	100	40	42	P C	14.	DATA STRUCTURES	AND FILES	PP	100	40	59	Р
05. HUMANITIES AND SOCIAL SCIENCE	S PP	100	40	41	P C	15.	DATA COMMUNICATION	ON	PP	100	40	40	Р
06. DIGITAL LABORATORY	TW	50	20	35	P C	16.	PROCESSOR INTERFA	ACING LABORATORY	TW	25	10	19	Р
07. DIGITAL LABORATORY	PR	50	20	28	P C	17.	PROCESSOR INTERFA	ACING LABORATORY	OR	50	20	35	Р
08. PROGRAMMING LABORATORY	TW	50	20	42	P C	18.	DATA STRUCTURES	AND FILES LAB	TW	25	10	20	Р
09. PROGRAMMING LABORATORY	PR	50	20	44	РС	19.	DATA STRUCTURES	AND FILES LAB	PR	50	20	38	Р
10. COMMUNICATION AND LANGUAGE LA	B. TW	50	20	41	РС	20.	OBJECT ORIENTED I	PROGRAMMING LAB	TW	50	20	48	Р
						21.	OBJECT ORIENTED I	PROGRAMMING LAB	PR	50	20	25	Р

GRAND TOTAL = 840/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS:

S8058598 POTEY AKSHAY MADHUKAR				NIL	IMA		, 71045578L	, s8058598 ,	PICT		, s80	58598	3
01. DISCRETE STRUCTURES	PP	100	40	51	РС	11.	ENG MATHS III		PP	100	40	40	Р
02. COMPUTER ORGANIZATION	PP	100	40	41	P C	12.	COMPUTER GRAPHIC	:S	PP	100	40	48	Р
03. DIGITAL ELECTRONICS & LOGIC DESI	GPP	100	40	40	P C	13.	PROCESSOR ARCHIT	ECTURE & INTER.	PP	100	40	AA	F
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	49	P C	14.	DATA STRUCTURES	AND FILES	PP	100	40	41	Р
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	45	P C	15.	DATA COMMUNICATI	ON	PP	100	40	53	Р
06. DIGITAL LABORATORY	TW	50	20	33	P C	16.	PROCESSOR INTERF	ACING LABORATORY	TW	25	10	10	Р
07. DIGITAL LABORATORY	PR	50	20	42	P C	17.	PROCESSOR INTERF	ACING LABORATORY	OR	50	20	AA	F
08. PROGRAMMING LABORATORY	TW	50	20	35	P C	18.	DATA STRUCTURES	AND FILES LAB	TW	25	10	12	Р
09. PROGRAMMING LABORATORY	PR	50	20	45	P C	19.	DATA STRUCTURES	AND FILES LAB	PR	50	20	40	Р
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	37	P C	20.	OBJECT ORIENTED	PROGRAMMING LAB	TW	50	20	20	Р
						21.	OBJECT ORIENTED	PROGRAMMING LAB	PR	50	20	32	Р

GRAND TOTAL = 714/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS:

S8058599 PRIYA KASHYAP				REE	TA		, 71045582〕	, s8058599 ,	PICT		, s80)5859	9
01. DISCRETE STRUCTURES	PP	100	40	62	P C	11	. ENG MATHS III		PP	100	40	48	Р
02. COMPUTER ORGANIZATION	PP	100	40	45	P C	12	. COMPUTER GRAPHIC	S	PP	100	40	50	Р
03. DIGITAL ELECTRONICS & LOGIC DESI	GPP	100	40	58	P C	13	. PROCESSOR ARCHIT	ECTURE & INTER.	PP	100	40	51	Р
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	58	P C	14	. DATA STRUCTURES	AND FILES	PP	100	40	73	Р
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	53	P C	15	. DATA COMMUNICATI	ON	PP	100	40	54	Р
06. DIGITAL LABORATORY	TW	50	20	38	P C	16	. PROCESSOR INTERF	ACING LABORATORY	TW	25	10	18	Р
07. DIGITAL LABORATORY	PR	50	20	42	P C	17	. PROCESSOR INTERF	ACING LABORATORY	OR	50	20	34	Р

								cols05					
08. PROGRAMMING LABORATORY	TW	50	20	39	P C	18.	DATA STRUCTURES		TW	25	10	16	Р
09. PROGRAMMING LABORATORY	PR	50	20	40	P C	19.	DATA STRUCTURES	AND FILES LAB	PR	50	20	28	Р
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	35	P C	20.	OBJECT ORIENTED	PROGRAMMING LAB	TW	50	20	36	Р
						21.	OBJECT ORIENTED	PROGRAMMING LAB	PR	50	20	20	Р
GRAND TOTAL = 898+02/1500, RESULT: F	TRST (71 455	[n 2]										
ORDN. 1 MARKS :	11(5)	LASS	[0.2]										
ORDIN. I PIARRO .													
UNIVERSITY O	F PUNE	,S.E.	(2008	PAT	.)(INFOR	MATION T	ECHNOLOGY)						
DATE : 18 AUG. 2011	CENT	ΓRE : I	PUNE I	NSTI	TUTE OF	COMPUTE	R TECHNOLOGY, PU	NE.	PAG	E NO.	33	(4	55)
NOTE: FIRST LINE : SEAT NO., NAME	OF THE	E CAND	IDATE,	МО	THER, PI	ERMANENT	REG. NO., PREV	IOUS SEAT NO., C	OLLEG	E, S	EAT N	10.	
OTHER LINES: HEAD OF PASSING,	MAX.	. MARKS	S, MI	N. P	ASS MARI	KS, MAR	KS OBTAINED, P/	F:PASS/FAIL, C:P	REVIO	US CAR	RY O	/ER	
S8058600 PUNTAMBEKAR SHREYA SHAIL	FSH			SEE	ΞΜΔ		, 71045584E	, s8058600 ,	PICT		, s80	15860	0
30030000 FONTAMBERAK SIIKETA SHATE	LJII			JLL	.MA		, /10433042	, 30030000 ,	FICI		, 300	,,,,,,,	.0
01. DISCRETE STRUCTURES	PP	100	40	40	P C	11.	ENG MATHS III		PP	100	40	18	F
02. COMPUTER ORGANIZATION	PP	100	40	44	P C	12.	COMPUTER GRAPHI	cs	PP	100	40	40	Р
03. DIGITAL ELECTRONICS & LOGIC DES	SIGPP	100	40	40	P C	13.	PROCESSOR ARCHI	TECTURE & INTER.	PP	100	40	28	F
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	40	P C	14.	DATA STRUCTURES	AND FILES	PP	100	40	40	Р
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	48	P C	15.	DATA COMMUNICAT	ION	PP	100	40	40	Р
06. DIGITAL LABORATORY	TW	50	20	27	P C	16.	PROCESSOR INTER	FACING LABORATORY	TW	25	10	19	Р
07. DIGITAL LABORATORY	PR	50	20	22	Р	17.	PROCESSOR INTER	FACING LABORATORY	OR	50	20	10	F
08. PROGRAMMING LABORATORY	TW	50	20	27	P C	18.	DATA STRUCTURES	AND FILES LAB	TW	25	10	13	Р
09. PROGRAMMING LABORATORY	PR	50	20	33	P C	19.	DATA STRUCTURES	AND FILES LAB	PR	50	20	17	F
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	23	P C	20.	OBJECT ORIENTED	PROGRAMMING LAB	TW	50	20	35	Р
						21.	OBJECT ORIENTED	PROGRAMMING LAB	PR	50	20	16	F
GRAND TOTAL = 620/1500, RESULT: FAIL	SAT	кт											
ORDN. 1 MARKS :	.5 A.I.												
ONDIA. I PIANNO .													

S8058601 RAIJADE DHANVANTARI BALKF	RISHAN	NA		RATN	NAPRABHA	, 71134975E , S8058601 , PICT , S805860	1
01. DISCRETE STRUCTURES	PP	100	40	43	P C	11. ENG MATHS III PP 100 40 40	Р
02. COMPUTER ORGANIZATION	PP	100	40	40	P C	12. COMPUTER GRAPHICS PP 100 40 61	Р
03. DIGITAL ELECTRONICS & LOGIC DESI	[GPP	100	40	55	P C	13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 54	Р
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	63	P C	14. DATA STRUCTURES AND FILES PP 100 40 63	Р
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	41	P C	15. DATA COMMUNICATION PP 100 40 51	Р
06. DIGITAL LABORATORY	TW	50	20	39	P C	16. PROCESSOR INTERFACING LABORATORY TW 25 10 19	Р
07. DIGITAL LABORATORY	PR	50	20	33	P C	17. PROCESSOR INTERFACING LABORATORY OR 50 20 32	Р
08. PROGRAMMING LABORATORY	TW	50	20	43	P C	18. DATA STRUCTURES AND FILES LAB TW 25 10 21	Р
09. PROGRAMMING LABORATORY	PR	50	20	43	P C	19. DATA STRUCTURES AND FILES LAB PR 50 20 40	Р
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	39	P C	20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 42	Р
						21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 36	Р

GRAND TOTAL = 898+02/1500, RESULT: FIRST CLASS [0.2]
ORDN. 1 MARKS:

S8058602 RAJOLE VISHAL VILASRAO				SUN:	ITA		, 71134976C	, s8058602 ,	PICT		, s80	5860	2
01. DISCRETE STRUCTURES	PP	100	40	40	РС	11	ENG MATHS III		PP	100	40	67	Р
02. COMPUTER ORGANIZATION	PP	100	40	52	P C	12	COMPUTER GRAPHIC	CS	PP	100	40	62	Р
03. DIGITAL ELECTRONICS & LOGIC DESI	GPP	100	40	55	P C	13	PROCESSOR ARCHIT	TECTURE & INTER.	PP	100	40	54	Р
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	45	P C	14	DATA STRUCTURES	AND FILES	PP	100	40	78	Р
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	48	P C	15	DATA COMMUNICATI	ION	PP	100	40	61	Р
06. DIGITAL LABORATORY	TW	50	20	35	P C	16	PROCESSOR INTERF	ACING LABORATORY	TW	25	10	20	Р
07. DIGITAL LABORATORY	PR	50	20	29	P C	17	PROCESSOR INTERF	ACING LABORATORY	OR	50	20	42	Р
08. PROGRAMMING LABORATORY	TW	50	20	41	P C	18	DATA STRUCTURES	AND FILES LAB	TW	25	10	20	Р
09. PROGRAMMING LABORATORY	PR	50	20	44	P C	19	DATA STRUCTURES	AND FILES LAB	PR	50	20	40	Р
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	41	P C	20	OBJECT ORIENTED	PROGRAMMING LAB	TW	50	20	46	Р
						21	OBJECT ORIENTED	PROGRAMMING LAB	PR	50	20	43	Р

GRAND TOTAL = 963/1500, RESULT: FIRST CLASS

UNIVERSITY OF													
DATE : 18 AUG. 2011	CENT	TRE : I	PUNE]	INSTI	TUTE O	F COMPUTI	ER TECHNOLOGY, PUNE	Ε.	PAG	E NO.	34	(45	56)
NOTE: FIRST LINE : SEAT NO., NAME	OF THE	E CAND:	IDATE,	, MC	THER,	PERMANEN ⁻	T REG. NO., PREVIO	OUS SEAT NO., C	COLLEG	Ε, 5	SEAT N	I O .	
OTHER LINES: HEAD OF PASSING,	MAX	. MARKS	S, MI	EN. P	ASS MA	RKS, MAI	RKS OBTAINED, P/F:	PASS/FAIL, C:P	PREVIO	US CAF	RRY OV	'ER	
S8058603 RISHABH PANDITA				USH	Α		, 71045589F	, s8058603 ,	PICT		, s80	58603	3
01. DISCRETE STRUCTURES	PP	100	40	49	P C	11	. ENG MATHS III		PP	100	40	40	Р
02. COMPUTER ORGANIZATION	PP	100	40	58	PC	12	. COMPUTER GRAPHICS	5	PP	100	40	60	Р
03. DIGITAL ELECTRONICS & LOGIC DES	IGPP	100	40	62	P C	13	. PROCESSOR ARCHITE	ECTURE & INTER.	PP	100	40	45	Р
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	52	P C	14	. DATA STRUCTURES A	AND FILES	PP	100	40	66	Р
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	49	P C	15	. DATA COMMUNICATIO	ON	PP	100	40	61	Р
06. DIGITAL LABORATORY	TW	50	20	30	P C	16	. PROCESSOR INTERFA	ACING LABORATORY	′ TW	25	10	16	Р
07. DIGITAL LABORATORY	PR	50	20	33	P C	17	. PROCESSOR INTERFA	ACING LABORATORY	OR	50	20	25	Р
08. PROGRAMMING LABORATORY	TW	50	20	31	P C	18	. DATA STRUCTURES A	AND FILES LAB	TW	25	10	15	Р
09. PROGRAMMING LABORATORY	PR	50	20	44	P C	19	. DATA STRUCTURES A	AND FILES LAB	PR	50	20	28	Р
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	40	P C	20	. OBJECT ORIENTED F	PROGRAMMING LAB	TW	50	20	30	Р
						21	. OBJECT ORIENTED F	PROGRAMMING LAB	PR	50	20	40	Р
GRAND TOTAL = 874/1500, RESULT: HIGH	ER SEG	COND CI	LASS										
ORDN. 1 MARKS :													
S8058604 ROHAN MARWADI				MEE	NAKSHI		, 71045590к	, s8058604 ,	PICT		, s80	58604	1
01. DISCRETE STRUCTURES	PP	100	40	54	P C	11	. ENG MATHS III		PP	100	40	45	Р
02. COMPUTER ORGANIZATION	PP	100	40	40	P C	12	. COMPUTER GRAPHICS	5	PP	100	40	41	Р
03. DIGITAL ELECTRONICS & LOGIC DES	IGPP	100	40	58	P C	13	. PROCESSOR ARCHITE	ECTURE & INTER.	PP	100	40	19	F

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04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	45	P C	14		PP	100	40	68	Р
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	51	P C	15	DATA COMMUNICATION	PP	100	40	49	Р
06. DIGITAL LABORATORY	TW	50	20	30	P C	16	PROCESSOR INTERFACING LABORATORY	TW	25	10	18	Р
07. DIGITAL LABORATORY	PR	50	20	28	P C	17	PROCESSOR INTERFACING LABORATORY	OR	50	20	22	Р
08. PROGRAMMING LABORATORY	TW	50	20	40	P C	18	DATA STRUCTURES AND FILES LAB	TW	25	10	17	Р
09. PROGRAMMING LABORATORY	PR	50	20	45	P C	19	DATA STRUCTURES AND FILES LAB	PR	50	20	22	Р
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	41	РС	20	OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	42	Р
						21.	OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	32	Р

GRAND TOTAL = 807/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS:

S8058605	SACHIN SUKHLECHA				SANG	GEETA		, 71045592F , S8058605 ,	PICT		, s805	8605	5
01. DISCRET	E STRUCTURES	PP	100	40	74	P C	11.	ENG MATHS III	PP	100	40	69	Р
02. COMPUTE	R ORGANIZATION	PP	100	40	43	P C	12.	COMPUTER GRAPHICS	PP	100	40	41	Р
03. DIGITAL	ELECTRONICS & LOGIC DESIG	GPP	100	40	63	P C	13.	PROCESSOR ARCHITECTURE & INTER.	PP	100	40	41	Р
04. FUNDAME	NTAL OF DATA STRUCTURES	PP	100	40	69	P C	14.	DATA STRUCTURES AND FILES	PP	100	40	82	Р
05. HUMANIT	IES AND SOCIAL SCIENCES	PP	100	40	46	P C	15.	DATA COMMUNICATION	PP	100	40	53	Р
06. DIGITAL	LABORATORY	TW	50	20	36	P C	16.	PROCESSOR INTERFACING LABORATORY	TW	25	10	20	Р
07. DIGITAL	LABORATORY	PR	50	20	41	P C	17.	PROCESSOR INTERFACING LABORATORY	OR	50	20	39	Р
08. PROGRAM	MING LABORATORY	TW	50	20	37	P C	18.	DATA STRUCTURES AND FILES LAB	TW	25	10	15	Р
09. PROGRAM	MING LABORATORY	PR	50	20	41	P C	19.	DATA STRUCTURES AND FILES LAB	PR	50	20	38	Р
10. COMMUNI	CATION AND LANGUAGE LAB.	TW	50	20	32	P C	20.	OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	31	Р
							21.	OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	42	Р

GRAND TOTAL = 953/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS:

UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(INFORMATION TECHNOLOGY)

DATE: 18 AUG. 2011 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.

PAGE NO. 35 (457)

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	NOTE:	FIRST LINE : SEAT NO., NAME O	F THE	CANDID	DATE,	MO	THER, PERMAI	NENT	REG. NO., PREVIOUS SEAT NO., CO	OLLEGI	E, S	EAT N	ο.	
		OTHER LINES: HEAD OF PASSING,	MAX.	MARKS,	MIN	l. P/	ASS MARKS,	MARI	(S OBTAINED, P/F:PASS/FAIL, C:P	REVIO	JS CAR	RY OV	ER	
	S80586	606 SALVE AJINKYA PRABHAKAR				KAV:	ITA		, 71045594в , ѕ8058606 ,	PICT		, s80	5860	6
	01. D	ISCRETE STRUCTURES	PP	100	40	55	P C	11.	ENG MATHS III	PP	100	40	40	Р
	02. C	OMPUTER ORGANIZATION	PP	100	40	58	P C	12.	COMPUTER GRAPHICS	PP	100	40	58	Р
	03. D	IGITAL ELECTRONICS & LOGIC DESIG	GPP	100	40	60	P C	13.	PROCESSOR ARCHITECTURE & INTER.	PP	100	40	47	Р
	04. F	UNDAMENTAL OF DATA STRUCTURES	PP	100	40	75	P C	14.	DATA STRUCTURES AND FILES	PP	100	40	70	Р
	05. н	UMANITIES AND SOCIAL SCIENCES	PP	100	40	52	P C	15.	DATA COMMUNICATION	PP	100	40	62	Р
	06. D	IGITAL LABORATORY	TW	50	20	33	P C	16.	PROCESSOR INTERFACING LABORATORY	TW	25	10	15	Р
	07. D	IGITAL LABORATORY	PR	50	20	36	P C	17.	PROCESSOR INTERFACING LABORATORY	OR	50	20	05	F
	08. PI	ROGRAMMING LABORATORY	TW	50	20	36	P C	18.	DATA STRUCTURES AND FILES LAB	TW	25	10	12	Р
	09. PI	ROGRAMMING LABORATORY	PR	50	20	48	P C	19.	DATA STRUCTURES AND FILES LAB	PR	50	20	36	Р
	10. co	OMMUNICATION AND LANGUAGE LAB.	TW	50	20	32	P C	20.	OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	36	Р

GRAND TOTAL = 913/1500, RESULT: FAILS A.T.K.T.
ORDN. 1 MARKS :

S8058607 SANJAY SINGH				PRE	MA	, 71053988G , S8058607 , PICT , S80586	07
01. DISCRETE STRUCTURES	PP	100	40	66	P C	11. ENG MATHS III PP 100 40 64	Р
02. COMPUTER ORGANIZATION	PP	100	40	65	P C	12. COMPUTER GRAPHICS PP 100 40 57	Р
03. DIGITAL ELECTRONICS & LOGIC DES	IGPP	100	40	67	P C	13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 44	Р
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	63	P C	14. DATA STRUCTURES AND FILES PP 100 40 75	Р
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	53	P C	15. DATA COMMUNICATION PP 100 40 61	Р
06. DIGITAL LABORATORY	TW	50	20	31	P C	16. PROCESSOR INTERFACING LABORATORY TW 25 10 20	Р
07. DIGITAL LABORATORY	PR	50	20	38	P C	17. PROCESSOR INTERFACING LABORATORY OR 50 20 39	Р
08. PROGRAMMING LABORATORY	TW	50	20	39	P C	18. DATA STRUCTURES AND FILES LAB TW 25 10 16	Р

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21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 47 P

09. PROGRAMMING LABORATORY	PR	50	20	48	Р	С	19.	DATA STRUCTURES	cols05 AND FILES LAB	PR	50	20	41	Р
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	38	Р	С	20.	OBJECT ORIENTED	PROGRAMMING LAB	TW	50	20	38	Р
							21.	OBJECT ORIENTED	PROGRAMMING LAB	PR	50	20	32	Р
												-		
GRAND TOTAL = 995/1500, RESULT: FIRST	CLAS	SS WITH	l DIST	INCT	ION	I								
ORDN. 1 MARKS :														
S8058608 SARTHAK MAJITHIA				P00	JA			, 71045598E	, s8058608 ,	PICT		, s80	58608	8
01. DISCRETE STRUCTURES	PP	100	40	63	Р	С	11.	ENG MATHS III		PP	100	40	26	F
02. COMPUTER ORGANIZATION	PP	100	40	52	Р	С	12.	COMPUTER GRAPHI	CS	PP	100	40	57	Р
03. DIGITAL ELECTRONICS & LOGIC DESI	GPP	100	40	62	Р	С	13.	PROCESSOR ARCHI	TECTURE & INTER.	PP	100	40	AA	F
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	63	Р	С	14.	DATA STRUCTURES	AND FILES	PP	100	40	28	F
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	48	Р	С	15.	DATA COMMUNICAT	ION	PP	100	40	48	Р
06. DIGITAL LABORATORY	TW	50	20	20	Р	С	16.	PROCESSOR INTER	FACING LABORATORY	TW	25	10	10	Р
07. DIGITAL LABORATORY	PR	50	20	30	Р	С	17.	PROCESSOR INTER	FACING LABORATORY	OR	50	20	AA	F
08. PROGRAMMING LABORATORY	TW	50	20	20	Р	С	18.	DATA STRUCTURES	AND FILES LAB	TW	25	10	15	Р
09. PROGRAMMING LABORATORY	PR	50	20	48	Р	С	19.	DATA STRUCTURES	AND FILES LAB	PR	50	20	43	Р
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	30	Р	С	20.	OBJECT ORIENTED	PROGRAMMING LAB	TW	50	20	20	Р
							21.	OBJECT ORIENTED	PROGRAMMING LAB	PR	50	20	39	Р
GRAND TOTAL = 722/1500, RESULT: FAILS	: A T	ΚТ												
ORDN. 1 MARKS :	7.1.	.K.I.												
ORDIN. I MARKS .														
UNIVERSITY OF	PUNE	,S.E.	(2008	PAT.)(:	INFORMAT:	ION T	ECHNOLOGY)						
DATE : 18 AUG. 2011	CENT	TRE : F	PUNE I	NSTI	TUT	E OF COM	IPUTE	R TECHNOLOGY, PU	NE.	PAGI	E NO.	36	(4!	58)
NOTE: FIRST LINE : SEAT NO., NAME O	F THE	E CANDI	DATE,	MO	THE	R, PERMA	NENT	REG. NO., PREV	IOUS SEAT NO., C	OLLEGI	Ξ, S	EAT N	0.	
OTHER LINES: HEAD OF PASSING,	MAX.	MARKS	S, MI	N. P.	ASS	MARKS,	MAR	KS OBTAINED, P/	F:PASS/FAIL, C:P	REVIO	JS CAR	RY OV	ER	

MEERA

S8058609 SARWAR PRIYANKA ASHOK

, 71045599C , S8058609 , PICT , S8058609 Page 244

01.	DISCRETE STRUCTURES	PP	100	40	59	P C	11.	ENG MATHS III	PP	100	40	20	F
02.	COMPUTER ORGANIZATION	PP	100	40	44	P C	12.	COMPUTER GRAPHICS	PP	100	40	50	Р
03.	DIGITAL ELECTRONICS & LOGIC DESIGNATION	GPP	100	40	52	P C	13.	PROCESSOR ARCHITECTURE & INTER.	PP	100	40	40	Р
04.	FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	49	P C	14.	DATA STRUCTURES AND FILES	PP	100	40	55	Р
05.	HUMANITIES AND SOCIAL SCIENCES	PP	100	40	55	P C	15.	DATA COMMUNICATION	PP	100	40	46	Р
06.	DIGITAL LABORATORY	TW	50	20	31	P C	16.	PROCESSOR INTERFACING LABORATORY	TW	25	10	19	Р
07.	DIGITAL LABORATORY	PR	50	20	22	P C	17.	PROCESSOR INTERFACING LABORATORY	OR	50	20	12	F
08.	PROGRAMMING LABORATORY	TW	50	20	38	P C	18.	DATA STRUCTURES AND FILES LAB	TW	25	10	14	Р
09.	PROGRAMMING LABORATORY	PR	50	20	30	P C	19.	DATA STRUCTURES AND FILES LAB	PR	50	20	10	F
10.	COMMUNICATION AND LANGUAGE LAB.	TW	50	20	31	P C	20.	OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	38	Р
							21.	OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	20#	Р

GRAND TOTAL = 735+03/1500, RESULT: FAILS A.T.K.T. #[0.163+0.1][0.163]

RESULT RESERVED FOR BKLG

ORDN. 1 MARKS : (21)2,

s805	S8610 SATPUTE ABHINANDAN ASHOK				SAV	ITA		, 71134977M , S8058610 ,	PICT		, s805	58610)
01.	DISCRETE STRUCTURES	PP	100	40	53	P C	11.	ENG MATHS III	PP	100	40	57	Р
02.	COMPUTER ORGANIZATION	PP	100	40	77	P C	12.	COMPUTER GRAPHICS	PP	100	40	68	Р
03.	DIGITAL ELECTRONICS & LOGIC DESIGNATION	GPP	100	40	63	P C	13.	PROCESSOR ARCHITECTURE & INTER.	PP	100	40	84	Р
04.	FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	56	P C	14.	DATA STRUCTURES AND FILES	PP	100	40	69	Р
05.	HUMANITIES AND SOCIAL SCIENCES	PP	100	40	59	P C	15.	DATA COMMUNICATION	PP	100	40	58	Р
06.	DIGITAL LABORATORY	TW	50	20	34	P C	16.	PROCESSOR INTERFACING LABORATORY	TW	25	10	19	Р
07.	DIGITAL LABORATORY	PR	50	20	29	P C	17.	PROCESSOR INTERFACING LABORATORY	OR	50	20	42	Р
08.	PROGRAMMING LABORATORY	TW	50	20	36	P C	18.	DATA STRUCTURES AND FILES LAB	TW	25	10	21	Р
09.	PROGRAMMING LABORATORY	PR	50	20	44	P C	19.	DATA STRUCTURES AND FILES LAB	PR	50	20	44	Р
10.	COMMUNICATION AND LANGUAGE LAB.	TW	50	20	32	P C	20.	OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	41	Р
							21.	OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	32	Р

GRAND TOTAL = 1018/1500, RESULT: FIRST CLASS WITH DISTINCTION
ORDN. 1 MARKS :

S8058611 SATPUTE SWAPNIL BHASKAR				SUR	EKHA		, 71045600L , S8058611 ,	PIC	Т	, s8	05862	11
01. DISCRETE STRUCTURES	PP	100	40	63	P C	11.	ENG MATHS III	PP	100	40	40	
02. COMPUTER ORGANIZATION	PP	100	40	41	P C	12.	COMPUTER GRAPHICS	PP	100	40	45	
03. DIGITAL ELECTRONICS & LOGIC DESI	GPP	100	40	41	P C	13.	PROCESSOR ARCHITECTURE & INTER	. PP	100	40	64	
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	66	P C	14.	DATA STRUCTURES AND FILES	PP	100	40	55	
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	40	P C	15.	DATA COMMUNICATION	PP	100	40	47	
06. DIGITAL LABORATORY	TW	50	20	34	P C	16.	PROCESSOR INTERFACING LABORATOR	RY TW	25	10	13	
07. DIGITAL LABORATORY	PR	50	20	29	P C	17.	PROCESSOR INTERFACING LABORATOR	RY OR	50	20	40	
08. PROGRAMMING LABORATORY	TW	50	20	37	P C	18.	DATA STRUCTURES AND FILES LAB	TW	25	10	16	
9. PROGRAMMING LABORATORY	PR	50	20	41	P C	19.	DATA STRUCTURES AND FILES LAB	PR	50	20	40	
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	35	P C	20.	OBJECT ORIENTED PROGRAMMING LAN	3 TW	50	20	30	
						21.	OBJECT ORIENTED PROGRAMMING LA	3 PR	50	20	36	
AND TOTAL = 853/1500, RESULT: HIGHE DN. 1 MARKS :	R SEC	COND CI	_ASS									
ON. 1 MARKS : UNIVERSITY OF	 PUNE	 ,S.E.	(2008									
DN. 1 MARKS :	 PUNE	 ,S.E.	(2008				r TECHNOLOGY, PUNE.	 PA(GE NO.	37	(4
DN. 1 MARKS : UNIVERSITY OF DATE : 18 AUG. 2011	PUNE	 ,S.E. ΓRE : !	 (2008 PUNE :	INSTI	TUTE OF	COMPUTE	FECHNOLOGY) R TECHNOLOGY, PUNE.					4
ON. 1 MARKS: UNIVERSITY OF DATE: 18 AUG. 2011 NOTE: FIRST LINE: SEAT NO., NAME O	PUNE CENT		(2008 PUNE : 	INSTI	TUTE OF	COMPUTE 	R TECHNOLOGY) R TECHNOLOGY, PUNE.	COLLE	 GE,	 SEAT	 NO.	4
ON. 1 MARKS: UNIVERSITY OF DATE: 18 AUG. 2011 NOTE: FIRST LINE: SEAT NO., NAME O OTHER LINES: HEAD OF PASSING,	PUNE CENT			INSTI , MO	TUTE OF (COMPUTE RMANENT S, MAR	<pre>FECHNOLOGY) R TECHNOLOGY, PUNE</pre>	COLLE	GE, OUS CA	 SEAT RRY O	NO.	
ON. 1 MARKS: UNIVERSITY OF DATE: 18 AUG. 2011 NOTE: FIRST LINE: SEAT NO., NAME O OTHER LINES: HEAD OF PASSING,	PUNE CENT			INSTI , MO	TUTE OF (COMPUTE RMANENT S, MAR	R TECHNOLOGY) R TECHNOLOGY, PUNE.	COLLE	GE, OUS CA	 SEAT RRY O	NO.	
ON. 1 MARKS: UNIVERSITY OF DATE: 18 AUG. 2011 NOTE: FIRST LINE: SEAT NO., NAME O OTHER LINES: HEAD OF PASSING,	PUNE CENT F THE MAX.			INSTI , MO IN. P	TUTE OF (COMPUTE RMANENT S, MAR	<pre>FECHNOLOGY) R TECHNOLOGY, PUNE</pre>	COLLE	 GE, DUS CA	 SEAT RRY O	NO.	
UNIVERSITY OF DATE: 18 AUG. 2011 OTHER LINE: SEAT NO., NAME O OTHER LINES: HEAD OF PASSING,	PUNE CENT F THE MAX.			INSTI , MO IN. P	TUTE OF (THER, PE	COMPUTE RMANENT S, MAR	<pre>FECHNOLOGY) R TECHNOLOGY, PUNE</pre>	COLLECTION	 GE, DUS CA	 SEAT RRY O	NO. NO. OVER	1
UNIVERSITY OF DATE: 18 AUG. 2011 NOTE: FIRST LINE: SEAT NO., NAME O OTHER LINES: HEAD OF PASSING, S8058612 SETHI SUPREET SATISHKUMAR O1. DISCRETE STRUCTURES	PUNE CENT THE MAX	, S.E., FRE : F	. (2008 PUNE : IDATE S, M:	INSTI , MO IN. P PRE	TUTE OF O	COMPUTE RMANENT .S, MAR	R TECHNOLOGY) R TECHNOLOGY, PUNE. REG. NO., PREVIOUS SEAT NO., KS OBTAINED, P/F:PASS/FAIL, C , 71045605M , S8058612 ,	COLLECTION	 GE, OUS CA 	SEAT RRY O	NO. NOER 	1
DN. 1 MARKS: UNIVERSITY OF DATE: 18 AUG. 2011 NOTE: FIRST LINE: SEAT NO., NAME O OTHER LINES: HEAD OF PASSING,	PUNE CENT HE MAX PP	. , S.E., FRE : F CANDO MARKS		MO IN. P PRE 50 49	TUTE OF (THER, PE ASS MARK ETI P C	COMPUTE RMANENT .S, MAR 11. 12.	R TECHNOLOGY) R TECHNOLOGY, PUNE. REG. NO., PREVIOUS SEAT NO., KS OBTAINED, P/F:PASS/FAIL, C , 71045605M , S8058612 , ENG MATHS III	COLLECTION OF PROPERTY OF PROP	GE, OUS CA T	SEAT RRY O , S8	NO. NO. OVER 	1

						cols05
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	40	Р	15. DATA COMMUNICATION PP 100 40 61 P
06. DIGITAL LABORATORY	TW	50	20	27	P C	16. PROCESSOR INTERFACING LABORATORY TW 25 10 13 P
07. DIGITAL LABORATORY	PR	50	20	25	Р	17. PROCESSOR INTERFACING LABORATORY OR 50 20 32 P
08. PROGRAMMING LABORATORY	TW	50	20	30	P C	18. DATA STRUCTURES AND FILES LAB TW 25 10 13 P
09. PROGRAMMING LABORATORY	PR	50	20	40	P C	19. DATA STRUCTURES AND FILES LAB PR 50 20 30 P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	26	P C	20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 36 P
						21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 42 P
GRAND TOTAL = $757/1500$, RESULT: FAILS	5 A.T.	.K.T.				
ORDN. 1 MARKS :						

S8058613 SHAH KUNAL ARVIND				PRA	MILA	, 71045607н	, s8058613 ,	PICT	-	, s80)5861	.3
01. DISCRETE STRUCTURES	PP	100	40	65	РС	11. ENG MATHS III		PP	100	40	50	Р
02. COMPUTER ORGANIZATION	PP	100	40	51	P C	12. COMPUTER GRAP	HICS	PP	100	40	40	Р
03. DIGITAL ELECTRONICS & LOGIC DES	IGPP	100	40	61	P C	13. PROCESSOR ARC	HITECTURE & INTER.	PP	100	40	48	Р
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	60	P C	14. DATA STRUCTUR	ES AND FILES	PP	100	40	63	Р
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	40	P C	15. DATA COMMUNIC	ATION	PP	100	40	62	Р
06. DIGITAL LABORATORY	TW	50	20	39	P C	16. PROCESSOR INT	ERFACING LABORATORY	TW	25	10	23	Р
07. DIGITAL LABORATORY	PR	50	20	26	P C	17. PROCESSOR INT	ERFACING LABORATORY	OR	50	20	36	Р
08. PROGRAMMING LABORATORY	TW	50	20	41	P C	18. DATA STRUCTUR	ES AND FILES LAB	TW	25	10	21	Р
09. PROGRAMMING LABORATORY	PR	50	20	45	P C	19. DATA STRUCTUR	ES AND FILES LAB	PR	50	20	44	Р
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	38	P C	20. OBJECT ORIENT	ED PROGRAMMING LAB	TW	50	20	44	Р
						21. OBJECT ORIENT	ED PROGRAMMING LAB	PR	50	20	33	Р

GRAND TOTAL = 930/1500, RESULT: FIRST CLASS ORDN. 1 MARKS :

\$8058614 SHAIKH SHEEBANRAZA ZAHEER NASEEM , 71134978K , \$8058614 , PICT , \$8058614

01. DISCRETE STRUCTURES PP 100 40 41 P C 11. ENG MATHS III PP 100 40 40 P

03 COMPUTER ORGANIZATION												
02. COMPUTER ORGANIZATION	PP	100	40	60	P C	12.	COMPUTER GRAPHICS	PP	100	40	45	Р
03. DIGITAL ELECTRONICS & LOGIC DESI	GPP	100	40	63	P C	13.	PROCESSOR ARCHITECTURE & INTER.	PP	100	40	67	Р
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	58	P C	14.	DATA STRUCTURES AND FILES	PP	100	40	72	Р
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	54	P C	15.	DATA COMMUNICATION	PP	100	40	64	Р
06. DIGITAL LABORATORY	TW	50	20	36	P C	16.	PROCESSOR INTERFACING LABORATORY	TW	25	10	18	Р
07. DIGITAL LABORATORY	PR	50	20	24	P C	17.	PROCESSOR INTERFACING LABORATORY	OR	50	20	32	Р
08. PROGRAMMING LABORATORY	TW	50	20	41	P C	18.	DATA STRUCTURES AND FILES LAB	TW	25	10	18	Р
09. PROGRAMMING LABORATORY	PR	50	20	38	P C	19.	DATA STRUCTURES AND FILES LAB	PR	50	20	40	Р
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	41	P C	20.	OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	40	Р
						21.	OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	37	Р
GRAND TOTAL = 929/1500, RESULT: FIRST ORDN. 1 MARKS :	CLAS	5										
· · · · · · · · · · · · · · · · · · ·			(2000		;							
UNIVERSITY OF	PUNE	> - (700X	ΡΔΙ)(TNFORMATI		FCHNOLOGY)					
UNIVERSITY OF DATE: 18 AUG. 2011								PAG	= NO.	38	(46	50)
DATE : 18 AUG. 2011	CENT	RE : P	UNE IN	NSTI [.]	TUTE OF COM	PUTE	ECHNOLOGY) R TECHNOLOGY, PUNE.		Ξ NO.			
DATE : 18 AUG. 2011	CENTI	RE : P	UNE IN	NSTI [.]	TUTE OF COM	PUTE	R TECHNOLOGY, PUNE.					
DATE: 18 AUG. 2011	CENT	RE : P	UNE IN	NSTI ⁻	TUTE OF COM	PUTE · ·	R TECHNOLOGY, PUNE.	 DLLEG	 E, S	 EAT N	 O.	
DATE: 18 AUG. 2011	CENT	RE : P	UNE IN	NSTI ⁻	TUTE OF COM	PUTE · ·	R TECHNOLOGY, PUNE	 DLLEG	 E, S	 EAT N	 O.	
DATE: 18 AUG. 2011	CENT	RE : P	UNE IN	MO [*]	TUTE OF COM THER, PERMA ASS MARKS,	PUTE · ·	R TECHNOLOGY, PUNE. REG. NO., PREVIOUS SEAT NO., CO KS OBTAINED, P/F:PASS/FAIL, C:PI	OLLEGI	 E, S JS CAR	EAT NO	 O. ER	
DATE: 18 AUG. 2011	CENT	RE : P	UNE IN	NSTI ⁻	TUTE OF COM THER, PERMA ASS MARKS,	PUTE · ·	R TECHNOLOGY, PUNE	 DLLEG	 E, S JS CAR	 EAT N	 O. ER	
DATE: 18 AUG. 2011	CENT	RE : P	UNE IN	MO [*]	TUTE OF COM THER, PERMA ASS MARKS,	PUTE NENT MAR	R TECHNOLOGY, PUNE. REG. NO., PREVIOUS SEAT NO., CO KS OBTAINED, P/F:PASS/FAIL, C:PI	OLLEGI	 E, S JS CAR	EAT NO	 O. ER	
DATE: 18 AUG. 2011 NOTE: FIRST LINE: SEAT NO., NAME O OTHER LINES: HEAD OF PASSING, S8058615 SHEIKH LINA JAMIL AHMED	CENTI THE MAX.	RE : PI CANDII MARKS	UNE IN DATE, MIN	MO [*] N. P	TUTE OF COM THER, PERMA ASS MARKS,	PUTE NENT MARI 	R TECHNOLOGY, PUNE. REG. NO., PREVIOUS SEAT NO., CO KS OBTAINED, P/F:PASS/FAIL, C:PI , 71134979H , S8058615 ,	DLLEGION	 E, S JS CAR 	EAT NORY OV	0. ER 	 5
DATE: 18 AUG. 2011 NOTE: FIRST LINE: SEAT NO., NAME O OTHER LINES: HEAD OF PASSING, S8058615 SHEIKH LINA JAMIL AHMED 01. DISCRETE STRUCTURES	CENTI THE MAX.	RE : PI CANDII MARKS	UNE IN DATE, MIN	MOTAL NISI	TUTE OF COM THER, PERMA ASS MARKS, HAT	PUTE NENT MARI 	R TECHNOLOGY, PUNE. REG. NO., PREVIOUS SEAT NO., CO KS OBTAINED, P/F:PASS/FAIL, C:PI , 71134979H , S8058615 , ENG MATHS III	DLLEGION REVION PICT PP		EAT NORY OV	O. ER 	 5 P
DATE: 18 AUG. 2011 NOTE: FIRST LINE: SEAT NO., NAME O OTHER LINES: HEAD OF PASSING, S8058615 SHEIKH LINA JAMIL AHMED 01. DISCRETE STRUCTURES 02. COMPUTER ORGANIZATION	CENTI THE MAX.	RE : PI CANDII MARKS 100 100	DATE, , MIN	MOTAL NISI	TUTE OF COM THER, PERMA ASS MARKS, HAT P C P C	PUTE NENT MARI 11. 12. 13.	R TECHNOLOGY, PUNE. REG. NO., PREVIOUS SEAT NO., CO KS OBTAINED, P/F:PASS/FAIL, C:PI , 71134979H , S8058615 , ENG MATHS III COMPUTER GRAPHICS	DLLEGION REVION PICT PP PP		EAT NORY OV , S80 40 40		P P
DATE: 18 AUG. 2011 NOTE: FIRST LINE: SEAT NO., NAME O OTHER LINES: HEAD OF PASSING, S8058615 SHEIKH LINA JAMIL AHMED 01. DISCRETE STRUCTURES 02. COMPUTER ORGANIZATION 03. DIGITAL ELECTRONICS & LOGIC DESI	CENTI F THE MAX.	RE : PI CANDII MARKS 100 100 100	UNE IN	MO N. PA NISI 44 64 60 85	TUTE OF COM THER, PERMA ASS MARKS, HAT P C P C P C	PUTE NENT MAR 11. 12. 13. 14.	R TECHNOLOGY, PUNE. REG. NO., PREVIOUS SEAT NO., CO KS OBTAINED, P/F:PASS/FAIL, C:PI , 71134979H , S8058615 , ENG MATHS III COMPUTER GRAPHICS PROCESSOR ARCHITECTURE & INTER.	DLLEGICAL PICT PP PP PP		EAT NORY OV , S80 40 40 40	0. ER 	P P
DATE: 18 AUG. 2011 NOTE: FIRST LINE: SEAT NO., NAME O OTHER LINES: HEAD OF PASSING, S8058615 SHEIKH LINA JAMIL AHMED 01. DISCRETE STRUCTURES 02. COMPUTER ORGANIZATION 03. DIGITAL ELECTRONICS & LOGIC DESI 04. FUNDAMENTAL OF DATA STRUCTURES	CENTI THE MAX. PP PP GPP	RE : PI CANDII MARKS 100 100 100 100	DATE, , MIN	MO' N. PA NISI 44 64 60 85	TUTE OF COM THER, PERMA ASS MARKS, HAT P C P C P C	PUTE NENT MAR 11. 12. 13. 14. 15.	R TECHNOLOGY, PUNE. REG. NO., PREVIOUS SEAT NO., CONSTRUCTION OF SEAT	PICT PP PP PP PP	100 100 100 100	EAT NORY OV , S80 40 40 40 40	0. ER 	P P P
DATE: 18 AUG. 2011 NOTE: FIRST LINE: SEAT NO., NAME O OTHER LINES: HEAD OF PASSING, S8058615 SHEIKH LINA JAMIL AHMED O1. DISCRETE STRUCTURES O2. COMPUTER ORGANIZATION O3. DIGITAL ELECTRONICS & LOGIC DESI O4. FUNDAMENTAL OF DATA STRUCTURES O5. HUMANITIES AND SOCIAL SCIENCES	CENTI CENTI F THE MAX. PP PP PP PP PP	RE : PI CANDII MARKS 100 100 100 100 100	DATE, , MIN 40 40 40 40 40 40	MO N. PA NISI 44 64 60 85 55 38	TUTE OF COM THER, PERMA ASS MARKS, HAT P C P C P C P C P C	PUTE NENT MAR 11. 12. 13. 14. 15. 16.	R TECHNOLOGY, PUNE. REG. NO., PREVIOUS SEAT NO., CONSTRUCTION REG.	DLLEGI REVIOI PICT PP PP PP PP TW	100 100 100 100 100	EAT NORY OV , S80 40 40 40 40 40	0. ER 5861! 61 61 70 71	P P P P
DATE: 18 AUG. 2011	CENTI	RE : PI CANDII MARKS 100 100 100 100 100 50	UNE IN	MO N. PA NISI 44 64 60 85 55 38	TUTE OF COM THER, PERMA ASS MARKS, HAT P C P C P C P C P C P C P C P	PUTE NENT MAR 11. 12. 13. 14. 15. 16. 17.	R TECHNOLOGY, PUNE. REG. NO., PREVIOUS SEAT NO., CONSIDER OF SEAT	DLLEGI REVIOI PICT PP PP PP PP TW	100 100 100 100 100 25	EAT NORY OV , \$80 40 40 40 40 40 40	0. ER 5861! 61 61 70 71 74	P P P P P
DATE: 18 AUG. 2011	CENTI CENTI F THE MAX.	RE : PI CANDII MARKS 100 100 100 100 50 50	UNE IN	MO N. PA NISI 44 64 60 85 55 38 26 43	TUTE OF COM THER, PERMA ASS MARKS, HAT P C P C P C P C P C P C P C P	PUTE NENT MAR 11. 12. 13. 14. 15. 16. 17. 18.	R TECHNOLOGY, PUNE. REG. NO., PREVIOUS SEAT NO., CONSISTENCY KS OBTAINED, P/F:PASS/FAIL, C:PI , 71134979H , S8058615 , ENG MATHS III COMPUTER GRAPHICS PROCESSOR ARCHITECTURE & INTER. DATA STRUCTURES AND FILES DATA COMMUNICATION PROCESSOR INTERFACING LABORATORY PROCESSOR INTERFACING LABORATORY	DLLEGICA REVIOL	100 100 100 100 100 25 50	EAT NORY OV , \$80 40 40 40 40 40 20	0. ER 	P P P P P P

10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 40 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 45 P 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 30 P

GRAND TOTAL = 1023/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS :

S8058616 SHETTY SHEFALI RAVINDRA				VAN	DANA		, 71045612D	, s8058616 ,	PICT		, s80	58616	5
01. DISCRETE STRUCTURES	PP	100	40	61	P C	11.	ENG MATHS III		PP	100	40	59	Р
02. COMPUTER ORGANIZATION	PP	100	40	62	P C	12.	COMPUTER GRAPHIC	S	PP	100	40	32*	Р
03. DIGITAL ELECTRONICS & LOGIC DESI	GPP	100	40	59	P C	13.	PROCESSOR ARCHIT	ECTURE & INTER.	PP	100	40	42	Р
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	56	P C	14.	DATA STRUCTURES	AND FILES	PP	100	40	64	Р
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	46	P C	15.	DATA COMMUNICATI	ON	PP	100	40	51	Р
06. DIGITAL LABORATORY	TW	50	20	35	P C	16.	PROCESSOR INTERF	ACING LABORATORY	TW	25	10	22	Р
07. DIGITAL LABORATORY	PR	50	20	36	P C	17.	PROCESSOR INTERF	ACING LABORATORY	OR	50	20	41	Р
08. PROGRAMMING LABORATORY	TW	50	20	41	P C	18.	DATA STRUCTURES	AND FILES LAB	TW	25	10	22	Р
09. PROGRAMMING LABORATORY	PR	50	20	42	P C	19.	DATA STRUCTURES	AND FILES LAB	PR	50	20	43	Р
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	40	P C	20.	OBJECT ORIENTED	PROGRAMMING LAB	TW	50	20	46	Р
						21.	OBJECT ORIENTED	PROGRAMMING LAB	PR	50	20	30	Р

GRAND TOTAL = 930/1500, RESULT: FIRST CLASS * [0.4]
ORDN. 1 MARKS:

S8058617 SHEWALE ANULA TUSHAR		NEETA	, 71045613в , ѕ8058617 ,	PICT	, s8058617
O1. DISCRETE STRUCTURES PP	100 40	49 P C	11. ENG MATHS III	PP 100	O 40 57 P
02. COMPUTER ORGANIZATION PP	100 40	50 P C	12. COMPUTER GRAPHICS	PP 100	0 40 23 F
03. DIGITAL ELECTRONICS & LOGIC DESIGPP	100 40	40 P C	13. PROCESSOR ARCHITECTURE & INTER.	PP 100	O 40 40 P
04. FUNDAMENTAL OF DATA STRUCTURES PP	100 40	42 P C	14. DATA STRUCTURES AND FILES	PP 100	O 40 55 P
05. HUMANITIES AND SOCIAL SCIENCES PP	100 40	40 P C	15. DATA COMMUNICATION	PP 100	O 40 40 P
06. DIGITAL LABORATORY TW	50 20	27 P C	16. PROCESSOR INTERFACING LABORATORY Page 249	7 TW 2!	5 10 16 P

07. DIGITAL LABORATORY	PR	50	20	30	P C	17.	PROCESSOR INTERFACING LABORATORY	OR	50	20	04	F		
08. PROGRAMMING LABORATORY	TW	50	20	29	P C	18.	DATA STRUCTURES AND FILES LAB	TW	25	10	14	Р		
09. PROGRAMMING LABORATORY	PR	50	20	38	P C	19.	DATA STRUCTURES AND FILES LAB	PR	50	20	09	F		
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	31	P C	20.	OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	37	Р		
						21.	OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	25	Р		
CDAND TOTAL COC /1500 DECILITY FATI		T												
GRAND TOTAL = 696/1500, RESULT: FAILS	A.I.	K. I.												
ORDN. 1 MARKS :														
UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(INFORMATION TECHNOLOGY)														
DATE : 18 AUG. 2011	CENT	RE : F	UNE I	NSTI	TUTE OF COM	MPUTE	R TECHNOLOGY, PUNE.	PAG	E NO.	39	(4	61)		
NOTE: FIRST LINE : SEAT NO., NAME O	F THE	CANDI	DATE,	MO	THER, PERMA	ANENT	REG. NO., PREVIOUS SEAT NO., C	OLLEG	E, S	EAT N	10.			
OTHER LINES: HEAD OF PASSING,	MAX.	MARKS	, MI	N. P.	ASS MARKS,	MAR	KS OBTAINED, P/F:PASS/FAIL, C:P	REVIO	US CAR	RY OV	'ER			
S8058618 SHINDE AKASH ARUN				SHO	ВНА		, 71134980M , S8058618 ,	PICT		, s80)5861	8		
01. DISCRETE STRUCTURES	PP	100	40		PC		ENG MATHS III	PP	100	40	40			
02. COMPUTER ORGANIZATION	PP	100	40		P C		COMPUTER GRAPHICS	PP	100	40	49	Р		
03. DIGITAL ELECTRONICS & LOGIC DESI	:GPP	100	40	49	PC									
04. FUNDAMENTAL OF DATA STRUCTURES							PROCESSOR ARCHITECTURE & INTER.	PP	100	40	54			
The state of the s	PP	100	40		P C	14.	DATA STRUCTURES AND FILES	PP	100	40	70	Р		
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	52	P C	14. 15.	DATA STRUCTURES AND FILES DATA COMMUNICATION	PP PP	100 100	40 40	70 54	P P		
05. HUMANITIES AND SOCIAL SCIENCES		100		52	P C	14. 15.	DATA STRUCTURES AND FILES	PP PP	100 100	40 40	70	P P		
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	52 39	P C	14. 15. 16.	DATA STRUCTURES AND FILES DATA COMMUNICATION	PP PP TW	100 100	40 40	70 54	P P P		
05. HUMANITIES AND SOCIAL SCIENCES06. DIGITAL LABORATORY	PP TW	100 50	40 20	52 39 23	P C	14. 15. 16. 17.	DATA STRUCTURES AND FILES DATA COMMUNICATION PROCESSOR INTERFACING LABORATORY	PP PP TW	100 100 25	40 40 10	70 54 21	P P P		
05. HUMANITIES AND SOCIAL SCIENCES06. DIGITAL LABORATORY07. DIGITAL LABORATORY	PP TW PR	100 50 50	40 20 20	52 39 23 41	P C P C	14. 15. 16. 17.	DATA STRUCTURES AND FILES DATA COMMUNICATION PROCESSOR INTERFACING LABORATORY PROCESSOR INTERFACING LABORATORY	PP PP TW OR	100 100 25 50	40 40 10 20	70 54 21 39	P P P P		
05. HUMANITIES AND SOCIAL SCIENCES06. DIGITAL LABORATORY07. DIGITAL LABORATORY08. PROGRAMMING LABORATORY	PP TW PR TW PR	100 50 50 50	40 20 20 20	5239234137	P C P C P C	14. 15. 16. 17. 18.	DATA STRUCTURES AND FILES DATA COMMUNICATION PROCESSOR INTERFACING LABORATORY PROCESSOR INTERFACING LABORATORY DATA STRUCTURES AND FILES LAB	PP PP TW OR TW PR	100 100 25 50 25	40 40 10 20 10	70 54 21 39 21	P P P P P		
05. HUMANITIES AND SOCIAL SCIENCES06. DIGITAL LABORATORY07. DIGITAL LABORATORY08. PROGRAMMING LABORATORY09. PROGRAMMING LABORATORY	PP TW PR TW PR	100 50 50 50 50	40 20 20 20 20	5239234137	P C P C P C P C	14. 15. 16. 17. 18. 19.	DATA STRUCTURES AND FILES DATA COMMUNICATION PROCESSOR INTERFACING LABORATORY PROCESSOR INTERFACING LABORATORY DATA STRUCTURES AND FILES LAB DATA STRUCTURES AND FILES LAB	PP PP TW OR TW PR TW	100 100 25 50 25 50	40 40 10 20 10 20	705421392143	P P P P P		

GRAND TOTAL = 922/1500, RESULT: FIRST CLASS
ORDN. 1 MARKS :

s8058619	SHINGANE ANKUSH DINANATH				VEEN	NA		, 71134981к , S8058619 ,	PICT		, S80	58619	9
01. DISCRE	TE STRUCTURES	PP	100	40	52	P C	11.	ENG MATHS III	PP	100	40	52	Р
02. COMPUT	ER ORGANIZATION	PP	100	40	69	РС	12.	COMPUTER GRAPHICS	PP	100	40	50	Р
03. DIGITA	L ELECTRONICS & LOGIC DESIG	GPP	100	40	53	РС	13.	PROCESSOR ARCHITECTURE & INTER.	PP	100	40	68	Р
04. FUNDAM	ENTAL OF DATA STRUCTURES	PP	100	40	61	РС	14.	DATA STRUCTURES AND FILES	PP	100	40	63	Р
05. HUMANI	TIES AND SOCIAL SCIENCES	PP	100	40	49	РС	15.	DATA COMMUNICATION	PP	100	40	69	Р
06. DIGITA	L LABORATORY	TW	50	20	37	РС	16.	PROCESSOR INTERFACING LABORATORY	TW	25	10	18	Р
07. DIGITA	L LABORATORY	PR	50	20	36	РС	17.	PROCESSOR INTERFACING LABORATORY	OR	50	20	32	Р
08. PROGRA	MMING LABORATORY	TW	50	20	44	РС	18.	DATA STRUCTURES AND FILES LAB	TW	25	10	19	Р
09. PROGRA	MMING LABORATORY	PR	50	20	44	РС	19.	DATA STRUCTURES AND FILES LAB	PR	50	20	24	Р
10. COMMUN	ICATION AND LANGUAGE LAB.	TW	50	20	40	РС	20.	OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	37	Р
							21.	OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	40	Р

GRAND TOTAL = 957/1500, RESULT: FIRST CLASS
ORDN. 1 MARKS :

s80	58620 SHIRUDE NEHA RAJIV				SMI	ТА		, 710	59492F	, s8058620 ,	PICT		, s80	58620	0
01.	DISCRETE STRUCTURES	PP	100	40	61	P C	11.	ENG MAT	HS III		PP	100	40	72	Р
02.	COMPUTER ORGANIZATION	PP	100	40	61	P C	12.	COMPUTE	R GRAPHIC	CS	PP	100	40	48	Р
03.	DIGITAL ELECTRONICS & LOGIC DESI	GPP	100	40	49	P C	13.	PROCESS	OR ARCHI	TECTURE & INTER.	PP	100	40	49	Р
04.	FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	54	P C	14.	DATA ST	RUCTURES	AND FILES	PP	100	40	64	Р
05.	HUMANITIES AND SOCIAL SCIENCES	PP	100	40	51	P C	15.	DATA CO	MMUNICAT	ION	PP	100	40	60	Р
06.	DIGITAL LABORATORY	TW	50	20	29	P C	16.	PROCESS	OR INTER	FACING LABORATORY	TW	25	10	19	Р
07.	DIGITAL LABORATORY	PR	50	20	33	P C	17.	PROCESS	OR INTER	FACING LABORATORY	OR	50	20	05	F
08.	PROGRAMMING LABORATORY	TW	50	20	41	P C	18.	DATA ST	RUCTURES	AND FILES LAB	TW	25	10	15	Р
09.	PROGRAMMING LABORATORY	PR	50	20	42	P C	19.	DATA ST	RUCTURES	AND FILES LAB	PR	50	20	30	Р
10.	COMMUNICATION AND LANGUAGE LAB.	TW	50	20	39	P C	20.	OBJECT	ORIENTED	PROGRAMMING LAB	TW	50	20	44	Р
							21.	OBJECT	-	PROGRAMMING LAB Page 251	PR	50	20	32	Р

GRAND TOTAL = 898/1500, RESULT: FAILS A.T.K.T.
ORDN. 1 MARKS :

02. COMPUTER ORGANIZATION

UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(INFORMATION TECHNOLOGY)														
DATE : 18 AUG. 2011	CENTRE : PUNE IN				NSTITUTE OF COMPUTER TECHNOLOGY, PUNE.					PAGE NO. 40 (462)				
NOTE: FIRST LINE : SEAT NO., NAME O)F THE	CANDI	DATE,	MO ⁻	THER,	PERMANENT	REG. NO., PREVIOUS SEAT NO., C	OLLEG	E, S	EAT NO	ο.			
OTHER LINES: HEAD OF PASSING,	MAX.	MARKS	, МІ	N. P.	ASS M	IARKS, MAR	KS OBTAINED, P/F:PASS/FAIL, C:P	REVIO	JS CAR	RY OVI	ΞR			
S8058621 SHUBHI YEDE				ANJ	U		, 71045620E , S8058621 ,	PICT		, S80	5862:	1		
01. DISCRETE STRUCTURES	PP	100	40	68	P C	11.	ENG MATHS III	PP	100	40	65	Р		
02. COMPUTER ORGANIZATION	PP	100	40	64	РС	12.	COMPUTER GRAPHICS	PP	100	40	49	Р		
03. DIGITAL ELECTRONICS & LOGIC DESI	[GPP	100	40	58	РС	13.	PROCESSOR ARCHITECTURE & INTER.	PP	100	40	60	Р		
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	72	РС	14.	DATA STRUCTURES AND FILES	PP	100	40	59	Р		
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	53	РС	15.	DATA COMMUNICATION	PP	100	40	62	Р		
06. DIGITAL LABORATORY	TW	50	20	32	РС	16.	PROCESSOR INTERFACING LABORATORY	TW	25	10	19	Р		
07. DIGITAL LABORATORY	PR	50	20	41	РС	17.	PROCESSOR INTERFACING LABORATORY	OR	50	20	45	Р		
08. PROGRAMMING LABORATORY	TW	50	20	38	РС	18.	DATA STRUCTURES AND FILES LAB	TW	25	10	17	Р		
09. PROGRAMMING LABORATORY	PR	50	20	42	РС	19.	DATA STRUCTURES AND FILES LAB	PR	50	20	42	Р		
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	38	РС	20.	OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	40	Р		
						21.	OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	36	Р		
GRAND TOTAL = 1000/1500, RESULT: FIRST CLASS WITH DISTINCTION														
ORDN. 1 MARKS :														
S8058623 SINGH PRABHAKAR KRISHNAKUMAR			GAY [*]	TRIDE	VI	, 71134982н , ѕ8058623 ,	PICT , S8058623							
01. DISCRETE STRUCTURES	PP	100	40	53	P C	11.	ENG MATHS III	PP	100	40	40	Р		

12. COMPUTER GRAPHICS

Page 252

PP 100 40 58 P

PP 100 40 52 P C

03.	DIGITAL ELECTRONICS & LOGIC DESIGNATION OF THE PROPERTY OF THE	GPP	100	40	50	P C	13.	PROCESSOR ARCHITECTURE & INTER.	PP	100	40	73	Р
04.	FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	64	P C	14.	DATA STRUCTURES AND FILES	PP	100	40	67	Р
05.	HUMANITIES AND SOCIAL SCIENCES	PP	100	40	54	P C	15.	DATA COMMUNICATION	PP	100	40	62	Р
06.	DIGITAL LABORATORY	TW	50	20	39	P C	16.	PROCESSOR INTERFACING LABORATORY	TW	25	10	20	Р
07.	DIGITAL LABORATORY	PR	50	20	33	P C	17.	PROCESSOR INTERFACING LABORATORY	OR	50	20	28	Р
08.	PROGRAMMING LABORATORY	TW	50	20	41	P C	18.	DATA STRUCTURES AND FILES LAB	TW	25	10	22	Р
09.	PROGRAMMING LABORATORY	PR	50	20	39	P C	19.	DATA STRUCTURES AND FILES LAB	PR	50	20	33	Р
10.	COMMUNICATION AND LANGUAGE LAB.	TW	50	20	38	P C	20.	OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	44	Р
							21.	OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	25	Р

GRAND TOTAL = 935/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS:

S8058624 SOMANI NEH	A NANDKISHOR			ARAT	ГІ		, 71045628L	, s8058624 ,	PICT		, s80	58624	4
01. DISCRETE STRUCTUR	ES PP	100	40	52	P C	11.	ENG MATHS III		PP	100	40	56	Р
02. COMPUTER ORGANIZA	ΓΙΟΝ PP	100	40	51	P C	12.	COMPUTER GRAPHIC	CS .	PP	100	40	49	Р
03. DIGITAL ELECTRONI	CS & LOGIC DESIGPP	100	40	52	P C	13.	PROCESSOR ARCHIT	ECTURE & INTER.	PP	100	40	15	F
04. FUNDAMENTAL OF DA	TA STRUCTURES PP	100	40	40	P C	14.	DATA STRUCTURES	AND FILES	PP	100	40	67	Р
05. HUMANITIES AND SO	CIAL SCIENCES PP	100	40	51	P C	15.	DATA COMMUNICATI	ON	PP	100	40	69	Р
06. DIGITAL LABORATOR	Y TW	50	20	26	P C	16.	PROCESSOR INTERF	ACING LABORATORY	TW	25	10	17	Р
07. DIGITAL LABORATOR	Y PR	50	20	37	P C	17.	PROCESSOR INTERF	ACING LABORATORY	OR	50	20	04	F
08. PROGRAMMING LABOR	ATORY TW	50	20	28	P C	18.	DATA STRUCTURES	AND FILES LAB	TW	25	10	15	Р
09. PROGRAMMING LABOR	ATORY PR	50	20	20	P C	19.	DATA STRUCTURES	AND FILES LAB	PR	50	20	37	Р
10. COMMUNICATION AND	LANGUAGE LAB. TW	50	20	36	P C	20.	OBJECT ORIENTED	PROGRAMMING LAB	TW	50	20	41	Р
						21.	OBJECT ORIENTED	PROGRAMMING LAB	PR	50	20	35	Р

GRAND TOTAL = 798/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS:

UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(INFORMATION TECHNOLOGY)

17. PROCESSOR INTERFACING LABORATORY OR 50 20 39 P
Page 254

DATE : 18 AUG. 2011 CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 41 (463) NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER S8058625 SOMWANSHI HARSHAL SURESH NUTAN , 710456293 , S8058625 , PICT , S8058625 01. DISCRETE STRUCTURES PP 100 40 40 PC 11. ENG MATHS III PP 100 40 47 P 102. COMPUTER ORGANIZATION PP 100 40 43 PC 12. COMPUTER GRAPHICS PP 100 40 45 P 103. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 40 66 P 14. DATA STRUCTURES AND FILES PP 100 40 57 P 105. HUMANITIES AND SOCIAL SCIENCES PP 100 40 44 PC 15. DATA COMMUNICATION PP 100 40 42 P 106. DIGITAL LABORATORY TW 50 20 31 PC 16. PROCESSOR INTERFACING LABORATORY TW 25 10 17 P 107. DIGITAL LABORATORY PR 50 20 37 PC 17. PROCESSOR INTERFACING LABORATORY OR 50 20 05 F 18. DATA STRUCTURES AND FILES LAB PR 50 20 33 P 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 31 P C 20. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 33 P 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 31 P C 20. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 33 P 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 31 P C 20. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 35 P 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 35 P 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 35 P 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 35 P 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 35 P 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 40 P													
NOTE: FIRST LINE : SEAT NO., NAME O	F THE	CANDII	DATE,	МОТ	HER, PER	MANENT	REG. NO., PREVIOUS SEAT NO., CO	OLLEGI	Ξ , Si	EAT NO			
OTHER LINES: HEAD OF PASSING,	MAX.	MARKS	, MIN	N. PA	ASS MARKS	, MARK	S OBTAINED, P/F:PASS/FAIL, C:P	REVIO	JS CARI	RY OVE	र		
-0050525							71045520-			-005	2625		
S8058625 SOMWANSHI HARSHAL SURESH				NUTA	AN		, /10456293 , \$8058625 ,	PICT	!	, 58058	3625		
01. DISCRETE STRUCTURES	PP	100	40	40	P C	11.	ENG MATHS III	PP	100	40 4	17 P		
02. COMPUTER ORGANIZATION	PP	100	40	43	P C	12.	COMPUTER GRAPHICS	PP	100	40 4	15 P		
03. DIGITAL ELECTRONICS & LOGIC DESI	GPP	100	40	69	P C	13.	PROCESSOR ARCHITECTURE & INTER.	PP	100	40 4	10 P		
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	66	Р	14.	DATA STRUCTURES AND FILES	PP	100	40 !	57 P		
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	44	P C	15.	DATA COMMUNICATION	PP	100	40 4	12 P		
06. DIGITAL LABORATORY	TW	50	20	31	P C	16.	PROCESSOR INTERFACING LABORATORY	TW	25	10 1	L7 P		
07. DIGITAL LABORATORY	PR	50	20	37	P C	17.	PROCESSOR INTERFACING LABORATORY	OR	50	20 ()5 F		
08. PROGRAMMING LABORATORY	TW	50	20	37	P C	18.	DATA STRUCTURES AND FILES LAB	TW	25	10	L5 P		
09. PROGRAMMING LABORATORY	PR	50	20	44	P C	19.	DATA STRUCTURES AND FILES LAB	PR	50	20	33 P		
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	31	P C	20.	OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	35 P		
						21.	OBJECT ORIENTED PROGRAMMING LAB	PR	50	20 4	10 P		
CRAND TOTAL 919.05/1500 DECULT. FA	T. C. A	T 1/ T	FO 10	527									
	ILS A	. I . K . I	.[0.10	02]									
ORDN. I MARKS :													
												•	
S8058626 SONAWANE JAYANT SHAM				RAJE	SHRI		, 71045630B , S8058626 ,	PICT		, s8058	3626		
01. DISCRETE STRUCTURES	PP	100	40	61	P C	11.	ENG MATHS III	PP	100	40 6	54 P		
02. COMPUTER ORGANIZATION	PP	100	40	56	P C	12.	COMPUTER GRAPHICS	PP	100	40 4	10 P		
03. DIGITAL ELECTRONICS & LOGIC DESI	GPP	100	40	46	P C	13.	PROCESSOR ARCHITECTURE & INTER.	PP	100	40 7	71 P		
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	58	P C	14.	DATA STRUCTURES AND FILES	PP	100	40 6	65 P		
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	51	P C	15.	DATA COMMUNICATION	PP	100	40 4	18 P		
06. DIGITAL LABORATORY	TW	50	20	32	P C	16.	PROCESSOR INTERFACING LABORATORY	TW	25	10 1	19 P		

PR 50 20 35 P C

07. DIGITAL LABORATORY

08. PROGRAMMING LABORATORY	TW	50	20	37	PC	18.	DATA STRUCTURES AND FILES LAB	TW	25	10	15	Р
09. PROGRAMMING LABORATORY	PR	50	20	44	P C	19.	DATA STRUCTURES AND FILES LAB	PR	50	20	42	Р
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	39	P C	20.	OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	38	Р
						21.	OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	32	Р
GRAND TOTAL = 932+05/1500, RESULT: F	IRST (CLASS[0.163]								
ORDN. 1 MARKS :												
S8058627 SONY PRERNA ASHOK				BHA	AGWAT	I	, 71134983F , S8058627 ,	PICT		, s80	05862	7
01. DISCRETE STRUCTURES	PP	100	40	40	P C	11.	ENG MATHS III	PP	100	40	40	Р
02. COMPUTER ORGANIZATION	PP	100	40	66	P C	12.	COMPUTER GRAPHICS	PP	100	40	49	Р
03. DIGITAL ELECTRONICS & LOGIC DES	IGPP	100	40	55	P C	13.	PROCESSOR ARCHITECTURE & INTER.	PP	100	40	55	Р
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	61	P C	14.	DATA STRUCTURES AND FILES	PP	100	40	62	Р
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	46	P C	15.	DATA COMMUNICATION	PP	100	40	61	Р
06. DIGITAL LABORATORY	TW	50	20	41	P C	16.	PROCESSOR INTERFACING LABORATORY	/ TW	25	10	20	Р
07. DIGITAL LABORATORY	PR	50	20	40	P C	17.	PROCESSOR INTERFACING LABORATORY	OR	50	20	40	Р
08. PROGRAMMING LABORATORY	TW	50	20	44	P C	18.	DATA STRUCTURES AND FILES LAB	TW	25	10	19	Р
09. PROGRAMMING LABORATORY	PR	50	20	37	P C	19.	DATA STRUCTURES AND FILES LAB	PR	50	20	45	Р
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	39	P C	20.	OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	39	Р
						21.	OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	42	Р
GRAND TOTAL = 941/1500, RESULT: FIRST	T CLAS	SS										
ORDN. 1 MARKS :												
UNIVERSITY OF	 PUNE	 .,S.E.	(2008	 PAT	 .)(I	NFORMATION	ECHNOLOGY)					
DATE : 18 AUG. 2011	CEN	TRE :	PUNE :	INSTI	TUTE	OF COMPUTE	R TECHNOLOGY, PUNE.	PAG	E NO.	42	(4	64)
NOTE: FIRST LINE : SEAT NO., NAME (OF THI	E CAND	IDATE	, MC	THER	R, PERMANENT	REG. NO., PREVIOUS SEAT NO., C	COLLEG	Ε,	SEAT N	١٥.	
OTHER LINES: HEAD OF PASSING,	MAX	. MARK	S, M	IN. P	PASS	MARKS, MAR	KS OBTAINED, P/F:PASS/FAIL, C:F	PREVIO	US CA	RRY O\	√ER	
							Page 255					

S8058628 SURADKAR SWAPNIL RAJENDRA				SAN	GITA		, 71134984D	, \$8058628 ,	PICT		, s80	58628	8
01. DISCRETE STRUCTURES	PP	100	40	28	F	11.	ENG MATHS III		PP	100	40	40	Р
02. COMPUTER ORGANIZATION	PP	100	40	57	P C	12.	COMPUTER GRAPHICS	5	PP	100	40	43	Р
03. DIGITAL ELECTRONICS & LOGIC DESI	GPP	100	40	53	P C	13.	PROCESSOR ARCHITI	ECTURE & INTER.	PP	100	40	48	Р
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	53	P C	14.	DATA STRUCTURES	AND FILES	PP	100	40	52	Р
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	55	P C	15.	DATA COMMUNICATIO	ON	PP	100	40	60	Р
06. DIGITAL LABORATORY	TW	50	20	34	P C	16.	PROCESSOR INTERFA	ACING LABORATORY	TW	25	10	16	Р
07. DIGITAL LABORATORY	PR	50	20	30	P C	17.	PROCESSOR INTERFA	ACING LABORATORY	OR	50	20	21	Р
08. PROGRAMMING LABORATORY	TW	50	20	38	P C	18.	DATA STRUCTURES	AND FILES LAB	TW	25	10	16	Р
09. PROGRAMMING LABORATORY	PR	50	20	35	PC	19.	DATA STRUCTURES	AND FILES LAB	PR	50	20	39	Р
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	35	PC	20.	OBJECT ORIENTED I	PROGRAMMING LAB	TW	50	20	41	Р
						21.	OBJECT ORIENTED I	PROGRAMMING LAB	PR	50	20	28	Р

GRAND TOTAL = 822/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS:

s805	88629 TAORI SANIKA SANJAY				RUP	ALI		, 71045640К	, s8058629 ,	PICT		, s80	58629	9
01.	DISCRETE STRUCTURES	PP	100	40	61	РС	11.	ENG MATHS III		PP	100	40	67	Р
02.	COMPUTER ORGANIZATION	PP	100	40	53	P C	12.	COMPUTER GRAPHIC	S	PP	100	40	53	Р
03.	DIGITAL ELECTRONICS & LOGIC DESIGNATION	GPP	100	40	49	P C	13.	PROCESSOR ARCHIT	ECTURE & INTER.	PP	100	40	48	Р
04.	FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	72	P C	14.	DATA STRUCTURES	AND FILES	PP	100	40	61	Р
05.	HUMANITIES AND SOCIAL SCIENCES	PP	100	40	50	P C	15.	DATA COMMUNICATI	ON	PP	100	40	56	Р
06.	DIGITAL LABORATORY	TW	50	20	34	P C	16.	PROCESSOR INTERF	ACING LABORATORY	TW	25	10	20	Р
07.	DIGITAL LABORATORY	PR	50	20	30	P C	17.	PROCESSOR INTERF	ACING LABORATORY	OR	50	20	35	Р
08.	PROGRAMMING LABORATORY	TW	50	20	42	P C	18.	DATA STRUCTURES	AND FILES LAB	TW	25	10	18	Р
09.	PROGRAMMING LABORATORY	PR	50	20	40	P C	19.	DATA STRUCTURES	AND FILES LAB	PR	50	20	20	Р
10.	COMMUNICATION AND LANGUAGE LAB.	TW	50	20	39	P C	20.	OBJECT ORIENTED	PROGRAMMING LAB	TW	50	20	45	Р
							21.	OBJECT ORIENTED	PROGRAMMING LAB	PR	50	20	38	Р

GRAND TOTAL = 931/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS:

S8058630 TATHE VISHAL BHAGWAT				KAL	PANA	4	, 71134985B , S8058630 ,	PICT		, s80	5863	0
01. DISCRETE STRUCTURES	PP	100	40	66	P C	11.	ENG MATHS III	PP	100	40	61	Р
02. COMPUTER ORGANIZATION	PP	100	40	75	P C	12.	COMPUTER GRAPHICS	PP	100	40	56	Р
03. DIGITAL ELECTRONICS & LOGIC DESI	GPP	100	40	57	P C	13.	PROCESSOR ARCHITECTURE & INTER.	PP	100	40	55	Р
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	58	P C	14.	DATA STRUCTURES AND FILES	PP	100	40	73	Р
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	57	P C	15.	DATA COMMUNICATION	PP	100	40	66	Р
06. DIGITAL LABORATORY	TW	50	20	35	P C	16.	PROCESSOR INTERFACING LABORATORY	TW	25	10	22	Р
07. DIGITAL LABORATORY	PR	50	20	36	P C	17.	PROCESSOR INTERFACING LABORATORY	OR	50	20	21	Р
08. PROGRAMMING LABORATORY	TW	50	20	39	P C	18.	DATA STRUCTURES AND FILES LAB	TW	25	10	21	Р
09. PROGRAMMING LABORATORY	PR	50	20	38	РС	19.	DATA STRUCTURES AND FILES LAB	PR	50	20	43	Р
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	40	РС	20.	OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	48	Р
						21.	OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	38	Р
GRAND TOTAL = 1005/1500, RESULT: FIRST	CLAS	SS WITH	I DIST	INCT	ION							
ORDN. 1 MARKS :												
UNIVERSITY OF	PUNE	 ,S.E.	(2008	 PAT.)(IN	 NFORMATION T	ECHNOLOGY)					
DATE : 18 AUG. 2011	CENT	ΓRE : P	PUNE I	NSTI	TUTE	OF COMPUTE	R TECHNOLOGY, PUNE.	PAG	E NO.	43	(4	65)
NOTE: FIRST LINE : SEAT NO., NAME O	F THE	E CANDI	DATE,	МО	THER	R, PERMANENT	REG. NO., PREVIOUS SEAT NO., C	OLLEG	E, S	EAT N	0.	
OTHER LINES: HEAD OF PASSING,	MAX.	. MARKS	s, MI	N. P	ASS	MARKS, MARI	(S OBTAINED, P/F:PASS/FAIL, C:P	REVIO	JS CAR	.RY OV	ER	

\$8058631 THOMBRE PRIYANKA MILIND

JAYMALA

, 71045649C
, \$8058631
, PICT
, \$8058631

01. DISCRETE STRUCTURES

PP 100 40 58 P C
11. ENG MATHS III

PP 100 40 14 F

02. COMPUTER ORGANIZATION

PP 100 40 27 F
12. COMPUTER GRAPHICS

PP 100 40 53 P

03. DIGITAL ELECTRONICS & LOGIC DESIGPP

100 40 50 P C
13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 25 F

04.	FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	55	PC	14.	DATA STRUCTURES AND FILES	PP	100	40	60	Р
05.	HUMANITIES AND SOCIAL SCIENCES	PP	100	40	47	P C	15.	DATA COMMUNICATION	PP	100	40	40	Р
06.	DIGITAL LABORATORY	TW	50	20	36	P C	16.	PROCESSOR INTERFACING LABORATORY	TW	25	10	20	Р
07.	DIGITAL LABORATORY	PR	50	20	20	P C	17.	PROCESSOR INTERFACING LABORATORY	OR	50	20	37	Р
08.	PROGRAMMING LABORATORY	TW	50	20	37	P C	18.	DATA STRUCTURES AND FILES LAB	TW	25	10	17	Р
09.	PROGRAMMING LABORATORY	PR	50	20	42	P C	19.	DATA STRUCTURES AND FILES LAB	PR	50	20	28	Р
10.	COMMUNICATION AND LANGUAGE LAB.	TW	50	20	36	P C	20.	OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	42	Р
							21.	OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	30	Р

GRAND TOTAL = 774/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS:

s805	8632 TIWARI ROHIT VINOD				SHAI	KUNTALA		, 71045652C , S8058632 ,	PICT		, s80!	58632	<u>?</u>
01.	DISCRETE STRUCTURES	PP	100	40	47	P C	11.	ENG MATHS III	PP	100	40	22	F
02.	COMPUTER ORGANIZATION	PP	100	40	40	P C	12.	COMPUTER GRAPHICS	PP	100	40	40	Р
03.	DIGITAL ELECTRONICS & LOGIC DESIGNATION	GPP	100	40	43	P C	13.	PROCESSOR ARCHITECTURE & INTER.	PP	100	40	40	Р
04.	FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	46	P C	14.	DATA STRUCTURES AND FILES	PP	100	40	47	Р
05.	HUMANITIES AND SOCIAL SCIENCES	PP	100	40	46	P C	15.	DATA COMMUNICATION	PP	100	40	53	Р
06.	DIGITAL LABORATORY	TW	50	20	20	P C	16.	PROCESSOR INTERFACING LABORATORY	TW	25	10	13	Р
07.	DIGITAL LABORATORY	PR	50	20	35	Р	17.	PROCESSOR INTERFACING LABORATORY	OR	50	20	30	Р
08.	PROGRAMMING LABORATORY	TW	50	20	28	P C	18.	DATA STRUCTURES AND FILES LAB	TW	25	10	11	Р
09.	PROGRAMMING LABORATORY	PR	50	20	32	P C	19.	DATA STRUCTURES AND FILES LAB	PR	50	20	10	F
10.	COMMUNICATION AND LANGUAGE LAB.	TW	50	20	27	P C	20.	OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	30	Р
							21.	OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	16	F

GRAND TOTAL = 676/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

, 71134986L , S8058633 , PICT Page 258 S8058633 UKARANDE SNEHAL ANIL JAYASHRI , S8058633

01.	DISCRETE STRUCTURES	PP	100	40	49	P C	11.	ENG MATHS III	PP	100	40	40	Р
02.	COMPUTER ORGANIZATION	PP	100	40	47	P C	12.	COMPUTER GRAPHICS	PP	100	40	62	Р
03.	DIGITAL ELECTRONICS & LOGIC DESIGNATION	GPP	100	40	65	P C	13.	PROCESSOR ARCHITECTURE & INTER.	PP	100	40	60	Р
04.	FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	66	P C	14.	DATA STRUCTURES AND FILES	PP	100	40	70	Р
05.	HUMANITIES AND SOCIAL SCIENCES	PP	100	40	57	P C	15.	DATA COMMUNICATION	PP	100	40	66	Р
06.	DIGITAL LABORATORY	TW	50	20	39	P C	16.	PROCESSOR INTERFACING LABORATORY	TW	25	10	23	Р
07.	DIGITAL LABORATORY	PR	50	20	35	P C	17.	PROCESSOR INTERFACING LABORATORY	OR	50	20	25	Р
08.	PROGRAMMING LABORATORY	TW	50	20	42	P C	18.	DATA STRUCTURES AND FILES LAB	TW	25	10	21	Р
09.	PROGRAMMING LABORATORY	PR	50	20	44	P C	19.	DATA STRUCTURES AND FILES LAB	PR	50	20	38	Р
10.	COMMUNICATION AND LANGUAGE LAB.	TW	50	20	37	P C	20.	OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	48	Р
							21.	OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	25	Р

GRAND TOTAL = 959/1500, RESULT: FIRST CLASS
ORDN. 1 MARKS :

UNIVERSITY OF	PUNE	 ,S.E.(2008	 PAT.)(INF	ORMATION	rechnology)					
DATE : 18 AUG. 2011	CENT	RE : P	UNE II	NSTI	TUTE (OF COMPUTE	R TECHNOLOGY, PUNE.	PA	GE NO.	44	(4	66)
NOTE: FIRST LINE : SEAT NO., NAME O	F THE	CANDI	DATE,	MO	THER,	PERMANENT	REG. NO., PREVIOUS SEAT NO.,	COLLE	GE,	SEAT N	١٥.	
OTHER LINES: HEAD OF PASSING,	MAX.	MARKS	, MII	N. P.	ASS MA	ARKS, MAR	KS OBTAINED, P/F:PASS/FAIL,	C:PREVI	OUS CA	RRY O	/ER	
S8058634 UPPOD BALAJI SAMBHAJI				SHO	ВНА		, 71045653M , S8058634	, PIC	Т	, s80)5863	4
01. DISCRETE STRUCTURES	PP	100	40	40	P C	11.	ENG MATHS III	PP	100	40	25	F
02. COMPUTER ORGANIZATION	PP	100	40	51	P C	12.	COMPUTER GRAPHICS	PP	100	40	40	Р
03. DIGITAL ELECTRONICS & LOGIC DESI	GPP	100	40	40	Р	13.	PROCESSOR ARCHITECTURE & INTE	R. PP	100	40	25	F
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	40	P C	14.	DATA STRUCTURES AND FILES	PP	100	40	43	Р
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	45	P C	15.	DATA COMMUNICATION	PP	100	40	47	Р
06. DIGITAL LABORATORY	TW	50	20	35	P C	16.	PROCESSOR INTERFACING LABORATE	ORY TW	25	10	13	Р
07. DIGITAL LABORATORY	PR	50	20	23	P C	17.	PROCESSOR INTERFACING LABORATE	ORY OR	50	20	28	Р
08. PROGRAMMING LABORATORY	TW	50	20	34	P C	18.	DATA STRUCTURES AND FILES LAB Page 259	TW	25	10	13	Р

09. PROGRAMMING LABORATORY	PR	50	20	20	Р	19.	DATA STRUCTURES AND FILES LAB	B PR	50	20	12	F
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	38	P C	20.	OBJECT ORIENTED PROGRAMMING L	AB TW	50	20	37	Р
						21.	OBJECT ORIENTED PROGRAMMING L	AB PR	50	20	17	F
CRAND TOTAL - 666/1500 RESULT. FATLS	- A T	v T										
GRAND TOTAL = 666/1500, RESULT: FAILS ORDN. 1 MARKS:	5 A.I	.K.I.										
ORDN. I MARKS .												
				• •								
S8058635 VADASADAWALA SHABNAM WAHI	ED BHA	AI		FAR	IDA		, 71045655н , ѕ8058635	, PIC	Г	, s80	05863	5
01. DISCRETE STRUCTURES	PP	100	40	49	P C	11.	ENG MATHS III	PP	100	40	54	Р
02. COMPUTER ORGANIZATION	PP	100	40	57	P C	12.	COMPUTER GRAPHICS	PP	100	40	46	Р
03. DIGITAL ELECTRONICS & LOGIC DESI	EGPP	100	40	57	P C	13.	PROCESSOR ARCHITECTURE & INTE	ER. PP	100	40	49	Р
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	52	P C	14.	DATA STRUCTURES AND FILES	PP	100	40	52	Р
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	52	P C	15.	DATA COMMUNICATION	PP	100	40	53	Р
06. DIGITAL LABORATORY	TW	50	20	37	P C	16.	PROCESSOR INTERFACING LABORAT	ORY TW	25	10	21	Р
07. DIGITAL LABORATORY	PR	50	20	38	P C	17.	PROCESSOR INTERFACING LABORAT	ORY OR	50	20	35	Р
08. PROGRAMMING LABORATORY	TW	50	20	41	P C	18.	DATA STRUCTURES AND FILES LAE	3 TW	25	10	19	Р
09. PROGRAMMING LABORATORY	PR	50	20	40	P C	19.	DATA STRUCTURES AND FILES LAE	B PR	50	20	26	Р
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	44	P C	20.	OBJECT ORIENTED PROGRAMMING L	AB TW	50	20	43	Р
						21.	OBJECT ORIENTED PROGRAMMING L	AB PR	50	20	30	Р
GRAND TOTAL = 895+05/1500, RESULT: F1	rpst (دا ۱۹۶۸	[n 2]									
ORDN. 1 MARKS :	LIKST (CLASS	[0.2]									
ORDIN. I MARKS .												
S8058636 VARSHA SINHA				SEE	MA SINHA	4	, 71058776н , S8058636	, PIC	Γ	, S80	05863	6
01. DISCRETE STRUCTURES	PP	100	40	46	P C	11.	ENG MATHS III	PP	100	40	68	Р
02. COMPUTER ORGANIZATION	PP	100	40	62	P C	12.	COMPUTER GRAPHICS	PP	100	40	63	Р
03. DIGITAL ELECTRONICS & LOGIC DESI	[GPP	100	40	70	P C	13.	PROCESSOR ARCHITECTURE & INTE	R. PP	100	40	48	Р
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	74	P C	14.	DATA STRUCTURES AND FILES	PP	100	40	64	Р

								co1s05					
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	60	Р (С	15.	DATA COMMUNICATION	PP	100	40	75	Р
06. DIGITAL LABORATORY	TW	50	20	41	Р (С	16.	PROCESSOR INTERFACING LABORATORY	TW	25	10	22	Р
07. DIGITAL LABORATORY	PR	50	20	38	Р (С	17.	PROCESSOR INTERFACING LABORATORY	OR	50	20	40	Р
08. PROGRAMMING LABORATORY	TW	50	20	45	Р (С	18.	DATA STRUCTURES AND FILES LAB	TW	25	10	20	Р
09. PROGRAMMING LABORATORY	PR	50	20	39	Р (С	19.	DATA STRUCTURES AND FILES LAB	PR	50	20	38	Р
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	42	Р (С	20.	OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	48	Р
							21.	OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	34	Р
GRAND TOTAL = 1037/1500, RESULT: FIRST	CLAS	S WITH	I DIST	INCT	ION								
ORDN. 1 MARKS :													
UNIVERSITY OF		 S F .		 ΡΔΤ	 Эст	 NEORMATI	 ON T	CECHNOLOGY)					
DATE : 18 AUG. 2011								R TECHNOLOGY, PUNE.	PΔG	E NO.	45	(4	67)
5/1/2 1 10 /1001 2011	CZITI		0.1.2			_ 0, 00,			.,		.5		· ,
NOTE: FIRST LINE : SEAT NO., NAME O	 F THE	CANDT	DATE.	 MO	 THFI	R. PFRMA	 NFNT	REG. NO PREVIOUS SEAT NO C	 OLLEG	 F S	 FAT N	 IO.	
								KS OBTAINED, P/F:PASS/FAIL, C:P					
S8058637 VISHWAS JAIN				GEE	TA			, 71045658B , S8058637 ,	PICT	•	, s80	5863	7
01. DISCRETE STRUCTURES	PP	100	40	60	Р (С	11.	ENG MATHS III	PP	100	40	75	Р
02. COMPUTER ORGANIZATION	PP	100	40	58	Р (С	12.	COMPUTER GRAPHICS	PP	100	40	49	Р
03. DIGITAL ELECTRONICS & LOGIC DESI	GPP	100	40	69	P	С	13.	PROCESSOR ARCHITECTURE & INTER.	PP	100	40	55	Р
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	70	P	С	14.	DATA STRUCTURES AND FILES	PP	100	40	66	Р
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	53	Р (С	15.	DATA COMMUNICATION	PP	100	40	58	Р
06. DIGITAL LABORATORY	TW	50	20	41	Р (С	16.	PROCESSOR INTERFACING LABORATORY	TW	25	10	20	Р
07. DIGITAL LABORATORY	PR	50	20	36	Р (С	17.	PROCESSOR INTERFACING LABORATORY	OR	50	20	42	Р
08. PROGRAMMING LABORATORY	TW	50	20	44	Р (С	18.	DATA STRUCTURES AND FILES LAB	TW	25	10	17	Р
09. PROGRAMMING LABORATORY	PR	50	20	44	Р (С		DATA CIRLICIURES AND ELLES LAR	PR	50	20	28	Р
40							19.	DATA STRUCTURES AND FILES LAB					
10. COMMUNICATION AND LANGUAGE LAB.		50	20	44	Р (С		OBJECT ORIENTED PROGRAMMING LAB		50	20	42	Р

GRAND TOTAL = 1005/1500, RESULT: FIRST CLASS WITH DISTINCTION

S8058638 VIVEK GIRIDHAR KANNAKE				PUS	НРА		, 71045659L	, s8058638 ,	PICT		, s80	5863	8
01. DISCRETE STRUCTURES	PP	100	40	40	РС	11.	ENG MATHS III		PP	100	40	23	F
02. COMPUTER ORGANIZATION	PP	100	40	43	P C	12.	COMPUTER GRAPHICS	S	PP	100	40	43	Р
03. DIGITAL ELECTRONICS & LOGIC DES	IGPP	100	40	40	P C	13.	PROCESSOR ARCHIT	ECTURE & INTER.	PP	100	40	40	Р
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	40	Р	14.	DATA STRUCTURES	AND FILES	PP	100	40	63	Р
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	48	P C	15.	DATA COMMUNICATION	ON	PP	100	40	47	Р
06. DIGITAL LABORATORY	TW	50	20	20	P C	16.	PROCESSOR INTERFA	ACING LABORATORY	TW	25	10	15	Р
07. DIGITAL LABORATORY	PR	50	20	21	Р	17.	PROCESSOR INTERFA	ACING LABORATORY	OR	50	20	28	Р
08. PROGRAMMING LABORATORY	TW	50	20	35	P C	18.	DATA STRUCTURES	AND FILES LAB	TW	25	10	14	Р
09. PROGRAMMING LABORATORY	PR	50	20	23	P C	19.	DATA STRUCTURES	AND FILES LAB	PR	50	20	22	Р
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	35	P C	20.	OBJECT ORIENTED	PROGRAMMING LAB	TW	50	20	38	Р
						21.	OBJECT ORIENTED	PROGRAMMING LAB	PR	50	20	38	Р

GRAND TOTAL = 716/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS:

S8058639 VIVERT JAIN				NID	HI		, 71045660D	, s8058639 ,	PICT		, \$80	58639	9
01. DISCRETE STRUCTURES	PP	100	40	64	РС	11.	ENG MATHS III		PP	100	40	53	Р
02. COMPUTER ORGANIZATION	PP	100	40	43	P C	12.	COMPUTER GRAPHICS	5	PP	100	40	42	Р
03. DIGITAL ELECTRONICS & LOGIC DES	EGPP	100	40	46	P C	13.	PROCESSOR ARCHITE	ECTURE & INTER.	PP	100	40	48	Р
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	52	P C	14.	DATA STRUCTURES A	AND FILES	PP	100	40	62	Р
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	47	P C	15.	DATA COMMUNICATIO	ON	PP	100	40	57	Р
06. DIGITAL LABORATORY	TW	50	20	42	P C	16.	PROCESSOR INTERFA	ACING LABORATORY	TW	25	10	20	Р
07. DIGITAL LABORATORY	PR	50	20	37	P C	17.	PROCESSOR INTERFA	ACING LABORATORY	OR	50	20	33	Р
08. PROGRAMMING LABORATORY	TW	50	20	42	P C	18.	DATA STRUCTURES A	AND FILES LAB	TW	25	10	18	Р
09. PROGRAMMING LABORATORY	PR	50	20	32	P C	19.	DATA STRUCTURES	AND FILES LAB	PR	50	20	14	F

10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	42	P C			cols05 ED PROGRAMMING LAB		50	20	44	Р
GRAND TOTAL = 874/1500, RESULT: FAILS	5 A T	νт				21.	OBJECT ORIENTE	ED PROGRAMMING LAB	PR	50	20	36	Р
ORDN. 1 MARKS:	3 A.I.	. K. I .											
UNIVERSITY OF	 PUNE	 .S.E.		 PAT	 .)(INF	ORMATION	· · · · · · · · · · · · · · · · · · ·						
DATE : 18 AUG. 2011							R TECHNOLOGY, F	PUNE.	PAG	GE NO.	46	(4	68)
NOTE: FIRST LINE : SEAT NO., NAME (OF THE	E CAND	IDATE	, MC	THER,	PERMANENT	REG. NO., PRE	EVIOUS SEAT NO., (COLLEC	GΕ,	SEAT N	10.	
OTHER LINES: HEAD OF PASSING,	MAX	. MARK	S, MI	IN. P	ASS MA	ARKS, MAR	KS OBTAINED, F	P/F:PASS/FAIL, C:F	PREVIO	OUS CA	RRY O	/ER	
S8058640 WAGHMODE SACHIN DADASO				SHC	ВНА		, 71045663〕	, s8058640 ,	PIC	Г	, s80)5864	0
01. DISCRETE STRUCTURES	PP	100	40	48	P C	11.	ENG MATHS III		PP	100	40	46	Р
02. COMPUTER ORGANIZATION	PP	100	40	44	P C	12.	COMPUTER GRAPH	HICS	PP	100	40	47	Р
03. DIGITAL ELECTRONICS & LOGIC DES	IGPP	100	40	51	P C	13.	PROCESSOR ARCH	HITECTURE & INTER.	PP	100	40	40	Р
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	43	P C	14.	DATA STRUCTURE	ES AND FILES	PP	100	40	58	Р
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	43	P C	15.	DATA COMMUNICA	ATION	PP	100	40	58	Р
06. DIGITAL LABORATORY	TW	50	20	28	P C	16.	PROCESSOR INTE	ERFACING LABORATORY	/ TW	25	10	17	Р
07. DIGITAL LABORATORY	PR	50	20	27	P C	17.	PROCESSOR INTE	ERFACING LABORATORY	OR	50	20	25	Р
08. PROGRAMMING LABORATORY	TW	50	20	34	P C	18.	DATA STRUCTURE	ES AND FILES LAB	TW	25	10	15	Р
09. PROGRAMMING LABORATORY	PR	50	20	35	P C	19.	DATA STRUCTURE	S AND FILES LAB	PR	50	20	33	Р
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	32	P C	20.	OBJECT ORIENTE	ED PROGRAMMING LAB	TW	50	20	38	Р
						21.	OBJECT ORIENTE	ED PROGRAMMING LAB	PR	50	20	35	Р
GRAND TOTAL = 797/1500, RESULT: SECON	ND CLA	ASS											
ORDN. 1 MARKS :													
S8058641 WAYKOLE VRUSHALI PRAVIN				ARU	INA		, 71045666C	, s8058641 ,	PIC	Γ	, s80)5864	1

						cols05	
01. DISCRETE STRUCTURES	PP	100	40	63	P C		7 P
02. COMPUTER ORGANIZATION	PP	100	40	50	P C	12. COMPUTER GRAPHICS PP 100 40 48	3 P
03. DIGITAL ELECTRONICS & LOGIC DESI	IGPP	100	40	56	P C	13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 40	6 P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	64	P C	14. DATA STRUCTURES AND FILES PP 100 40 64	4 P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	45	P C	15. DATA COMMUNICATION PP 100 40 40	6 P
06. DIGITAL LABORATORY	TW	50	20	40	P C	16. PROCESSOR INTERFACING LABORATORY TW 25 10 19	9 P
07. DIGITAL LABORATORY	PR	50	20	39	P C	17. PROCESSOR INTERFACING LABORATORY OR 50 20 38	3 P
08. PROGRAMMING LABORATORY	TW	50	20	40	P C	18. DATA STRUCTURES AND FILES LAB TW 25 10 20) Р
09. PROGRAMMING LABORATORY	PR	50	20	44	P C	19. DATA STRUCTURES AND FILES LAB PR 50 20 40	6 P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	40	P C	20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 44	4 P
						21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 40) Р

GRAND TOTAL = 949/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS:

S805	58642 YADAV SNEHA HANAMANT				NANI	DA		, 71134987յ	, s8058642 ,	PICT		, s80	58642	2
01.	DISCRETE STRUCTURES	PP	100	40	45	P C	11.	ENG MATHS III		PP	100	40	59	Р
02.	COMPUTER ORGANIZATION	PP	100	40	49	P C	12.	COMPUTER GRAPHIC	S	PP	100	40	44	Р
03.	DIGITAL ELECTRONICS & LOGIC DESI	GPP	100	40	65	P C	13.	PROCESSOR ARCHIT	ECTURE & INTER.	PP	100	40	60	Р
04.	FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	65	P C	14.	DATA STRUCTURES	AND FILES	PP	100	40	77	Р
05.	HUMANITIES AND SOCIAL SCIENCES	PP	100	40	50	P C	15.	DATA COMMUNICATION	ON	PP	100	40	59	Р
06.	DIGITAL LABORATORY	TW	50	20	39	P C	16.	PROCESSOR INTERF	ACING LABORATORY	TW	25	10	24	Р
07.	DIGITAL LABORATORY	PR	50	20	29	P C	17.	PROCESSOR INTERF	ACING LABORATORY	OR	50	20	39	Р
08.	PROGRAMMING LABORATORY	TW	50	20	43	P C	18.	DATA STRUCTURES	AND FILES LAB	TW	25	10	22	Р
09.	PROGRAMMING LABORATORY	PR	50	20	40	P C	19.	DATA STRUCTURES	AND FILES LAB	PR	50	20	46	Р
10.	COMMUNICATION AND LANGUAGE LAB.	TW	50	20	38	P C	20.	OBJECT ORIENTED	PROGRAMMING LAB	TW	50	20	47	Р
							21.	OBJECT ORIENTED	PROGRAMMING LAB	PR	50	20	32	Р

GRAND TOTAL = 972/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS:

DATE : 18 AUG. 2011	CENT	RE : I	PUNE :	INSTI	TUTE OF	COMPUTE	R TECHNOLOGY, PUNE.	PAG	GE NO.	47	(4	169)
NOTE: FIRST LINE : SEAT NO., NAME OF	THE	CAND:	IDATE	, MO	THER, PE	ERMANENT	REG. NO., PREVIOUS SEAT NO., KS OBTAINED, P/F:PASS/FAIL, C:	COLLEC	ΞE,	SEAT 1	NO.	
S8058643 ZAJRIYA AASHAY MAHESH					AYA		, 71045670M , S8058643 ,	PICT		 , s80		
01. DISCRETE STRUCTURES	PP	100	40	65	P C	11.	ENG MATHS III	PP	100	40	49	Р
02. COMPUTER ORGANIZATION	PP	100	40	40	P C	12.	COMPUTER GRAPHICS	PP	100	40	46	Р
03. DIGITAL ELECTRONICS & LOGIC DESIG	SPP	100	40	72	P C	13.	PROCESSOR ARCHITECTURE & INTER.	PP	100	40	49	Р
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	52	P C	14.	DATA STRUCTURES AND FILES	PP	100	40	69	Р
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	53	P C	15.	DATA COMMUNICATION	PP	100	40	50	Р
06. DIGITAL LABORATORY	TW	50	20	40	P C	16.	PROCESSOR INTERFACING LABORATOR	Y TW	25	10	21	Р
07. DIGITAL LABORATORY	PR	50	20	36	P C	17.	PROCESSOR INTERFACING LABORATOR	Y OR	50	20	38	Р
08. PROGRAMMING LABORATORY	TW	50	20	39	P C	18.	DATA STRUCTURES AND FILES LAB	TW	25	10	18	Р
09. PROGRAMMING LABORATORY	PR	50	20	32	P C	19.	DATA STRUCTURES AND FILES LAB	PR	50	20	43	Р
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	32	P C	20.	OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	38	Р
						21.	OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	40	Р
AND TOTAL = 922/1500, RESULT: FIRST DN. 1 MARKS:												
S8058644 ABNAVE NRUPEN PRAKASH				HEM	ILATA		, 70925316J , S8058644 ,	PIC	г	, s80	05864	14
01. DISCRETE STRUCTURES	PP	100	40	40	P C	11.	ENG MATHS III	PP	100	40	40	Р
02. COMPUTER ORGANIZATION	PP	100	40	40	P C	12.	COMPUTER GRAPHICS	PP	100	40	40	Р
03. DIGITAL ELECTRONICS & LOGIC DESIG	SPP	100	40	46	P C	13.	PROCESSOR ARCHITECTURE & INTER.	PP	100	40	40	Р
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	52	РС	14.	DATA STRUCTURES AND FILES	PP	100	40	48	Р
OT. I UNDAMENTAL OF DATA STRUCTURES												

							cols05		
06.	DIGITAL LABORATORY	TW	50	20	23	P C	. PROCESSOR INTERFACING LABORATORY TW 25 10	10	PC
07.	DIGITAL LABORATORY	PR	50	20	22	P C	. PROCESSOR INTERFACING LABORATORY OR 50 20	20	P C
08.	PROGRAMMING LABORATORY	TW	50	20	24	P C	. DATA STRUCTURES AND FILES LAB TW 25 10	10	P C
09.	PROGRAMMING LABORATORY	PR	50	20	32	P C	. DATA STRUCTURES AND FILES LAB PR 50 20	33	Р
10.	COMMUNICATION AND LANGUAGE LAB.	TW	50	20	25	P C	. OBJECT ORIENTED PROGRAMMING LAB TW 50 20	20	P C
							. OBJECT ORIENTED PROGRAMMING LAB PR 50 20	20	РС

GRAND TOTAL = 665/1500, RESULT: PASS CLASS

ORDN. 1 MARKS :

S8058647 ANIKET PALLEWAD				NEE	LAWATI	, 70925330D , S8058647 , PICT , S805	8647
01. DISCRETE STRUCTURES	PP	100	40	41	P C	1. ENG MATHS III PP 100 40	40 P
02. COMPUTER ORGANIZATION	PP	100	40	51	P C	2. COMPUTER GRAPHICS PP 100 40	47 P C
03. DIGITAL ELECTRONICS & LOGIC DESI	EGPP	100	40	40	P C	3. PROCESSOR ARCHITECTURE & INTER. PP 100 40	40 P C
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	43	P C	4. DATA STRUCTURES AND FILES PP 100 40	46 P C
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	51	P C	5. DATA COMMUNICATION PP 100 40	40 P C
06. DIGITAL LABORATORY	TW	50	20	32	P C	6. PROCESSOR INTERFACING LABORATORY TW 25 10	10 P C
07. DIGITAL LABORATORY	PR	50	20	23	P C	7. PROCESSOR INTERFACING LABORATORY OR 50 20	22 P C
08. PROGRAMMING LABORATORY	TW	50	20	35	P C	8. DATA STRUCTURES AND FILES LAB TW 25 10	13 P C
09. PROGRAMMING LABORATORY	PR	50	20	30	P C	9. DATA STRUCTURES AND FILES LAB PR 50 20	20 P C
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	20	P C	O. OBJECT ORIENTED PROGRAMMING LAB TW 50 20	25 P C
						1. OBJECT ORIENTED PROGRAMMING LAB PR 50 20	22 P C

GRAND TOTAL = 691/1500, RESULT: PASS CLASS

ORDN. 1 MARKS :

UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(INFORMATION TECHNOLOGY)

DATE: 18 AUG. 2011 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 48 (470)

NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

				cols05	
OTHER LINES: HEAD OF PASSING,	MAX. MARKS,	MIN. PASS MARKS,	MARKS OBTAINED,	P/F:PASS/FAIL,	C:PREVIOUS CARRY OVER

S8058650 AVASARE ARUN RAKHAMAJI				PADI	MINI		, 70925345в	, s8058650 ,	PICT		, s80	5865(0
01. DISCRETE STRUCTURES	PP	100	40	40	P C	11	ENG MATHS III		PP	100	40	40	Р
02. COMPUTER ORGANIZATION	PP	100	40	50	РС	12	COMPUTER GRAPHIC	S	PP	100	40	42	P C
03. DIGITAL ELECTRONICS & LOGIC DESIG	GPP	100	40	45	РС	13	PROCESSOR ARCHIT	ECTURE & INTER.	PP	100	40	21	F
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	43	P C	14	DATA STRUCTURES	AND FILES	PP	100	40	45	P C
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	40	P C	15	DATA COMMUNICATI	ON	PP	100	40	40	P C
06. DIGITAL LABORATORY	TW	50	20	27	P C	16	PROCESSOR INTERF	ACING LABORATORY	TW	25	10	14	P C
07. DIGITAL LABORATORY	PR	50	20	31	РС	17	PROCESSOR INTERF	ACING LABORATORY	OR	50	20	35	P C
08. PROGRAMMING LABORATORY	TW	50	20	40	РС	18	DATA STRUCTURES	AND FILES LAB	TW	25	10	19	P C
09. PROGRAMMING LABORATORY	PR	50	20	33	P C	19	DATA STRUCTURES	AND FILES LAB	PR	50	20	30	P C
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	38	P C	20	OBJECT ORIENTED	PROGRAMMING LAB	TW	50	20	38	P C
						21	OBJECT ORIENTED	PROGRAMMING LAB	PR	50	20	32	P C

GRAND TOTAL = 743/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS:

S8058652 BACHHAV HARSHAL RAGHUNATH	1			VIM	AL		, 70925349Е	, s8058652 ,	PICT		, s80	5865	2
01. DISCRETE STRUCTURES	PP	100	40	AA	F	11.	ENG MATHS III		PP	100	40	AA	F
02. COMPUTER ORGANIZATION	PP	100	40	14	F	12.	COMPUTER GRAPHIC	S	PP	100	40	AA	F
03. DIGITAL ELECTRONICS & LOGIC DESI	[GPP	100	40	AA	F	13.	PROCESSOR ARCHIT	ECTURE & INTER.	PP	100	40	AA	F
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	80	F	14.	DATA STRUCTURES	AND FILES	PP	100	40	AA	F
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	40	P C	15.	DATA COMMUNICATI	ON	PP	100	40	AA	F
06. DIGITAL LABORATORY	TW	50	20	25	P C	16.	PROCESSOR INTERF	ACING LABORATORY	TW	25	10	10	P C
07. DIGITAL LABORATORY	PR	50	20	AA	F	17.	PROCESSOR INTERF	ACING LABORATORY	OR	50	20	AA	F
08. PROGRAMMING LABORATORY	TW	50	20	32	P C	18.	DATA STRUCTURES	AND FILES LAB	TW	25	10	10	P C
09. PROGRAMMING LABORATORY	PR	50	20	AA	F	19.	DATA STRUCTURES	AND FILES LAB	PR	50	20	AA	F
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	25	PC	20.	OBJECT ORIENTED	PROGRAMMING LAB	TW	50	20	20	P C

cols05 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 AA F

GRAND TOTAL = 184/1500, RESULT: FAILS

ORDN. 1 MARKS:

S8058653 BERDE SIDDESH VISHWANATH				VAIS	SHALI		, 71073822G	, \$8058653 ,	PICT		, s80	5865	3
01. DISCRETE STRUCTURES	PP	100	40	40	P C	11.	ENG MATHS III		PP	100	40	AA	F
02. COMPUTER ORGANIZATION	PP	100	40	51	P C	12.	COMPUTER GRAPHIC	CS .	PP	100	40	56	P C
03. DIGITAL ELECTRONICS & LOGIC DESIGNATION	GPP	100	40	59	P C	13.	PROCESSOR ARCHIT	ECTURE & INTER.	PP	100	40	40	P C
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	50	P C	14.	DATA STRUCTURES	AND FILES	PP	100	40	55	P C
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	51	P C	15.	DATA COMMUNICATI	ON	PP	100	40	52	P C
06. DIGITAL LABORATORY	TW	50	20	34	P C	16.	PROCESSOR INTERF	ACING LABORATORY	TW	25	10	18	P C
07. DIGITAL LABORATORY	PR	50	20	30	P C	17.	PROCESSOR INTERF	ACING LABORATORY	OR	50	20	31	P C
08. PROGRAMMING LABORATORY	TW	50	20	39	РС	18.	DATA STRUCTURES	AND FILES LAB	TW	25	10	15	P C
09. PROGRAMMING LABORATORY	PR	50	20	30	РС	19.	DATA STRUCTURES	AND FILES LAB	PR	50	20	36	P C
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	38	P C	20.	OBJECT ORIENTED	PROGRAMMING LAB	TW	50	20	36	P C
						21.	OBJECT ORIENTED	PROGRAMMING LAB	PR	50	20	32	РС

GRAND TOTAL = 793/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS :

UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(INFORMATION TECHNOLOGY)

DATE: 18 AUG. 2011 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 49 (471)

DATE . 10 AUG. 2011 CENTRE . FUNE INSTITUTE OF COMPUTER TECHNOLOGY, FUNE. FAGE NO. 45 (471)

NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

\$8058654 BHALERAO MANGESH GANESH NANDA , 70925361D , \$8058654 , PICT , \$8058654

01. DISCRETE STRUCTURES PP 100 40 52 P C 11. ENG MATHS III PP 100 40 44 P C

								cols05					
02.	COMPUTER ORGANIZATION	PP	100	40	40	P C	12.	COMPUTER GRAPHICS	PP	100	40	51	P C
03.	DIGITAL ELECTRONICS & LOGIC DESI	GPP	100	40	45	P C	13.	PROCESSOR ARCHITECTURE & INTER.	PP	100	40	05	F
04.	FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	40	P C	14.	DATA STRUCTURES AND FILES	PP	100	40	63	P C
05.	HUMANITIES AND SOCIAL SCIENCES	PP	100	40	44	P C	15.	DATA COMMUNICATION	PP	100	40	40	P C
06.	DIGITAL LABORATORY	TW	50	20	33	P C	16.	PROCESSOR INTERFACING LABORATORY	TW	25	10	14	P C
07.	DIGITAL LABORATORY	PR	50	20	22	P C	17.	PROCESSOR INTERFACING LABORATORY	OR	50	20	22	P C
08.	PROGRAMMING LABORATORY	TW	50	20	32	РС	18.	DATA STRUCTURES AND FILES LAB	TW	25	10	13	P C
09.	PROGRAMMING LABORATORY	PR	50	20	30	РС	19.	DATA STRUCTURES AND FILES LAB	PR	50	20	26	Р
10.	COMMUNICATION AND LANGUAGE LAB.	TW	50	20	36	P C	20.	OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	25	P C
							21.	OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	32	РС

GRAND TOTAL = 709/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS:

S	8058655 BHARADIYA MAYURI SATISHCH	IANDRA			ANIT	ΓΑ		, 71073843к	, S8058655 ,	PICT		, s80!	5865	5
C	1. DISCRETE STRUCTURES	PP	100	40	45	P C	11.	ENG MATHS III		PP	100	40	40	Р
C	2. COMPUTER ORGANIZATION	PP	100	40	43	P C	12.	COMPUTER GRAPHICS	5	PP	100	40	43	P C
C	3. DIGITAL ELECTRONICS & LOGIC DESI	GPP	100	40	58	P C	13.	PROCESSOR ARCHITE	ECTURE & INTER.	PP	100	40	49	P C
C	4. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	54	P C	14.	DATA STRUCTURES A	AND FILES	PP	100	40	68	P C
C	5. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	53	P C	15.	DATA COMMUNICATIO	ON	PP	100	40	44	P C
C	6. DIGITAL LABORATORY	TW	50	20	38	P C	16.	PROCESSOR INTERFA	ACING LABORATORY	TW	25	10	18	P C
C	7. DIGITAL LABORATORY	PR	50	20	37	P C	17.	PROCESSOR INTERFA	ACING LABORATORY	OR	50	20	36	РС
C	8. PROGRAMMING LABORATORY	TW	50	20	35	P C	18.	DATA STRUCTURES A	AND FILES LAB	TW	25	10	18	РС
C	9. PROGRAMMING LABORATORY	PR	50	20	25	P C	19.	DATA STRUCTURES A	AND FILES LAB	PR	50	20	39	P C
1	0. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	35	P C	20.	OBJECT ORIENTED F	PROGRAMMING LAB	TW	50	20	38	P C
							21.	OBJECT ORIENTED F	PROGRAMMING LAB	PR	50	20	36	РС

GRAND TOTAL = 852/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS:

S8058657 DEORE NIKHIL BAPUSAHEB				MANI	DAKINI		, 70801397L , S8058657 ,	PICT		, s80	5865	7
01. DISCRETE STRUCTURES	PP	100	40	53	P C	11.	ENG MATHS III	PP	100	40	40	Р
02. COMPUTER ORGANIZATION	PP	100	40	41	P C	12.	COMPUTER GRAPHICS	PP	100	40	49	PС
03. DIGITAL ELECTRONICS & LOGIC DESI	GPP	100	40	40	P C	13.	PROCESSOR ARCHITECTURE & INTER.	PP	100	40	40	Р
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	40	P C	14.	DATA STRUCTURES AND FILES	PP	100	40	58	P C
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	40	P C	15.	DATA COMMUNICATION	PP	100	40	48	Р
06. DIGITAL LABORATORY	TW	50	20	25	P C	16.	PROCESSOR INTERFACING LABORATORY	TW	25	10	10	P C
07. DIGITAL LABORATORY	PR	50	20	21	P C	17.	PROCESSOR INTERFACING LABORATORY	OR	50	20	04	F
08. PROGRAMMING LABORATORY	TW	50	20	33	P C	18.	DATA STRUCTURES AND FILES LAB	TW	25	10	10	P C
09. PROGRAMMING LABORATORY	PR	50	20	28	P C	19.	DATA STRUCTURES AND FILES LAB	PR	50	20	08	F
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	32	P C	20.	OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	20	P C
						21.	OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	25	Р
UNIVERSITY OF DATE : 18 AUG. 2011							ECHNOLOGY, PUNE.	 PAG	 E NO.	50		72)
DATE : 18 AUG. 2011	CENTI	RE : P	UNE II	NSTI	FUTE OF COM	PUTEF	R TECHNOLOGY, PUNE.					 72)
DATE: 18 AUG. 2011	CENTI F THE	RE : PI	UNE I	NSTI ⁻	TUTE OF COM	PUTEF	R TECHNOLOGY, PUNE.	 OLLEG	 E, S	 EAT N	 O.	 72)
DATE: 18 AUG. 2011	CENTI F THE	RE : PI	UNE I	NSTI ⁻	TUTE OF COM	PUTEF	R TECHNOLOGY, PUNE	 OLLEG	 E, S	 EAT N	 O.	 72)
DATE: 18 AUG. 2011	CENTI F THE	RE : PI	UNE I	NSTI ⁻	TUTE OF COMI	PUTEF	R TECHNOLOGY, PUNE	 OLLEG	 E, S US CAR	 EAT N	 O. ER	
DATE: 18 AUG. 2011 NOTE: FIRST LINE: SEAT NO., NAME OF OTHER LINES: HEAD OF PASSING,	CENTI F THE	RE : PI	UNE I	MOTAL ANAC	TUTE OF COMI	PUTER NENT MARK	R TECHNOLOGY, PUNE. REG. NO., PREVIOUS SEAT NO., CO SOBTAINED, P/F:PASS/FAIL, C:PO	· · · OLLEG REVIO	 E, S US CAR	 EAT N RY OV 	 O. ER	
DATE: 18 AUG. 2011 NOTE: FIRST LINE: SEAT NO., NAME OF OTHER LINES: HEAD OF PASSING, S8058658 DESHPANDE AJINKYA AJIT	CENTI THE MAX.	RE : PI CANDII MARKS	UNE II DATE, , MII	MOTAL ANAC	TUTE OF COMPLETE O	PUTER NENT MARK	R TECHNOLOGY, PUNE. REG. NO., PREVIOUS SEAT NO., CONTAINED, P/F:PASS/FAIL, C:PONTAINED, 71073825M, S8058658,	OLLEGREVIO	 E, S US CAR	EAT N RY OV 	O. ER 58658	
DATE: 18 AUG. 2011 NOTE: FIRST LINE: SEAT NO., NAME OF OTHER LINES: HEAD OF PASSING, S8058658 DESHPANDE AJINKYA AJIT 01. DISCRETE STRUCTURES	CENTION OF THE MAX. PP PP	RE : PI CANDII MARKS	UNE IN DATE, , MIN	MOTON. PA	TUTE OF COMI	PUTER NENT MARK	R TECHNOLOGY, PUNE. REG. NO., PREVIOUS SEAT NO., CONTAINED, P/F:PASS/FAIL, C:PONTAINED, 71073825M, S8058658, ENG MATHS III	OLLEG REVIO PICT	E, S US CAR 	EAT N RY OV , S80	O. ER 58658 16 46	 8 F
DATE: 18 AUG. 2011 NOTE: FIRST LINE: SEAT NO., NAME OF OTHER LINES: HEAD OF PASSING, S8058658 DESHPANDE AJINKYA AJIT O1. DISCRETE STRUCTURES O2. COMPUTER ORGANIZATION	CENTION OF THE MAX. PP PP	RE : PI CANDII MARKS	UNE II DATE, MII	MOTANA (40) 52 49	TUTE OF COMPLETE O	PUTER NENT MARK	R TECHNOLOGY, PUNE. REG. NO., PREVIOUS SEAT NO., CO (S OBTAINED, P/F:PASS/FAIL, C:PO , 71073825M , S8058658 , ENG MATHS III COMPUTER GRAPHICS	OLLEG REVIO PICT PP PP		EAT N RY OV , S80 40 40	O. ER	 8 F P C
DATE: 18 AUG. 2011 NOTE: FIRST LINE: SEAT NO., NAME OF OTHER LINES: HEAD OF PASSING, S8058658 DESHPANDE AJINKYA AJIT O1. DISCRETE STRUCTURES O2. COMPUTER ORGANIZATION O3. DIGITAL ELECTRONICS & LOGIC DESI	CENTION OF THE MAX. PP PP PP	RE: PI CANDII MARKS 100 100 100	UNE II DATE, , MII 40 40 40	NSTITUTE NOT	TUTE OF COMI	PUTER NENT MARK 11. 12. 13. 14.	R TECHNOLOGY, PUNE. REG. NO., PREVIOUS SEAT NO., CONTROL OF PROCESSOR ARCHITECTURE & INTER.	OLLEG REVIO PICT PP PP		EAT N RY OV , S80 40 40 40	0. ER 58658 16 46 55 54	 8 F P C

										1-05					
07. DIGITAL LABORATORY	PR	50	20	30	Р (2	17.	PROCESSOR I		cols05 ACING LABORATORY	OR	50	20	42	РС
08. PROGRAMMING LABORATORY	TW	50	20	37	Р (2	18.	DATA STRUCT	TURES	AND FILES LAB	TW	25	10	20	P C
09. PROGRAMMING LABORATORY	PR	50	20	32	Р (2	19.	DATA STRUCT	TURES	AND FILES LAB	PR	50	20	45	РС
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	35	Р (2	20.	OBJECT ORIE	ENTED	PROGRAMMING LAB	TW	50	20	41	РС
							21.	OBJECT ORIE	ENTED	PROGRAMMING LAB	PR	50	20	40	РС

GRAND TOTAL = 827/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS:

S8058659 GATHE KAPILCHAND NAMDEORA	.0			SUNA	ANDA		, 70925424F	, s8058659 ,	PICT		, s80	58659	9
01. DISCRETE STRUCTURES	PP	100	40	40	P C	11	. ENG MATHS III		PP	100	40	42	Р
02. COMPUTER ORGANIZATION	PP	100	40	45	P C	12	. COMPUTER GRAPHIC	CS	PP	100	40	45	P C
03. DIGITAL ELECTRONICS & LOGIC DESI	GPP	100	40	41	P C	13	. PROCESSOR ARCHIT	TECTURE & INTER.	PP	100	40	40	P C
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	47	P C	14	. DATA STRUCTURES	AND FILES	PP	100	40	47	P C
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	44	P C	15	. DATA COMMUNICATI	ION	PP	100	40	42	P C
06. DIGITAL LABORATORY	TW	50	20	35	P C	16	. PROCESSOR INTERF	FACING LABORATORY	TW	25	10	11	P C
07. DIGITAL LABORATORY	PR	50	20	20	P C	17	. PROCESSOR INTERF	FACING LABORATORY	OR	50	20	24	P C
08. PROGRAMMING LABORATORY	TW	50	20	37	P C	18	. DATA STRUCTURES	AND FILES LAB	TW	25	10	13	P C
09. PROGRAMMING LABORATORY	PR	50	20	26	P C	19	. DATA STRUCTURES	AND FILES LAB	PR	50	20	40	Р
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	42	P C	20	. OBJECT ORIENTED	PROGRAMMING LAB	TW	50	20	37	P C
						21	. OBJECT ORIENTED	PROGRAMMING LAB	PR	50	20	35	РС

GRAND TOTAL = 753/1500, RESULT: SECOND CLASS
ORDN. 1 MARKS :

S8058662 JADHAV DEEPANJAN MADHAV				ARUNA	, 70925445J , S8058662 ,	PICT	-	, s80	5866	2
01. DISCRETE STRUCTURES	PP	100	40	40 P C	11. ENG MATHS III	PP	100	40	20	F
02. COMPUTER ORGANIZATION	PP	100	40	55 P C	12. COMPUTER GRAPHICS	PP	100	40	53	P C
03. DIGITAL ELECTRONICS & LOGIC DESI	GPP	100	40	51 P C	13. PROCESSOR ARCHITECTURE & INTER. Page 271	PP	100	40	43	P C

PP 100 40 61 P C

14. DATA STRUCTURES AND FILES

05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	48	РС	15.	DATA COMMUNICATION	PP	100	40	47	P C
06. DIGITAL LABORATORY	TW	50	20	34	P C	16.	PROCESSOR INTERFACING LABORATORY	TW	25	10	11	РC
07. DIGITAL LABORATORY	PR	50	20	28	P C	17.	PROCESSOR INTERFACING LABORATORY	OR	50	20	21	РC
08. PROGRAMMING LABORATORY	TW	50	20	41	P C	18.	DATA STRUCTURES AND FILES LAB	TW	25	10	12	РC
09. PROGRAMMING LABORATORY	PR	50	20	20	P C	19.	DATA STRUCTURES AND FILES LAB	PR	50	20	AA	F
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	41	P C	20.	OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	34	РC
						21.	OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	AA	F
GRAND TOTAL = 706/1500, RESULT: FAILS ORDN. 1 MARKS :	6 A.T.	К.Т.										
UNIVERSITY OF	 PUNE	 ,S.E.	(2008	 PAT.		 ION T						
							R TECHNOLOGY, PUNE.	PAG	E NO.	51	(4	73)
NOTE: FIRST LINE : CEAT NO NAME (L TUE	CANDI	DATE	МО	TUED DEDMA	NENT	DEC NO DREVIOUS SEAT NO C	OLL FG	F C	EAT N	IO	
NOTE: FIRST LINE : SEAT NO., NAME (יר וחם	CAND	LDAIL,	MO	THEN, PERMA	AINLIN I	REG. NO., PREVIOUS SEAT NO., C	OLLLG	⊏, ⊃	'		
· ·			•				(S OBTAINED, P/F:PASS/FAIL, C:P		•			
OTHER LINES: HEAD OF PASSING,	MAX.	MARKS	5, MI	N. P	ASS MARKS,	MARI		REVIO	US CAR	RY OV	/ER	
OTHER LINES: HEAD OF PASSING,	MAX.	MARKS	5, MI	N. P	ASS MARKS,	MARI	(S OBTAINED, P/F:PASS/FAIL, C:P	REVIO	US CAR	RY OV	/ER 	
OTHER LINES: HEAD OF PASSING,	MAX.	MARKS	5, MI	N. P	ASS MARKS,	MARI	CS OBTAINED, P/F:PASS/FAIL, C:P	REVIO	US CAR	RY OV	/ER 	3
OTHER LINES: HEAD OF PASSING, S8058663 JADHAV PRASHANT DAYANAND	MAX.	MARKS	5, MI	IN. P	ASS MARKS, AN	MARI 11.	(S OBTAINED, P/F:PASS/FAIL, C:P, 70503890E , S8058663 ,	REVIO	US CAR	RY OV 	/ER)5866	3 F
OTHER LINES: HEAD OF PASSING, S8058663 JADHAV PRASHANT DAYANAND 01. DISCRETE STRUCTURES	MAX.	MARKS	40	N. P MAL AA	ASS MARKS, AN F	MARH 11. 12.	(S OBTAINED, P/F:PASS/FAIL, C:P, 70503890E , \$8058663 , ENG MATHS III	REVIO PICT PP	US CAR	RY OV , S80 40	/ER)5866 AA	3 F F
OTHER LINES: HEAD OF PASSING, S8058663 JADHAV PRASHANT DAYANAND O1. DISCRETE STRUCTURES O2. COMPUTER ORGANIZATION	PP PP	MARKS	40 40	MAL AA AA	ASS MARKS, AN F F	MARH 11. 12.	(S OBTAINED, P/F:PASS/FAIL, C:P, 70503890E , S8058663 , ENG MATHS III COMPUTER GRAPHICS	REVIO PICT PP PP	100 100 100	RY OV , \$80 40 40	/ER 05866 AA AA AA	3 F F
OTHER LINES: HEAD OF PASSING, S8058663 JADHAV PRASHANT DAYANAND 01. DISCRETE STRUCTURES 02. COMPUTER ORGANIZATION 03. DIGITAL ELECTRONICS & LOGIC DESI	PP PP	100 100 100	40 40 40	MAL AA AA	ASS MARKS, AN F F F	11. 12. 13.	(S OBTAINED, P/F:PASS/FAIL, C:P , 70503890E , S8058663 , ENG MATHS III COMPUTER GRAPHICS PROCESSOR ARCHITECTURE & INTER.	REVIO PICT PP PP	100 100 100	RY OV , \$80 40 40 40	/ER 05866 AA AA AA	3 F F F
OTHER LINES: HEAD OF PASSING, S8058663 JADHAV PRASHANT DAYANAND O1. DISCRETE STRUCTURES O2. COMPUTER ORGANIZATION O3. DIGITAL ELECTRONICS & LOGIC DESI O4. FUNDAMENTAL OF DATA STRUCTURES	PP PP PP	100 100 100 100	40 40 40 40	MAL AA AA AA AA	ASS MARKS, AN F F F F	11. 12. 13. 14.	(S OBTAINED, P/F:PASS/FAIL, C:P , 70503890E , S8058663 , ENG MATHS III COMPUTER GRAPHICS PROCESSOR ARCHITECTURE & INTER. DATA STRUCTURES AND FILES	PICT PP PP PP PP	100 100 100 100	RY OV , \$80 40 40 40	/ER 05866 AA AA AA 40	3 F F F P C
OTHER LINES: HEAD OF PASSING, S8058663 JADHAV PRASHANT DAYANAND O1. DISCRETE STRUCTURES O2. COMPUTER ORGANIZATION O3. DIGITAL ELECTRONICS & LOGIC DESI O4. FUNDAMENTAL OF DATA STRUCTURES O5. HUMANITIES AND SOCIAL SCIENCES	PP PP PP PP	100 100 100 100 100	40 40 40 40 40 40	MAL AA AA AA AA AA	ASS MARKS, AN F F F F F	11. 12. 13. 14. 15.	(S OBTAINED, P/F:PASS/FAIL, C:P , 70503890E , S8058663 , ENG MATHS III COMPUTER GRAPHICS PROCESSOR ARCHITECTURE & INTER. DATA STRUCTURES AND FILES DATA COMMUNICATION	PICT PP PP PP PP TW	100 100 100 100 100	RY OV , \$80 40 40 40 40	/ER 05866 AA AA AA 40 40	3 F F P C P C
OTHER LINES: HEAD OF PASSING, S8058663 JADHAV PRASHANT DAYANAND O1. DISCRETE STRUCTURES O2. COMPUTER ORGANIZATION O3. DIGITAL ELECTRONICS & LOGIC DESI O4. FUNDAMENTAL OF DATA STRUCTURES O5. HUMANITIES AND SOCIAL SCIENCES O6. DIGITAL LABORATORY	PP PP PP TW	100 100 100 100 100 50	40 40 40 40 40 40 20	MAL AA AA AA AA AA	ASS MARKS, AN F F F F F F	11. 12. 13. 14. 15. 16.	(S OBTAINED, P/F:PASS/FAIL, C:P , 70503890E , S8058663 , ENG MATHS III COMPUTER GRAPHICS PROCESSOR ARCHITECTURE & INTER. DATA STRUCTURES AND FILES DATA COMMUNICATION PROCESSOR INTERFACING LABORATORY	PICT PP PP PP PP TW	100 100 100 100 100 25	RY OV , \$80 40 40 40 40 40 10	/ER 05866 AA AA AA 40 40 10 AA	3 F F P C P C
OTHER LINES: HEAD OF PASSING, S8058663 JADHAV PRASHANT DAYANAND 01. DISCRETE STRUCTURES 02. COMPUTER ORGANIZATION 03. DIGITAL ELECTRONICS & LOGIC DESI 04. FUNDAMENTAL OF DATA STRUCTURES 05. HUMANITIES AND SOCIAL SCIENCES 06. DIGITAL LABORATORY 07. DIGITAL LABORATORY	PP PP TW PR	100 100 100 100 100 50	40 40 40 40 40 20 20	MAL AA AA AA AA AA AA	ASS MARKS, AN F F F F F F	11. 12. 13. 14. 15. 16. 17.	(S OBTAINED, P/F:PASS/FAIL, C:P , 70503890E , \$8058663 , ENG MATHS III COMPUTER GRAPHICS PROCESSOR ARCHITECTURE & INTER. DATA STRUCTURES AND FILES DATA COMMUNICATION PROCESSOR INTERFACING LABORATORY PROCESSOR INTERFACING LABORATORY	PICT PP PP PP PP TW OR	100 100 100 100 100 25 50	RY OV , \$80 40 40 40 40 40 10 20	/ER 05866 AA AA AA 40 40 10 AA	3 F F C P C P C F C
OTHER LINES: HEAD OF PASSING, S8058663 JADHAV PRASHANT DAYANAND 01. DISCRETE STRUCTURES 02. COMPUTER ORGANIZATION 03. DIGITAL ELECTRONICS & LOGIC DESI 04. FUNDAMENTAL OF DATA STRUCTURES 05. HUMANITIES AND SOCIAL SCIENCES 06. DIGITAL LABORATORY 07. DIGITAL LABORATORY 08. PROGRAMMING LABORATORY	PP PP PP TW PR TW PR	100 100 100 100 100 50 50	40 40 40 40 40 20 20	MAL AA AA AA AA AA AA AA	ASS MARKS, AN F F F F F F F	11. 12. 13. 14. 15. 16. 17. 18.	(S OBTAINED, P/F:PASS/FAIL, C:P , 70503890E , \$8058663 , ENG MATHS III COMPUTER GRAPHICS PROCESSOR ARCHITECTURE & INTER. DATA STRUCTURES AND FILES DATA COMMUNICATION PROCESSOR INTERFACING LABORATORY PROCESSOR INTERFACING LABORATORY DATA STRUCTURES AND FILES LAB	REVIO PICT PP PP PP PP TW OR TW PR	100 100 100 100 100 25 50 25	RY OV , \$80 40 40 40 40 10 20 10	/ER 05866 AA AA AA 40 40 10 AA 10	3 F F C P C P C F C

04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 46 P C

GRAND TOTAL = 120/1500, RESULT: FAILS RESULT RESERVED FOR OTHR

ORDN. 1 MARKS:

s8058665	KAKADE SHASHIKANT AVINASH				NANI	DA			, 70925460в , ѕ8058665 ,	PICT		, s80	5866	5
01. DISCR	RETE STRUCTURES	PP	100	40	44	P C	1	11.	ENG MATHS III	PP	100	40	46	P C
02. COMPL	JTER ORGANIZATION	PP	100	40	52	P C	1	12.	COMPUTER GRAPHICS	PP	100	40	40	P C
03. DIGIT	AL ELECTRONICS & LOGIC DESI	GPP	100	40	53	P C	1	13.	PROCESSOR ARCHITECTURE & INTER.	PP	100	40	54	Р
04. FUNDA	MENTAL OF DATA STRUCTURES	PP	100	40	45	P C	1	14.	DATA STRUCTURES AND FILES	PP	100	40	50	P C
05. HUMAN	NITIES AND SOCIAL SCIENCES	PP	100	40	40	P C	1	15.	DATA COMMUNICATION	PP	100	40	40	P C
06. DIGIT	AL LABORATORY	TW	50	20	42	P C	1	16.	PROCESSOR INTERFACING LABORATORY	/ TW	25	10	15	P C
07. DIGIT	AL LABORATORY	PR	50	20	38	P C	1	17.	PROCESSOR INTERFACING LABORATORY	OR	50	20	31	P C
08. PROGR	RAMMING LABORATORY	TW	50	20	38	P C	1	18.	DATA STRUCTURES AND FILES LAB	TW	25	10	10	P C
09. PROGR	RAMMING LABORATORY	PR	50	20	30	P C	-	19.	DATA STRUCTURES AND FILES LAB	PR	50	20	37	P C
10. COMMU	UNICATION AND LANGUAGE LAB.	TW	50	20	42	P C	2	20.	OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	32	P C
							2	21.	OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	38	РС

GRAND TOTAL = 817+08/1500, RESULT: HIGHER SECOND CLASS [0.2]
ORDN. 1 MARKS :

S8058666 KHANDAGALE SURAJ PANDURA	NG			SUN	NITA	, 70925476」 , S8058666 , PICT , S8058666	
01. DISCRETE STRUCTURES	PP	100	40	40	P C	11. ENG MATHS III PP 100 40 56 I	Р
02. COMPUTER ORGANIZATION	PP	100	40	51	P C	12. COMPUTER GRAPHICS PP 100 40 61 I	Р
03. DIGITAL ELECTRONICS & LOGIC DES	IGPP	100	40	64	P C	13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 40	Р
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	51	P C	14. DATA STRUCTURES AND FILES PP 100 40 59	Р
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	48	P C	15. DATA COMMUNICATION PP 100 40 45 I	Р
06. DIGITAL LABORATORY	TW	50	20	36	P C	16. PROCESSOR INTERFACING LABORATORY TW 25 10 15	Р
07. DIGITAL LABORATORY	PR	50	20	30	P C	17. PROCESSOR INTERFACING LABORATORY OR 50 20 40 I	Р
08. PROGRAMMING LABORATORY	TW	50	20	41	P C	18. DATA STRUCTURES AND FILES LAB TW 25 10 13 Page 273	Р

09. PROGRAMMING LABORATORY	PR	50	20	30	P C	19.	DATA STRUCTURES AND FILES LAB	PR	50	20	42	Р
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	40	P C	20.	OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	33	Р
						21.	OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	28	Р
CRAND TOTAL - 962/1500 RECULT: UTCHE	-D CE	COND CI	۸۵۵									
GRAND TOTAL = 863/1500, RESULT: HIGHE	EK SEC	OND CL	.ASS									
ORDN. 1 MARKS :												
UNIVERSITY OF	PUNE	,S.E.	(2008	PAT.	.)(INFORMAT	ION T	ECHNOLOGY)		• •		•	
DATE : 18 AUG. 2011	CENT	RE : F	PUNE I	NSTI	TUTE OF COM	IPUTE	R TECHNOLOGY, PUNE.	PAG	E NO.	52	(4	174)
NOTE: FIRST LINE : SEAT NO., NAME O	OF THE	CANDI	DATE,	МО	THER, PERMA	NENT	REG. NO., PREVIOUS SEAT NO., C	OLLEG	Ε,	SEAT N	١Ο.	
OTHER LINES: HEAD OF PASSING,	MAX.	MARKS	s, MI	N. P	ASS MARKS,	MAR	KS OBTAINED, P/F:PASS/FAIL, C:P	REVIO	US CAI	RRY O\	√ER	
S8058667 KOTAWAL SACHIN DILIP				SHA	SHIKALA		, 71073833B , S8058667 ,	PICT		, s80	05866	57
							, , ,			,		
01. DISCRETE STRUCTURES	PP	100	40	40	P C	11.	ENG MATHS III	PP	100	40	22	F
02. COMPUTER ORGANIZATION	PP	100	40	45	P C	12.	COMPUTER GRAPHICS	PP	100	40	42	P C
03. DIGITAL ELECTRONICS & LOGIC DESI	GPP	100	40	52	P C	13.	PROCESSOR ARCHITECTURE & INTER.	PP	100	40	40	P C
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	42	P C	14.	DATA STRUCTURES AND FILES	PP	100	40	47	P C
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	50	P C	15.	DATA COMMUNICATION	PP	100	40	48	P C
06. DIGITAL LABORATORY	TW	50	20	35	P C	16.	PROCESSOR INTERFACING LABORATORY	TW	25	10	19	P C
07. DIGITAL LABORATORY	PR	50	20	38	P C	17.	PROCESSOR INTERFACING LABORATORY	OR	50	20	37	P C
08. PROGRAMMING LABORATORY	TW	50	20	39	P C	18.	DATA STRUCTURES AND FILES LAB	TW	25	10	22	P C
09. PROGRAMMING LABORATORY	PR	50	20	22	P C	19.	DATA STRUCTURES AND FILES LAB	PR	50	20	41	P C
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	36	P C	20.	OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	41	P C
						21.	OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	36	P C
GRAND TOTAL = 794/1500, RESULT: FAILS	: A T	νт										
ORDN. 1 MARKS :	, A.I.	K. I .										
ONDN. I MARKS .												

S80	058669 MAGAR ASHWINI DEELIP				SHA	LINI		, 71073835յ	cols05 , S8058669 ,	PICT		, s80	5866	9
01	DISCRETE STRUCTURES	PP	100	40	48	P C	11.	ENG MATHS III		PP	100	40	40	Р
02	. COMPUTER ORGANIZATION	PP	100	40	40	РС	12.	COMPUTER GRAPHI	:CS	PP	100	40	51	РС
03	DIGITAL ELECTRONICS & LOGIC DESI	GPP	100	40	60	P C	13.	PROCESSOR ARCHI	TECTURE & INTER.	PP	100	40	47	РС
04	. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	51	P C	14.	DATA STRUCTURES	AND FILES	PP	100	40	54	P C
05	. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	53	P C	15.	DATA COMMUNICAT	TION	PP	100	40	44	P C
06	DIGITAL LABORATORY	TW	50	20	36	P C	16.	PROCESSOR INTER	RFACING LABORATORY	TW	25	10	16	P C
07	DIGITAL LABORATORY	PR	50	20	25	P C	17.	PROCESSOR INTER	RFACING LABORATORY	OR	50	20	24	P C
08	PROGRAMMING LABORATORY	TW	50	20	36	P C	18.	DATA STRUCTURES	AND FILES LAB	TW	25	10	19	P C
09	PROGRAMMING LABORATORY	PR	50	20	20	P C	19.	DATA STRUCTURES	AND FILES LAB	PR	50	20	41	P C
10	COMMUNICATION AND LANGUAGE LAB.	TW	50	20	32	P C	20.	OBJECT ORIENTED	PROGRAMMING LAB	TW	50	20	42	P C
							21.	OBJECT ORIENTED	PROGRAMMING LAB	PR	50	20	23	РС

GRAND TOTAL = 802/1500, RESULT: SECOND CLASS
ORDN. 1 MARKS :

S8058670 MAHAJAN SACHINRAVINDRA				SAR	ALA		, 70701521К	, s8058670 ,	PICT		, s80	5867	0
01. DISCRETE STRUCTURES	PP	100	40	AA	F	11	ENG MATHS III		PP	100	40	AA	F
02. COMPUTER ORGANIZATION	PP	100	40	AA	F	12	COMPUTER GRAPHIC	S	PP	100	40	48	P C
03. DIGITAL ELECTRONICS & LOGIC DESI	GPP	100	40	AA	F	13	PROCESSOR ARCHIT	ECTURE & INTER.	PP	100	40	40	P C
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	AA	F	14	DATA STRUCTURES	AND FILES	PP	100	40	45	P C
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	AA	F	15	DATA COMMUNICATI	ON	PP	100	40	57	P C
06. DIGITAL LABORATORY	TW	50	20	AA	F	16	PROCESSOR INTERF	ACING LABORATORY	TW	25	10	11	P C
07. DIGITAL LABORATORY	PR	50	20	AA	F	17	PROCESSOR INTERF	ACING LABORATORY	OR	50	20	20	P C
08. PROGRAMMING LABORATORY	TW	50	20	AA	F	18	DATA STRUCTURES	AND FILES LAB	TW	25	10	12	P C
09. PROGRAMMING LABORATORY	PR	50	20	AA	F	19	DATA STRUCTURES	AND FILES LAB	PR	50	20	25	Р
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	AA	F	20	OBJECT ORIENTED	PROGRAMMING LAB	TW	50	20	24	P C
						21	OBJECT ORIENTED	PROGRAMMING LAB	PR	50	20	26	Р

GRAND TOTAL = 308/1500, RESULT: FAILS RESULT RESERVED FOR OTHR

ORDN. 1 MARKS:

UNIVERSITY OF	 PUNE	 ,S.E.	(2008	 PAT	 .)(IN		ECHNOLOGY)						
DATE : 18 AUG. 2011	CENT	TRE :	PUNE :	INSTI	TUTE	OF COMPUTE	R TECHNOLOGY, PUNE.		PAGE	NO.	53	(4	75)
NOTE: FIRST LINE : SEAT NO., NAME OF PASSING,	OF THE	E CAND	IDATE	, MC	THER	, PERMANENT		NO., COL	LEGE	, S	EAT N	Ο.	
·						·							
S8058671 MAYANK BHATNAGAR				SHE	ETAL		, 70925502м , ѕ805	8671 , P	PICT		, s80	5867	1
01. DISCRETE STRUCTURES	PP	100	40	63	P C	11.	ENG MATHS III	P	P :	100	40	24	F
02. COMPUTER ORGANIZATION	PP	100	40	41	РС	12.	COMPUTER GRAPHICS	Р	Р :	100	40	42	РС
03. DIGITAL ELECTRONICS & LOGIC DES	IGPP	100	40	67	P C	13.	PROCESSOR ARCHITECTURE &	INTER. P	Р :	100	40	40	P C
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	41	P C	14.	DATA STRUCTURES AND FILE	S P	P :	100	40	47	P C
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	43	РС	15.	DATA COMMUNICATION	Р	Р :	100	40	42	РС
06. DIGITAL LABORATORY	TW	50	20	42	РС	16.	PROCESSOR INTERFACING LA	BORATORY T	W	25	10	21	РС
07. DIGITAL LABORATORY	PR	50	20	40	РС	17.	PROCESSOR INTERFACING LA	BORATORY C	R	50	20	37	РС
08. PROGRAMMING LABORATORY	TW	50	20	41	РС	18.	DATA STRUCTURES AND FILE	S LAB T	W	25	10	22	P C
09. PROGRAMMING LABORATORY	PR	50	20	38	РС	19.	DATA STRUCTURES AND FILE	S LAB P	rR	50	20	35	P C
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	40	РС	20.	OBJECT ORIENTED PROGRAMM	ING LAB T	W	50	20	46	РС
						21.	OBJECT ORIENTED PROGRAMM	ING LAB P	'R	50	20	40	P C
GRAND TOTAL = 852/1500, RESULT: FAILS	S A.T	.К.Т.											
S8058672 MONISH PATEL				NEE	TA		, 70925508L , s805	8672 , P	TOI		, s80	5867	2
01. DISCRETE STRUCTURES	PP	100	40	40	РС	11.	ENG MATHS III	P	P :	100	40	40	P C
02. COMPUTER ORGANIZATION	PP	100	40	40	P C	12.	COMPUTER GRAPHICS	P	P :	100	40	40	P C
03. DIGITAL ELECTRONICS & LOGIC DES	IGPP	100	40	23	F	13.	PROCESSOR ARCHITECTURE &	INTER. P	P :	100	40	40	Р
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	49	РС	14.	DATA STRUCTURES AND FILE Page 276	S P	P :	100	40	40	P C

								cols05					
0!	5. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	42	P C	15.	DATA COMMUNICATION	PP	100	40	40	P C
06	6. DIGITAL LABORATORY	TW	50	20	32	P C	16.	PROCESSOR INTERFACING LABORATORY	TW	25	10	10	P C
07	7. DIGITAL LABORATORY	PR	50	20	20	P C	17.	PROCESSOR INTERFACING LABORATORY	OR	50	20	25	Р
08	8. PROGRAMMING LABORATORY	TW	50	20	38	P C	18.	DATA STRUCTURES AND FILES LAB	TW	25	10	10	P C
09	9. PROGRAMMING LABORATORY	PR	50	20	30	P C	19.	DATA STRUCTURES AND FILES LAB	PR	50	20	20	P C
10	O. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	35	P C	20.	OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	20	P C
							21.	OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	17	F
GRAI	ND TOTAL = 651/1500, RESULT: FAILS	A.T.K	С.Т.										
ORDI	N. 1 MARKS :												
S	8058673 MUNOT PAYAL KAMLESHKUMAR				MEEN	NAKSHI		, 70925517к , S8058673 ,	PICT		, s805	58673	3
02	1. DISCRETE STRUCTURES	PP	100	40	51	P C	11.	ENG MATHS III	PP	100	40	27	F

S805	86/3 MUNOT PAYAL KAMLESHKUMAR				MEEN	IAKSHI		, /092551/K , S80586/3 ,	PICT		, 580	5867	3
01.	DISCRETE STRUCTURES	PP	100	40	51	P C	11.	ENG MATHS III	PP	100	40	27	F
02.	COMPUTER ORGANIZATION	PP	100	40	40	P C	12.	COMPUTER GRAPHICS	PP	100	40	12	F
03.	DIGITAL ELECTRONICS & LOGIC DESI	GPP	100	40	50	P C	13.	PROCESSOR ARCHITECTURE & INTER.	PP	100	40	14	F
04.	FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	45	P C	14.	DATA STRUCTURES AND FILES	PP	100	40	40	P C
05.	HUMANITIES AND SOCIAL SCIENCES	PP	100	40	40	P C	15.	DATA COMMUNICATION	PP	100	40	15	F
06.	DIGITAL LABORATORY	TW	50	20	35	P C	16.	PROCESSOR INTERFACING LABORATORY	TW	25	10	11	P C
07.	DIGITAL LABORATORY	PR	50	20	20	P C	17.	PROCESSOR INTERFACING LABORATORY	OR	50	20	AA	F
08.	PROGRAMMING LABORATORY	TW	50	20	33	P C	18.	DATA STRUCTURES AND FILES LAB	TW	25	10	15	P C
09.	PROGRAMMING LABORATORY	PR	50	20	20	P C	19.	DATA STRUCTURES AND FILES LAB	PR	50	20	27	P C
10.	COMMUNICATION AND LANGUAGE LAB.	TW	50	20	40	P C	20.	OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	41	P C
							21.	OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	AA	F

GRAND TOTAL = 576/1500, RESULT: FAILS

RESULT RESERVED FOR BKLG

ORDN. 1 MARKS:

UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(INFORMATION TECHNOLOGY)

DATE: 18 AUG. 2011 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 54 (476)

Page 278

NOTE: FIRST LINE : SEAT NO.,	NAME OF THE CANDIDATE,	MOTHER, PERMANENT REG. NO.,	PREVIOUS SEAT NO.,	COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PA	SSING, MAX. MARKS, MIN	I. PASS MARKS, MARKS OBTAINED	, P/F:PASS/FAIL, C	::PREVIOUS CARRY OVER

s805	8674 N Y GARIN				YI	MALIVATTEY		, 70925519F , S8058674	, P	ICT		, s80	58674	4
01.	DISCRETE STRUCTURES	PP	100	40	45	P C	11.	ENG MATHS III	Р	P :	100	40	40	Р
02.	COMPUTER ORGANIZATION	PP	100	40	40	P C	12.	COMPUTER GRAPHICS	Р	P :	100	40	42	P C
03.	DIGITAL ELECTRONICS & LOGIC DESIGNATION	GPP	100	40	41	P C	13.	PROCESSOR ARCHITECTURE & INTE	R. P	Р :	100	40	27	F
04.	FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	40	P C	14.	DATA STRUCTURES AND FILES	Р	Р :	100	40	40	P C
05.	HUMANITIES AND SOCIAL SCIENCES	PP	100	40	40	P C	15.	DATA COMMUNICATION	Р	Р :	100	40	40	P C
06.	DIGITAL LABORATORY	TW	50	20	33	P C	16.	PROCESSOR INTERFACING LABORAT	ORY T	W	25	10	16	P C
07.	DIGITAL LABORATORY	PR	50	20	30	P C	17.	PROCESSOR INTERFACING LABORAT	ORY O	R	50	20	35	P C
08.	PROGRAMMING LABORATORY	TW	50	20	35	P C	18.	DATA STRUCTURES AND FILES LAB	з т	W	25	10	18	P C
09.	PROGRAMMING LABORATORY	PR	50	20	30	P C	19.	DATA STRUCTURES AND FILES LAB	8 P	R	50	20	40	P C
10.	COMMUNICATION AND LANGUAGE LAB.	TW	50	20	38	P C	20.	OBJECT ORIENTED PROGRAMMING L	AB T	W	50	20	44	P C
							21.	OBJECT ORIENTED PROGRAMMING L	AB P	R	50	20	29	Р

GRAND TOTAL = 743/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS:

, 70925525L S8058675 NAVANDE NEHAL MALLIKARJUN , s8058675 , , S8058675 PRABHA PICT 01. DISCRETE STRUCTURES 100 40 49 P C 11. ENG MATHS III 100 40 40 P 02. COMPUTER ORGANIZATION 100 40 P C 100 40 12. COMPUTER GRAPHICS 43 P C 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 40 59 P C 100 40 40 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 04. FUNDAMENTAL OF DATA STRUCTURES 100 40 54 P C 14. DATA STRUCTURES AND FILES 100 40 52 P C 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 48 P C 15. DATA COMMUNICATION 100 40 58 P C 06. DIGITAL LABORATORY 50 TW 20 38 P C 16. PROCESSOR INTERFACING LABORATORY TW 25 10 17 P C 07. DIGITAL LABORATORY 50 20 25 P C 17. PROCESSOR INTERFACING LABORATORY OR 50 20 35 P C 08. PROGRAMMING LABORATORY TW 50 20 32 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 18 P C 09. PROGRAMMING LABORATORY 50 20 34 P C 19. DATA STRUCTURES AND FILES LAB PR 50 20 25 P C

10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	41	P C	20	OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	40	PC
						21	OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	20	P C
GRAND TOTAL = 808/1500, RESULT: SECON	ום כו	ΔSS										
ORDN. 1 MARKS :	10 CL	133										
S8058684 SHIVADE PRATHAMESH SHRIKA	ANT			NIL	IMA		, 70925608G , S8058684 ,	PICT		, s80	5868	4
01. DISCRETE STRUCTURES	PP	100	40	44	P C	11	ENG MATHS III	PP	100	40	25	F
02. COMPUTER ORGANIZATION	PP	100	40	41	P C	12	COMPUTER GRAPHICS	PP	100	40	40	P C
03. DIGITAL ELECTRONICS & LOGIC DESI	GPP	100	40	43	P C	13	PROCESSOR ARCHITECTURE & INTER.	PP	100	40	45	P C
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	57	P C	14	DATA STRUCTURES AND FILES	PP	100	40	49	P C
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	43	P C	15	DATA COMMUNICATION	PP	100	40	40	P C
06. DIGITAL LABORATORY	TW	50	20	34	P C	16	PROCESSOR INTERFACING LABORATORY	TW	25	10	14	P C
07. DIGITAL LABORATORY	PR	50	20	28	P C	17	PROCESSOR INTERFACING LABORATORY	OR	50	20	21	P C
08. PROGRAMMING LABORATORY	TW	50	20	36	P C	18	DATA STRUCTURES AND FILES LAB	TW	25	10	16	P C
09. PROGRAMMING LABORATORY	PR	50	20	33	P C	19	DATA STRUCTURES AND FILES LAB	PR	50	20	33	P C
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	38	P C	20	OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	28	P C
						21	OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	33	P C
GRAND TOTAL = 741/1500, RESULT: FAILS	S A.T	.K.T.										
ORDN. 1 MARKS :												
UNIVERSITY OF	PUNE	,S.E.((2008	PAT.	.)(IN	IFORMATION	TECHNOLOGY)					
DATE : 18 AUG. 2011	CEN	TRE : P	UNE 1	INSTI	TUTE	OF COMPUT	ER TECHNOLOGY, PUNE.	PAGE	E NO.	55	(4	77)
NOTE: FIRST LINE : SEAT NO., NAME O)F THI	E CANDI	DATE,	, MO	THER,	, PERMANEN	REG. NO., PREVIOUS SEAT NO., CO	OLLEGE	<u>:</u> , s	EAT N	Ю.	
OTHER LINES: HEAD OF PASSING,	MAX	. MARKS	, M	IN. P	ASS M	MARKS, MA	RKS OBTAINED, P/F:PASS/FAIL, C:PF	REVIOL	JS CAR	.RY OV	'ER	
S8058685 SHRIKANTH JAIKUMAR				BHU	VNA		, 70801622н , ѕ8058685 ,	PICT		, s80	5868	5

01.	DISCRETE STRUCTURES	PP	100	40	40	P C	11.	ENG MATHS III	PP	100	40	66	Р
02.	COMPUTER ORGANIZATION	PP	100	40	40	P C	12.	COMPUTER GRAPHICS	PP	100	40	28	F
03.	DIGITAL ELECTRONICS & LOGIC DESIGNATION	GPP	100	40	53	P C	13.	PROCESSOR ARCHITECTURE & INTER.	PP	100	40	47	Р
04.	FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	70	Р	14.	DATA STRUCTURES AND FILES	PP	100	40	59	Р
05.	HUMANITIES AND SOCIAL SCIENCES	PP	100	40	40	P C	15.	DATA COMMUNICATION	PP	100	40	54	Р
06.	DIGITAL LABORATORY	TW	50	20	20	P C	16.	PROCESSOR INTERFACING LABORATORY	TW	25	10	21	Р
07.	DIGITAL LABORATORY	PR	50	20	32	P C	17.	PROCESSOR INTERFACING LABORATORY	OR	50	20	35	Р
08.	PROGRAMMING LABORATORY	TW	50	20	20	P C	18.	DATA STRUCTURES AND FILES LAB	TW	25	10	21	Р
09.	PROGRAMMING LABORATORY	PR	50	20	29	P C	19.	DATA STRUCTURES AND FILES LAB	PR	50	20	44	Р
10.	COMMUNICATION AND LANGUAGE LAB.	TW	50	20	20	P C	20.	OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	39	Р
							21.	OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	33	Р

GRAND TOTAL = 811/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

s8058686	SUYASH PANDEY				SHA	ILA			, 70925625G	, s8058686 ,	PICT		, s80	58686	5
01. DISCR	RETE STRUCTURES	PP	100	40	58	P C	1	L1.	ENG MATHS III		PP	100	40	40	P C
02. COMPL	TER ORGANIZATION	PP	100	40	48	РС	1	L2.	COMPUTER GRAPHICS	S	PP	100	40	40	РС
03. DIGIT	AL ELECTRONICS & LOGIC DESI	GPP	100	40	46	РС	1	L3.	PROCESSOR ARCHITI	ECTURE & INTER.	PP	100	40	AA	F
04. FUNDA	MENTAL OF DATA STRUCTURES	PP	100	40	50	РС	1	L4.	DATA STRUCTURES	AND FILES	PP	100	40	40	РС
05. HUMAN	ITTIES AND SOCIAL SCIENCES	PP	100	40	40	РС	1	L5.	DATA COMMUNICATION	ON	PP	100	40	40	РС
06. DIGIT	AL LABORATORY	TW	50	20	21	P C	1	L6.	PROCESSOR INTERFA	ACING LABORATORY	TW	25	10	10	P C
07. DIGIT	AL LABORATORY	PR	50	20	27	P C	1	L7.	PROCESSOR INTERFA	ACING LABORATORY	OR	50	20	AA	F
08. PROGR	AMMING LABORATORY	TW	50	20	23	P C	1	L8.	DATA STRUCTURES A	AND FILES LAB	TW	25	10	10	P C
09. PROGR	AMMING LABORATORY	PR	50	20	30	P C	1	L9.	DATA STRUCTURES A	AND FILES LAB	PR	50	20	AA	F
10. COMMU	NICATION AND LANGUAGE LAB.	TW	50	20	25	РС	2	20.	OBJECT ORIENTED I	PROGRAMMING LAB	TW	50	20	20	РС
							2	21.	OBJECT ORIENTED I	PROGRAMMING LAB	PR	50	20	32	Р

GRAND TOTAL = 600/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS:

S8058687 THAKUR BHAVANA PRAKASH				ARA	DHANA		, 70925633н , ѕ8058687 ,	PIC	Γ	, sa	05868	37
01. DISCRETE STRUCTURES	PP	100	40	40	P C	11.	ENG MATHS III	PP	100	40	54	P C
02. COMPUTER ORGANIZATION	PP	100	40	51	P C	12.	COMPUTER GRAPHICS	PP	100	40	48	P C
03. DIGITAL ELECTRONICS & LOGIC DESI	EGPP	100	40	40	P C	13.	PROCESSOR ARCHITECTURE & INTER.	PP	100	40	AA	F
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	40	P C	14.	DATA STRUCTURES AND FILES	PP	100	40	51	P C
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	44	P C	15.	DATA COMMUNICATION	PP	100	40	40	P C
06. DIGITAL LABORATORY	TW	50	20	39	P C	16.	PROCESSOR INTERFACING LABORATOR	Y TW	25	10	19	P C
07. DIGITAL LABORATORY	PR	50	20	32	P C	17.	PROCESSOR INTERFACING LABORATOR	Y OR	50	20	32	P C
08. PROGRAMMING LABORATORY	TW	50	20	34	P C	18.	DATA STRUCTURES AND FILES LAB	TW	25	10	17	P C
09. PROGRAMMING LABORATORY	PR	50	20	23	P C	19.	DATA STRUCTURES AND FILES LAB	PR	50	20	35	P C
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	33	P C	20.	OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	41	P C
						21.	OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	28	Р (
	5 A.T.	.к.т.										
			 (2008	 PAT.	 .)(INFORM		ECHNOLOGY)					
RAND TOTAL = 741/1500, RESULT: FAILS RDN. 1 MARKS : UNIVERSITY OF DATE : 18 AUG. 2011	 PUNE	 ,S.E.				MATION T			 GE NO.		(4	
RDN. 1 MARKS : UNIVERSITY OF	PUNE	 ,S.E. ΓRE : Ι	PUNE]	INSTI	TUTE OF	MATION T	ECHNOLOGY) R TECHNOLOGY, PUNE.	PAG	GE NO.	 56 SEAT !		 (78)
RDN. 1 MARKS: UNIVERSITY OF DATE: 18 AUG. 2011 NOTE: FIRST LINE: SEAT NO., NAME OF	PUNE CENT	 ,S.E. ΓRE : I E CAND	PUNE]	INSTI , MO	TUTE OF	MATION T COMPUTE RMANENT	ECHNOLOGY) R TECHNOLOGY, PUNE.	PAG 	GE NO.	 SEAT I	 NO.	 !78)
RDN. 1 MARKS: UNIVERSITY OF DATE: 18 AUG. 2011 NOTE: FIRST LINE: SEAT NO., NAME OF COMMERCE OF PASSING,	PUNE CENT DF THE	 ,S.E. FRE : I E CAND:	PUNE 3	INSTI , MO IN. P	TUTE OF THER, PE	MATION T COMPUTE RMANENT S, MARI	TECHNOLOGY) R TECHNOLOGY, PUNE.	PAG COLLEG	GE NO. GE, S	 SEAT ! RRY O	NO. VER	
NOTE: FIRST LINE: SEAT NO., NAME OF OTHER LINES: HEAD OF PASSING,	PUNE CENT DF THE	 ,S.E. FRE : I E CAND:	PUNE 3	INSTI , MO IN. P	TUTE OF THER, PE ASS MARK	MATION T COMPUTE RMANENT S, MARI	TECHNOLOGY) R TECHNOLOGY, PUNE. REG. NO., PREVIOUS SEAT NO., KS OBTAINED, P/F:PASS/FAIL, C:	PAG COLLEG	GE NO. GE, S	SEAT I	NO. VER	
UNIVERSITY OF DATE: 18 AUG. 2011 NOTE: FIRST LINE: SEAT NO., NAME O OTHER LINES: HEAD OF PASSING, S8058691 VIVEK KUMAR	PUNE CENT DF THE	 ,S.E. FRE : I E CAND:	PUNE 3	, MO IN. P	TUTE OF THER, PE ASS MARK	MATION T COMPUTED RMANENT S, MARD	TECHNOLOGY) R TECHNOLOGY, PUNE. REG. NO., PREVIOUS SEAT NO., KS OBTAINED, P/F:PASS/FAIL, C:	PAG COLLEG PREVIG	GE NO. GE, S	SEAT I	 NO. VER	
UNIVERSITY OF DATE: 18 AUG. 2011 NOTE: FIRST LINE: SEAT NO., NAME O OTHER LINES: HEAD OF PASSING, S8058691 VIVEK KUMAR 01. DISCRETE STRUCTURES	PUNE CENT OF THE	, S.E. ΓRE : I CANDO MARKS	PUNE 1	, MO IN. P SUM	TUTE OF THER, PE ASS MARK	MATION T COMPUTED RMANENT S, MARI	TECHNOLOGY) R TECHNOLOGY, PUNE. REG. NO., PREVIOUS SEAT NO., KS OBTAINED, P/F:PASS/FAIL, C: , 70925651F , S8058691 ,	PAG COLLEG PREVIG 	GE NO. GE, S DUS CAI	 SEAT ! RRY 0\ 	NO. VER 05869)1
NOTE: FIRST LINE: SEAT NO., NAME OF COTHER LINES: HEAD OF PASSING,	PUNE CENT MAX PP PP		PUNE 1 IDATE,	INSTI , MO IN. P SUM 55 41	TUTE OF THER, PE ASS MARK AN	TATION TOMPUTED STATES AND	TECHNOLOGY) R TECHNOLOGY, PUNE. REG. NO., PREVIOUS SEAT NO., KS OBTAINED, P/F:PASS/FAIL, C: , 70925651F , S8058691 , ENG MATHS III	PAG COLLEG PREVIG PICT	GE NO. GE, S DUS CAI	SEAT I RRY 0\ , S80	NO. VER 05869)1 P

05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 53 P C

15. DATA COMMUNICATION Page 281

PP 100 40 41 P C

06.	DIGITAL LABORATORY	TW	50	20	35	P C	16	. PROCESSOR INTERFACIN	IG LABORATORY	TW	25	10	10	P C
07.	DIGITAL LABORATORY	PR	50	20	28	P C	17	. PROCESSOR INTERFACIN	G LABORATORY	OR	50	20	24	P C
08.	PROGRAMMING LABORATORY	TW	50	20	35	P C	18	. DATA STRUCTURES AND	FILES LAB	TW	25	10	12	P C
09.	PROGRAMMING LABORATORY	PR	50	20	30	Р	19	. DATA STRUCTURES AND	FILES LAB	PR	50	20	25	Р
10.	COMMUNICATION AND LANGUAGE LAB.	TW	50	20	37	P C	20	. OBJECT ORIENTED PROG	RAMMING LAB	TW	50	20	25	P C
							21	. OBJECT ORIENTED PROG	RAMMING LAB	PR	50	20	22	Р

GRAND TOTAL = 737/1500, RESULT: PASS CLASS

ORDN. 1 MARKS:

S8058692 WAGH ABHISHEK KARBHARI			KRA	NTI	, 70925654L , S8058692 , PICT , S805869	, s8058692	
01. DISCRETE STRUCTURES	PP	100	40	46	P C	11. ENG MATHS III PP 100 40 23	F
02. COMPUTER ORGANIZATION	PP	100	40	40	P C	12. COMPUTER GRAPHICS PP 100 40 40	P C
03. DIGITAL ELECTRONICS & LOGIC DESI	GPP	100	40	40	P C	13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 09	F
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	40	P C	14. DATA STRUCTURES AND FILES PP 100 40 43	P C
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	40	P C	15. DATA COMMUNICATION PP 100 40 30	F
06. DIGITAL LABORATORY	TW	50	20	25	P C	16. PROCESSOR INTERFACING LABORATORY TW 25 10 10	P C
07. DIGITAL LABORATORY	PR	50	20	30	P C	17. PROCESSOR INTERFACING LABORATORY OR 50 20 20	P C
08. PROGRAMMING LABORATORY	TW	50	20	25	P C	18. DATA STRUCTURES AND FILES LAB TW 25 10 10	P C
09. PROGRAMMING LABORATORY	PR	50	20	28	P C	19. DATA STRUCTURES AND FILES LAB PR 50 20 20	P C
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	21	P C	20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 20	P C
						21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 22	РС

GRAND TOTAL = 582/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS: