DATE										
NOTE: HER LIN	FIRST LINE : SEAT NO., NAME CES: SUB.CODE, SUB.NAME, SUB.TY	F TH PE,	E CANDI MAX.MAR	DATE, KS, M	MOTHI IN.PAS	ER, PERMANE S MARKS, OE	NT REG. NO., MARKS, TH MAR	PREVIOUS SEAT KS, OBTAINED,	NO., CO	OLLEGE, SEAT NO. S/FAIL, C:PREV.CARRY O
	3001 AAROHI VAIDYA						,7152708			
SEM.:	1									
204181	SIGNALS & SYSTEMS	PP	100 40	47	25	72 P				
204181	SIGNALS & SYSTEMS	TW	25 10			18 P				
204182	ELECTRONIC DEVICES & CIRCUITS	S PP	100 40	46	33	79 P				
204182	ELECTRONIC DEVICES & CIRCUITS	PR	50 20			43 P				
204183	NETWORK THEORY		100 40	39	43	82 P				
204183	NETWORK THEORY					22 P				
204184	DATA STRUCTURES & ALGORITHMS			40	27	67 P				
204184	DATA STRUCTURES & ALGORITHMS	OR	50 20			39 P				
204185	DIGITAL ELECTRONICS			40	32	72 P				
	DIGITAL ELECTRONICS					31 P				
204186	ELEX. MEASURING INSTRU & TOOL FIRST SEM TOTAL = 563/750	.STW	50 20			38 P				
	3002 AASHUTOSH P K BHAIYA					NA		1J ,PICT		
SEM.:	1									
	SIGNALS & SYSTEMS	pр	100 40	24	16	40 P				
	SIGNALS & SYSTEMS			- '	10	15 P				
	ELECTRONIC DEVICES & CIRCUITS			32	16	48 P				
	ELECTRONIC DEVICES & CIRCUITS			32	_0	32 P				
204183			100 40	27	22	49 P				
204183	NETWORK THEORY		25 10			17 P				
204184	DATA STRUCTURES & ALGORITHMS			27	21	48 P				
204184	DATA STRUCTURES & ALGORITHMS					32 P				
204185			100 40	27	21	48 P				
204185						23 P				
	ELEX. MEASURING INSTRU & TOOL					28 P				
201100	FIRST SEM TOTAL = 380/750	.5111	30 20			20 1				
	3003 ABHISHEK KUMAR SINGH					NANTI		3E ,PICT		
SEM.:	1									
	SIGNALS & SYSTEMS	PP	100 40	40	36	76 P				
204181	SIGNALS & SYSTEMS	TW	25 10			12 P				
204182	ELECTRONIC DEVICES & CIRCUITS	S PP	100 40	37	39	76 P				
204182	ELECTRONIC DEVICES & CIRCUITS	PR	50 20			25 P				
204183	NETWORK THEORY	PP	100 40	38	40	78 P				
204183	NETWORK THEORY	TW	25 10			20 P				
204184	DATA STRUCTURES & ALGORITHMS	PP	100 40	30	30	60 P				
204184	DATA STRUCTURES & ALGORITHMS	OR	50 20			31 P				
204185	DIGITAL ELECTRONICS	PP	100 40	29	30	59 P				
204185	DIGITAL ELECTRONICS		50 20			23 P				
204186	ELEX. MEASURING INSTRU & TOOL	.STW	50 20			26 P				
	FIRST SEM TOTAL = 486/750									

	E: 25 JAN 2016 CENTRE:							PAGE NO. 2
NOTE: THER LIN	FIRST LINE : SEAT NO., NAME (NES: SUB.CODE, SUB.NAME, SUB.T	OF THE C	ANDIDATE	, MOTH	IER, PERMANEN SS MARKS, OE	T REG. NO., PREV MARKS, TH MARKS,	IOUS SEAT OBTAINED,	NO., COLLEGE, SEAT NO. P/F:PASS/FAIL, C:PREV.CARRY OV
	33005 AHER VINAY NARENDRA							,S140053005
SEM.:	:1							
204181	SIGNALS & SYSTEMS	PP 100	40 15	10	F			
	SIGNALS & SYSTEMS				14 P			
	ELECTRONIC DEVICES & CIRCUIT			15	42 P			
					27 P			
204183	NETWORK THEORY	PP 100		15	42 P			
204183	NETWORK THEORY				19 P			
204184	DATA STRUCTURES & ALGORITHMS			32	61 P			
204184	DATA STRUCTURES & ALGORITHMS				21 P			
204185	DIGITAL ELECTRONICS			09	<u>-</u>			
204185					22 P			
204186	ELEX. MEASURING INSTRU & TOO FIRST SEM TOTAL =/750	LSTW 50	20		28 P			
	33006 AHIRE DIPALI ANIL				GALA			,s140053006
SEM.:	:1							
	SIGNALS & SYSTEMS	PP 100	40 23	17	40 P			
	SIGNALS & SYSTEMS			Δ,	21 P			
	ELECTRONIC DEVICES & CIRCUIT			15	45 P			
	ELECTRONIC DEVICES & CIRCUIT				30 P			
204183	NETWORK THEORY	PP 100		22	40 P			
204183	NETWORK THEORY	TW 25			22 P			
204184	DATA STRUCTURES & ALGORITHMS			27	54 P			
204184					10 F			
204185		PP 100		21	56 P			
	DIGITAL ELECTRONICS				28 P			
	ELEX. MEASURING INSTRU & TOO				30 P			
	FIRST SEM TOTAL =/750							
	53007 AKSHAY SHRIKANT DESHMU							,s140053007
SEM.:	:1							
204181	SIGNALS & SYSTEMS	PP 100	40 27	16	43 P			
204181	SIGNALS & SYSTEMS	TW 25	10		14 P			
204182	ELECTRONIC DEVICES & CIRCUIT	S PP 100	40 40	20	60 P			
204182	ELECTRONIC DEVICES & CIRCUIT	S PR 50	20		5 F			
204183	NETWORK THEORY	PP 100	40 26	16	42 P			
204183	NETWORK THEORY	TW 25	10		19 P			
204184	DATA STRUCTURES & ALGORITHMS	PP 100	40 23	40	63 P			
204184	DATA STRUCTURES & ALGORITHMS	OR 50	20		23 P			
204185	DIGITAL ELECTRONICS	PP 100	40 32	23	55 P			
204185	DIGITAL ELECTRONICS	PR 50	20		26 P			
204186	ELEX. MEASURING INSTRU & TOO	LSTW 50	20		29 P			
	FIRST SEM TOTAL =/750							

	E: 25 JAN 2016 CENTRE: PI						-			PAGE NO. 3	
NOTE:	FIRST LINE : SEAT NO., NAME OF	F TH	HE CAND	IDATE,	MOTH	IER, PEI	RMANENT REG.	NO., PRE	/IOUS SEAT	NO., COLLEGE,	SEAT NO.
	33008 AMRAO CHETAN DEEPAK							 71527109в			
							,		,	,	
SEM.:			100 10								
	SIGNALS & SYSTEMS				15						
	SIGNALS & SYSTEMS					18 P					
	ELECTRONIC DEVICES & CIRCUITS				08	F					
	ELECTRONIC DEVICES & CIRCUITS				10	37 P					
204183			100 40		10	F					
204183	NETWORK THEORY				2.5	20 P					
204184	DATA STRUCTURES & ALGORITHMS				25	56 P					
204184					20	29 P					
204185	DIGITAL ELECTRONICS				28	56 P					
204185						27 P					
204186	FIRST SEM TOTAL =/750	SIW	30 20	,		35 P					
	33009 ANDURKAR SHUBHAM VYANKA									,s1400530	
							,		,	, =	
SEM.:			100 40	20	27	47.5					
	SIGNALS & SYSTEMS				27						
			25 10		1.5	16 P					
	ELECTRONIC DEVICES & CIRCUITS				15	44 P					
	ELECTRONIC DEVICES & CIRCUITS				2.2	35 P					
	NETWORK THEORY				23	43 P					
204183			25 10		27	17 P					
204184	DATA STRUCTURES & ALGORITHMS				27	52 P					
204184					1 5	22 P					
204185			100 40		Т2	44 P					
204185						26 P					
204186	ELEX. MEASURING INSTRU & TOOLS FIRST SEM TOTAL = 378/750	SIW	50 20	1		32 P					
	33010 ANIKET ASHOK BHALERAO				 ENUS			71527111D			
							,		,	, =	
SEM.:		D.D.	100 40		2.4	F.C. 5					
			100 40		24	56 P					
	SIGNALS & SYSTEMS		25 10		22	23 P					
	ELECTRONIC DEVICES & CIRCUITS				22	62 P					
	ELECTRONIC DEVICES & CIRCUITS				20	43 P					
204183	NETWORK THEORY		100 40		39	78 P					
204183			25 10		40	23 P					
204184	DATA STRUCTURES & ALGORITHMS				40	76 P					
204184	DATA STRUCTURES & ALGORITHMS				2.4	42 P					
204185	DIGITAL ELECTRONICS		100 40		34	64 P					
204185						33 P					
204186	ELEX. MEASURING INSTRU & TOOLS FIRST SEM TOTAL = 544/750	STW	50 20	1		44 P					

	6 S.E.(2014 PAT.)(ELECTRONICS : 25 JAN 2016 CENTRE: P			-	F CC	MPUTER	TECHNOLOGY, P	UNE.		PAGE NO. 4	
NOTE:	FIRST LINE : SEAT NO., NAME O ES: SUB.CODE, SUB.NAME, SUB.TY	F TI	HE CA	NDIDATE	., M	OTHER,	PERMANENT REG	. NO., PRE	VIOUS SEAT	Γ NO., COLLEGE,	SEAT NO.
	3011 ANUSHKA NARAYAN	• • •								,s140053	
SEM.:	1										
	SIGNALS & SYSTEMS	PP	100	40 27	· 3	50 5	7 P				
	SIGNALS & SYSTEMS						.8 P				
204182	ELECTRONIC DEVICES & CIRCUITS) 1		7 P				
204182	ELECTRONIC DEVICES & CIRCUITS						8 P				
204183	NETWORK THEORY	PP	100	40 27	· 3	1 5	8 P				
204183	NETWORK THEORY					2	0 P				
204184	DATA STRUCTURES & ALGORITHMS	PP	100	40 32	: 3	0 6	52 P				
204184	DATA STRUCTURES & ALGORITHMS	OR	50	20		3	4 P				
204185	DIGITAL ELECTRONICS	PP	100	40 28	1	.7 4	.5 P				
204185							7 P				
	ELEX. MEASURING INSTRU & TOOL FIRST SEM TOTAL = 451/750						5 P				
	3012 ARPIT BALDWA									,s140053	
SEM.:	1										
	SIGNALS & SYSTEMS	PP	100	40 28	3	50 5	8 P				
204181	SIGNALS & SYSTEMS	TW	25	10		1	.9 P				
204182	ELECTRONIC DEVICES & CIRCUITS	PP	100	40 33	1	.5 4	8 P				
204182	ELECTRONIC DEVICES & CIRCUITS	PR	50	20		3	5 P				
204183	NETWORK THEORY	PP	100	40 20	2	6 4	6 P				
204183	NETWORK THEORY	TW	25	10		2	0 P				
204184	DATA STRUCTURES & ALGORITHMS	PP	100	40 28	3	7 6	5 P				
204184	DATA STRUCTURES & ALGORITHMS	OR	50	20		2	3 P				
204185	DIGITAL ELECTRONICS	PP	100	40 37	2	7 6	4 P				
204185	DIGITAL ELECTRONICS	PR	50	20		3	0 P				
204186	ELEX. MEASURING INSTRU & TOOL FIRST SEM TOTAL = 441/750	STW	50	20		3	3 P				
	3013 ARYA K SHAJI SHAJI KOLO							,71527124F			
									-	,	
SEM.:											
	SIGNALS & SYSTEMS				3		9 P				
	SIGNALS & SYSTEMS		25				0 P				
	ELECTRONIC DEVICES & CIRCUITS				3		7 P				
	ELECTRONIC DEVICES & CIRCUITS						8 P				
204183			100		2		0 P				
204183	NETWORK THEORY		25				2 P				
204184					3		'9 P				
204184							0 P				
204185	DIGITAL ELECTRONICS		100		2		57 P				
204185	DIGITAL ELECTRONICS		50	_			9 P				
204186	ELEX. MEASURING INSTRU & TOOL FIRST SEM TOTAL = 539/750	STW	50	20		3	8 P				

	: 25 JAN 2016 CE							PAGE NO. 5	
NOTE:	FIRST LINE : SEAT NO.,	NAME OF THE CANDI	DATE,	MOTHE	R, PERMANEN	NT REG. NO., PREV	/IOUS SEAT	NO., COLLEGE, SEAT NO. P/F:PASS/FAIL, C:PREV.CARR	
S14005	3014 AVHAD TEJAL VIPI	N		NAYAN	4	,71527129G	,PICT	,\$140053014	
SEM.:	1								
204181	SIGNALS & SYSTEMS	PP 100 40	33	27	60 P				
204181	SIGNALS & SYSTEMS	TW 25 10			19 P				
204182	ELECTRONIC DEVICES & C	IRCUITS PP 100 40	44	31	75 P				
204182	ELECTRONIC DEVICES & C	IRCUITS PR 50 20			38 P				
204183	NETWORK THEORY	PP 100 40	43	25	68 P				
204183	NETWORK THEORY	TW 25 10			19 P				
204184	DATA STRUCTURES & ALGO	RITHMS PP 100 40	39	33	72 P				
204184	DATA STRUCTURES & ALGO	RITHMS OR 50 20			40 P				
204185	DIGITAL ELECTRONICS		32	27	59 P				
204185	DIGITAL ELECTRONICS	PR 50 20			33 P				
204186	ELEX. MEASURING INSTRU FIRST SEM TOTAL = 521/				38 P				
								C1400F201F	
S14005	3015 AWATE NETRA NANDI	KUMAR		MEENAI	KSHI	,/152/131J	, PICI	,s140053015	
SEM.:									
	SIGNALS & SYSTEMS		25	27	52 P				
	SIGNALS & SYSTEMS				18 P				
	ELECTRONIC DEVICES & C		42	35	77 P				
	ELECTRONIC DEVICES & C.		2.5	2=	39 P				
204183	NETWORK THEORY	PP 100 40	35	37	72 P				
204183	NETWORK THEORY	TW 25 10	2.0	4.5	21 P				
204184			36	45	81 P				
204184	DATA STRUCTURES & ALGO		21	22	40 P				
204185			31	22	53 P				
		PR 50 20			30 P				
204186	ELEX. MEASURING INSTRU FIRST SEM TOTAL = 520/				37 P				
S14005	3016 AYUSH BHADAURIA			SUMAN		,71527132G	, PICT	,s140053016	
SEM.:									
	SIGNALS & SYSTEMS		24	24	48 P				
	SIGNALS & SYSTEMS				10 P				
204182	ELECTRONIC DEVICES & C	IRCUITS PP 100 40	46	33	79 P				
204182	ELECTRONIC DEVICES & C				25 P				
	NETWORK THEORY	PP 100 40	28	21	49 P				
204183	NETWORK THEORY				18 P				
204184	DATA STRUCTURES & ALGO		35	36	71 P				
204184	DATA STRUCTURES & ALGO				12 F				
204185			36	22	58 P				
204185		PR 50 20			12 F				
204186	ELEX. MEASURING INSTRU FIRST SEM TOTAL =/7				23 P				

	E: 25 JAN 2016 CENTRE:							PAGE NO. 6
NOTE: THER LIN	FIRST LINE : SEAT NO., NAME SES: SUB.CODE, SUB.NAME, SUB.T	OF THE CAN	DIDATE, ARKS, MI	MOTH	HER, PERMANEN SS MARKS, OE	T REG. NO., PREV MARKS, TH MARKS,	IOUS SEAT OBTAINED,	NO., COLLEGE, SEAT NO. P/F:PASS/FAIL, C:PREV.CARRY OVE
	3017 BADOLE ROHIT AVINASH							,S140053017
SEM.:	1							
204181	SIGNALS & SYSTEMS	PP 100 4	0 12	08	F			
204181	SIGNALS & SYSTEMS	TW 25 1	.0		21 P			
204182	ELECTRONIC DEVICES & CIRCUIT	S PP 100 4	0 26	08	F			
204182	ELECTRONIC DEVICES & CIRCUIT	S PR 50 2	0		38 P			
204183	NETWORK THEORY	PP 100 4		07	F			
204183	NETWORK THEORY				21 P			
204184	DATA STRUCTURES & ALGORITHMS	PP 100 4	0 16	33	49 P			
204184	DATA STRUCTURES & ALGORITHMS				25 P			
204185		PP 100 4		21				
204185	DIGITAL ELECTRONICS				30 P			
	ELEX. MEASURING INSTRU & TOO FIRST SEM TOTAL =/750				40 P			
	3018 BAGDARE ABHISHEK PRASH							,S140053018
SEM.:	1							
	SIGNALS & SYSTEMS	DD 100 /	0 28	2.2	60 P			
		TW 25 1		32	17 P			
	ELECTRONIC DEVICES & CIRCUIT			20	52 P			
	ELECTRONIC DEVICES & CIRCUIT			20	37 P			
204183	NETWORK THEORY	PP 100 4		15	44 P			
204183	NETWORK THEORY	TW 25 1		13	19 P			
204184	DATA STRUCTURES & ALGORITHMS			31	58 P			
204184				31	33 P			
204185	DIGITAL ELECTRONICS	PP 100 4		23	50 P			
				23	31 P			
	ELEX. MEASURING INSTRU & TOO				31 P			
	FIRST SEM TOTAL = 432/750							
	3019 BAGUL OMKAR SANJAY				SHALI			,s140053019
SEM.:	1							
	SIGNALS & SYSTEMS	PP 100 4	0 23	27	50 P			
	SIGNALS & SYSTEMS				17 P			
	ELECTRONIC DEVICES & CIRCUIT			19	62 P			
	ELECTRONIC DEVICES & CIRCUIT			-	36 P			
204183	NETWORK THEORY	PP 100 4		23	51 P			
204183	NETWORK THEORY	TW 25 1		_ =	19 P			
204184	DATA STRUCTURES & ALGORITHMS			28	64 P			
204184					25 P			
204185	DIGITAL ELECTRONICS			24	50 P			
204185	DIGITAL ELECTRONICS	PR 50 2		- '	24 P			
	ELEX. MEASURING INSTRU & TOO				31 P			
_01100	FIRST SEM TOTAL = $429/750$	50 2	-		J			

	E : 25 JAN 2016 CENTRE: P						•				
NOTE:	FIRST LINE : SEAT NO., NAME ONES: SUB.CODE, SUB.NAME, SUB.TY	F TH	HE CAN	NDIDATE,	МОТН	IER, PERM	ANENT REG.	NO., PRE	VIOUS SEAT	NO., COLLEGE,	SEAT NO.
	53020 BAHETI BALAJI NAVALKISH									,s140053	
SEM.:	:1										
204181	SIGNALS & SYSTEMS	PP	100 4	10 34	41	75 P					
204181	SIGNALS & SYSTEMS	TW	25 1	LO		21 P					
204182					34	81 P					
	ELECTRONIC DEVICES & CIRCUITS					32 P					
204183					37	73 P					
204183						22 P					
204184					35	74 P					
204184						28 P					
204185					23	56 P					
204185						30 P					
	ELEX. MEASURING INSTRU & TOOL FIRST SEM TOTAL = 537/750					45 P					
	33021 BAJAJ MADHUR SANDIP									,s140053	
SEM.:	•1										
	SIGNALS & SYSTEMS	PР	100 4	ın 11	04	F					
	SIGNALS & SYSTEMS				0.	12 P					
	ELECTRONIC DEVICES & CIRCUITS				20	40 P					
	ELECTRONIC DEVICES & CIRCUITS					10 F					
204183			100 4		11	F					
204183			25 1			18 P					
204184					29	50 P					
204184						10 F					
204185					11	F					
204185						10 F					
	ELEX. MEASURING INSTRU & TOOL					25 P					
	FIRST SEM TOTAL =/750										
	53022 BAND VIPIN DILIP					IDA				,s140053	
SEM.:	:1										
204181	SIGNALS & SYSTEMS	PP	100 4	10 20	24	44 P					
204181	SIGNALS & SYSTEMS	TW	25 1	LO		16 P					
204182	ELECTRONIC DEVICES & CIRCUITS	PP	100 4	10 40	28	68 P					
204182	ELECTRONIC DEVICES & CIRCUITS	PR	50 2	20		30 P					
204183	NETWORK THEORY	PP	100 4	10 19	22	41 P					
204183	NETWORK THEORY	TW	25 1	LO		19 P					
204184	DATA STRUCTURES & ALGORITHMS	PP	100 4	10 18	30	48 P					
204184	DATA STRUCTURES & ALGORITHMS	OR	50 2	20		9 F					
204185	DIGITAL ELECTRONICS	PP	100 4	10 23	17	40 P					
204185	DIGITAL ELECTRONICS	PR	50 2	20		25 P					
204186	ELEX. MEASURING INSTRU & TOOL	STW	50 2	20		22 P					
	FIRST SEM TOTAL =/750										

DAIE .	25 JAN 2016 CENTRE: PU	JINE	INSITIO	IE UF	COMPO	IEK IECHNULUGI. 1	PUNE.			
NOTE: FI	RST LINE : SEAT NO., NAME OF	= тн	IE CANDII	DATE,	мотні	ER, PERMANENT REG	G. NO., PRE	VIOUS SEAT	NO., COLLEGE, SEAT NO.	
OTHER LINES	: SUB.CODE, SUB.NAME, SUB.TYF	PΕ,	MAX.MARI	KS, MI	N.PAS	S MARKS, OE MARKS	S, TH MARKS,	OBTAINED,	P/F:PASS/FAIL, C:PREV.CARRY	OVER
S14005302	23 BANDAL NEHAL ROHIDAS				SUREI	KHA	,71527142D	,PICT	,s140053023	
SEM.:1										
	IGNALS & SYSTEMS			35	36	71 P				
	IGNALS & SYSTEMS					22 P				
	LECTRONIC DEVICES & CIRCUITS			44	44	88 P				
	LECTRONIC DEVICES & CIRCUITS					37 P				
	ETWORK THEORY			41	42	83 P				
	ETWORK THEORY					22 P				
	ATA STRUCTURES & ALGORITHMS			32	36	68 P				
	ATA STRUCTURES & ALGORITHMS				~-	30 P				
	IGITAL ELECTRONICS			23	27					
	IGITAL ELECTRONICS					22 P				
	LEX. MEASURING INSTRU & TOOLS	S I W	50 20			35 P				
	IRST SEM TOTAL = 528/750									
	24 BANSODE ASHWINI DIGAMBER								c140052024	• • •
314003302	24 DANSODE ASHWINI DIGAMBER	`			DAIV	АЗПАСА	,710330048	, PICI	,3140033024	
SEM.:1										
	IGNALS & SYSTEMS	DР	100 40	20	09	F				
	IGNALS & SYSTEMS			20	03	20 P				
	LECTRONIC DEVICES & CIRCUITS			30	27	57 P				
	LECTRONIC DEVICES & CIRCUITS			30	_,	35 P				
	ETWORK THEORY		100 40	19	13	F				
			25 10			20 P				
	ATA STRUCTURES & ALGORITHMS			24	32	56 P				
	ATA STRUCTURES & ALGORITHMS					25 P				
			100 40	22	20	42 P				
204185 D	IGITAL ELECTRONICS	PR	50 20			23 P				
204186 EI	LEX. MEASURING INSTRU & TOOLS	STW	50 20			39 P				
F	IRST SEM TOTAL =/750									
S14005302	25 BARAPATRE VRUNAL NARENDE	RA			VAISI	HALI	,71527143в	,PICT	,s140053025	
SEM.:1										
204181 S	IGNALS & SYSTEMS	PP	100 40	26	15	41 P				
204181 S	IGNALS & SYSTEMS	TW	25 10			16 P				
204182 EI	LECTRONIC DEVICES & CIRCUITS	PP	100 40	34	26	60 P				
204182 EI	LECTRONIC DEVICES & CIRCUITS	PR	50 20			32 P				
204183 NI	ETWORK THEORY	PP	100 40	27	00	F				
204183 NI	ETWORK THEORY	TW	25 10			17 P				
204184 DA	ATA STRUCTURES & ALGORITHMS	PP	100 40	30	22	52 P				
204184 DA	ATA STRUCTURES & ALGORITHMS	OR	50 20			30 P				
204185 D	IGITAL ELECTRONICS	PP	100 40	35	17	52 P				
204185 D	IGITAL ELECTRONICS	PR	50 20			11 F				
	LEX. MEASURING INSTRU & TOOLS	STW	50 20			33 P				
F.	IRST SEM TOTAL =/750									

	E: 25 JAN 2016 CENT					•		PAGE NO.		
NOTE:	FIRST LINE : SEAT NO., N	NAME OF THE CANDID	DATE,	MOTHE	R, PERMANENT	REG. NO., PRE	/IOUS SEAT	NO., C	OLLEGE,	SEAT NO.
	3026 BHADGAONKAR SARANG									
SEM.:	1									
204181	SIGNALS & SYSTEMS	PP 100 40	43	27	70 P					
204181	SIGNALS & SYSTEMS	TW 25 10			20 P					
204182	ELECTRONIC DEVICES & CIR	RCUITS PP 100 40	43	38	81 P					
204182	ELECTRONIC DEVICES & CIR	RCUITS PR 50 20			44 P					
204183	NETWORK THEORY	PP 100 40	44	35	79 P					
204183	NETWORK THEORY	TW 25 10			21 P					
204184	DATA STRUCTURES & ALGORI	THMS PP 100 40	33	37	70 P					
204184	DATA STRUCTURES & ALGORI	THMS OR 50 20			34 P					
204185	DIGITAL ELECTRONICS	PP 100 40	40	29	69 P					
204185	DIGITAL ELECTRONICS	PR 50 20			30 P					
	ELEX. MEASURING INSTRU & FIRST SEM TOTAL = 552/75	50			34 P					
	33027 BHIMANI JINKAL MAH									
SEM.:	1									
204181	SIGNALS & SYSTEMS	PP 100 40	22	11	F					
204181	SIGNALS & SYSTEMS	TW 25 10			21 P					
204182	ELECTRONIC DEVICES & CIR	RCUITS PP 100 40	28	31	59 P					
204182	ELECTRONIC DEVICES & CIR	RCUITS PR 50 20			42 P					
204183	NETWORK THEORY	PP 100 40	28	15	43 P					
204183	NETWORK THEORY	TW 25 10			20 P					
204184	DATA STRUCTURES & ALGORI	THMS PP 100 40	25	35	60 P					
204184	DATA STRUCTURES & ALGORI	THMS OR 50 20			34 P					
204185	DIGITAL ELECTRONICS	PP 100 40	30	23	53 P					
204185	DIGITAL ELECTRONICS	PR 50 20			38 P					
204186	ELEX. MEASURING INSTRU & FIRST SEM TOTAL =/750				40 P					
	33028 BHIRUD AKSHAY PRAD					,71527155F			 514005302	
	4									
SEM.:		DD 100 40	20	1 [44 5					
	SIGNALS & SYSTEMS		29	тэ	44 P					
	SIGNALS & SYSTEMS ELECTRONIC DEVICES & CIR		26	20	18 P 56 P					
	ELECTRONIC DEVICES & CIR		30	20	30 P					
	NETWORK THEORY	PP 100 40	20	20	37 P 40 P					
204183	NETWORK THEORY		20	20	40 P 20 P					
204183	DATA STRUCTURES & ALGORI		24	28	52 P					
204184	DATA STRUCTURES & ALGORI		۷4	۷٥	32 P 8 F					
204184			32	23	о г 55 Р					
204185	DIGITAL ELECTRONICS DIGITAL ELECTRONICS		32	۷ ع	33 P					
					33 P					
	FIRST SEM TOTAL =/750									

DATE	.6 S.E.(2014 PAT.)(ELECTRONICS E: 25 JAN 2016 CENTRE: F	PUNE	INSTIT				•			PAGE NO. 10	
NOTE:	FIRST LINE : SEAT NO., NAME O	OF TH	HE CAND	IDATE,	МОТН	IER, PE	ERMANENT REG.	NO., PRE	/IOUS SEAT	NO., COLLEGE,	SEAT NO.
	3029 BHISE AKSHAY UDDHAV									,S1400530	
SEM.:	1										
204181	SIGNALS & SYSTEMS	PP	100 40	38	21	59 F	•				
204181	SIGNALS & SYSTEMS	TW	25 10			18 F					
204182	ELECTRONIC DEVICES & CIRCUITS	S PP	100 40	41	28	69 F					
204182	ELECTRONIC DEVICES & CIRCUITS	S PR	50 20			35 F					
204183	NETWORK THEORY	PP	100 40	35	38	73 F					
204183	NETWORK THEORY	TW	25 10			21 F					
204184	DATA STRUCTURES & ALGORITHMS	PP	100 40	33	36	69 F					
204184	DATA STRUCTURES & ALGORITHMS	OR	50 20			25 F					
204185	DIGITAL ELECTRONICS	PP	100 40	33	28	61 F					
204185	DIGITAL ELECTRONICS	PR	50 20			30 F	•				
204186	ELEX. MEASURING INSTRU & TOOL FIRST SEM TOTAL = 492/750	_STW	50 20			32 F	•				
	3030 BHOITE ANIKET DEEPAK									,\$1400530	
SEM.:	1										
	SIGNALS & SYSTEMS	PР	100 40	20	20	40 F	•				
	SIGNALS & SYSTEMS					22 F					
	ELECTRONIC DEVICES & CIRCUITS				25	62 F					
	ELECTRONIC DEVICES & CIRCUITS					35 F					
	NETWORK THEORY				23	51 F					
204183	NETWORK THEORY		25 10			22 F					
204184	DATA STRUCTURES & ALGORITHMS	PP	100 40	31	28	59 F	•				
204184	DATA STRUCTURES & ALGORITHMS	OR	50 20			30 F	•				
204185	DIGITAL ELECTRONICS	PP	100 40	27	29	56 F	•				
204185	DIGITAL ELECTRONICS	PR	50 20			30 F	•				
204186	ELEX. MEASURING INSTRU & TOOL FIRST SEM TOTAL = 447/750	_STW	50 20			40 F					
s14005	3031 BHUJBAL AJINKYA RAVINDE				VIJA			 71527160в	,PICT	,\$1400530	 031
SEM.:	1										
	SIGNALS & SYSTEMS	PP	100 40	24	22	46 F	•				
	SIGNALS & SYSTEMS		25 10			17 F					
204182					15	44 F					
204182	ELECTRONIC DEVICES & CIRCUITS					32 F					
204183	NETWORK THEORY		100 40		12	F					
204183	NETWORK THEORY		25 10			18 F	•				
204184			100 40		20	50 F					
204184	DATA STRUCTURES & ALGORITHMS					22 F					
204185	DIGITAL ELECTRONICS	PP	100 40	31	16	47 F	•				
204185	DIGITAL ELECTRONICS	PR	50 20			25 F	•				
204186	ELEX. MEASURING INSTRU & TOOL FIRST SEM TOTAL =/750	_STW	50 20			35 F					

	E: 25 JAN 2016 CENTRE:							PAGE NO. 11
NOTE: HER LIN	FIRST LINE : SEAT NO., NAME (NES: SUB.CODE, SUB.NAME, SUB.T	OF THE CAN YPE, MAX.M	DIDATE,	MOTH	HER, PERMANEN SS MARKS, OE	T REG. NO., PREV MARKS, TH MARKS,	IOUS SEAT OBTAINED,	P/F:PASS/FAIL, C:PREV.CARRY OVE
	3032 BIDKAR SHUBHAM MANOJ							,S140053032
SEM.:	1							
204181	SIGNALS & SYSTEMS	PP 100 4	0 31	15	46 P			
204181	SIGNALS & SYSTEMS	TW 25 1			20 P			
	ELECTRONIC DEVICES & CIRCUIT			30	70 P			
	ELECTRONIC DEVICES & CIRCUIT				35 P			
204183	NETWORK THEORY	PP 100 4		22	44 P			
204183	NETWORK THEORY				20 P			
204184	DATA STRUCTURES & ALGORITHMS			27	64 P			
204184	DATA STRUCTURES & ALGORITHMS				26 P			
204185		PP 100 4		21				
204185	DIGITAL ELECTRONICS				29 P			
	ELEX. MEASURING INSTRU & TOO FIRST SEM TOTAL = 448/750				37 P			
	3033 BIRAJDAR RUSHABH UMAKA							,s140053033
SEM.:	1							
	SIGNALS & SYSTEMS	DD 100 /	0 20	21	41 P			
	SIGNALS & SYSTEMS			21	41 P 20 P			
	ELECTRONIC DEVICES & CIRCUIT			23	56 P			
	ELECTRONIC DEVICES & CIRCUIT			23	33 P			
204183	NETWORK THEORY	PP 100 4		20	48 P			
204183	NETWORK THEORY	TW 25 1		20	20 P			
204184	DATA STRUCTURES & ALGORITHMS			33	57 P			
204184				33	25 P			
204185	DIGITAL ELECTRONICS	PP 100 4		21	49 P			
		PR 50 2		21	32 P			
	ELEX. MEASURING INSTRU & TOO				31 P			
	FIRST SEM TOTAL = 412/750							
	3034 BOBADE SRUSHTI PRAKASH				SHANA			,s140053034
SEM.:	1							
	SIGNALS & SYSTEMS	PP 100 4	0 24	20	44 P			
	SIGNALS & SYSTEMS				19 P			
	ELECTRONIC DEVICES & CIRCUIT			16	53 P			
	ELECTRONIC DEVICES & CIRCUIT				34 P			
204183	NETWORK THEORY	PP 100 4		24	45 P			
204183	NETWORK THEORY	Tw 25 1			22 P			
204184	DATA STRUCTURES & ALGORITHMS			23	50 P			
204184				=	36 P			
204185	DIGITAL ELECTRONICS			19	43 P			
204185	DIGITAL ELECTRONICS	PR 50 2			26 P			
	ELEX. MEASURING INSTRU & TOO				38 P			
	FIRST SEM TOTAL = 410/750	-						

	6 S.E.(2014 PAT.)(ELECTRONICS : 25 JAN 2016 CENTRE: P			-	COMP	UTER T	ECHNOLOGY, PU	JNE.		PAGE NO. 12	
	FIRST LINE : SEAT NO., NAME O										
OTHER LIN	ES: SUB.CODE, SUB.NAME, SUB.TY	PE,	MAX.N	MARKS, M	IN.PA	SS MARI	KS, OE MARKS,	TH MARKS,	OBTAINED,	P/F:PASS/FAIL,	C:PREV.CARRY OVER
S14005	3035 BOMALE ADITYA SANJAY				YAS	HODA	,	71527166M	,PICT	,S140053	035
SEM.:											
	SIGNALS & SYSTEMS				31	68					
	SIGNALS & SYSTEMS					24					
204182					28	66					
204182	ELECTRONIC DEVICES & CIRCUITS				10	36					
204183	NETWORK THEORY				19	54					
204183	NETWORK THEORY				2.0	23					
204184	DATA STRUCTURES & ALGORITHMS				30	66					
204184	DATA STRUCTURES & ALGORITHMS				2.5	38					
204185	DIGITAL ELECTRONICS				25						
204185						32					
	ELEX. MEASURING INSTRU & TOOL FIRST SEM TOTAL = 497/750					35					
	3036 BORA HARSHAL MAHESH									,s140053	
SEM.:	1										
	- SIGNALS & SYSTEMS	PP	100 4	10 25	28	53	Р				
	SIGNALS & SYSTEMS					19					
	ELECTRONIC DEVICES & CIRCUITS				23	60	Р				
204182	ELECTRONIC DEVICES & CIRCUITS	PR	50 2	20		39	Р				
204183	NETWORK THEORY	PP	100 4	10 23	22	45	Р				
204183	NETWORK THEORY	TW	25 1	10		18	Р				
204184	DATA STRUCTURES & ALGORITHMS	PP	100 4	10 34	24	58	Р				
204184	DATA STRUCTURES & ALGORITHMS	OR	50 2	20		32	Р				
204185	DIGITAL ELECTRONICS	PP	100 4	40 38	15	53	Р				
204185	DIGITAL ELECTRONICS	PR	50 2	20		34	Р				
204186	ELEX. MEASURING INSTRU & TOOL	STW	50 2	20		33	Р				
	FIRST SEM TOTAL = 444/750										
	3037 BOROLE NEHAL RAJENDRA							71527169F			
SEM.:	1										
	SIGNALS & SYSTEMS	PP	100 4	40 48	34	82	Р				
	SIGNALS & SYSTEMS		25 1			22					
204182	ELECTRONIC DEVICES & CIRCUITS	PP	100 4	40 47	41	88	Р				
204182	ELECTRONIC DEVICES & CIRCUITS	PR	50 2	20		40	Р				
204183			100 4		38	75					
204183	NETWORK THEORY	TW	25 1	10		21	Р				
204184	DATA STRUCTURES & ALGORITHMS	PP	100 4	40 37	35	72	Р				
204184	DATA STRUCTURES & ALGORITHMS	OR	50 2	20		40	Р				
204185	DIGITAL ELECTRONICS				23	56					
204185	DIGITAL ELECTRONICS	PR	50 2	20		37	Р				
204186	ELEX. MEASURING INSTRU & TOOL	STW	50 2	20		40	Р				
	FIRST SEM TOTAL = 573/750										

	6 S.E.(2014 PAT.)(ELECTRONICS : 25 JAN 2016 CENTRE: P			-	E OF	СОМРИТ	ER	TECHNOLOGY,	PUNE.		PAGE NO	0. 13	
NOTE:	FIRST LINE : SEAT NO., NAME OF	F TH	HE CA	NDIDA	ATE,	MOTHE	ER,	PERMANENT RE	G. NO., PRE	VIOUS SEA	T NO.,	COLLEGE,	SEAT NO.
	3038 BOTRE AKSHAYRAJ GANPAT								,71527170K				
SEM.:	1												
204181	SIGNALS & SYSTEMS	PP	100	40	37	24	61	Р					
204181	SIGNALS & SYSTEMS	TW	25	10			21	Р					
204182	ELECTRONIC DEVICES & CIRCUITS	PP	100	40	43	34	77	Р					
204182	ELECTRONIC DEVICES & CIRCUITS	PR	50	20			38	Р					
204183	NETWORK THEORY	PP	100	40	43	25	68	Р					
204183	NETWORK THEORY	TW	25	10			21	Р					
204184	DATA STRUCTURES & ALGORITHMS	PP	100	40	35	31	66	Р					
204184	DATA STRUCTURES & ALGORITHMS	OR	50	20			35	Р					
204185	DIGITAL ELECTRONICS	PP	100	40	40	27	67	Р					
204185	DIGITAL ELECTRONICS	PR	50	20			34	Р					
204186	ELEX. MEASURING INSTRU & TOOLS FIRST SEM TOTAL = 523/750	STW	50	20			35	Р					
s14005	3039 CHANDOLE PALLAVI BHARAT												
CEM .	1												
SEM.:		DD	100	40	22	22		D					
	SIGNALS & SYSTEMS SIGNALS & SYSTEMS		25		23	32	55 22						
	ELECTRONIC DEVICES & CIRCUITS				11	3./	78						
	ELECTRONIC DEVICES & CIRCUITS				77	34	38						
204183	NETWORK THEORY		100		34	22	56						
204183			25		34	22	22						
204184	DATA STRUCTURES & ALGORITHMS				28	24	52						
204184	DATA STRUCTURES & ALGORITHMS						32						
204185			100		32	25	57						
204185	DIGITAL ELECTRONICS				<i>J</i> _	23	26						
	ELEX. MEASURING INSTRU & TOOLS						39						
201200	FIRST SEM TOTAL = 477/750	·	30				33						
S14005	3040 CHANGEDE SHRENIK SANJAY					SHAIL	AJA.		,71527174в	,PICT		,S1400530	040
SEM.:													
	SIGNALS & SYSTEMS				27	34	61						
204181	SIGNALS & SYSTEMS	TW	25	10			21	Р					
	ELECTRONIC DEVICES & CIRCUITS				45	33	78						
204182	ELECTRONIC DEVICES & CIRCUITS	PR	50	20			40	Р					
204183			100		32	34	66						
204183			25				22						
204184					40	37	77						
204184	DATA STRUCTURES & ALGORITHMS						32						
204185	DIGITAL ELECTRONICS				33	30	63						
204185	DIGITAL ELECTRONICS						33						
204186	ELEX. MEASURING INSTRU & TOOLS FIRST SEM TOTAL = 536/750	STW	50	20			43	Р					

	: 25 JAN 2016 CENTRE:										
NOTE: HER LIN	FIRST LINE : SEAT NO., NAME (ES: SUB.CODE, SUB.NAME, SUB.T	OF THE YPE, MA	CANDIDAT X.MARKS,	E, MIN	MOTHER,	PERMANI MARKS, OI	ENT REG. NO., E MARKS, TH MAR	PREVIOUS SEA KS, OBTAINED	AT NO., C O, P/F:PAS	OLLEGE, S/FAIL, (SEAT NO. C:PREV.CARRY OV
	3041 CHARVI AJAY GUPTA							6J ,PICT			
SEM.:	1										
204181	SIGNALS & SYSTEMS	PP 10	0 40 4	10	39 7	'9 P					
204181	SIGNALS & SYSTEMS	TW 2	5 10		1	.8 P					
	ELECTRONIC DEVICES & CIRCUITS			10		'6 P					
	ELECTRONIC DEVICES & CIRCUITS					89 P					
204183	NETWORK THEORY	PP 10		13	_	'9 P					
204183	NETWORK THEORY					20 P					
204184	DATA STRUCTURES & ALGORITHMS			89		32 P					
204184	DATA STRUCTURES & ALGORITHMS					88 P					
204185	DIGITAL ELECTRONICS			31		52 P					
204185	DIGITAL ELECTRONICS					84 P					
	FIRST SEM TOTAL = 558/750					81 P					
	3042 CHAUDHARI DARSHAN DURG										
SEM.:											
	SIGNALS & SYSTEMS			14		80 P					
	SIGNALS & SYSTEMS					23 P					
	ELECTRONIC DEVICES & CIRCUITS			13		87 P					
	ELECTRONIC DEVICES & CIRCUITS			_		12 P					
204183	NETWORK THEORY	PP 10		22		55 P					
204183	NETWORK THEORY	TW 2				23 P					
204184	DATA STRUCTURES & ALGORITHMS			80		59 P					
204184						85 P					
204185	DIGITAL ELECTRONICS	PP 10		89		'4 P					
	DIGITAL ELECTRONICS					32 P					
204186	FIRST SEM TOTAL = 562/750	LSTW 5	0 20		2	12 P					
	3043 CHAUDHARI MITAL DURGADA							 8E ,PICT			
							ŕ	•	,		
SEM.:		DD 10	0 40 3		10	15 5					
	SIGNALS & SYSTEMS			26		15 P					
	SIGNALS & SYSTEMS			11		20 P					
	ELECTRONIC DEVICES & CIRCUIT			ΣŢ		54 P					
	ELECTRONIC DEVICES & CIRCUITS					35 P					
204183	NETWORK THEORY	PP 10		22		16 P					
204183	NETWORK THEORY	TW 2		. 4		22 P					
204184	DATA STRUCTURES & ALGORITHMS			34		'5 P					
204184						32 P					
204185	DIGITAL ELECTRONICS			29		53 P					
204185	DIGITAL ELECTRONICS	PR 5				25 P					
204186	ELEX. MEASURING INSTRU & TOOL FIRST SEM TOTAL = 464/750	LSTW 5	0 20		3	87 P					

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NOTE: HER LIN	FIRST LINE : SEAT NO., NAME (NES: SUB.CODE, SUB.NAME, SUB.T	OF THE YPE, MA	CANDIDA X.MARKS	TE, , MI	MOTHI N.PAS	ER, PERMANE S MARKS, OE	NT REG. NO., PE MARKS, TH MARK	REVIOUS SEAT	T NO., COLLEC , P/F:PASS/FAI	GE, SEAT NO. IL, C:PREV.CARRY OV
	3044 CHAUDHARI SAMEER SATISI									
SEM.:	1									
204181	SIGNALS & SYSTEMS	PP 10	0 40	33	34	67 P				
204181	SIGNALS & SYSTEMS	TW 2	5 10			21 P				
	ELECTRONIC DEVICES & CIRCUITS			33	35	68 P				
	ELECTRONIC DEVICES & CIRCUITS					34 P				
204183	NETWORK THEORY	PP 10		33	21	54 P				
204183	NETWORK THEORY					22 P				
204184	DATA STRUCTURES & ALGORITHMS			30	31	61 P				
204184	DATA STRUCTURES & ALGORITHMS					8 F				
204185	DIGITAL ELECTRONICS			29	32					
204185	DIGITAL ELECTRONICS					31 P				
	FIRST SEM TOTAL =/750					39 P				
	3045 CHAVAN MONIKA RADHAKIS					ITA			, \$1400	
SEM.:	1									
	SIGNALS & SYSTEMS	pp 1∩	0 40	26	15	41 P				
	SIGNALS & SYSTEMS			20	13	22 P				
	ELECTRONIC DEVICES & CIRCUITS			42	34	76 P				
	ELECTRONIC DEVICES & CIRCUITS			72	J-T	70 T				
204183	NETWORK THEORY	PP 10		29	34	63 P				
204183	NETWORK THEORY	TW 2		23	<i>3</i> 1	22 P				
204184	DATA STRUCTURES & ALGORITHMS			24	38	62 P				
204184				27	30	30 P				
204185	DIGITAL ELECTRONICS	PP 10		30	28	58 P				
	DIGITAL ELECTRONICS			30	20	35 P				
	ELEX. MEASURING INSTRU & TOO					41 P				
204100	FIRST SEM TOTAL = 485/750	LSIW 3	0 20			71 1				
	3046 CHHAJED KSHITIJ SUBHASI					HYA			,s1400	
SEM.:	1									
204181	SIGNALS & SYSTEMS	PP 10	0 40	28	31	59 P				
	SIGNALS & SYSTEMS					20 P				
	ELECTRONIC DEVICES & CIRCUITS			36	28	64 P				
	ELECTRONIC DEVICES & CIRCUITS					36 P				
204183	NETWORK THEORY	PP 10		33	19	52 P				
204183	NETWORK THEORY	TW 2				20 P				
204184	DATA STRUCTURES & ALGORITHMS			35	44	79 P				
204184						35 P				
204185	DIGITAL ELECTRONICS			37	34	71 P				
204185	DIGITAL ELECTRONICS	PR 5				32 P				
	ELEX. MEASURING INSTRU & TOO					34 P				
	FIRST SEM TOTAL = $502/750$	3	- - •							

	E: 25 JAN 2016 CENTRE: P									PAGE N			
NOTE: HER LIN	FIRST LINE : SEAT NO., NAME CHES: SUB.CODE, SUB.NAME, SUB.TY)F TH 'PE,	HE CANDID MAX.MARK	ATE, S, M	MOTHE IN.PASS	ER, P S MAR	ERMANENT REG. KS, OE MARKS,	NO., PRENTH MARKS,	VIOUS SEAT OBTAINED,	NO., P/F:P	COLLEGE, ASS/FAIL,	SEAT NO. C:PREV.CARRY	OVI
	33047 CHIKHALE TEJASWINI SAYA												• • •
SEM.:	1												
204181	SIGNALS & SYSTEMS	PP	100 40	31	29	60	Р						
204181	SIGNALS & SYSTEMS	TW	25 10			22	Р						
	ELECTRONIC DEVICES & CIRCUITS			38	34	72							
	ELECTRONIC DEVICES & CIRCUITS					38							
204183	NETWORK THEORY		100 40	25	30	55							
204183	NETWORK THEORY					22							
204184	DATA STRUCTURES & ALGORITHMS			21	34	55							
204184	DATA STRUCTURES & ALGORITHMS					31							
204185	DIGITAL ELECTRONICS			38	37								
204185	DIGITAL ELECTRONICS					34							
	ELEX. MEASURING INSTRU & TOOL FIRST SEM TOTAL = 506/750					42							
	3048 CHINMAY NITIN INDURKAR							71527184к					
SEM.:	1												
	SIGNALS & SYSTEMS	DD	100 40	40	33	73	D						
	SIGNALS & SYSTEMS			70	33	21							
	ELECTRONIC DEVICES & CIRCUITS			35	24	59							
	ELECTRONIC DEVICES & CIRCUITS			55	24	39							
204183	NETWORK THEORY		100 40	30	34	64							
204183	NETWORK THEORY		25 10	30	34	21							
204184	DATA STRUCTURES & ALGORITHMS			31	36	67							
204184	DATA STRUCTURES & ALGORITHMS			71	30	32							
204185	DIGITAL ELECTRONICS		100 40	29	21	50							
204185			50 20	23	21	35							
	ELEX. MEASURING INSTRU & TOOL					39							
204180	FIRST SEM TOTAL = 500/750	.31W	30 20			33	r						
	3049 CHIRAG							 71527185н					
SEM.:	1												
	SIGNALS & SYSTEMS	pр	100 40	33	17	50	P						
	SIGNALS & SYSTEMS		25 10	,,		19							
	ELECTRONIC DEVICES & CIRCUITS			39	30	69							
	ELECTRONIC DEVICES & CIRCUITS			33	30	30							
204183	NETWORK THEORY		100 40	30	27	57							
204183	NETWORK THEORY		25 10	50	<i>-</i> 1	18							
204184	DATA STRUCTURES & ALGORITHMS			32	39	71							
204184				J.L	33	22							
204185	DIGITAL ELECTRONICS			30	23	53							
204185	DIGITAL ELECTRONICS DIGITAL ELECTRONICS		50 20	50	د ع	35							
	ELEX. MEASURING INSTRU & TOOL					34							
704T00	FIRST SEM TOTAL = 458/750	W۱ د.	JU 20			J4	Г						

	: 25 JAN 2016 CENTRE: PU											
NOTE:	FIRST LINE : SEAT NO., NAME OF ES: SUB.CODE, SUB.NAME, SUB.TYP	ТН	IE CA	NDIDA	TE,	мотн	ER, PI	ERMANENT REG.	NO., PRE	VIOUS SEAT	NO., COLLEGE	SEAT NO.
s14005	3050 CHOUKSEY SONAL NARENDRA					SHAR	DA	,	71633610D	,PICT	, \$140053	8050
SEM.:	1											
	SIGNALS & SYSTEMS	PP	100	40	27	27	54 ו	P				
204181	SIGNALS & SYSTEMS	TW	25	10			22 1	P				
204182	ELECTRONIC DEVICES & CIRCUITS	PP	100	40	39	32	71 ו	P				
204182	ELECTRONIC DEVICES & CIRCUITS	PR	50	20			40 I	P				
204183	NETWORK THEORY	PP	100	40	22	27	49 I	P				
204183	NETWORK THEORY	TW	25	10			21	P				
204184	DATA STRUCTURES & ALGORITHMS	PP	100	40	34	33	67 I	P				
204184	DATA STRUCTURES & ALGORITHMS	OR	50	20			26 1	P				
204185	DIGITAL ELECTRONICS	PP	100	40	33	27	60 I	P				
204185	DIGITAL ELECTRONICS	PR	50	20			30 I	P				
	ELEX. MEASURING INSTRU & TOOLS FIRST SEM TOTAL = 477/750						37 ।					
	3051 DABADGAONKAR SATYAM SANJ										,S140053	
SEM.:	1											
	SIGNALS & SYSTEMS	PP	100	40	32	29	61	P				
204181	SIGNALS & SYSTEMS	TW	25	10			21	P				
204182	ELECTRONIC DEVICES & CIRCUITS	PP	100	40	42	39	81	P				
204182	ELECTRONIC DEVICES & CIRCUITS	PR	50	20			39 ו	P				
204183	NETWORK THEORY	PP	100	40	33	35	68 1	P				
204183	NETWORK THEORY	TW	25	10			21	P				
204184	DATA STRUCTURES & ALGORITHMS	PP	100	40	34	31	65 I	P				
204184	DATA STRUCTURES & ALGORITHMS	OR	50	20			36 ।	P				
204185	DIGITAL ELECTRONICS	PP	100	40	37	20	57 I	P				
204185	DIGITAL ELECTRONICS	PR	50	20			31 ו	P				
204186	ELEX. MEASURING INSTRU & TOOLS FIRST SEM TOTAL = 519/750	TW	50	20			39 ।	P				
	3052 DAHIPHALE VIJAY DATTU								71527190D			
SEM.:			100	40	2.4	24	4.5	_				
	SIGNALS & SYSTEMS				24	21	45 1					
	SIGNALS & SYSTEMS				2.5	0.5	17					
204182					35	05						
	ELECTRONIC DEVICES & CIRCUITS				2.4	1.0	33					
204183	NETWORK THEORY				24	Т6	40 1					
204183	NETWORK THEORY				2 -	2.0	18					
204184	DATA STRUCTURES & ALGORITHMS				35	26	61 1					
204184	DATA STRUCTURES & ALGORITHMS				10	24	25 1					
204185	DIGITAL ELECTRONICS				19	21	40 1					
204185	DIGITAL ELECTRONICS						21					
204186	ELEX. MEASURING INSTRU & TOOLS FIRST SEM TOTAL =/750	TW	50	20			24	P				

DATE	L6 S.E.(2014 PAT.)(ELECTRONICS E: 25 JAN 2016 CENTRE: I	PUNE	INST	TTUTE									
NOTE: OTHER LIN	FIRST LINE : SEAT NO., NAME (NES: SUB.CODE, SUB.NAME, SUB.T	OF T	HE CA	ANDIDAT MARKS	TE, , MIN	MOTHE	R, P MAR	ERMANENT REG. KS, OE MARKS,	. NO., PRI , TH MARKS	EVIOUS SEAT , OBTAINED,	NO., P/F:P	COLLEGE, PASS/FAIL,	SEAT NO. C:PREV.CARRY OVE
	53053 DEOSALE JINENDRA ANJANI												
SEM.:	1												
204181	SIGNALS & SYSTEMS	PP	100	40	34	34	68	Р					
204181	SIGNALS & SYSTEMS	TW	25	10			20	Р					
204182	ELECTRONIC DEVICES & CIRCUITS	S PP	100	40 3	37	38	75	Р					
204182	ELECTRONIC DEVICES & CIRCUITS	S PR	50				36	Р					
204183	NETWORK THEORY		100		32	32	64	Р					
204183	NETWORK THEORY	TW	25	10			21	Р					
204184	DATA STRUCTURES & ALGORITHMS	PP	100	40 2	25	36	61	Р					
204184	DATA STRUCTURES & ALGORITHMS	OR	50	20			28	Р					
204185	DIGITAL ELECTRONICS	PP	100	40 2	29	29	58	Р					
204185	DIGITAL ELECTRONICS	PR	50	20			33	Р					
204186	ELEX. MEASURING INSTRU & TOOI FIRST SEM TOTAL = 497/750	LSTW	50	20			33	P					
	33054 DESHMUKH RUCHA UMESH							,					
SEM.:	.1												
	SIGNALS & SYSTEMS	PР	100	40	34	39	73	P					
	SIGNALS & SYSTEMS				,	33	22						
	ELECTRONIC DEVICES & CIRCUITS			_	41	26	67						
	ELECTRONIC DEVICES & CIRCUITS					20	35						
204183	NETWORK THEORY		100		36	35	71						
204183	NETWORK THEORY		25		50	33	23						
204184	DATA STRUCTURES & ALGORITHMS				28	44	72						
204184					20	77	42						
	DIGITAL ELECTRONICS		100		38	33	71						
	DIGITAL ELECTRONICS		50		50	33	37						
							40						
204180	FIRST SEM TOTAL = 553/750	LSIW	30	20			40	r					
	33055 DESHMUKH TANMAYI VINOD								 ,71527199н				
SEM.:	1												
	SIGNALS & SYSTEMS	PP	100	40 1	18	26	44	Р					
	SIGNALS & SYSTEMS		25				18						
	ELECTRONIC DEVICES & CIRCUITS				36	22	58						
	ELECTRONIC DEVICES & CIRCUITS						22						
204183	NETWORK THEORY		100		21	19	40						
204183	NETWORK THEORY		25				20						
204184	DATA STRUCTURES & ALGORITHMS				19	29	48						
204184							9						
204185	DIGITAL ELECTRONICS		100		29	33	62						
	DIGITAL ELECTRONICS		50				22						
204186	ELEX. MEASURING INSTRU & TOOI						34						
20,1100	FIRST SEM TOTAL =/750		30				J r	•					

NOTE: HER LIN	FIRST LINE : SEAT NO., NAME O	OF THE CAND PE, MAX.MA	DIDATE, ARKS, M	MOTH IN.PAS	IER, PERMANEN SS MARKS, OE	T REG. NO., PREN MARKS, TH MARKS,	IOUS SEAT OBTAINED,	NO., COLLEGE, SEAT NO. P/F:PASS/FAIL, C:PREV.CARRY OV
	3056 DESHPANDE MANSI VASANT							,s140053056
SEM.:	1							
204181	SIGNALS & SYSTEMS	PP 100 40	38	32	70 P			
204181	SIGNALS & SYSTEMS	TW 25 10)		16 P			
	ELECTRONIC DEVICES & CIRCUITS			35	76 P			
	ELECTRONIC DEVICES & CIRCUITS				36 P			
204183	NETWORK THEORY	PP 100 40		30	63 P			
204183	NETWORK THEORY				21 P			
204184	DATA STRUCTURES & ALGORITHMS			37	76 P			
204184	DATA STRUCTURES & ALGORITHMS				29 P			
204185		PP 100 40		27				
204185	DIGITAL ELECTRONICS				35 P			
	ELEX. MEASURING INSTRU & TOOL FIRST SEM TOTAL = 521/750				37 P			
	3057 DESHPANDE RISHIKESH RA							,s140053057
SEM.:	1							
	SIGNALS & SYSTEMS	DD 100 40	20	08	=			
	SIGNALS & SYSTEMS			00	15 P			
	ELECTRONIC DEVICES & CIRCUITS			0.8	F			
	ELECTRONIC DEVICES & CIRCUITS			00	30 P			
204183	NETWORK THEORY	PP 100 40		12	F			
204183	NETWORK THEORY	TW 25 10		12	18 P			
204184	DATA STRUCTURES & ALGORITHMS			07	F			
204184				07	23 P			
204185	DIGITAL ELECTRONICS	PP 100 40		21	40 P			
	DIGITAL ELECTRONICS			21	15 F			
	ELEX. MEASURING INSTRU & TOOL				29 P			
204100	FIRST SEM TOTAL =/750	.31W 30 20	,		23 F			
	3058 DESHPANDE SUKHADA SUNIL							,s140053058
SEM.:	1							
	- SIGNALS & SYSTEMS	PP 100 40	26	30	56 P			
	SIGNALS & SYSTEMS				20 P			
	ELECTRONIC DEVICES & CIRCUITS			41	81 P			
	ELECTRONIC DEVICES & CIRCUITS				36 P			
204183	NETWORK THEORY			30	63 P			
204183	NETWORK THEORY	TW 25 10		-	23 P			
204184	DATA STRUCTURES & ALGORITHMS			39	72 P			
204184					23 P			
204185	DIGITAL ELECTRONICS			39	71 P			
204185		PR 50 20			28 P			
	ELEX. MEASURING INSTRU & TOOL				37 P			
- 1_00	FIRST SEM TOTAL = 510/750	2020			•			

	E: 25 JAN 2016 CENTRE: I							PAGE NO. 20
NOTE: THER LIN	FIRST LINE : SEAT NO., NAME (NES: SUB.CODE, SUB.NAME, SUB.T	OF THE CA	NDIDATE, MARKS, M	MOTH IN.PAS	ER, PERMANE S MARKS, OE	NT REG. NO., PRE MARKS, TH MARKS,	VIOUS SEAT OBTAINED,	P/F:PASS/FAIL, C:PREV.CARRY OVE
	3059 DESHPANDE UMANG SATISH							,s140053059
SEM.:	1							
204181	SIGNALS & SYSTEMS	PP 100	40 28	35	63 P			
204181	SIGNALS & SYSTEMS	TW 25			20 P			
204182	ELECTRONIC DEVICES & CIRCUITS	S PP 100	40 42	39	81 P			
204182	ELECTRONIC DEVICES & CIRCUITS	S PR 50			36 P			
204183	NETWORK THEORY	PP 100		30	62 P			
204183	NETWORK THEORY				21 P			
204184	DATA STRUCTURES & ALGORITHMS	PP 100	40 37	44	81 P			
204184	DATA STRUCTURES & ALGORITHMS	OR 50	20		25 P			
204185		PP 100		26				
204185	DIGITAL ELECTRONICS	PR 50	20		42 P			
	ELEX. MEASURING INSTRU & TOOK FIRST SEM TOTAL = 525/750				33 P			
	3060 DEVADE SAYALI KALYAN							,s140053060
SEM.:	1							
	SIGNALS & SYSTEMS	PP 100	40 26	21	47 P			
	SIGNALS & SYSTEMS			21	21 P			
	ELECTRONIC DEVICES & CIRCUITS			22	59 P			
	ELECTRONIC DEVICES & CIRCUITS			22	30 P			
204183	NETWORK THEORY	PP 100		24	45 P			
204183	NETWORK THEORY	TW 25		24	22 P			
204184	DATA STRUCTURES & ALGORITHMS			36	60 P			
204184				30	30 P			
204185	DIGITAL ELECTRONICS	PP 100		10	42 P			
	DIGITAL ELECTRONICS			13	32 P			
	ELEX. MEASURING INSTRU & TOO				42 P			
204100	FIRST SEM TOTAL = 430/750	LSIW 30	20		72 1			
	33061 DEVANG VINAY DAVE				 А			,s140053061
SEM.:	1							
	SIGNALS & SYSTEMS	PP 100	40 28	18	46 P			
	SIGNALS & SYSTEMS			— -	19 P			
	ELECTRONIC DEVICES & CIRCUITS			36	73 P			
	ELECTRONIC DEVICES & CIRCUITS				39 P			
204183	NETWORK THEORY			26	46 P			
204183	NETWORK THEORY	TW 25		= *	20 P			
204184	DATA STRUCTURES & ALGORITHMS			43	69 P			
204184					8 F			
204185	DIGITAL ELECTRONICS			28	59 P			
204185		PR 50			33 P			
	ELEX. MEASURING INSTRU & TOO				34 P			
_0.100	FIRST SEM TOTAL =/750				J			

	.6 S.E.(2014 PAT.)(ELECTRONICS E: 25 JAN 2016 CENTRE: P		-		COMPIL	ITER TECHNOLOGY R	DINE		PAGE NO 21	
	CENTRE: 1									
NOTE:	FIRST LINE : SEAT NO., NAME O	FT	HE CAND	IDATE,	MOTH	IER, PERMANENT REG	. NO., PRE	VIOUS SEAT	NO., COLLEGE,	SEAT NO.
OTHER LIN	IES: SUB.CODE, SUB.NAME, SUB.TY	PE,	MAX.MA	RKS, MI	N.PAS	SS MARKS, OE MARKS	, TH MARKS,	OBTAINED,	P/F:PASS/FAIL, (::PREV.CARRY OVER
s14005	3062 DHAKANE KIRANKUMAR SHAH	ADE	0		SHOB	SHA	,71633612L	,PICT	,S14005306	52
SEM.:			100 40		2.5	F.4 -				
	SIGNALS & SYSTEMS									
	SIGNALS & SYSTEMS					22 P				
204182					22	56 P				
204182	ELECTRONIC DEVICES & CIRCUITS				27	46 P				
204183	NETWORK THEORY				27	50 P				
204183	NETWORK THEORY				20	21 P				
204184	DATA STRUCTURES & ALGORITHMS				30	70 P				
204184					22	25 P				
204185					22					
204185	ELEX. MEASURING INSTRU & TOOL					30 P				
204166	FIRST SEM TOTAL = 459/750	SIW	30 20	,		41 P				
	FIRST SEM TOTAL = 439/730									
	33063 DHONDGE KANCHAN VIJAY									
314003	JOOS BHONDGE KANCHAN VIJAT				NAND	, I I V I	,710330133	,1101	,514005500	,,,
SEM.:	1									
	SIGNALS & SYSTEMS	PР	100 40	33	17	50 P				
	SIGNALS & SYSTEMS					24 P				
	ELECTRONIC DEVICES & CIRCUITS				35	76 P				
	ELECTRONIC DEVICES & CIRCUITS				33	43 P				
204183			100 40		29	57 P				
204183	NETWORK THEORY		25 10		•	22 P				
204184					35	57 P				
204184		OR	50 20)		27 P				
204185			100 40		28	66 P				
204185						26 P				
	ELEX. MEASURING INSTRU & TOOL	STW	50 20)		45 P				
	FIRST SEM TOTAL = 493/750									
S14005	3064 DHUMALE AJINKYA DNYANES	HWA	R		SUNI	TA	,71633614G	,PICT	, \$14005306	54
SEM.:	1									
204181	SIGNALS & SYSTEMS	PP	100 40	19	21	40 P				
204181	SIGNALS & SYSTEMS	TW	25 10)		21 P				
204182	ELECTRONIC DEVICES & CIRCUITS	PP	100 40	21	26	47 P				
204182	ELECTRONIC DEVICES & CIRCUITS	PR	50 20)		30 P				
204183	NETWORK THEORY	PP	100 40	20	27	47 P				
204183	NETWORK THEORY	TW	25 10)		19 P				
204184	DATA STRUCTURES & ALGORITHMS	PP	100 40	27	34	61 P				
204184	DATA STRUCTURES & ALGORITHMS	OR	50 20)		26 P				
204185	DIGITAL ELECTRONICS	PP	100 40	20	21	41 P				
204185	DIGITAL ELECTRONICS	PR	50 20)		13 F				
204186	ELEX. MEASURING INSTRU & TOOL	STW	50 20)		34 P				
	FIRST SEM TOTAL =/750									

	.6 S.E.(2014 PAT.)(ELECTRONICS		-	UTE OF	COMPLI	ITED TE	CUNOLOGY DU	INIT		DACE NO. 22	
	E : 25 JAN 2016 CENTRE: P										
NOTE:	FIRST LINE : SEAT NO., NAME O	F T	HE CAND	IDATE,	мотн	IER, PE	ERMANENT REG.	NO., PRE	VIOUS SEAT	NO., COLLEGE,	SEAT NO.
OTHER LIN	IES: SUB.CODE, SUB.NAME, SUB.TY	PΕ,	MAX.MAI	RKS, MI	N.PAS	S MARK	KS, OE MARKS,	TH MARKS,	OBTAINED,	P/F:PASS/FAIL,	C:PREV.CARRY OVER
S14005	3065 DIKHALE SNEHAL SUBHASH				JAYS	HREE	,	71527217K	,PICT	,S140053	3065
SEM.:											
	SIGNALS & SYSTEMS				34						
	SIGNALS & SYSTEMS					21 F					
204182					24	64 F					
204182	ELECTRONIC DEVICES & CIRCUITS				24	35 F					
204183	NETWORK THEORY				31	61 F					
204183	NETWORK THEORY				20	22 F					
204184	DATA STRUCTURES & ALGORITHMS				39	79 F					
204184					20	21 F					
204185 204185					30	60 F 21 F					
	ELEX. MEASURING INSTRU & TOOL					39 F					
204180	FIRST SEM TOTAL = 485/750	SIW	30 20			39 F					
	FIRST SEM TOTAL = 463/730										
										,\$140053	
31.003	Jobb Bobb Manage Management				1-17 (1511	., (• ±	,	710330132	,, 101	,511005	,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,
SEM.:	1										
	SIGNALS & SYSTEMS	PP	100 40	27	23	50 F	•				
	SIGNALS & SYSTEMS					22 F					
	ELECTRONIC DEVICES & CIRCUITS				29	66 F					
	ELECTRONIC DEVICES & CIRCUITS					38 F					
204183			100 40		24	43 F					
204183	NETWORK THEORY	TW	25 10			21 F	•				
204184	DATA STRUCTURES & ALGORITHMS	PP	100 40	31	39	70 F	•				
204184	DATA STRUCTURES & ALGORITHMS	OR	50 20			27 F	•				
204185	DIGITAL ELECTRONICS	PP	100 40	37	33	70 F	•				
204185	DIGITAL ELECTRONICS	PR	50 20			24 F	•				
204186	ELEX. MEASURING INSTRU & TOOL	STW	50 20			37 F	•				
	FIRST SEM TOTAL = 468/750										
s14005	3067 DONGRE ADITYA SANDIP				SANG	SITA	,	71527221H	,PICT	,S140053	3067
SEM.:											
	SIGNALS & SYSTEMS				15	40 F	•				
	SIGNALS & SYSTEMS		25 10			10 F					
	ELECTRONIC DEVICES & CIRCUITS				07	F					
	ELECTRONIC DEVICES & CIRCUITS					23 F					
204183			100 40		22	40 F					
204183			25 10		24	16 F					
204184					3 T	52 F					
204184					10	7 F					
204185	DIGITAL ELECTRONICS				TR	40 F					
204185			50 20			22 F					
204186		5 I W	50 20			26 F	•				
	FIRST SEM TOTAL =/750										

	6 S.E.(2014 PAT.)(ELECTRONICS : 25 JAN 2016 CENTRE: P			-	COMPU	JTER TECHNO	LOGY, PU	INE.		PAGE NO. 23	
NOTE:	FIRST LINE : SEAT NO., NAME O ES: SUB.CODE, SUB.NAME, SUB.TY	 F T	HE CAN	IDIDATE,	МОТН	IER, PERMAN	ENT REG.	NO., PRE	 VIOUS SEAT	NO., COLLEGE,	SEAT NO.
S14005	3068 GADIYA RUSHABH KANTILAL				SUNI	TA	,	71633616C	,PICT	,S140053	068
SEM.:											
	SIGNALS & SYSTEMS				24						
	SIGNALS & SYSTEMS				2.2	22 P					
204182	ELECTRONIC DEVICES & CIRCUITS				32	65 P					
204182	ELECTRONIC DEVICES & CIRCUITS				2.4	37 P					
204183	NETWORK THEORY				24	53 P					
204183	NETWORK THEORY				42	22 P					
204184 204184	DATA STRUCTURES & ALGORITHMS DATA STRUCTURES & ALGORITHMS				43	72 P 30 P					
204184	DIGITAL ELECTRONICS				30						
204185					30	30 P 31 P					
	ELEX. MEASURING INSTRU & TOOL					36 P					
204100	FIRST SEM TOTAL = 471/750	SIW	30 2	.0		30 F					
	3069 GAIKWAD SAMRUDDHI AVINA										
CEM .	1										
SEM.:	SIGNALS & SYSTEMS	D.D.	100 4	۱۸ 21	26	57 D					
	SIGNALS & SYSTEMS				20	57 P 18 P					
	ELECTRONIC DEVICES & CIRCUITS				30	72 P					
	ELECTRONIC DEVICES & CIRCUITS				30	72 P					
204183	NETWORK THEORY		100 4		34	63 P					
204183	NETWORK THEORY		25 1		٥.	21 P					
204184	DATA STRUCTURES & ALGORITHMS				31	59 P					
204184	DATA STRUCTURES & ALGORITHMS					24 P					
204185	DIGITAL ELECTRONICS		100 4		32	54 P					
204185	DIGITAL ELECTRONICS		50 2			31 P					
204186	ELEX. MEASURING INSTRU & TOOL	STW	50 2	20		36 P					
	FIRST SEM TOTAL = 471/750										
	3070 GANGISHETTI SRAVAN				PADM			71527226J			
SEM.:	1										
	SIGNALS & SYSTEMS	PР	100 4	10 42	41	83 P					
	SIGNALS & SYSTEMS		25 1			23 P					
	ELECTRONIC DEVICES & CIRCUITS				43	91 P					
	ELECTRONIC DEVICES & CIRCUITS				• =	37 P					
204183	NETWORK THEORY		100 4		48	84 P					
204183	NETWORK THEORY		25 1			23 P					
204184					46	86 P					
204184						32 P					
204185	DIGITAL ELECTRONICS		100 4		30	69 P					
204185	DIGITAL ELECTRONICS		50 2			38 P					
204186	ELEX. MEASURING INSTRU & TOOL	STW	50 2	20		40 P					
	FIRST SEM TOTAL = 606/750										

BRANCH: 16 S.E.(2014 PAT.)(ELECTRONICS &TELECOM)

	E: 25 JAN 2016 CENTRE:										
NOTE:	FIRST LINE : SEAT NO., NAME	OF TH	IE CAI	NDIDATE	Ξ, Μ	OTHER,	PERMANENT	REG. NO., P	REVIOUS SEA	T NO., COLLEGE,	SEAT NO.
	IES: SUB.CODE, SUB.NAME, SUB.										
	3071 GANGRAS ANURAG ASHUTO										
								,	,	,	
SEM.:					_		_				
	SIGNALS & SYSTEMS				5 2		6 P				
	SIGNALS & SYSTEMS						0 P				
	ELECTRONIC DEVICES & CIRCUIT				9 2		4 P				
	ELECTRONIC DEVICES & CIRCUIT						4 P				
204183	NETWORK THEORY				3		0 P				
204183	NETWORK THEORY				2 4		6 P				
204184	DATA STRUCTURES & ALGORITHM				3 4		8 P				
204184	DATA STRUCTURES & ALGORITHM				7 2		0 P				
204185					7 2		7 P				
	DIGITAL ELECTRONICS						A F				
	ELEX. MEASURING INSTRU & TOO FIRST SEM TOTAL =/750						2 P				
	3072 GANVIR SAVINAY ARVIND									,s140053	
SEM.:	1										
	SIGNALS & SYSTEMS	PP	100 4	10 29	9 2	1 5	0 P				
	SIGNALS & SYSTEMS				_		5 P				
	ELECTRONIC DEVICES & CIRCUIT				1 2		2 P				
	ELECTRONIC DEVICES & CIRCUIT						4 P				
204183	NETWORK THEORY		100 4		4 2		2 P				
204183	NETWORK THEORY	TW	25 :				9 P				
204184	DATA STRUCTURES & ALGORITHM	S PP	100 4	40 29	9 4	1 7	0 P				
204184	DATA STRUCTURES & ALGORITHM	S OR	50 2	20		2	4 P				
204185	DIGITAL ELECTRONICS	PP	100 4	40 28	3 2	5 5	3 P				
204185	DIGITAL ELECTRONICS	PR	50 2	20		2	6 P				
204186	ELEX. MEASURING INSTRU & TO	OLSTW	50 2	20		2	4 P				
	FIRST SEM TOTAL = 419/750										
	3073 GARJE PRIYANKA BABASA									,s140053	
SEM.:		D.D.	100	10 20	1 1	7 4	7 D				
	SIGNALS & SYSTEMS) 1		7 P				
	SIGNALS & SYSTEMS ELECTRONIC DEVICES & CIRCUIT				7 1		6 P 5 P				
					, Т		5 P				
	ELECTRONIC DEVICES & CIRCUIT NETWORK THEORY		100		2 2						
204183			25 2		2 2		2 P 9 P				
204183	NETWORK THEORY DATA STRUCTURES & ALGORITHM:				3 3		9 P 2 P				
204184	DATA STRUCTURES & ALGORITHM				ر ح		2 P F				
204184					3 2	_	- 6 Р				
204185	DIGITAL ELECTRONICS DIGITAL ELECTRONICS		50 2		∠ ر		6 Р				
	ELEX. MEASURING INSTRU & TO						о Р 3 Р				
704T00	FIRST SEM TOTAL =/750	OLSIW	JU 1	_0		3	J F				

	6 S.E.(2014 PAT.)(ELECTRONICS		-	TE 05	COMPLI	TED T	TECHNOLOGY BU	INIT		DACE NO. 25	
	: 25 JAN 2016 CENTRE: F									PAGE NO. 25	
	FIRST LINE : SEAT NO., NAME (
	IES: SUB.CODE, SUB.NAME, SUB.TY			•		•		•		•	
	3074 GAWAI PRATIK MADHUKARRA										
311003	307 T GAMALE FRONTER PROBLEM (A)	.0			51111	10 (L) (,	7 1327 23 TK	,, 101	,511005	707 1
SEM.:	1										
	SIGNALS & SYSTEMS	PP	100 40	19	21	40	P				
	SIGNALS & SYSTEMS			13		18					
	ELECTRONIC DEVICES & CIRCUITS			29	15	44					
204182	ELECTRONIC DEVICES & CIRCUITS					33					
204183	NETWORK THEORY			18	12						
204183	NETWORK THEORY					20					
204184	DATA STRUCTURES & ALGORITHMS			20	22	42					
	DATA STRUCTURES & ALGORITHMS			20		25					
	DIGITAL ELECTRONICS			19	26						
	DIGITAL ELECTRONICS				20						
	ELEX. MEASURING INSTRU & TOOL					31					
201100	FIRST SEM TOTAL = $/750$		30 20			31	•				
	TIRST SEM TOTAL - 7750										
										,S140053	
311003	SOF GAWARDE RARTIR DIET				SANDI		,	713272301	,1 101	,511005	,013
SEM.:	1										
	SIGNALS & SYSTEMS	DD	100 40	23	18	41	D				
	SIGNALS & SYSTEMS			23	10	13					
	ELECTRONIC DEVICES & CIRCUITS			30	15	45					
	ELECTRONIC DEVICES & CIRCUITS			30	13	30					
204183	NETWORK THEORY		100 40	30	26	56					
204183	NETWORK THEORY		25 10	30	20	18					
204184	DATA STRUCTURES & ALGORITHMS			31	34	65					
204184	DATA STRUCTURES & ALGORITHMS DATA STRUCTURES & ALGORITHMS			31	34	28					
204184			100 40	33	20	62					
	DIGITAL ELECTRONICS		50 20	33	23	28					
	ELEX. MEASURING INSTRU & TOOL					28					
204166		LSIW	30 20			20	r				
	FIRST SEM TOTAL = 414/750										
	2076 CHARLETIC CHELLAL TAXMANE										
514005	3076 GHADLING SNEHAL JAYWAN	Į.			AASH	А	,	71633617M	, PICI	,S140053	0076
CEM .	1										
SEM.:		DD	100 40	10	17		F				
	SIGNALS & SYSTEMS		100 40	13	17						
	SIGNALS & SYSTEMS		25 10	26	20	22					
	ELECTRONIC DEVICES & CIRCUITS			36	28	64					
	ELECTRONIC DEVICES & CIRCUITS			2.0	2.6	25					
204183	NETWORK THEORY		100 40	28	26	54					
204183	NETWORK THEORY		25 10	2.4	24	22					
	DATA STRUCTURES & ALGORITHMS			24	31	55					
204184				2 -	2.0	22					
204185	DIGITAL ELECTRONICS		100 40	25	28	53					
204185	DIGITAL ELECTRONICS		50 20			26					
204186	ELEX. MEASURING INSTRU & TOOL	LSTW	50 20			39	P				
	FIRST SEM TOTAL =/750										

BRANCH: 16 S.E.(2014 PAT.)(ELECTRONICS &TELECOM)

	: 25 JAN 2016 CENTRE: PU											
NOTE:	FIRST LINE : SEAT NO., NAME OF ES: SUB.CODE, SUB.NAME, SUB.TYP	ТН	IE CA	NDIDA	ATE,	MOTHE	ER, F	PERMANENT REG	. NO., PRE	/IOUS SEAT	NO., COLLEGE,	SEAT NO.
S14005	3077 GUPTA SANKALP RAJESH					SANDH	łΥΑ		,71527247м	,PICT	,S140053	077
SEM.:	1											
	SIGNALS & SYSTEMS	PP	100	40	16	13		F				
204181	SIGNALS & SYSTEMS	TW	25	10			18	Р				
204182	ELECTRONIC DEVICES & CIRCUITS	PP	100	40	27	21	48	Р				
204182	ELECTRONIC DEVICES & CIRCUITS	PR	50	20			34	Р				
204183	NETWORK THEORY	PP	100	40	27	21	48	Р				
204183	NETWORK THEORY	TW	25	10			21	Р				
204184	DATA STRUCTURES & ALGORITHMS	PP	100	40	22	36	58	Р				
204184	DATA STRUCTURES & ALGORITHMS	OR	50	20			9	F				
204185	DIGITAL ELECTRONICS	PP	100	40	30	20	50	Р				
204185	DIGITAL ELECTRONICS	PR	50	20			26	Р				
	ELEX. MEASURING INSTRU & TOOLS FIRST SEM TOTAL =/750						31					
	3078 GWALHERKAR KRUTIKA MANOJ											
SEM.:	1											
	SIGNALS & SYSTEMS	PP	100	40	24	19	43	Р				
204181	SIGNALS & SYSTEMS	TW	25	10			18	Р				
204182	ELECTRONIC DEVICES & CIRCUITS	PP	100	40	42	36	78	Р				
204182	ELECTRONIC DEVICES & CIRCUITS	PR	50	20			35	Р				
204183	NETWORK THEORY	PP	100	40	39	20	59	Р				
204183	NETWORK THEORY	TW	25	10			19	Р				
204184	DATA STRUCTURES & ALGORITHMS	PP	100	40	29	38	67	Р				
204184	DATA STRUCTURES & ALGORITHMS	OR	50	20			21	Р				
204185	DIGITAL ELECTRONICS	PP	100	40	26	25	51	Р				
204185	DIGITAL ELECTRONICS	PR	50	20			28	Р				
204186	ELEX. MEASURING INSTRU & TOOLS FIRST SEM TOTAL = 453/750	STW	50	20			34	P				
	3079 HALYAL SHUBHAM RAJESH								, 71527249н			
SEM.:	1											
	SIGNALS & SYSTEMS	pр	100	40	33	15	48	P				
	SIGNALS & SYSTEMS				55		18					
204182					34	24	58					
	ELECTRONIC DEVICES & CIRCUITS				٠.	- '	38					
204183	NETWORK THEORY				35	28	63					
204183	NETWORK THEORY					_0	20					
204184	DATA STRUCTURES & ALGORITHMS				34	34	68					
204184	DATA STRUCTURES & ALGORITHMS						23					
204185	DIGITAL ELECTRONICS				34	24	58					
204185			50				29					
204186	ELEX. MEASURING INSTRU & TOOLS						35					

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	3080 HEMANT KUMAR DUKRE							,71527251к				
SEM.:												
204181	SIGNALS & SYSTEMS	PP	100 40	21	33	54	Р					
204181	SIGNALS & SYSTEMS	TW	25 10			18	Р					
	ELECTRONIC DEVICES & CIRCUIT			38	28	66						
	ELECTRONIC DEVICES & CIRCUIT					34						
204183	NETWORK THEORY		100 40	30	38	68						
204183	NETWORK THEORY					20						
204184	DATA STRUCTURES & ALGORITHMS			46	40	86						
204184	DATA STRUCTURES & ALGORITHMS					26						
204185	DIGITAL ELECTRONICS			34	20	54						
204185	DIGITAL ELECTRONICS					29						
	ELEX. MEASURING INSTRU & TOO FIRST SEM TOTAL = 490/750					35						
	3081 INGALE RAVEENA RAVINDR							, 71527255в				
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SEM.:	SIGNALS & SYSTEMS	D D	100 40	20	20	40	D					
				20	20	40						
	SIGNALS & SYSTEMS			2 5	22	21						
	ELECTRONIC DEVICES & CIRCUIT			35	33	68						
204182	ELECTRONIC DEVICES & CIRCUIT NETWORK THEORY		100 40	41	34	35 75						
204183	NETWORK THEORY		25 10	41	34	20						
204183	DATA STRUCTURES & ALGORITHMS			24	44	68						
204184				24	44	24						
				25	22							
204185	DIGITAL ELECTRONICS		100 40	23	22	47						
						30						
204186	ELEX. MEASURING INSTRU & TOO FIRST SEM TOTAL = 460/750	LSIW	30 20			32	P					
	3082 ISHAN TRIPATHI				UMA			, 71527257J				
SEM.:	1											
	SIGNALS & SYSTEMS	pр	100 40	21	21	42	P					
	SIGNALS & SYSTEMS		25 10			17						
	ELECTRONIC DEVICES & CIRCUIT			37	22	59						
	ELECTRONIC DEVICES & CIRCUIT			<i>31</i>		28						
204183	NETWORK THEORY		100 40	29	24	53						
204183	NETWORK THEORY		25 10	_ ,	4 T	19						
204183	DATA STRUCTURES & ALGORITHMS			30	34	64						
204184				50	J-T	25						
204185	DIGITAL ELECTRONICS			24	20	44						
204185	DIGITAL ELECTRONICS DIGITAL ELECTRONICS		50 20	4	20	22						
	ELEX. MEASURING INSTRU & TOO					36						
∠∪ 1 100	FIRST SEM TOTAL = 409/750	LJIW	JU 20			30	Г					

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	3083 JADHAV ASHRU BABAN									,\$1400530	
SEM.:	1										
204181	SIGNALS & SYSTEMS	PP	100 4	0 26	15	41	P				
204181	SIGNALS & SYSTEMS	TW	25 1	.0		21	P				
204182	ELECTRONIC DEVICES & CIRC	UITS PP	100 4	0 34	40	74	P				
204182	ELECTRONIC DEVICES & CIRC	UITS PR	50 2	20		37	P				
204183	NETWORK THEORY	PP	100 4	0 35	31	66	P				
204183	NETWORK THEORY	TW	25 1	.0		19	P				
204184	DATA STRUCTURES & ALGORIT	HMS PP	100 4	0 28	45	73	P				
204184	DATA STRUCTURES & ALGORIT	HMS OR	50 2	20		8	F				
204185	DIGITAL ELECTRONICS				27	55	P				
204185	DIGITAL ELECTRONICS	PR	50 2	20		31	P				
	ELEX. MEASURING INSTRU & FIRST SEM TOTAL =/750					38					
	3084 JADHAV PRASANNA YESI										
SEM.:	1										
204181	SIGNALS & SYSTEMS	PP	100 4	0 31	35	66	P				
204181	SIGNALS & SYSTEMS	TW	25 1	.0		23	P				
204182	ELECTRONIC DEVICES & CIRC	UITS PP	100 4	0 47	46	93	P				
204182	ELECTRONIC DEVICES & CIRC	UITS PR	50 2	20		42	P				
204183	NETWORK THEORY	PP	100 4	0 33	39	72	P				
204183	NETWORK THEORY	TW	25 1	.0		23	P				
204184	DATA STRUCTURES & ALGORIT	HMS PP	100 4	0 34	38	72	P				
204184	DATA STRUCTURES & ALGORIT	HMS OR	50 2	<u>.</u> 0		34	P				
204185	DIGITAL ELECTRONICS	PP	100 4	0 39	32	71	P				
204185	DIGITAL ELECTRONICS	PR	50 2	20		31	P				
204186	ELEX. MEASURING INSTRU & FIRST SEM TOTAL = 568/750		50 2	20		41	P				
	3085 JADHAV SANJIVANI PR							, 71633618к			
<u>-</u>	4										
SEM.:		55	100 4	0 44	24	70	n.				
	SIGNALS & SYSTEMS		100 4		34	78					
	SIGNALS & SYSTEMS		25 1		41	24					
	ELECTRONIC DEVICES & CIRC				41	88					
	ELECTRONIC DEVICES & CIRC				40	40					
	NETWORK THEORY		100 4		49	81					
204183	NETWORK THEORY		25 1		4.2	23					
204184	DATA STRUCTURES & ALGORITI				43	76					
204184	DATA STRUCTURES & ALGORITH				20	32					
204185		PP :			30	68					
204185	DIGITAL ELECTRONICS		50 2			44					
204186	ELEX. MEASURING INSTRU & FIRST SEM TOTAL = 600/750		50 2	:U		46	P				

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	53086 JAMBHALIKAR ADITYA RAVI								,S140053086
SEM.:	1								
	SIGNALS & SYSTEMS				16	43 P			
204181	SIGNALS & SYSTEMS	TW	25 10)		21 P			
204182					32	63 P			
	ELECTRONIC DEVICES & CIRCUITS					43 P			
204183					24	50 P			
204183	NETWORK THEORY					21 P			
204184					32	66 P			
204184						21 P			
204185					35	62 P			
204185						38 P			
204186	ELEX. MEASURING INSTRU & TOOL FIRST SEM TOTAL = 459/750	.STW	50 20)		31 P			
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	SIGNALS & SYSTEMS				23	50 P			
	SIGNALS & SYSTEMS					18 P			
	ELECTRONIC DEVICES & CIRCUITS				35	70 P			
	ELECTRONIC DEVICES & CIRCUITS					34 P			
204183	NETWORK THEORY		100 40		31	63 P			
204183	NETWORK THEORY		25 10			19 P			
204184					45	80 P			
204184						36 P			
204185			100 40		27	53 P			
204185			50 20			37 P			
204186	ELEX. MEASURING INSTRU & TOOL FIRST SEM TOTAL = 495/750	.STW	50 20)		35 P			
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314003	JIGISHA TATIL				OJWA	LA	, , , , , , , , , , , , , , , , , , , ,	,1101	,3140033000
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204181	SIGNALS & SYSTEMS	PP	100 40	30	41	71 P			
204181	SIGNALS & SYSTEMS	TW	25 10)		18 P			
204182	ELECTRONIC DEVICES & CIRCUITS	PP	100 40) 48	37	85 P			
204182	ELECTRONIC DEVICES & CIRCUITS	PR	50 20)		40 P			
204183	NETWORK THEORY	PP	100 40	36	43	79 P			
204183	NETWORK THEORY	TW	25 10)		21 P			
204184	DATA STRUCTURES & ALGORITHMS	PP	100 40	35	44	79 P			
204184	DATA STRUCTURES & ALGORITHMS	OR	50 20)		34 P			
204185	DIGITAL ELECTRONICS	PP	100 40	39	30	69 P			
204185	DIGITAL ELECTRONICS	PR	50 20)		34 P			
	FLEV MEACURING INCIDES 8 TOOL	CTM	50 20	1		38 P			
204186	ELEX. MEASURING INSTRU & TOOL	.5 I W	30 20	J		30 P			

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STATE SUBLICIONES SUBLINANCE, SUBLIVER, MAX.MARKS, MIN.PASS MARKS, OF MARKS, TH MARKS, ORTAINED, P/F:PASS/PAIL, CIPREV.CARRY OVER STATE STATE SUBLICIONES SUBLIVER SUBLIVER SUBLIVIORIS SUBLIVER SUBLIVIORIS												
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BRANCH: 16 S.E.(2014 PAT.)(ELECTRONICS &TELECOM)

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204186 ELEX. MEASURING INSTRU & TOOLSTW 50 20 23 P	204185			26	15				
ETRCT CEN TÔTAL (7EC)	204186	FIRST SEM TOTAL =/750	OOLSTW 50 20			23 P			

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	3095 JUNAID S PATEL				7			,s140053095
SEM.:	1							
204181	SIGNALS & SYSTEMS	PP 100 40	19	09	F			
204181	SIGNALS & SYSTEMS	TW 25 10			17 P			
204182	ELECTRONIC DEVICES & CIRCUITS	5 PP 100 40	16	11	F			
204182	ELECTRONIC DEVICES & CIRCUITS	S PR 50 20			12 F			
204183	NETWORK THEORY	PP 100 40	24	22	46 P			
204183	NETWORK THEORY	TW 25 10			17 P			
204184	DATA STRUCTURES & ALGORITHMS	PP 100 40	34	25	59 P			
204184	DATA STRUCTURES & ALGORITHMS	OR 50 20			28 P			
204185	DIGITAL ELECTRONICS	PP 100 40	24	16	40 P			
204185	DIGITAL ELECTRONICS	PR 50 20			10 F			
	ELEX. MEASURING INSTRU & TOOL FIRST SEM TOTAL =/750				35 P			
	3096 KADAM DIPEN ASHOKRAO							,s140053096
SEM.:	1							
	SIGNALS & SYSTEMS	PP 100 40	AA	AA	F			
		TW 25 10	,		10 P			
	ELECTRONIC DEVICES & CIRCUITS		19		F			
-	ELECTRONIC DEVICES & CIRCUITS				28 P			
204183	NETWORK THEORY	PP 100 40	13	02	F			
204183	NETWORK THEORY	TW 25 10			13 P			
204184	DATA STRUCTURES & ALGORITHMS		18		41 P			
204184					8 F			
204185	DIGITAL ELECTRONICS	PP 100 40	ΔΔ	AA	F			
204185	DIGITAL ELECTRONICS		701		AA F			
	ELEX. MEASURING INSTRU & TOOL				21 P			
	FIRST SEM TOTAL =/750	-0 00 -0						
	3097 KADAM SHIVANJALI RAJENI							,s140053097
SEM.:	1							
	SIGNALS & SYSTEMS	PP 100 40	22	18	40 P			
	SIGNALS & SYSTEMS		-		23 P			
	ELECTRONIC DEVICES & CIRCUITS		41		69 P			
	ELECTRONIC DEVICES & CIRCUITS		_		35 P			
204183	NETWORK THEORY	PP 100 40	29		50 P			
204183	NETWORK THEORY	TW 25 10			22 P			
204184	DATA STRUCTURES & ALGORITHMS		27		65 P			
204184	DATA STRUCTURES & ALGORITHMS				30 P			
204185	DIGITAL ELECTRONICS		35		61 P			
204185	DIGITAL ELECTRONICS		33		27 P			
204186	ELEX. MEASURING INSTRU & TOOL				40 P			
204100	FIRST SEM TOTAL = 462/750	_5.W 50 20			.0 1			

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								P/F:PASS/FAIL, C:PREV.CARRY OV
	33098 KADAM SHRADDHA SHALI					,71633624D		
SEM.:	1							
204181	SIGNALS & SYSTEMS	PP 100 40	16	24	40 P			
204181	SIGNALS & SYSTEMS	TW 25 10			21 P			
204182	ELECTRONIC DEVICES & CIRCU	JITS PP 100 40	28	20	48 P			
204182	ELECTRONIC DEVICES & CIRCU	JITS PR 50 20			32 P			
204183	NETWORK THEORY	PP 100 40	22	29	51 P			
204183	NETWORK THEORY	TW 25 10			20 P			
204184	DATA STRUCTURES & ALGORITH	MS PP 100 40	16	38	54 P			
204184	DATA STRUCTURES & ALGORITH	IMS OR 50 20			25 P			
204185			31	34	65 P			
204185	DIGITAL ELECTRONICS	PR 50 20			27 P			
	ELEX. MEASURING INSTRU & T FIRST SEM TOTAL = 423/750				40 P			
	3099 KAKADE ROSHANI DATTU					,71633625в		,s140053099
SEM.:	1							
204181	SIGNALS & SYSTEMS	PP 100 40	25	20	45 P			
204181	SIGNALS & SYSTEMS	TW 25 10			21 P			
204182	ELECTRONIC DEVICES & CIRCU	JITS PP 100 40	33	32	65 P			
204182	ELECTRONIC DEVICES & CIRCU	JITS PR 50 20			30 P			
204183	NETWORK THEORY	PP 100 40	18	27	45 P			
204183	NETWORK THEORY	TW 25 10			21 P			
204184	DATA STRUCTURES & ALGORITH	MS PP 100 40	23	39	62 P			
204184	DATA STRUCTURES & ALGORITH	IMS OR 50 20			26 P			
204185	DIGITAL ELECTRONICS	PP 100 40	30	25	55 P			
204185	DIGITAL ELECTRONICS	PR 50 20			26 P			
204186	ELEX. MEASURING INSTRU & T FIRST SEM TOTAL = 427/750	TOOLSTW 50 20			31 P			
	33100 KALE UMA PRAFULLCHAN				ANA			,s140053100
CEM .	1							
SEM.:		DD 100 40	10	26	44 5			
	SIGNALS & SYSTEMS		18	26	44 P			
	SIGNALS & SYSTEMS	TW 25 10	2 F	26	19 P			
	ELECTRONIC DEVICES & CIRCL		35	36	71 P			
	ELECTRONIC DEVICES & CIRCL		24	20	36 P			
	NETWORK THEORY	PP 100 40	24	30	54 P			
204183 204184	NETWORK THEORY DATA STRUCTURES & ALGORITH		17	34	22 P 51 P			
204184	DATA STRUCTURES & ALGORITH		Τ/	34	27 P			
204184		PP 100 40	22	20	27 P 42 P			
204185	DIGITAL ELECTRONICS DIGITAL ELECTRONICS		44	20	42 P 30 P			
	ELEX. MEASURING INSTRU & 1				30 P 38 P			
70 4 100	FIRST SEM TOTAL = 434/750	OOLSIW JU ZU			J0 F			

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S14005	3101 KAMBLE KAJAL VIJAY				GEETA		,	71633626L	,PICT	, S.	140053101	
SEM.:	1											
	SIGNALS & SYSTEMS	PP	100 40	0 25	15	40 P						
204181	SIGNALS & SYSTEMS	TW	25 10	0		20 P						
204182	ELECTRONIC DEVICES & CIRCUIT	ΓS PP	100 40	0 33	21	54 P						
204182	ELECTRONIC DEVICES & CIRCUIT	ΓS PR	50 20	0		37 P						
204183	NETWORK THEORY	PP	100 40	0 26	15	41 P						
204183	NETWORK THEORY	TW	25 10	0		20 P						
204184	DATA STRUCTURES & ALGORITHMS	S PP	100 40	0 27	38	65 P						
204184	DATA STRUCTURES & ALGORITHMS	S OR	50 20	0		25 P						
204185	DIGITAL ELECTRONICS	PP	100 40	0 30	26	56 P						
204185	DIGITAL ELECTRONICS	PR	50 20	0		31 P						
204186	ELEX. MEASURING INSTRU & TOO FIRST SEM TOTAL = 427/750					38 P						
	3102 KAMBLE PRAJAKTA SHRIMA											
SEM.:	1											
204181	SIGNALS & SYSTEMS	PP	100 40	0 17	13	F						
204181	SIGNALS & SYSTEMS	TW	25 10	0		19 P						
204182	ELECTRONIC DEVICES & CIRCUIT	ΓS PP	100 40	0 30	15	45 P						
204182	ELECTRONIC DEVICES & CIRCUIT	ΓS PR	50 20	0		27 P						
204183	NETWORK THEORY	PP	100 40	0 24	16	40 P						
204183	NETWORK THEORY	TW	25 10	0		19 P						
204184	DATA STRUCTURES & ALGORITHMS	S PP	100 40	0 29	26	55 P						
204184	DATA STRUCTURES & ALGORITHMS	S OR	50 20	0		24 P						
204185	DIGITAL ELECTRONICS	PP	100 40	0 23	26	49 P						
204185	DIGITAL ELECTRONICS	PR	50 20	0		30 P						
204186	ELEX. MEASURING INSTRU & TOO FIRST SEM TOTAL =/750	DLSTW	50 20	0		34 P						
S14005	3104 KAMBLE RAJAT JEETENDRA	4			RESHM	A	,	71527289G	,PICT	, S.	140053104	
SEM.:				_								
	SIGNALS & SYSTEMS				15	41 P						
	SIGNALS & SYSTEMS		25 10			19 P						
204182	ELECTRONIC DEVICES & CIRCUIT				22	55 P						
	ELECTRONIC DEVICES & CIRCUIT					30 P						
204183	NETWORK THEORY		100 40		08	F						
204183	NETWORK THEORY		25 10			20 P						
204184	DATA STRUCTURES & ALGORITHMS				20	40 P						
204184	DATA STRUCTURES & ALGORITHMS				2.0	28 P						
204185	DIGITAL ELECTRONICS				20	44 P						
204185	DIGITAL ELECTRONICS		50 20			33 P						
204186	ELEX. MEASURING INSTRU & TOO FIRST SEM TOTAL =/750	DLSTW	50 20	υ		33 P						

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	3105 KANDGE MANDAR SANJAY												
SEM.:	1												
204181	SIGNALS & SYSTEMS	PP	100	40	35	34	69 P						
204181	SIGNALS & SYSTEMS	TW	25	10			16 P						
204182					41	24	65 P						
	ELECTRONIC DEVICES & CIRCUITS						34 P						
204183	NETWORK THEORY				36	33	69 P						
204183	NETWORK THEORY						20 P						
204184	DATA STRUCTURES & ALGORITHMS				39	32	71 P						
204184	DATA STRUCTURES & ALGORITHMS						22 P						
204185	DIGITAL ELECTRONICS				35	26	61 P						
204185	DIGITAL ELECTRONICS						31 P						
	ELEX. MEASURING INSTRU & TOOL FIRST SEM TOTAL = 495/750						37 P						
	3106 KANJIA ANERI PARESHKUMA						LA					,s140053	
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SEM.:		D.D.	100	40	10	2.5	F4 B						
	SIGNALS & SYSTEMS SIGNALS & SYSTEMS				19	33	54 P 21 P						
	ELECTRONIC DEVICES & CIRCUITS				/ 1	20	69 P						
	ELECTRONIC DEVICES & CIRCUITS				41	20	34 P						
204182	NETWORK THEORY		100		24	32	56 P						
204183	NETWORK THEORY		25		Z T	32	20 P						
204184	DATA STRUCTURES & ALGORITHMS				29	3.4	63 P						
204184	DATA STRUCTURES & ALGORITHMS				23	J7	29 P						
204185	DIGITAL ELECTRONICS		100		32	25	57 P						
204185	DIGITAL ELECTRONICS				<i>32</i>	23	37 r 32 P						
	ELEX. MEASURING INSTRU & TOOL						41 P						
204100	FIRST SEM TOTAL = 476/750	_5 i w	30	20			71 1						
	3107 KARAN JUNEJA						AM					,s140053	
SEM.:	1												
	SIGNALS & SYSTEMS	PP	100	40	31	15	46 P						
	SIGNALS & SYSTEMS		25		-		14 P						
	ELECTRONIC DEVICES & CIRCUITS				39	17	56 P						
	ELECTRONIC DEVICES & CIRCUITS						33 P						
204183	NETWORK THEORY		100		27	20	47 P						
204183	NETWORK THEORY		25				19 P						
204184					23	18	41 P						
204184	DATA STRUCTURES & ALGORITHMS						21 P						
204185	DIGITAL ELECTRONICS				27	16	43 P						
204185	DIGITAL ELECTRONICS		50				30 P						
204186							33 P						
	FIRST SEM TOTAL = 383/750		-										

	:: 25 JAN 2016 CENTRE:									PAGE N		
NOTE: HER LIN	FIRST LINE : SEAT NO., NAME (IES: SUB.CODE, SUB.NAME, SUB.T	OF TH YPE,	IE CANDID MAX.MARK	ATE, S, MI	MOTHE IN.PASS	ER, PEF S MARKS	RMANENT REG 5, OE MARKS	. NO., PRE , TH MARKS,	VIOUS SEAT OBTAINED,	NO., P/F:P	COLLEGE, ASS/FAIL,	SEAT NO. C:PREV.CARRY OV
	3108 KARPE ANJALI SHRIKANT											
SEM.:	1											
204181	SIGNALS & SYSTEMS	PP	100 40	24	21	45 P						
204181	SIGNALS & SYSTEMS	TW	25 10			21 P						
	ELECTRONIC DEVICES & CIRCUIT			28	25	53 P						
	ELECTRONIC DEVICES & CIRCUIT					32 P						
204183	NETWORK THEORY		100 40	19	31	50 P						
204183	NETWORK THEORY					15 P						
204184	DATA STRUCTURES & ALGORITHMS			21	39	60 P						
204184	DATA STRUCTURES & ALGORITHMS					9 F						
204185	DIGITAL ELECTRONICS			32	33							
204185	DIGITAL ELECTRONICS					24 P						
	ELEX. MEASURING INSTRU & TOO FIRST SEM TOTAL =/750					35 P						
	3109 KASTURE DEEPALI VINAYA											
SEM.:	1											
	SIGNALS & SYSTEMS	DD	100 40	10	12	F						
	SIGNALS & SYSTEMS			19	13	г 20 Р						
	ELECTRONIC DEVICES & CIRCUIT			12	10	62 P						
	ELECTRONIC DEVICES & CIRCUIT			43	19	33 P						
204183	NETWORK THEORY		100 40	27	26	53 P						
204183	NETWORK THEORY		25 10	21	20	21 P						
204183	DATA STRUCTURES & ALGORITHMS			27	23	50 P						
204184				21	23	26 P						
204184	DIGITAL ELECTRONICS		100 40	16	24	40 P						
				10	24	26 P						
	ELEX. MEASURING INSTRU & TOO					35 P						
204180	FIRST SEM TOTAL =/750	LSIW	30 20			33 P						
	3110 KATIKAR VIKRANT RAJEEV							, 71527305в				
SEM.:	1											
	SIGNALS & SYSTEMS	PР	100 40	36	28	64 P						
	SIGNALS & SYSTEMS		25 10	20		15 P						
	ELECTRONIC DEVICES & CIRCUIT			41	26	67 P						
	ELECTRONIC DEVICES & CIRCUIT					38 P						
204183	NETWORK THEORY		100 40	36	34	70 P						
204183	NETWORK THEORY		25 10	50	٠.	19 P						
204184	DATA STRUCTURES & ALGORITHMS			36	33	69 P						
204184				30	33	21 P						
204185	DIGITAL ELECTRONICS			35	27	62 P						
204185	DIGITAL ELECTRONICS		50 20	55		30 P						
	ELEX. MEASURING INSTRU & TOO					34 P						
_04±00	FIRST SEM TOTAL = 489/750	LUIVV	JU 20			J 7 F						

	6 S.E.(2014 PAT.)(ELECTRONICS : 25 JAN 2016 CENTRE: P		-	UTE OF	COMPUT	TER TECHNOLOGY, P	UNE.		PAGE NO. 37	
NOTE:	FIRST LINE : SEAT NO., NAME O	 F TI	HE CAND	IDATE,	МОТНЕ	ER, PERMANENT REG	PRE	VIOUS SEAT	NO., COLLEGE,	SEAT NO.
	3111 KAUSHIK DASS								,s1400531	
311003	SIII IMOSIIIM BAGS				174-11		,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,	,, 101	,51100551	
SEM.:	1 SIGNALS & SYSTEMS	DD	100 40	40	16	86 D				
	SIGNALS & SYSTEMS SIGNALS & SYSTEMS				40	86 P 21 P				
204181	ELECTRONIC DEVICES & CIRCUITS				43	89 P				
204182	ELECTRONIC DEVICES & CIRCUITS				73	40 P				
204183	NETWORK THEORY				43	89 P				
204183	NETWORK THEORY				43	23 P				
204184	DATA STRUCTURES & ALGORITHMS				<i>1</i> 1	81 P				
204184	DATA STRUCTURES & ALGORITHMS				71	27 P				
204185	DIGITAL ELECTRONICS				33	70 P				
204185					33	38 P				
	ELEX. MEASURING INSTRU & TOOL					44 P				
201100	FIRST SEM TOTAL = 608/750	.5	30 20							
	3112 KHADE YASHWANT BHASKAR									
SEM.:	1									
	SIGNALS & SYSTEMS	DD	100 40	26	32	58 P				
	SIGNALS & SYSTEMS				32	15 P				
	ELECTRONIC DEVICES & CIRCUITS				28	74 P				
	ELECTRONIC DEVICES & CIRCUITS					37 P				
204183	NETWORK THEORY		100 40		41	80 P				
204183	NETWORK THEORY		25 10			20 P				
204184	DATA STRUCTURES & ALGORITHMS				27	61 P				
204184						22 P				
204185			100 40		28	61 P				
204185			50 20			29 P				
204186	ELEX. MEASURING INSTRU & TOOL	STW	50 20			31 P				
	FIRST SEM TOTAL = 488/750									
	3113 KHAMAMKAR RACHANA NITIN						,71527311G			
SEM.:	1									
	SIGNALS & SYSTEMS	חם	100 40	31	22	54 P				
	SIGNALS & SYSTEMS SIGNALS & SYSTEMS		25 10		۷۵	54 P 21 P				
	ELECTRONIC DEVICES & CIRCUITS				35	80 P				
	ELECTRONIC DEVICES & CIRCUITS ELECTRONIC DEVICES & CIRCUITS				<i>J J</i>	34 P				
204182	NETWORK THEORY		100 40		34	63 P				
204183	NETWORK THEORY		25 10		J -T	19 P				
204183					36	72 P				
204184					50	72 P 28 P				
204184	DIGITAL ELECTRONICS		100 40		28	70 P				
204185			50 20		20	70 Р 37 Р				
204185						41 P				
201100	FIRST SEM TOTAL = 519/750		30 20							

	6 S.E.(2014 PAT.)(ELECTRONICS : 25 JAN 2016 CENTRE: P			-	COMPI	UTER TECHNO	OLOGY, PU	INE.		PAGE NO. 38	
NOTE:	FIRST LINE : SEAT NO., NAME O ES: SUB.CODE, SUB.NAME, SUB.TY	 F TH	HE CAN	 NDIDATE,	 МОТІ	HER, PERMAN	 NENT REG.	NO., PRE	 VIOUS SEAT	NO., COLLEGE,	SEAT NO.
S14005	3114 KHATRI GEETA HARISH				KAMI	LA	,	71527315K	,PICT	,S1400531	114
SEM.:	1										
	SIGNALS & SYSTEMS	PP	100 4	10 36	28	64 P					
204181	SIGNALS & SYSTEMS	TW	25 1	LO		20 P					
204182	ELECTRONIC DEVICES & CIRCUITS	PP	100 4	10 41	29	70 P					
204182	ELECTRONIC DEVICES & CIRCUITS	PR	50 2	20		44 P					
204183	NETWORK THEORY	PP	100 4	10 36	39	75 P					
204183	NETWORK THEORY	TW	25 1	LO		22 P					
204184	DATA STRUCTURES & ALGORITHMS	PP	100 4	10 40	32	72 P					
204184	DATA STRUCTURES & ALGORITHMS	OR	50 2	20		35 P					
204185	DIGITAL ELECTRONICS	PP	100 4	10 24	26	50 P					
204185	DIGITAL ELECTRONICS	PR	50 2	20		35 P					
	ELEX. MEASURING INSTRU & TOOL FIRST SEM TOTAL = 526/750					39 P					
	3115 KOKATE PUJA SURESH										
SEM.:	1										
204181	SIGNALS & SYSTEMS	PP	100 4	10 23	24	47 P					
204181	SIGNALS & SYSTEMS	TW	25 1	LO		21 P					
204182	ELECTRONIC DEVICES & CIRCUITS	PP	100 4	10 44	38	82 P					
204182	ELECTRONIC DEVICES & CIRCUITS	PR	50 2	20		35 P					
204183	NETWORK THEORY	PP	100 4	10 18	24	42 P					
204183	NETWORK THEORY	TW	25 1	LO		21 P					
204184	DATA STRUCTURES & ALGORITHMS	PP	100 4	10 29	40	69 P					
204184	DATA STRUCTURES & ALGORITHMS	OR	50 2	20		27 P					
204185	DIGITAL ELECTRONICS	PP	100 4	10 33	21	54 P					
204185	DIGITAL ELECTRONICS	PR	50 2	20		23 P					
204186	ELEX. MEASURING INSTRU & TOOL FIRST SEM TOTAL = 461/750	STW	50 2	20		40 P					
	3116 KONDE ATHARWA AJIT					GEETA		71527324J			
314003	JIIO KONDE ATTIANWA AJIT				JAN	GLLIA	,	7 1327 3243	, , , , ,	,3140033	
SEM.:	1										
204181	SIGNALS & SYSTEMS	PP	100 4	10 27	15	42 P					
204181	SIGNALS & SYSTEMS	TW	25 1	LO		19 P					
204182	ELECTRONIC DEVICES & CIRCUITS	PP	100 4	10 30	20	50 P					
204182	ELECTRONIC DEVICES & CIRCUITS	PR	50 2	20		37 P					
204183	NETWORK THEORY	PP	100 4	10 30	15	45 P					
204183	NETWORK THEORY	TW	25 1	LO		20 P					
204184	DATA STRUCTURES & ALGORITHMS	PP	100 4	10 26	31	57 P					
204184	DATA STRUCTURES & ALGORITHMS	OR	50 2	20		25 P					
204185	DIGITAL ELECTRONICS	PP	100 4	10 27	15	42 P					
204185	DIGITAL ELECTRONICS	PR	50 2	20		21 P					
204186	ELEX. MEASURING INSTRU & TOOL FIRST SEM TOTAL = 396/750	STW	50 2	20		38 P					

	6 S.E.(2014 PAT.)(ELECTRONICS		-							20	
	: 25 JAN 2016 CENTRE: P										
	FIRST LINE : SEAT NO., NAME O										
	IES: SUB.CODE, SUB.NAME, SUB.TY			•		,		•		•	
	3117 KONDE ESHWARA AJIT									, \$140053	
							·		•	•	
SEM.:	1										
204181	SIGNALS & SYSTEMS	PP	100 40	31	22	53 P					
204181	SIGNALS & SYSTEMS	TW	25 10			18 P					
204182	ELECTRONIC DEVICES & CIRCUITS	PP	100 40	35	38	73 P					
204182	ELECTRONIC DEVICES & CIRCUITS	PR	50 20			36 P					
204183	NETWORK THEORY	PP	100 40	28	31	59 P					
204183	NETWORK THEORY	TW	25 10			19 P					
204184	DATA STRUCTURES & ALGORITHMS	PP	100 40	24	31	55 P					
204184	DATA STRUCTURES & ALGORITHMS	OR	50 20			23 P					
204185	DIGITAL ELECTRONICS	PP	100 40	32	27	59 P					
204185	DIGITAL ELECTRONICS	PR	50 20			20 P					
204186	ELEX. MEASURING INSTRU & TOOL	.STW	50 20			33 P					
	FIRST SEM TOTAL = $448/750$										
S14005	3118 KOTHEKAR BHAKTI SUNIL				CHIT	RA	,	71633629E	,PICT	,S140053	118
SEM.:											
	SIGNALS & SYSTEMS				22						
	SIGNALS & SYSTEMS					22 P					
	ELECTRONIC DEVICES & CIRCUITS			35	30	65 P					
	ELECTRONIC DEVICES & CIRCUITS					39 P					
204183	NETWORK THEORY		100 40	24	18	42 P					
204183	NETWORK THEORY		25 10			22 P					
204184				24	37	61 P					
204184						24 P					
204185			100 40	23	28	51 P					
204185						26 P					
204186	ELEX. MEASURING INSTRU & TOOL	.STW	50 20			34 P					
	FIRST SEM TOTAL = 426/750										
S14005	3119 KUCHERIYA PRATIK PREMCH	IAND.	JI		PRAB	BHA	,	71527332к	,PICT	,S140053	119
C=::	1										
SEM.:		-	100 40	24	10	40.5					
	SIGNALS & SYSTEMS			21	19	40 P					
	SIGNALS & SYSTEMS		25 10	2.2	20	10 P					
	ELECTRONIC DEVICES & CIRCUITS			33	30	63 P					
	ELECTRONIC DEVICES & CIRCUITS			20	20	30 P					
204183			100 40	20	20	40 P					
204183	NETWORK THEORY		25 10	22	22	14 P					
204184				23	22	45 P					
204184				20	27	10 F					
204185	DIGITAL ELECTRONICS		50 20	29	21	56 P 15 F					
204185											
204186	FIRST SEM TOTAL =/750	SIW	JU 20			20 P					
	FIRST SEM TOTAL =//30										

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NOTE: THER LIN	FIRST LINE : SEAT NO., NAME (OF TH	HE CANDII MAX.MAR	OATE, (S, M	MOTHE IN.PASS	ER, PERMA S MARKS,	ANENT REG. OE MARKS,	NO., PRE TH MARKS,	VIOUS SEAT OBTAINED,	NO., P/F:P	COLLEGE, ASS/FAIL,	SEAT NO. C:PREV.CARRY OV
	3120 KULKARNI HARSH MAHESH											
SEM.:												
204181	SIGNALS & SYSTEMS	PP	100 40	21	24	45 P						
	SIGNALS & SYSTEMS					16 P						
	ELECTRONIC DEVICES & CIRCUIT			38	33	71 P						
	ELECTRONIC DEVICES & CIRCUIT					37 P						
204183	NETWORK THEORY		100 40	24	22	46 P						
204183	NETWORK THEORY					19 P						
204184	DATA STRUCTURES & ALGORITHMS			24	28	52 P						
204184	DATA STRUCTURES & ALGORITHMS					25 P						
204185	DIGITAL ELECTRONICS			27	20							
204185	DIGITAL ELECTRONICS					23 P						
	ELEX. MEASURING INSTRU & TOO FIRST SEM TOTAL = 414/750					33 P						
	33121 KULKARNI ISHA SHRIKANT							71527336в				
SEM.:	1											
	SIGNALS & SYSTEMS	PP	100 40	33	41	74 P						
	SIGNALS & SYSTEMS					21 P						
	ELECTRONIC DEVICES & CIRCUIT			47	40	87 P						
	ELECTRONIC DEVICES & CIRCUIT					38 P						
204183	NETWORK THEORY		100 40	47	37	84 P						
204183	NETWORK THEORY		25 10			18 P						
204184	DATA STRUCTURES & ALGORITHMS			41	48	89 P						
204184					.0	27 P						
204185	DIGITAL ELECTRONICS		100 40	41	27	68 P						
	DIGITAL ELECTRONICS		50 20		_,	44 P						
	ELEX. MEASURING INSTRU & TOO					41 P						
201100	FIRST SEM TOTAL = 591/750	LJIW	30 20			11 1						
	3122 KULKARNI RIGVED RAJESH					ADA		71527340L				
SEM.:	1											
	SIGNALS & SYSTEMS	PP	100 40	30	29	59 P						
	SIGNALS & SYSTEMS		25 10			20 P						
	ELECTRONIC DEVICES & CIRCUIT			39	29	68 P						
	ELECTRONIC DEVICES & CIRCUIT					40 P						
204183	NETWORK THEORY		100 40	33	29	62 P						
204183	NETWORK THEORY		25 10	-		19 P						
204184	DATA STRUCTURES & ALGORITHMS			37	35	72 P						
204184						27 P						
204185	DIGITAL ELECTRONICS			38	27	65 P						
204185	DIGITAL ELECTRONICS		50 20	20		34 P						
	ELEX. MEASURING INSTRU & TOO					35 P						
_5.100	FIRST SEM TOTAL = 501/750		30 20			JJ 1						

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NOTE: THER LIN	FIRST LINE : SEAT NO., NAME (OF TH YPE,	IE CANDID MAX.MARK	ATE, S, M	MOTHE IN.PASS	R, PE MARK	RMANENT REG S, OE MARKS	. NO., PRE , TH MARKS,	VIOUS SEAT OBTAINED,	NO., P/F:P	COLLEGE, ASS/FAIL,	SEAT NO. C:PREV.CARRY OV
	3123 KULKARNI SHEKHAR DIPAK							,71633630J				
SEM.:												
204181	SIGNALS & SYSTEMS	PP	100 40	26	29	55 P						
204181	SIGNALS & SYSTEMS	TW	25 10			21 P						
204182	ELECTRONIC DEVICES & CIRCUITS	S PP	100 40	43	32	75 P						
204182	ELECTRONIC DEVICES & CIRCUITS	S PR	50 20			32 P						
204183	NETWORK THEORY		100 40	29	15	44 P						
204183	NETWORK THEORY					22 P						
204184	DATA STRUCTURES & ALGORITHMS	PP	100 40	29	36	65 P						
204184	DATA STRUCTURES & ALGORITHMS	OR	50 20			28 P						
204185	DIGITAL ELECTRONICS	PP	100 40	30	34							
204185	DIGITAL ELECTRONICS	PR	50 20			23 P						
	ELEX. MEASURING INSTRU & TOO FIRST SEM TOTAL = 466/750					37 P						
	3125 KUMBHARKAR POOJA ASHOK											
SEM.:	1											
	SIGNALS & SYSTEMS	DD	100 40	30	45	75 P						
	SIGNALS & SYSTEMS			30	73	73 F 21 P						
	ELECTRONIC DEVICES & CIRCUITS			3.8	40	78 P						
	ELECTRONIC DEVICES & CIRCUITS			30	40	39 P						
204183	NETWORK THEORY		100 40	38	44	82 P						
204183	NETWORK THEORY		25 10	30	77	22 P						
204184	DATA STRUCTURES & ALGORITHMS			35	48	83 P						
204184				55	40	35 P						
204185	DIGITAL ELECTRONICS		100 40	30	40	79 P						
	DIGITAL ELECTRONICS			55	40	40 P						
	ELEX. MEASURING INSTRU & TOO					40 P						
204100	FIRST SEM TOTAL = 594/750	LSIW	30 20			40 F						
	3126 KURANI ANKIT DEEPAK							,71527345м				
SEM.:	1											
	- SIGNALS & SYSTEMS	PP	100 40	28	46	74 P						
	SIGNALS & SYSTEMS		25 10			20 P						
	ELECTRONIC DEVICES & CIRCUITS			47	47	94 P						
	ELECTRONIC DEVICES & CIRCUITS			• •	· -	39 P						
204183	NETWORK THEORY		100 40	33	45	78 P						
204183	NETWORK THEORY		25 10		: -	22 P						
204184	DATA STRUCTURES & ALGORITHMS			40	46	86 P						
204184				. •	÷ •	30 P						
204185	DIGITAL ELECTRONICS			39	29	68 P						
204185	DIGITAL ELECTRONICS		50 20	23	_5	30 P						
	ELEX. MEASURING INSTRU & TOO					37 P						
_0.100	FIRST SEM TOTAL = 578/750		33 20			J. 1						

NOTE:	FIRST LINE : SEAT NO., NAME (• • • •								
	ES: SUB.CODE, SUB.NAME, SUB.TY	YPE, M	MAX.MARK	S, M	IN.PASS	S MAR	RKS, OE MA	ARKS, TH MARKS,	OBTAINED,	P/F:P	ASS/FAIL,	C:PREV.CARRY O
	3127 LAYKUMAR BHARAT PATEL											
SEM.:	1											
204181	SIGNALS & SYSTEMS	PP 1	-00 40	31	42	73	Р					
204181	SIGNALS & SYSTEMS	TW	25 10			21	Р					
	ELECTRONIC DEVICES & CIRCUITS			43	37	80						
	ELECTRONIC DEVICES & CIRCUITS					42						
204183	NETWORK THEORY		-00 40	36	35	71						
204183	NETWORK THEORY					23						
204184	DATA STRUCTURES & ALGORITHMS			44	42	86						
204184	DATA STRUCTURES & ALGORITHMS					32						
204185	DIGITAL ELECTRONICS			42	27							
204185	DIGITAL ELECTRONICS					30						
	ELEX. MEASURING INSTRU & TOOL FIRST SEM TOTAL = 564/750					37						
	3128 LIMAYE SWANAND RAGHUNAT											
SEM.:	1											
	SIGNALS & SYSTEMS	DD 1	00 40	35	11	79	D					
	SIGNALS & SYSTEMS			33	44	23						
	ELECTRONIC DEVICES & CIRCUITS			19	44	93						
	ELECTRONIC DEVICES & CIRCUITS			43	44	40						
204183	NETWORK THEORY		00 40	27	42	69						
204183	NETWORK THEORY		25 10	_,	12	23						
204184	DATA STRUCTURES & ALGORITHMS			45	46	91						
204184				73	40	43						
204185	DIGITAL ELECTRONICS		00 40	37	24	61						
	DIGITAL ELECTRONICS			57	24	33						
	ELEX. MEASURING INSTRU & TOOL					41						
	FIRST SEM TOTAL = 596/750											
	3130 LOKHANDE HARSHVARDHAN F											
SEM.:	1											
	- SIGNALS & SYSTEMS	PP 1	.00 40	20	20	40	Р					
	SIGNALS & SYSTEMS			-	-	21						
	ELECTRONIC DEVICES & CIRCUITS			35	35	70						
	ELECTRONIC DEVICES & CIRCUITS			-	=	28						
204183	NETWORK THEORY		.00 40	22	33	55						
204183	NETWORK THEORY		25 10	=		20						
204184	DATA STRUCTURES & ALGORITHMS			28	44	72						
204184				_•		30						
204185	DIGITAL ELECTRONICS			23	20	43						
204185	DIGITAL ELECTRONICS					16						
	ELEX. MEASURING INSTRU & TOOL					43						
_01100	FIRST SEM TOTAL =/750	_	JU 20			τ.J						

	6 S.E.(2014 PAT.)(ELECTRONICS : 25 JAN 2016 CENTRE: PU			-	OF	COMPUT	ΓER	TECHNOLOGY,	PUNE.	,	PAGE N	0. 43	
NOTE:	FIRST LINE : SEAT NO., NAME OF	F TI	HE CA	NDIDA	TE,	МОТНЕ	ΞR,	PERMANENT RE	EG. NO., PRE	VIOUS SEA	T NO.,	COLLEGE,	SEAT NO.
	3131 LOLGE OMKAR DATTATRYA								,71527353в				
SEM.:	1												
204181	SIGNALS & SYSTEMS	PP	100	40	18	22	40	Р					
204181	SIGNALS & SYSTEMS	TW	25	10			10	Р					
204182	ELECTRONIC DEVICES & CIRCUITS	PP	100	40	18	AA		F					
204182	ELECTRONIC DEVICES & CIRCUITS	PR	50	20			10	F					
204183	NETWORK THEORY	PP	100	40	32	20	52	Р					
204183	NETWORK THEORY	TW	25	10			17	Р					
204184	DATA STRUCTURES & ALGORITHMS	PP	100	40	23	17	40	Р					
204184	DATA STRUCTURES & ALGORITHMS	OR	50	20			28	Р					
204185	DIGITAL ELECTRONICS	PP	100	40	17	06		F					
204185	DIGITAL ELECTRONICS	PR	50	20			8	F					
204186	ELEX. MEASURING INSTRU & TOOLS FIRST SEM TOTAL =/750						20						
s14005	3132 LONDHE KAJAL VIJAY												
SEM.:	1												
	SIGNALS & SYSTEMS	DD	100	40	11	26		_					
			25		14	20	22						
	ELECTRONIC DEVICES & CIRCUITS				31	28	59						
	ELECTRONIC DEVICES & CIRCUITS				J L	20	33						
204183	NETWORK THEORY		100		31	30	61						
204183			25		J L	30	21						
204184	DATA STRUCTURES & ALGORITHMS				29	36	65						
204184						30	24						
204185					31	27	58						
204185	DIGITAL ELECTRONICS				_		24						
	ELEX. MEASURING INSTRU & TOOLS						33						
	FIRST SEM TOTAL =/750							·					
S14005	3133 LONKAR AKASH MORESHWAR					KALPA	ANA		,71633634м	,PICT		,S1400531	133
SEM.:	1												
	SIGNALS & SYSTEMS	PP	100	40	20	20	40						
204181	SIGNALS & SYSTEMS	TW	25	10			21	Р					
204182	ELECTRONIC DEVICES & CIRCUITS	PP	100	40	40	33	73	Р					
204182	ELECTRONIC DEVICES & CIRCUITS						35						
204183			100		21	21	42						
204183			25		_		18						
204184					21	37	58						
204184	DATA STRUCTURES & ALGORITHMS					_	27						
204185	DIGITAL ELECTRONICS				37	25	62						
204185	DIGITAL ELECTRONICS						30						
204186	ELEX. MEASURING INSTRU & TOOLS FIRST SEM TOTAL = 443/750	STW	50	20			37	Р					

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NOTE: HER LIN	FIRST LINE : SEAT NO., NAME CHES: SUB.CODE, SUB.NAME, SUB.TY	OF THE	CANDIDA AX.MARKS	ATE, S, MI	MOTHE IN.PASS	R, P	ERMANENT REC KS, OE MARKS	G. NO., PREN G, TH MARKS,	VIOUS SEAT OBTAINED,	NO., P/F:P	COLLEGE, ASS/FAIL,	SEAT NO. C:PREV.CARRY
	3134 LOYA TEJUS KAILASH											
SEM.:	1											
204181	SIGNALS & SYSTEMS	PP 1	00 40	32	07		F					
	SIGNALS & SYSTEMS					18	Р					
	ELECTRONIC DEVICES & CIRCUITS			43	15	58						
	ELECTRONIC DEVICES & CIRCUITS					36						
204183	NETWORK THEORY		.00 40	34	26	60						
204183	NETWORK THEORY					20						
204184	DATA STRUCTURES & ALGORITHMS			27	29	56						
204184	DATA STRUCTURES & ALGORITHMS					22						
204185	DIGITAL ELECTRONICS			25	23	48						
204185	DIGITAL ELECTRONICS					30						
	ELEX. MEASURING INSTRU & TOOL FIRST SEM TOTAL =/750					35						
	3135 LUNAWAT KARISHMA SANDEE											
CEM .	1											
SEM.:	SIGNALS & SYSTEMS	DD 1	00 40	27	2.5	F 2	D					
				21	23							
	SIGNALS & SYSTEMS			42	22	16						
	ELECTRONIC DEVICES & CIRCUITS			42	22	64						
204182	ELECTRONIC DEVICES & CIRCUITS NETWORK THEORY		.00 40	28	22	34 50						
204183	NETWORK THEORY		25 10	20	22	30 17						
204183	DATA STRUCTURES & ALGORITHMS			20	37	66						
				29	37	28						
204184				40	22							
204185	DIGITAL ELECTRONICS			40	22	62						
	DIGITAL ELECTRONICS					26						
204160	ELEX. MEASURING INSTRU & TOOL FIRST SEM TOTAL = 447/750	-51W	30 20			32	۲					
	3136 MACHALE SHREYASH KISHOR							,71527359M				
SEM.:	1											
	SIGNALS & SYSTEMS	PP 1	.00 40	20	06		F					
	SIGNALS & SYSTEMS					13						
	ELECTRONIC DEVICES & CIRCUITS			28	15	43						
	ELECTRONIC DEVICES & CIRCUITS					25						
204183	NETWORK THEORY			27	07							
204183	NETWORK THEORY		25 10	_ ′	<i>.</i>	19						
204183	DATA STRUCTURES & ALGORITHMS			19	33	52						
204184					<i>J J</i>	30						
204184	DIGITAL ELECTRONICS			32	15	47						
204185	DIGITAL ELECTRONICS DIGITAL ELECTRONICS			<i>ع</i> د	Τ)	20						
ZU4186	ELEX. MEASURING INSTRU & TOOL FIRST SEM TOTAL =/750	-21W	3U ZU			29	r					

BRANCH: 16 S.E.(2014 PAT.)(ELECTRONICS &TELECOM)

	l6 S.E.(2014 PAT.)(ELECTRONICS E: 25 JAN 2016 CENTRE: P		-		COMP	UTER TECH	HNOLOGY, PL	JNE.		PAGE NO. 45
										. NO COLLECT STAT NO
	FIRST LINE : SEAT NO., NAME O									no., college, Seal no. , P/F:PASS/FAIL, C:PREV.CARRY OVER
	53137 MADHAVE AISHWARYA RAJEN									,S140053137
SEM.:	:1									
204181	SIGNALS & SYSTEMS	PP	100 40	0 20	20	40 P				
204181	SIGNALS & SYSTEMS	TW	25 10	0		17 P				
204182	ELECTRONIC DEVICES & CIRCUITS	PP.	100 40	0 23	32	55 P				
204182	ELECTRONIC DEVICES & CIRCUITS	PR	50 20	0		35 P				
204183	NETWORK THEORY	PP	100 40	0 25	15	40 P				
204183	NETWORK THEORY	TW	25 10	0		18 P				
204184	DATA STRUCTURES & ALGORITHMS	PP	100 40	0 21	36	57 P				
204184	DATA STRUCTURES & ALGORITHMS	OR	50 20	0		31 P				
204185					22	45 P				
204185	DIGITAL ELECTRONICS	PR	50 20	0		32 P				
	ELEX. MEASURING INSTRU & TOOL FIRST SEM TOTAL = 402/750					32 P				
	53138 MAHADIK PRADNYA PRAKASH									,S140053138
SEM.:	:1									
204181	SIGNALS & SYSTEMS	PP	100 40	0 22	18	40 P				
204181	SIGNALS & SYSTEMS	TW	25 10	0		22 P				
204182	ELECTRONIC DEVICES & CIRCUITS	PP	100 40	0 38	23	61 P				
204182	ELECTRONIC DEVICES & CIRCUITS	PR	50 20	0		39 P				
204183	NETWORK THEORY	PP	100 40	0 20	26	46 P				
204183	NETWORK THEORY	TW	25 10	0		20 P				
204184	DATA STRUCTURES & ALGORITHMS	PP	100 40	0 21	38	59 P				
204184	DATA STRUCTURES & ALGORITHMS	OR	50 20	0		29 P				
204185	DIGITAL ELECTRONICS	PP	100 40	0 29	29	58 P				
204185	DIGITAL ELECTRONICS	PR	50 20	0		26 P				
204186	ELEX. MEASURING INSTRU & TOOL FIRST SEM TOTAL = 445/750	.STW	50 20	0		45 P				
	•									
S14005	53139 MAHAJAN LEENA HUKUMCHAN	ID			SADI	HANA	:	,71633636н	,PICT	,S140053139
SEM.:										
	SIGNALS & SYSTEMS	PP	100 40	0 21	19	40 P				
	SIGNALS & SYSTEMS		25 10			22 P				
	ELECTRONIC DEVICES & CIRCUITS				26	63 P				
	ELECTRONIC DEVICES & CIRCUITS					35 P				
204183			100 40		27	51 P				
204183			25 10			22 P				
204184	DATA STRUCTURES & ALGORITHMS				39	70 P				
204184					- -	30 P				
204185					29	54 P				
204185						22 P				
204186	ELEX. MEASURING INSTRU & TOOL FIRST SEM TOTAL = 455/750	.STW	50 20	U		46 P				

	6 S.E.(2014 PAT.)(ELECTRONICS : 25 JAN 2016 CENTRE: P			-	COMPU	TER TECHNOLOG	SY, PUNE			PAGE NO. 46	
NOTE:	FIRST LINE : SEAT NO., NAME O ES: SUB.CODE, SUB.NAME, SUB.TY	 F TI	HE CAN	 DIDATE,	МОТН	ER, PERMANENT	REG. N		 /IOUS SEAT	NO., COLLEGE,	SEAT NO.
	21.40 MALIATAN DRANTAL T DENDUM										
S14005	3140 MAHAJAN PRANJALI BINDUM	АИН	ΑV		ATDA	А	, / 1	.32736ZM	, PICI	,5140053	140
SEM.:	1										
204181	SIGNALS & SYSTEMS	PP	100 4	0 38	43	81 P					
204181	SIGNALS & SYSTEMS	TW	25 1	0		23 P					
204182	ELECTRONIC DEVICES & CIRCUITS	PP	100 4	0 49	37	86 P					
204182	ELECTRONIC DEVICES & CIRCUITS					40 P					
204183	NETWORK THEORY	PP	100 4	0 33	41	74 P					
204183	NETWORK THEORY	TW	25 1	0		20 P					
204184	DATA STRUCTURES & ALGORITHMS	PP	100 4	0 34	44	78 P					
204184	DATA STRUCTURES & ALGORITHMS					38 P					
204185	DIGITAL ELECTRONICS				22	60 P					
204185						40 P					
	ELEX. MEASURING INSTRU & TOOL FIRST SEM TOTAL = 584/750					44 P					
	3141 MAHAJAN TEJASWI RAJENDR										
SEM.:	1										
	SIGNALS & SYSTEMS	DD	100 4	0 35	33	68 P					
	SIGNALS & SYSTEMS				33	22 P					
	ELECTRONIC DEVICES & CIRCUITS				26	67 P					
	ELECTRONIC DEVICES & CIRCUITS					35 P					
204183	NETWORK THEORY		100 4		35	66 P					
204183	NETWORK THEORY		25 1			22 P					
204184	DATA STRUCTURES & ALGORITHMS				46	75 P					
204184	DATA STRUCTURES & ALGORITHMS	OR	50 2	0		32 P					
204185	DIGITAL ELECTRONICS	PP	100 4	0 28	29	57 P					
204185	DIGITAL ELECTRONICS	PR	50 2	0		37 P					
204186	ELEX. MEASURING INSTRU & TOOL	STW	50 2	0		39 P					
	FIRST SEM TOTAL = 520/750										
	3142 MAHIMA MUNDRA				REKH				,PICT		
	1										
SEM.:			100 4	0 22	2.2	F.C. 75					
	SIGNALS & SYSTEMS				23	56 P					
	SIGNALS & SYSTEMS		25 1		22	18 P					
	ELECTRONIC DEVICES & CIRCUITS				22	66 P					
	ELECTRONIC DEVICES & CIRCUITS				20	35 P					
204183 204183	NETWORK THEORY NETWORK THEORY		100 4 25 1		30	60 P 19 P					
204183					29	19 P 62 P					
204184					23	62 P 25 P					
204184	DIGITAL ELECTRONICS		100 4		22	23 P 48 P					
204185	DIGITAL ELECTRONICS DIGITAL ELECTRONICS		50 2		<i>L L</i>	46 P 24 P					
204185						24 P 37 P					
70 4 100	FIRST SEM TOTAL = 450/750	W I C	JU 2	•		<i>31</i> F					

	6 S.E.(2014 PAT.)(ELECTRONICS : 25 JAN 2016 CENTRE: P		-	E OF	COMPU ⁻	TER TECHNOLOG	Y, PUN	NE.	PAGE NO. 47	
	FIRST LINE : SEAT NO., NAME O									
	ES: SUB.CODE, SUB.NAME, SUB.TY			•		•		•	•	
	3143 MAJI SHREYA SAMBHUNATH									
SEM.:			100 40	2.2	2.6	50 -				
	SIGNALS & SYSTEMS			33	26	59 P				
	SIGNALS & SYSTEMS			4.1	2.4	21 P				
	ELECTRONIC DEVICES & CIRCUITS			41	34	75 P				
204182	ELECTRONIC DEVICES & CIRCUITS			20	40	30 P				
204183	NETWORK THEORY			30	40	70 P				
204183	NETWORK THEORY			42	20	23 P				
204184	DATA STRUCTURES & ALGORITHMS			42	39	81 P				
204184	DATA STRUCTURES & ALGORITHMS			2.2	26	42 P				
204185	DIGITAL ELECTRONICS			33	26					
204185						33 P				
	ELEX. MEASURING INSTRU & TOOLS FIRST SEM TOTAL = 537/750					44 P				
	3144 MANASI RAVINDRA JAGTAP									
SEM.:	1									
204181	SIGNALS & SYSTEMS	PP 3	100 40	14	12	F				
204181	SIGNALS & SYSTEMS	TW	25 10			17 P				
204182	ELECTRONIC DEVICES & CIRCUITS	PP 1	100 40	34	18	52 P				
204182	ELECTRONIC DEVICES & CIRCUITS	PR	50 20			37 P				
204183	NETWORK THEORY	PP 1	100 40	18	24	42 P				
204183	NETWORK THEORY	TW	25 10			19 P				
204184	DATA STRUCTURES & ALGORITHMS	PP 1	100 40	31	41	72 P				
204184	DATA STRUCTURES & ALGORITHMS	OR	50 20			25 P				
204185	DIGITAL ELECTRONICS	PP 3	100 40	28	26	54 P				
204185	DIGITAL ELECTRONICS	PR	50 20			38 P				
204186	ELEX. MEASURING INSTRU & TOOL	STW	50 20			37 P				
	FIRST SEM TOTAL =/750									
	3145 MANDE SHIVANEE VISHWAS					ADHA		71527372〕		
SEM.:	1									
204181	SIGNALS & SYSTEMS	PP 1	100 40	22	18	40 P				
204181	SIGNALS & SYSTEMS	TW	25 10			21 P				
204182	ELECTRONIC DEVICES & CIRCUITS	PP 1	100 40	41	26	67 P				
204182	ELECTRONIC DEVICES & CIRCUITS	PR	50 20			36 P				
204183	NETWORK THEORY	PP 1	100 40	32	23	55 P				
204183	NETWORK THEORY	TW	25 10			22 P				
204184	DATA STRUCTURES & ALGORITHMS	PP :	100 40	34	33	67 P				
204184	DATA STRUCTURES & ALGORITHMS	OR	50 20			30 P				
204185	DIGITAL ELECTRONICS	PP :	100 40	28	26	54 P				
204185	DIGITAL ELECTRONICS	PR	50 20			26 P				
204186	ELEX. MEASURING INSTRU & TOOL	STW	50 20			40 P				
	FIRST SEM TOTAL = 458/750									

	E: 25 JAN 2016 CENTRE:									PAGE NO			
NOTE: HER LIN	FIRST LINE : SEAT NO., NAME JES: SUB.CODE, SUB.NAME, SUB.T	OF THE	CANDIDA AX.MARKS	ATE, S, MI	MOTHE IN.PASS	ER, PER S MARKS	MANENT REG , OE MARKS	. NO., PRE , TH MARKS,	VIOUS SEAT OBTAINED,	NO., P/F:P/	COLLEGE, ASS/FAIL,	SEAT NO. C:PREV.CARR	RY OVE
	3146 MANDORA TEJAS SANDIP												
SEM.:	1												
204181	SIGNALS & SYSTEMS	PP 1	00 40	33	18	51 P							
	SIGNALS & SYSTEMS					18 P							
	ELECTRONIC DEVICES & CIRCUIT			36	31	67 P							
	ELECTRONIC DEVICES & CIRCUIT					28 P							
204183	NETWORK THEORY		00 40	38	40	78 P							
204183	NETWORK THEORY					20 P							
204184	DATA STRUCTURES & ALGORITHMS			25	38	63 P							
204184	DATA STRUCTURES & ALGORITHMS					24 P							
204185	DIGITAL ELECTRONICS			37	24								
204185	DIGITAL ELECTRONICS					28 P							
204186	ELEX. MEASURING INSTRU & TOO FIRST SEM TOTAL = 475/750	LSTW	50 20			37 P							
	3147 MANGIRE SNEHAL VIHANG							, 71527376м					
SEM.:	1												
	SIGNALS & SYSTEMS	pp 1	00 40	20	20	40 P							
	SIGNALS & SYSTEMS			20	20	16 P							
	ELECTRONIC DEVICES & CIRCUIT			40	35	75 P							
	ELECTRONIC DEVICES & CIRCUIT			40	33	29 P							
204183	NETWORK THEORY		00 40	31	33	64 P							
204183	NETWORK THEORY		25 10	3_	33	20 P							
204184	DATA STRUCTURES & ALGORITHMS			30	42	72 P							
204184				30		22 P							
204185	DIGITAL ELECTRONICS		00 40	28	27	55 P							
					_,	27 P							
	ELEX. MEASURING INSTRU & TOO					32 P							
	FIRST SEM TOTAL = 452/750												
	3148 MANTRI NIKITA RAJESH							,71527380к					
SEM.:	1												
	SIGNALS & SYSTEMS	PP 1	00 40	27	21	48 P							
204181	SIGNALS & SYSTEMS	TW	25 10			18 P							
204182	ELECTRONIC DEVICES & CIRCUIT	S PP 1	00 40	37	21	58 P							
204182	ELECTRONIC DEVICES & CIRCUIT	S PR	50 20			32 P							
204183	NETWORK THEORY	PP 1	00 40	20	20	40 P							
204183	NETWORK THEORY	TW	25 10			18 P							
204184	DATA STRUCTURES & ALGORITHMS	PP 1	00 40	24	39	63 P							
204184	DATA STRUCTURES & ALGORITHMS	OR	50 20			24 P							
204185	DIGITAL ELECTRONICS	PP 1	00 40	26	19	45 P							
204185	DIGITAL ELECTRONICS	PR	50 20			26 P							
204186	ELEX. MEASURING INSTRU & TOO	LSTW	50 20			34 P							
	FIRST SEM TOTAL = 406/750												

	E: 25 JAN 2016 CENTRE:									PAGE N		
NOTE: THER LIN	FIRST LINE : SEAT NO., NAME SES: SUB.CODE, SUB.NAME, SUB.T	OF TH	HE CANDIC	OATE, (S, M	MOTHE IN.PASS	ER, PERMA MARKS,	NENT REG. OE MARKS,	NO., PRE	VIOUS SEAT OBTAINED,	NO., P/F:P	COLLEGE, ASS/FAIL,	SEAT NO. C:PREV.CARRY OV
	3149 MANTRI RUCHI VENUGOPAL											
SEM.:	1											
204181	SIGNALS & SYSTEMS	PP	100 40	28	35	63 P						
204181	SIGNALS & SYSTEMS	TW	25 10			23 P						
204182	ELECTRONIC DEVICES & CIRCUIT	S PP	100 40	36	41	77 P						
204182	ELECTRONIC DEVICES & CIRCUIT					38 P						
204183	NETWORK THEORY		100 40	39	39	78 P						
204183	NETWORK THEORY					22 P						
204184	DATA STRUCTURES & ALGORITHMS	S PP	100 40	38	47	85 P						
204184	DATA STRUCTURES & ALGORITHMS					29 P						
204185	DIGITAL ELECTRONICS			30	21							
204185	DIGITAL ELECTRONICS	PR	50 20			33 P						
	ELEX. MEASURING INSTRU & TOO FIRST SEM TOTAL = 539/750					40 P						
	3150 MANWAR PRASHIK KISHOR								,PICT			
SEM.:	1											
	SIGNALS & SYSTEMS	DD	100 40	21	20	54 P						
	SIGNALS & SYSTEMS			34	20	34 P 20 P						
	ELECTRONIC DEVICES & CIRCUIT			40	34	20 P 74 P						
	ELECTRONIC DEVICES & CIRCUIT			40	34	74 P 28 P						
204182	NETWORK THEORY		100 40	41	36	20 P 77 P						
204183	NETWORK THEORY		25 10	41	30	77 P 22 P						
204184	DATA STRUCTURES & ALGORITHMS			31	38	69 P						
204184				31	30	35 P						
204184	DIGITAL ELECTRONICS		100 40	21	25	56 P						
			50 20	21	23	30 P						
	DIGITAL ELECTRONICS					30 P 39 P						
204100	ELEX. MEASURING INSTRU & TOO FIRST SEM TOTAL = 504/750	JLSIW	30 20			39 P						
	3151 MARKAD NIKHIL NANDKISH								,PICT			
SEM.:	1											
	SIGNALS & SYSTEMS	PP	100 40	38	19	57 P						
	SIGNALS & SYSTEMS		25 10			20 P						
	ELECTRONIC DEVICES & CIRCUIT			31	21	52 P						
	ELECTRONIC DEVICES & CIRCUIT					29 P						
204183	NETWORK THEORY		100 40	37	18	55 P						
204183	NETWORK THEORY		25 10			22 P						
204184	DATA STRUCTURES & ALGORITHMS			35	37	72 P						
204184						38 P						
204185	DIGITAL ELECTRONICS			27	15	42 P						
204185	DIGITAL ELECTRONICS		50 20			30 P						
	ELEX. MEASURING INSTRU & TOO					44 P						
	FIRST SEM TOTAL = 461/750	,,				- · ·						

DATE	.6 S.E.(2014 PAT.)(ELECTRONICS E: 25 JAN 2016 CENTRE: F						•			
NOTE:	FIRST LINE : SEAT NO., NAME CHES: SUB.CODE, SUB.NAME, SUB.TY	F TH	HE CAN	IDIDATE,	МОТН	ER, P	ERMANENT RE	G. NO., PREV	COUS SEAT NO	., COLLEGE, SEAT NO.
	3152 MARLIWAR PRADHYUMNA ANI									
SEM.:										
	SIGNALS & SYSTEMS				42	78				
	SIGNALS & SYSTEMS					23				
204182					41	81				
	ELECTRONIC DEVICES & CIRCUITS				4.5	42				
204183					43	78				
204183	NETWORK THEORY				4.0	23				
204184	DATA STRUCTURES & ALGORITHMS				42	80				
204184					2=	44				
204185					37	76				
204185						41				
204186	ELEX. MEASURING INSTRU & TOOL FIRST SEM TOTAL = 606/750	.SIW	50 2	20		40	Р			
	3153 MAYURI DNYANESHWAR MAHA									,s140053153
								,	,	,
SEM.:										
	SIGNALS & SYSTEMS				29	62				
	SIGNALS & SYSTEMS					18				
	ELECTRONIC DEVICES & CIRCUITS				18	59				
	ELECTRONIC DEVICES & CIRCUITS					36				
204183	NETWORK THEORY		100 4		27	57				
204183	NETWORK THEORY		25 1			21				
204184					40	69				
204184						33				
204185			100 4		23	61				
204185						7				
204186	ELEX. MEASURING INSTRU & TOOL FIRST SEM TOTAL =/750	.STW	50 2	20		38	P			
S14005	3154 MEHARE VAISHNAVI PRABHA	KAR			VIJA	YA		,71527392C	,PICT	,S140053154
SEM.:	1									
204181	SIGNALS & SYSTEMS	PP	100 4	10 18	15		F			
204181	SIGNALS & SYSTEMS	TW	25 1	_0		18	Р			
204182	ELECTRONIC DEVICES & CIRCUITS	PP	100 4	10 35	26	61	Р			
204182	ELECTRONIC DEVICES & CIRCUITS	PR	50 2	20		30	Р			
204183	NETWORK THEORY	PP	100 4	10 29	22	51	Р			
204183	NETWORK THEORY	TW	25 1	_0		19	Р			
204184	DATA STRUCTURES & ALGORITHMS	PP	100 4	10 17	36	53	Р			
204184	DATA STRUCTURES & ALGORITHMS	OR	50 2	20		30	Р			
204185	DIGITAL ELECTRONICS	PP	100 4	10 19	25	44	Р			
204405	DIGITAL ELECTRONICS	PR	50 2	20		5	F			
204185										
	ELEX. MEASURING INSTRU & TOOL	.STW	50 2	20		33	Р			

	6 S.E.(2014 PAT.)(ELECTRONICS : 25 JAN 2016 CENTRE: PU			-	E OF	COMPUT	ER	TECHNOLOGY,	PUNE.		PAGE N	o. 51	
NOTE:	FIRST LINE : SEAT NO., NAME OF	F TI	HE CA	NDID	ATE,	MOTHE	R,	PERMANENT RE	G. NO., PRE	VIOUS SEA	T NO.,	COLLEGE,	SEAT NO.
	3155 MEHETRE PRIYANKA BHAUSAH												
SEM.:	1												
204181	SIGNALS & SYSTEMS	PP	100	40	33	35	68	ВР					
204181	SIGNALS & SYSTEMS	TW	25	10			23	P					
204182	ELECTRONIC DEVICES & CIRCUITS	PP	100	40	48	33	81	. P					
204182	ELECTRONIC DEVICES & CIRCUITS	PR	50	20			34	P					
204183	NETWORK THEORY	PP	100	40	44	34	78	P					
204183	NETWORK THEORY	TW	25	10			22	! Р					
204184	DATA STRUCTURES & ALGORITHMS	PP	100	40	42	43	85	P					
204184	DATA STRUCTURES & ALGORITHMS	OR	50	20			43	Р					
204185	DIGITAL ELECTRONICS	PP	100	40	37	39	76	Р					
204185	DIGITAL ELECTRONICS	PR	50	20			36	P					
204186	ELEX. MEASURING INSTRU & TOOLS FIRST SEM TOTAL = 586/750	STW	50	20			40) P					
s14005	3156 MEHTA STUTI PUNIT			• • • •									
SEM.:	1												
	SIGNALS & SYSTEMS	PР	100	40	32	36	68	l P					
			25		32	30	21						
	ELECTRONIC DEVICES & CIRCUITS				42	30		 ! Р					
	ELECTRONIC DEVICES & CIRCUITS				72	30							
204183	NETWORK THEORY		100		36	38		P					
204183			25		30	30		Р					
204184	DATA STRUCTURES & ALGORITHMS				36	45		. P					
204184	DATA STRUCTURES & ALGORITHMS							БР					
204185			100		38	36	74						
204185	DIGITAL ELECTRONICS) P					
	ELEX. MEASURING INSTRU & TOOLS							. P					
	FIRST SEM TOTAL = 575/750							-					
S14005	3157 MENGAJE PRANAY NILKANTH					VIMAL			,71527398в	,PICT		,S140053	157
SEM.:													
	SIGNALS & SYSTEMS				36	26		. P					
			25) P					
	ELECTRONIC DEVICES & CIRCUITS				43	25		Р					
204182	ELECTRONIC DEVICES & CIRCUITS) P					
204183			100		28	27		Р					
204183			25) P					
204184					32	27		P					
204184	DATA STRUCTURES & ALGORITHMS) P					
204185	DIGITAL ELECTRONICS				31	22		Р					
204185	DIGITAL ELECTRONICS) P					
204186	ELEX. MEASURING INSTRU & TOOLS FIRST SEM TOTAL = 472/750	STW	50	20			36	5 P					

	6 S.E.(2014 PAT.)(ELECTRONICS : 25 JAN 2016 CENTRE: P		-	UTE OF	COMPU	TER TECHNO	OLOGY, PU	INE.		PAGE NO. 52	
NOTE:	FIRST LINE : SEAT NO., NAME O	F TI	HE CAND	IDATE,	МОТН	ER, PERMAI	NENT REG.	NO., PRE	VIOUS SEAT	NO., COLLEGE,	SEAT NO.
	3158 MHASKE SAURABH SUKUMAR										
314003	3130 MINASKE SAUKABH SUKUMAK				FION	A	,	71327400H	, PICI	,3140033	136
SEM.:											
	SIGNALS & SYSTEMS				19	57 P					
	SIGNALS & SYSTEMS					19 P					
204182	ELECTRONIC DEVICES & CIRCUITS				31	70 P					
204182	ELECTRONIC DEVICES & CIRCUITS				22	25 P					
204183	NETWORK THEORY				32	62 P					
204183	NETWORK THEORY				2.4	19 P					
204184	DATA STRUCTURES & ALGORITHMS				34	75 P					
204184	DATA STRUCTURES & ALGORITHMS				22	36 P					
204185	DIGITAL ELECTRONICS				22	57 P					
204185	DIGITAL ELECTRONICS ELEX. MEASURING INSTRU & TOOL					35 P 33 P					
	FIRST SEM TOTAL = 488/750										
	3159 MHETRE SAYALI NARAYAN										
SEM.:	1										
	SIGNALS & SYSTEMS	DD	100 40	31	21	52 P					
	SIGNALS & SYSTEMS				21	22 P					
	ELECTRONIC DEVICES & CIRCUITS				30	70 P					
	ELECTRONIC DEVICES & CIRCUITS				30	35 P					
204183	NETWORK THEORY		100 40		29	61 P					
204183	NETWORK THEORY		25 10			21 P					
204184	DATA STRUCTURES & ALGORITHMS				42	77 P					
204184	DATA STRUCTURES & ALGORITHMS	OR	50 20			27 P					
204185	DIGITAL ELECTRONICS	PP	100 40	25	21	46 P					
204185			50 20			12 F					
204186	ELEX. MEASURING INSTRU & TOOL	STW	50 20			35 P					
	FIRST SEM TOTAL =/750										
	3160 MINCHANAL VISHVARADHYA							71527402D			
SEM.:	1										
	SIGNALS & SYSTEMS	DD	100 40	40	35	75 P					
	SIGNALS & SYSTEMS SIGNALS & SYSTEMS		25 10		<i>J J</i>	73 P 24 P					
	ELECTRONIC DEVICES & CIRCUITS				37	76 P					
	ELECTRONIC DEVICES & CIRCUITS				<i>.</i> 1	70 P					
204183			100 40		43	87 P					
204183	NETWORK THEORY		25 10		. 5	23 P					
204184					41	80 P					
204184						43 P					
204185	DIGITAL ELECTRONICS				32	70 P					
204185	DIGITAL ELECTRONICS		50 20		- -	35 P					
204186						45 P					
	FIRST SEM TOTAL = 588/750		· _ •								

	E: 25 JAN 2016 CENTRE:									PAGE N		
NOTE: HER LIN	FIRST LINE : SEAT NO., NAME NES: SUB.CODE, SUB.NAME, SUB.T	OF TH YPE,	E CANDID	ATE,	MOTHI IN.PASS	ER, PERM S MARKS,	ANENT REG. OE MARKS,	NO., PRE	VIOUS SEAT OBTAINED,	NO., P/F:P	COLLEGE, ASS/FAIL,	SEAT NO. C:PREV.CARRY OV
	33161 MISHRA AYUSHI DEEPAK											
SEM.:	1											
204181	SIGNALS & SYSTEMS	PP	100 40	29	17	46 P						
	SIGNALS & SYSTEMS					19 P						
	ELECTRONIC DEVICES & CIRCUIT			42	36	78 P						
						34 P						
204183	NETWORK THEORY		100 40	32	32	64 P						
204183	NETWORK THEORY					21 P						
204184	DATA STRUCTURES & ALGORITHMS			26	33	59 P						
204184	DATA STRUCTURES & ALGORITHMS					44 P						
204185	DIGITAL ELECTRONICS			26	27							
204185						30 P						
	ELEX. MEASURING INSTRU & TOO FIRST SEM TOTAL = 484/750					36 P						
	33162 MISTARI TUSHAR VILAS							71527405j				
SEM.:	.1											
	SIGNALS & SYSTEMS	DD	100 40	26	21	47 P						
	SIGNALS & SYSTEMS			20	21	47 P 18 P						
	ELECTRONIC DEVICES & CIRCUIT			37	33	70 P						
	ELECTRONIC DEVICES & CIRCUIT			37	33	70 P 31 P						
204183	NETWORK THEORY		100 40	34	31	65 P						
204183	NETWORK THEORY		25 10	74	31	21 P						
204184	DATA STRUCTURES & ALGORITHMS			33	36	69 P						
204184				33	30	43 P						
204185			100 40	35	29	64 P						
	DIGITAL ELECTRONICS			33	23	30 P						
	ELEX. MEASURING INSTRU & TOO					35 P						
	FIRST SEM TOTAL = 493/750											
	33163 MODAK NIRANJAN PARAG							71527407E				
SEM.:	1											
	SIGNALS & SYSTEMS	PP	100 40	32	AA	F						
	SIGNALS & SYSTEMS					21 P						
	ELECTRONIC DEVICES & CIRCUIT			45	30	75 P						
	ELECTRONIC DEVICES & CIRCUIT					32 P						
204183	NETWORK THEORY		100 40	23	24	47 P						
204183	NETWORK THEORY		25 10			22 P						
204184	DATA STRUCTURES & ALGORITHMS			40	23	63 P						
204184					=	32 P						
204185	DIGITAL ELECTRONICS			36	27	63 P						
204185					= =	38 P						
	ELEX. MEASURING INSTRU & TOO					38 P						
	FIRST SEM TOTAL = $/750$	· · ·				- - ·						

	E: 25 JAN 2016 CENTRE: P						•		
NOTE:	FIRST LINE : SEAT NO., NAME ONES: SUB.CODE, SUB.NAME, SUB.TY	F TH	HE CAN	NDIDATE,	MOTH	IER, PERMANE	NT REG. NO., PREV	IOUS SEAT NO	O., COLLEGE, SEAT NO.
	53164 MODOKAR SAYALI RAJESH								,s140053164
SEM.:									
	SIGNALS & SYSTEMS				24	53 P			
	SIGNALS & SYSTEMS					17 P			
204182					28	59 P			
	ELECTRONIC DEVICES & CIRCUITS					31 P			
204183					22	51 P			
204183	NETWORK THEORY					20 P			
204184					23	53 P			
204184					24	31 P			
204185					31	62 P			
204185						30 P			
204186	ELEX. MEASURING INSTRU & TOOL FIRST SEM TOTAL = 437/750	.SIW	5U 2	20		30 P			
	33165 MOLE SHRAVANI UDAYSING								,s140053165
SEM.:			100	40 10	22	_			
	SIGNALS & SYSTEMS				22				
	SIGNALS & SYSTEMS				4-	22 P			
	ELECTRONIC DEVICES & CIRCUITS				1/	48 P			
	ELECTRONIC DEVICES & CIRCUITS				22	30 P			
204183			100 4		22	40 P			
204183	NETWORK THEORY		25 1		27	19 P			
204184					27	51 P			
204184						30 P			
204185			100 4		26	51 P			
204185						31 P			
204186	ELEX. MEASURING INSTRU & TOOL FIRST SEM TOTAL = $/750$.STW	50 2	20		34 P			
	53166 MRADUL BADOLE								,s140053166
							•	·	,
SEM.:			100	40 7.5	10	_			
	SIGNALS & SYSTEMS		100 4		10	F			
	SIGNALS & SYSTEMS		25 1		4 =	16 P			
	ELECTRONIC DEVICES & CIRCUITS				17	49 P			
	ELECTRONIC DEVICES & CIRCUITS				2.5	35 P			
204183			100 4		25	50 P			
204183			25 1		2.2	20 P			
204184					22	43 P			
204184						26 P -			
204185					06	F			
204185			50 2			30 P			
204186	ELEX. MEASURING INSTRU & TOOL	.STW	50 2	20		31 P			
	FIRST SEM TOTAL = $/750$								

	6 S.E.(2014 PAT.)(ELECTRONICS : 25 JAN 2016 CENTRE: P		-		COMPU	TER T	TECHNOLOGY, PU	JNE.		PAGE NO. 55	
	FIRST LINE : SEAT NO., NAME O			•		•		•		•	
OTHER LIN	ES: SUB.CODE, SUB.NAME, SUB.TY	PΕ,	MAX.MA	RKS, MI	N.PAS	S MAF	RKS, OE MARKS,	, TH MARKS,	OBTAINED,	P/F:PASS/FAIL,	, C:PREV.CARRY OVER
S14005	3167 NAGARE SWAPNALI SANJAY				PUSHI	PA	,	,71633639в	,PICT	,S140053	3167
SEM.:			100 10	2.0	24	4.0	_				
	SIGNALS & SYSTEMS				21						
	SIGNALS & SYSTEMS				2.7	22					
	ELECTRONIC DEVICES & CIRCUITS				37	77					
204182	ELECTRONIC DEVICES & CIRCUITS				27	42					
204183	NETWORK THEORY				27	59					
204183	NETWORK THEORY				2.5	22					
204184	DATA STRUCTURES & ALGORITHMS				35	68					
204184	DATA STRUCTURES & ALGORITHMS				27	36 71					
204185	DIGITAL ELECTRONICS				37						
204185						30					
204186	ELEX. MEASURING INSTRU & TOOL	SIW	50 20			45	Р				
	FIRST SEM TOTAL = 521/750										
	3168 NANGARE AKSHAY ARUN									,s140053	
314003	3100 NANGARE AKSHAT AKUN				AINTIA	4	,	,713274330	, PICI	,314003.)100
SEM.:	1										
	SIGNALS & SYSTEMS	DD	100 40	27	23	50	D				
	SIGNALS & SYSTEMS				23	23					
	ELECTRONIC DEVICES & CIRCUITS				22	61					
	ELECTRONIC DEVICES & CIRCUITS					36					
204183			100 40		21	46					
204183	NETWORK THEORY		25 10			23					
204184					28	68					
204184						36					
	DIGITAL ELECTRONICS		100 40		34	69					
	DIGITAL ELECTRONICS		50 20			40					
	ELEX. MEASURING INSTRU & TOOL					40					
	FIRST SEM TOTAL = 492/750										
s14005	3170 NAVRANGE ROHIT VILASRAO				VAIS	HALI	,	,71527437G	,PICT	,s140053	3170
SEM.:	1										
204181	SIGNALS & SYSTEMS	PP	100 40	28	21	49	Р				
204181	SIGNALS & SYSTEMS	TW	25 10			17	Р				
204182	ELECTRONIC DEVICES & CIRCUITS	PP	100 40	41	23	64	P				
204182	ELECTRONIC DEVICES & CIRCUITS	PR	50 20			34	P				
204183	NETWORK THEORY	PP	100 40	29	19	48	P				
204183	NETWORK THEORY	TW	25 10			20	P				
204184	DATA STRUCTURES & ALGORITHMS	PP	100 40	33	36	69	P				
204184	DATA STRUCTURES & ALGORITHMS	OR	50 20			9	F				
204185	DIGITAL ELECTRONICS	PP	100 40	26	18	44	Р				
204185	DIGITAL ELECTRONICS	PR	50 20			10	F				
204186	ELEX. MEASURING INSTRU & TOOL	STW	50 20			36	Р				
	FIRST SEM TOTAL =/750										

	E: 25 JAN 2016 CENTRE:									PAGE NO		
NOTE: HER LIN	FIRST LINE : SEAT NO., NAME NES: SUB.CODE, SUB.NAME, SUB.T	OF TH	HE CANDIDA	ATE, S, MI	MOTHE N.PASS	R, P MAR	ERMANENT REG KS, OE MARKS	. NO., PRE	VIOUS SEAT OBTAINED,	NO., P/F:P/	COLLEGE, ASS/FAIL,	SEAT NO. C:PREV.CARRY O
	33171 NEEL PARAG PENDHARKAR							,71527438E				
SEM.:	1											
204181	SIGNALS & SYSTEMS	PP	100 40	30	24	54	Р					
204181	SIGNALS & SYSTEMS	TW	25 10			19	Р					
	ELECTRONIC DEVICES & CIRCUIT			44	24	68						
	ELECTRONIC DEVICES & CIRCUIT					30						
204183	NETWORK THEORY		100 40	31	32	63						
204183	NETWORK THEORY					21						
204184	DATA STRUCTURES & ALGORITHMS			33	31	64						
204184	DATA STRUCTURES & ALGORITHMS					47						
204185	DIGITAL ELECTRONICS			41	32	73						
204185	DIGITAL ELECTRONICS					30						
	ELEX. MEASURING INSTRU & TOO FIRST SEM TOTAL = 507/750					38						
	33172 NEHA							,71527439C				
SEM.:	1											
204181	SIGNALS & SYSTEMS	PP	100 40	25	15	40	Р					
204181	SIGNALS & SYSTEMS	TW	25 10			19	Р					
204182	ELECTRONIC DEVICES & CIRCUIT	S PP	100 40	26	00		F					
204182	ELECTRONIC DEVICES & CIRCUIT	S PR	50 20			34	Р					
204183	NETWORK THEORY	PP	100 40	28	16	44	Р					
204183	NETWORK THEORY	TW	25 10			21	Р					
204184	DATA STRUCTURES & ALGORITHMS	PP	100 40	32	33	65	Р					
204184	DATA STRUCTURES & ALGORITHMS	OR	50 20			36	Р					
204185	DIGITAL ELECTRONICS	PP	100 40	34	23	57	Р					
204185	DIGITAL ELECTRONICS	PR	50 20			40	Р					
	ELEX. MEASURING INSTRU & TOO					35	Р					
	FIRST SEM TOTAL =/750											
	33173 NIMBALKAR SNEHAL DIPAK							,71633640F				
SEM.:	1											
204181	SIGNALS & SYSTEMS	PP	100 40	23	24	47	Р					
204181	SIGNALS & SYSTEMS	TW	25 10			22	Р					
204182	ELECTRONIC DEVICES & CIRCUIT	S PP	100 40	45	19	64	Р					
204182	ELECTRONIC DEVICES & CIRCUIT	S PR	50 20			44	Р					
204183	NETWORK THEORY	PP	100 40	23	17	40	Р					
204183	NETWORK THEORY	TW	25 10			22	Р					
204184	DATA STRUCTURES & ALGORITHMS	PP	100 40	27	36	63	Р					
204184	DATA STRUCTURES & ALGORITHMS	OR	50 20			38						
204185	DIGITAL ELECTRONICS	PP	100 40	31	23	54	Р					
204185	DIGITAL ELECTRONICS					35						
204186	ELEX. MEASURING INSTRU & TOO	LSTW	50 20			43	Р					

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NOTE:	FIRST LINE : SEAT NO., N	NAME OF THE CANDID	ATE,	MOTHER	R, PERMANE	NT REG. NO., PREV	IOUS SEAT	NO., COLLEGE, SEAT NO.
								P/F:PASS/FAIL, C:PREV.CARRY OV
	3174 NIMJE PIYUSH MADAN							,s140053174
SEM.:	1							
204181	SIGNALS & SYSTEMS	PP 100 40	35	29	64 P			
204181	SIGNALS & SYSTEMS	TW 25 10			20 P			
204182	ELECTRONIC DEVICES & CIR	CUITS PP 100 40	38	33	71 P			
204182	ELECTRONIC DEVICES & CIR	CUITS PR 50 20			40 P			
204183	NETWORK THEORY	PP 100 40	35	37	72 P			
204183	NETWORK THEORY	TW 25 10			19 P			
204184	DATA STRUCTURES & ALGORI	THMS PP 100 40	35	39	74 P			
204184	DATA STRUCTURES & ALGORI	THMS OR 50 20			33 P			
204185	DIGITAL ELECTRONICS	PP 100 40	23	33	56 P			
204185	DIGITAL ELECTRONICS	PR 50 20			38 P			
	ELEX. MEASURING INSTRU & FIRST SEM TOTAL = 524/75	50			37 P			
	3175 NITNAWARE VISHWAJI							,s140053175
SEM.:	1							
204181	SIGNALS & SYSTEMS	PP 100 40	17	06	F			
204181	SIGNALS & SYSTEMS	TW 25 10			21 P			
204182	ELECTRONIC DEVICES & CIR	RCUITS PP 100 40	31	19	50 P			
204182	ELECTRONIC DEVICES & CIR	RCUITS PR 50 20			24 P			
204183	NETWORK THEORY	PP 100 40	20	20	40 P			
204183	NETWORK THEORY	TW 25 10			21 P			
204184	DATA STRUCTURES & ALGORI	THMS PP 100 40	22	22	44 P			
204184	DATA STRUCTURES & ALGORI	THMS OR 50 20			21 P			
204185	DIGITAL ELECTRONICS	PP 100 40	24	16	40 P			
204185	DIGITAL ELECTRONICS	PR 50 20			7 F			
204186	ELEX. MEASURING INSTRU & FIRST SEM TOTAL =/750				39 P			
	3176 OSWAL NEHUL VIKRAM			MEENA		,71527449L		,s140053176
SEM.:	1							
	SIGNALS & SYSTEMS	PP 100 40	34	20	63 P			
	SIGNALS & SYSTEMS	TW 25 10	J 7	23	23 P			
	ELECTRONIC DEVICES & CIR		48	43	91 P			
	ELECTRONIC DEVICES & CIR		70	T J	38 P			
204182	NETWORK THEORY	PP 100 40	37	46	83 P			
204183	NETWORK THEORY	TW 25 10	37	70	23 P			
204183	DATA STRUCTURES & ALGORI		38	34	72 P			
204184	DATA STRUCTURES & ALGORI		20	J4	72 P 42 P			
204184		PP 100 40	37	28	42 P 65 P			
			37	40				
204185	DIGITAL ELECTRONICS ELEX. MEASURING INSTRU &				35 P 39 P			
204186	FIRST SEM TOTAL = 574/75				שט צ			

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NOTE:	FIRST LINE : SEAT NO.,	NAME OF THE CAN	DIDATE,	MOTHER	R, PERMANEN	T REG. NO., PREV	IOUS SEAT	NO., COLLEGE, SEAT NO. P/F:PASS/FAIL, C:PREV.CARRY
S14005	3177 OSWAL RUSHABHKUMA	AR RAJESHKUMAR		NEETA		,71527450D	,PICT	,S140053177
SEM.:	1							
	SIGNALS & SYSTEMS	PP 100 4	0 35	23	58 P			
	SIGNALS & SYSTEMS				21 P			
204182	ELECTRONIC DEVICES & CI	RCUITS PP 100 4	0 45	34	79 P			
204182	ELECTRONIC DEVICES & CI	RCUITS PR 50 2	0		35 P			
204183	NETWORK THEORY	PP 100 4	0 29	34	63 P			
204183	NETWORK THEORY	TW 25 1	.0		22 P			
204184	DATA STRUCTURES & ALGOR	RITHMS PP 100 4	0 32	43	75 P			
204184	DATA STRUCTURES & ALGOR	RITHMS OR 50 2	0		40 P			
204185	DIGITAL ELECTRONICS	PP 100 4	0 29	37	66 P			
204185	DIGITAL ELECTRONICS	PR 50 2	0		30 P			
	ELEX. MEASURING INSTRU FIRST SEM TOTAL = 532/7	'50			43 P			
	3178 PANDAGALE SHRUTI							,s140053178
SEM.:	1							
204181	SIGNALS & SYSTEMS	PP 100 4	0 30	16	46 P			
204181	SIGNALS & SYSTEMS	TW 25 1	.0		17 P			
204182	ELECTRONIC DEVICES & CI	RCUITS PP 100 4	0 40	26	66 P			
204182	ELECTRONIC DEVICES & CI	RCUITS PR 50 2	0		41 P			
204183	NETWORK THEORY	PP 100 4	0 35	25	60 P			
204183	NETWORK THEORY	TW 25 1	.0		19 P			
204184	DATA STRUCTURES & ALGOR	RITHMS PP 100 4	0 34	37	71 P			
204184	DATA STRUCTURES & ALGOR	RITHMS OR 50 2	0		35 P			
204185	DIGITAL ELECTRONICS	PP 100 4	0 30	21	51 P			
204185	DIGITAL ELECTRONICS	PR 50 2	0		30 P			
204186	ELEX. MEASURING INSTRU FIRST SEM TOTAL = 463/7		0		27 P			
	3179 PANDE ADITI UMAKA			ANJAL				,s140053179
SEM.:	1							
	SIGNALS & SYSTEMS	PP 100 4	0 31	21	52 P			
	SIGNALS & SYSTEMS	TW 25 1			21 P			
	ELECTRONIC DEVICES & CI			40	80 P			
	ELECTRONIC DEVICES & CI			. 5	46 P			
204183	NETWORK THEORY	PP 100 4		37	65 P			
204183	NETWORK THEORY	TW 25 1		<i>3.</i>	23 P			
204184	DATA STRUCTURES & ALGOR			34	61 P			
204184	DATA STRUCTURES & ALGOR			J .	8 F			
204185		PP 100 4		29	63 P			
204185		PR 50 2			45 P			
204186	ELEX. MEASURING INSTRU FIRST SEM TOTAL =/75	& TOOLSTW 50 2			38 P			

	6 S.E.(2014 PAT.)(ELECTRONICS : 25 JAN 2016 CENTRE: P		-		COMPU	TER TECHN	NOLOGY. PU	INE.		PAGE NO. 59	
NOTE:	FIRST LINE : SEAT NO., NAME O ES: SUB.CODE, SUB.NAME, SUB.TY	 F TI	HE CAND	 DIDATE,	MOTH	ER, PERMA	ANENT REG.	NO., PRE	vious seat	NO., COLLEGE,	SEAT NO.
S14005	3180 PAPULWAR YOGESHWARI PRA	KASI	Н		MANG	LA	,	71633642в	,PICT	,s140053	180
SEM.:	1										
204181	SIGNALS & SYSTEMS	PP	100 40	16	02	F					
204181	SIGNALS & SYSTEMS	TW	25 10)		21 P					
204182	ELECTRONIC DEVICES & CIRCUITS	PP	100 40) 19	10	F					
204182	ELECTRONIC DEVICES & CIRCUITS	PR	50 20)		30 P					
204183	NETWORK THEORY	PP	100 40	15	05	F					
204183	NETWORK THEORY	TW	25 10)		19 P					
204184	DATA STRUCTURES & ALGORITHMS	PP	100 40	06	17	F					
204184	DATA STRUCTURES & ALGORITHMS					24 P					
204185	DIGITAL ELECTRONICS	PP	100 40	23	09	F					
204185	DIGITAL ELECTRONICS	PR	50 20)		9 F					
	ELEX. MEASURING INSTRU & TOOL FIRST SEM TOTAL =/750					29 P					
	3181 PARATE DEVYANI VIJAY										
SEM.:	1										
	SIGNALS & SYSTEMS	PР	100 40) 35	27	62 P					
	SIGNALS & SYSTEMS					23 P					
	ELECTRONIC DEVICES & CIRCUITS				29	70 P					
	ELECTRONIC DEVICES & CIRCUITS					43 P					
204183	NETWORK THEORY		100 40		31	61 P					
204183	NETWORK THEORY		25 10		31	23 P					
204184	DATA STRUCTURES & ALGORITHMS				42	82 P					
204184						40 P					
204185			100 40		38	74 P					
204185			50 20		30	44 P					
	ELEX. MEASURING INSTRU & TOOL					41 P					
201100	FIRST SEM TOTAL = 563/750	.5	30 20	,		12 1					
	3182 PARDESHI ABHIJEET VIJAY							71633643L			
SEM.:	1										
	SIGNALS & SYSTEMS	рD	100 40) 27	17	44 P					
	SIGNALS & SYSTEMS		25 10		11	21 P					
	ELECTRONIC DEVICES & CIRCUITS				25	60 P					
	ELECTRONIC DEVICES & CIRCUITS				_ ,	33 P					
204182			100 40		15	33 P 44 P					
204183	NETWORK THEORY		25 10		10	20 P					
204183					29	20 P 51 P					
204184					23	31 P					
204184	DIGITAL ELECTRONICS				22	32 P 46 P					
			50 20		44	46 P 28 P					
204185											
204186	ELEX. MEASURING INSTRU & TOOL FIRST SEM TOTAL = 420/750	.51W	3U ZU	,		41 P					

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NOTE:	FIRST LINE : SEAT NO., NAME OF ES: SUB.CODE, SUB.NAME, SUB.TYP	F TH	HE CA	NDIDA	TE,	MOTH	IER, PE	ERMANENT REG	. NO., PRE	VIOUS SEAT	NO., COLLEG	E, SEAT NO.
S14005	3183 PARDESHI SHUBHAM DINESH					DEEF	PIKA		,71633644〕	,PICT	,S1400	53183
SEM.:	1											
	SIGNALS & SYSTEMS	PP	100	40	28	17	45 F	•				
204181	SIGNALS & SYSTEMS	TW	25	10			22 F	•				
204182	ELECTRONIC DEVICES & CIRCUITS	PP	100	40	41	32	73 F					
204182	ELECTRONIC DEVICES & CIRCUITS	PR	50	20			32 F					
204183	NETWORK THEORY	PP	100	40	35	25	60 F					
204183	NETWORK THEORY	TW	25	10			22 F					
204184	DATA STRUCTURES & ALGORITHMS	PP	100	40	29	40	69 F					
204184	DATA STRUCTURES & ALGORITHMS	OR	50	20			38 F					
204185	DIGITAL ELECTRONICS	PP	100	40	38	39	77 F					
204185	DIGITAL ELECTRONICS	PR	50	20			35 F	•				
	ELEX. MEASURING INSTRU & TOOLS FIRST SEM TOTAL = 512/750						39 F					
	3184 PATHAK NISHANT MANOJ										,S1400	
SEM.:	1											
204181	SIGNALS & SYSTEMS	PP	100	40	30	30	60 F	•				
204181	SIGNALS & SYSTEMS	TW	25	10			19 F					
204182	ELECTRONIC DEVICES & CIRCUITS	PP	100	40	39	27	66 F					
204182	ELECTRONIC DEVICES & CIRCUITS	PR	50	20			31 F					
204183	NETWORK THEORY	PP	100	40	33	26	59 F	•				
204183	NETWORK THEORY	TW	25	10			21 F	•				
204184	DATA STRUCTURES & ALGORITHMS	PP	100	40	34	36	70 F	•				
204184	DATA STRUCTURES & ALGORITHMS	OR	50	20			37 F	•				
204185	DIGITAL ELECTRONICS	PP	100	40	35	21	56 F	•				
204185	DIGITAL ELECTRONICS	PR	50	20			10 F	=				
204186	ELEX. MEASURING INSTRU & TOOLS FIRST SEM TOTAL =/750	STW	50	20			35 F	•				
	3185 PATIL HRUSHIKESH PRAMOD								, 71527470j			
C F -	1											
SEM.:	SIGNALS & SYSTEMS	חם	100	40	33	38	71 F	5				
	SIGNALS & SYSTEMS SIGNALS & SYSTEMS				J J	٥٥	71 F					
204181					46	39	23 F					
	ELECTRONIC DEVICES & CIRCUITS ELECTRONIC DEVICES & CIRCUITS				-10	39	42 F					
204182					40	36	76 F					
204183	NETWORK THEORY				1 0	50	76 F					
204183	DATA STRUCTURES & ALGORITHMS				41	41	82 F					
204184	DATA STRUCTURES & ALGORITHMS DATA STRUCTURES & ALGORITHMS				т⊥	71	36 F					
204184	DIGITAL ELECTRONICS				32	21	63 F					
204185	DIGITAL ELECTRONICS DIGITAL ELECTRONICS				J	ЭI	35 F					
204186							42 F					
704100	FIRST SEM TOTAL = 577/750	٧٧ ، ر	50	20			74 1					

DATE	L6 S.E.(2014 PAT.)(ELECTRONICE: 25 JAN 2016 CENTRE:	PUNE	INST	ITUTE									
NOTE: OTHER LIN	FIRST LINE : SEAT NO., NAME NES: SUB.CODE, SUB.NAME, SUB.T	OF T	HE CA MAX.	NDIDAT	ΓE, , MIN	MOTHER	, PE	ERMANENT REG. KS, OE MARKS,	. NO., PR , TH MARKS	EVIOUS SEAT	Γ NO., , P/F:F	COLLEGE, PASS/FAIL,	SEAT NO. C:PREV.CARRY OVE
	3186 PATIL PRAJAKTA BHAUSAH												
SEM.:	1												
204181	SIGNALS & SYSTEMS	PP	100	40 3	38	30	68 F	•					
204181	SIGNALS & SYSTEMS	TW	25	10			24 F						
204182	ELECTRONIC DEVICES & CIRCUIT	S PP	100	40 4	12	29	71 F						
204182	ELECTRONIC DEVICES & CIRCUIT	S PR	50	20			44 F	•					
204183	NETWORK THEORY		100		30	33	63 F						
204183	NETWORK THEORY	TW	25	10			23 F						
204184	DATA STRUCTURES & ALGORITHMS	PP	100	40 2	28	39	67 F						
204184	DATA STRUCTURES & ALGORITHMS	OR	50	20			30 F						
204185	DIGITAL ELECTRONICS	PP	100	40 2	28	22	50 F						
204185	DIGITAL ELECTRONICS	PR	50	20			25 F						
204186	ELEX. MEASURING INSTRU & TOO FIRST SEM TOTAL = 502/750	LSTW	50	20			37 F	0					
	3187 PATIL ROHINI JIVAN							,					
SEM.:	1												
	SIGNALS & SYSTEMS	DD	100	40 2	25	24	/Q E	5					
	SIGNALS & SYSTEMS				- 3		22 F						
	ELECTRONIC DEVICES & CIRCUIT		_	-	12		78 F						
	ELECTRONIC DEVICES & CIRCUIT			-	72		35 F						
204183	NETWORK THEORY		100		20		44 F						
204183	NETWORK THEORY		25		20		22 F						
204183	DATA STRUCTURES & ALGORITHMS				29		63 F						
204184					2.9		43 F						
			100		20								
	DIGITAL ELECTRONICS				29		62 F						
	DIGITAL ELECTRONICS		50				36 F						
204186	ELEX. MEASURING INSTRU & TOO FIRST SEM TOTAL = 497/750	LSIW	50	20			43 F	,					
s14005	33188 PATIL SHRIKANT SUNIL	• • • •							 , 71527476н				
SEM.:	1												
204181	SIGNALS & SYSTEMS	PP	100	40 2	26	15	41 F	•					
	SIGNALS & SYSTEMS		25				16 F						
204182	ELECTRONIC DEVICES & CIRCUIT	S PP	100	40 4	12	23	65 F	•					
	ELECTRONIC DEVICES & CIRCUIT						33 F						
204183	NETWORK THEORY		100		37		57 F						
204183	NETWORK THEORY		25				19 F						
204184	DATA STRUCTURES & ALGORITHMS				34		62 F						
204184							29 F						
204185	DIGITAL ELECTRONICS		100		38		56 F						
			50				30 F						
204186	ELEX. MEASURING INSTRU & TOO						31 F						
20,1100	FIRST SEM TOTAL = 439/750		30	_•			r						

	.6 S.E.(2014 PAT.)(ELECTRONICS E: 25 JAN 2016 CENTRE: PI		-	E OF	COMPUT	ER T	ΓECHNOLOGY,	PUNE.		PAGE NO	o. 62	
	FIRST LINE : SEAT NO., NAME OF SUB.CODE, SUB.NAME, SUB.TY											OVED
	3189 PATIL SHRUTI BABASAHEB											
SEM.:	1											
204181	SIGNALS & SYSTEMS	PP	100 40	31	29	60	Р					
204181	SIGNALS & SYSTEMS	TW	25 10			22	P					
204182	ELECTRONIC DEVICES & CIRCUITS	PP	100 40	44	39	83	P					
204182	ELECTRONIC DEVICES & CIRCUITS	PR	50 20			40	Р					
204183	NETWORK THEORY	PP	100 40	39	36	75	Р					
204183	NETWORK THEORY	TW	25 10			23	Р					
204184	DATA STRUCTURES & ALGORITHMS	PP	100 40	34	44	78	Р					
204184						38	Р					
204185	DIGITAL ELECTRONICS			37	34	71						
	DIGITAL ELECTRONICS					20						
204186	ELEX. MEASURING INSTRU & TOOLS	STW	50 20			37	Р					
	FIRST SEM TOTAL = 547/750											
	2100 DAWAD ANKLICH CANTAV											• • •
S14003	3190 PAWAR ANKUSH SANJAY				KANJA	ANA		,71633647C	, PICI		,5140053190	
SEM.:	1											
204181	SIGNALS & SYSTEMS	PP	100 40	24	26	50	Р					
204181	SIGNALS & SYSTEMS	TW	25 10			21	Р					
204182	ELECTRONIC DEVICES & CIRCUITS	PP	100 40	41	29	70	Р					
204182	ELECTRONIC DEVICES & CIRCUITS	PR	50 20			35	Р					
204183	NETWORK THEORY	PP	100 40	22	25	47	Р					
204183	NETWORK THEORY	TW	25 10			20	Р					
204184	DATA STRUCTURES & ALGORITHMS	PP	100 40	32	40	72	Р					
204184	DATA STRUCTURES & ALGORITHMS	OR	50 20			31	P					
204185	DIGITAL ELECTRONICS	PP	100 40	34	28	62	P					
204185	DIGITAL ELECTRONICS	PR	50 20			5	F					
204186	ELEX. MEASURING INSTRU & TOOLS	STW	50 20			41	Р					
	FIRST SEM TOTAL = $/750$											
	2101 PANAR CANTANT RANGUANER											
S14005	3191 PAWAR GAUTAMI RAMCHANDRA	А			BHAGY	ASHI	KEE	,71527480F	, PICI		,5140053191	
SEM.:	1											
	SIGNALS & SYSTEMS	PP	100 40	23	17	40	Р					
204181	SIGNALS & SYSTEMS	TW	25 10			16	Р					
204182	ELECTRONIC DEVICES & CIRCUITS	PP	100 40	32	25	57	Р					
204182	ELECTRONIC DEVICES & CIRCUITS	PR	50 20			35	Р					
204183	NETWORK THEORY	PP	100 40	19	23	42	Р					
204183	NETWORK THEORY	TW	25 10			20	Р					
204184	DATA STRUCTURES & ALGORITHMS	PP	100 40	20	42	62	Р					
204184	DATA STRUCTURES & ALGORITHMS	OR	50 20			7	F					
204185	DIGITAL ELECTRONICS	PP	100 40	20	27	47	Р					
204185	DIGITAL ELECTRONICS	PR	50 20			26	Р					
204186	ELEX. MEASURING INSTRU & TOOLS	STW	50 20			32	Р					
	FIRST SEM TOTAL =/750											

	E: 25 JAN 2016 CENTRE: F									PAGE NO		
NOTE: THER LIN	FIRST LINE : SEAT NO., NAME ONES: SUB.CODE, SUB.NAME, SUB.T	OF TH	E CANDIDA	ATE, S, M]	MOTHE IN.PASS	R, P MAR	ERMANENT REC	G. NO., PRE' S, TH MARKS,	VIOUS SEAT OBTAINED,	NO., P/F:P/	COLLEGE, ASS/FAIL,	SEAT NO. C:PREV.CARRY O
	53192 PAWAR NIKITA NIVAS							,71527482В				
SEM.:	:1											
204181	SIGNALS & SYSTEMS	PP :	100 40	19	09		=					
204181	SIGNALS & SYSTEMS	TW	25 10			16	•					
	ELECTRONIC DEVICES & CIRCUITS			32	17	49						
						33						
204183	NETWORK THEORY		100 40	22	22	44						
204183						19						
204184	DATA STRUCTURES & ALGORITHMS			25	23	48						
204184						22						
204185				24	21							
204185						4						
	ELEX. MEASURING INSTRU & TOOL FIRST SEM TOTAL =/750					30						
	53193 PAWAR POOJA YASHWANT							,71633648M				
SEM.:	-1											
	SIGNALS & SYSTEMS	DD ·	100 40	25	20	45	5					
	SIGNALS & SYSTEMS			23	20	22						
	ELECTRONIC DEVICES & CIRCUITS			35	31	66						
	ELECTRONIC DEVICES & CIRCUITS			33	31	34						
204183	NETWORK THEORY		100 40	25	17	42						
204183			25 10		Ξ,	22						
204184				30	43	73						
204184				30	.5	31						
204185			100 40	29	35	64						
	DIGITAL ELECTRONICS					7						
	ELEX. MEASURING INSTRU & TOOL					39						
	FIRST SEM TOTAL =/750											
	53194 PAWAR VAIBHAV BHAGAWAN							,71527484〕				
SEM.:	:1											
204181	SIGNALS & SYSTEMS	PP :	100 40	26	29	55	•					
204181	SIGNALS & SYSTEMS	TW	25 10			21	>					
204182	ELECTRONIC DEVICES & CIRCUITS	S PP	100 40	36	29	65	>					
204182	ELECTRONIC DEVICES & CIRCUITS	S PR	50 20			30	>					
204183	NETWORK THEORY	PP :	100 40	29	21	50	>					
204183	NETWORK THEORY	TW	25 10			21	>					
204184	DATA STRUCTURES & ALGORITHMS	PP :	100 40	41	37	78	•					
204184	DATA STRUCTURES & ALGORITHMS	OR	50 20			37	•					
204185	DIGITAL ELECTRONICS	PP :	100 40	33	24	57	•					
204185	DIGITAL ELECTRONICS	PR	50 20			8	=					
204186	ELEX. MEASURING INSTRU & TOOL	LSTW	50 20			38	>					
	FIRST SEM TOTAL =/750											

	: 25 JAN 2016 CENTRE: F							•					
NOTE:	FIRST LINE : SEAT NO., NAME CES: SUB.CODE, SUB.NAME, SUB.TY	F TI	HE CAI	NDIDAT	Ε,	MOTHE	R, PERMANI	ENT REG.	NO., PREV	'IOUS SEAT	NO.,	COLLEGE,	SEAT NO.
	3195 PAWAR. MAYURI GOKUL								71633649к				
SEM.:	1												
204181	SIGNALS & SYSTEMS	PP	100	40 2	6	20	46 P						
204181	SIGNALS & SYSTEMS	TW	25	10			24 P						
204182					0	37	77 P						
	ELECTRONIC DEVICES & CIRCUITS						30 P						
204183	NETWORK THEORY				7	22	59 P						
204183	NETWORK THEORY						21 P						
204184	DATA STRUCTURES & ALGORITHMS				4	39	63 P						
204184	DATA STRUCTURES & ALGORITHMS						41 P						
204185	DIGITAL ELECTRONICS				6	25	51 P						
204185	DIGITAL ELECTRONICS						30 P						
204186	ELEX. MEASURING INSTRU & TOOL FIRST SEM TOTAL = 484/750	.STW	50 7	20			42 P						
	3196 PETHKAR KAUSTUBH VIJAY								 71633650c				
CEM .	1												
SEM.:	SIGNALS & SYSTEMS	DD	100	40 2	1	27	51 P						
	SIGNALS & SYSTEMS				4	21	18 P						
	ELECTRONIC DEVICES & CIRCUITS				Λ	1 2	18 P 58 P						
	ELECTRONIC DEVICES & CIRCUITS				O	10	29 P						
204183	NETWORK THEORY		100		6	23	49 P						
204183	NETWORK THEORY		25		O	23	18 P						
204184	DATA STRUCTURES & ALGORITHMS				5	37	62 P						
204184					,	31	38 P						
204185			100		6	24	50 P						
204185	DIGITAL ELECTRONICS				O	4	9 F						
	ELEX. MEASURING INSTRU & TOOL						38 P						
204100	FIRST SEM TOTAL =/750	. J I W	30 /	20			30 F						
	3197 PHASE SIDHANT RAJKUMAR						RI		 71633651м				
SEM.:	1												
	SIGNALS & SYSTEMS	PP	100 4	40 2	9	22	51 P						
	SIGNALS & SYSTEMS		25				21 P						
204182	ELECTRONIC DEVICES & CIRCUITS	PP	100 4	40 4	7	33	80 P						
204182	ELECTRONIC DEVICES & CIRCUITS	PR	50 2	20			34 P						
204183	NETWORK THEORY	PP	100 4	40 3	4	22	56 P						
204183	NETWORK THEORY	TW	25	10			19 P						
204184	DATA STRUCTURES & ALGORITHMS	PP	100 4	40 3	4	38	72 P						
204184	DATA STRUCTURES & ALGORITHMS						40 P						
204185	DIGITAL ELECTRONICS	PP	100	40 4	1	30	71 P						
204185	DIGITAL ELECTRONICS		50 2				30 P						
204186	ELEX. MEASURING INSTRU & TOOL	.STW	50 2	20			34 P						
	FIRST SEM TOTAL = 508/750												

	.6 S.E.(2014 PAT.)(ELECTRONICS E: 25 JAN 2016 CENTRE: PI		-	TE OF	COMPUT	ER TECHNOLOGY,	PUNE.		PAGE NO. 65	
NOTE:	FIRST LINE : SEAT NO., NAME OF	F TH	E CANDI	DATE,	MOTHE	R, PERMANENT RE	G. NO., PRE	VIOUS SEAT	NO., COLLEGE,	SEAT NO.
	3198 POORSWANI NISHANT ARUN									
SEM.:		D.D.	100 40	42	20	01 p				
	SIGNALS & SYSTEMS			42	39	81 P				
	SIGNALS & SYSTEMS			4.0	40	24 P				
	ELECTRONIC DEVICES & CIRCUITS			48	40	88 P				
204182	ELECTRONIC DEVICES & CIRCUITS			4-7		45 P				
204183	NETWORK THEORY			47	44	91 P				
204183	NETWORK THEORY					23 P				
204184	DATA STRUCTURES & ALGORITHMS			42	43	85 P				
204184	DATA STRUCTURES & ALGORITHMS					38 P				
204185	DIGITAL ELECTRONICS			41	39	80 P				
204185						45 P				
	ELEX. MEASURING INSTRU & TOOLS FIRST SEM TOTAL = 644/750					44 P				
	3199 PRACHI KADU								,s1400531	
SEM.:	1									
204181	SIGNALS & SYSTEMS	PP	100 40	25	15	40 P				
204181	SIGNALS & SYSTEMS	TW	25 10			18 P				
	ELECTRONIC DEVICES & CIRCUITS			39	18	57 P				
	ELECTRONIC DEVICES & CIRCUITS					36 P				
204183	NETWORK THEORY		100 40	28	15	43 P				
204183			25 10			19 P				
204184				27	27	54 P				
204184						35 P				
204185			100 40	26	15	41 P				
	DIGITAL ELECTRONICS		50 20			26 P				
	ELEX. MEASURING INSTRU & TOOLS					37 P				
_000	FIRST SEM TOTAL = 406/750	•								
S14005	3200 PRAKASH KUMAR				MANJU	JDEVI	,71547751L	,PICT	,S1400532	200
SEM.:	1									
204181	SIGNALS & SYSTEMS	PP	100 40	17	23	40 P				
204181	SIGNALS & SYSTEMS	TW	25 10			21 P				
204182	ELECTRONIC DEVICES & CIRCUITS	PP	100 40	37	22	59 P				
204182	ELECTRONIC DEVICES & CIRCUITS	PR	50 20			32 P				
204183	NETWORK THEORY	PP	100 40	24	17	41 P				
204183	NETWORK THEORY	TW	25 10			18 P				
204184	DATA STRUCTURES & ALGORITHMS	PP	100 40	30	38	68 P				
204184	DATA STRUCTURES & ALGORITHMS	OR	50 20			36 P				
204185	DIGITAL ELECTRONICS	PP	100 40	30	30	60 P				
204185	DIGITAL ELECTRONICS	PR	50 20			32 P				
204186	ELEX. MEASURING INSTRU & TOOLS	STW	50 20			38 P				
	FIRST SEM TOTAL = 445/750									

	E: 25 JAN 2016 CENTRE:									PAGE N		
NOTE: HER LIN	FIRST LINE : SEAT NO., NAME NES: SUB.CODE, SUB.NAME, SUB.T	OF TH	HE CANDIC MAX.MARK	OATE,	MOTHE IN.PASS	ER, PE S MARI	ERMANENT REG (S, OE MARKS	. NO., PRE , TH MARKS,	VIOUS SEAT OBTAINED,	NO., P/F:P	COLLEGE, ASS/FAIL,	SEAT NO. C:PREV.CARRY
	33201 PRANAV DILIP BOROLE											
SEM.:	1											
204181	SIGNALS & SYSTEMS	PP	100 40	32	22	54 F	•					
	SIGNALS & SYSTEMS					18 F						
	ELECTRONIC DEVICES & CIRCUIT			33	28	61 F						
	ELECTRONIC DEVICES & CIRCUIT					32 F						
204183	NETWORK THEORY		100 40	32	24	56 F						
204183	NETWORK THEORY					20 F						
204184	DATA STRUCTURES & ALGORITHMS			34	35	69 F						
204184	DATA STRUCTURES & ALGORITHMS					38 F						
204185	DIGITAL ELECTRONICS			31	32							
204185	DIGITAL ELECTRONICS					30 F						
	ELEX. MEASURING INSTRU & TOO FIRST SEM TOTAL = 476/750					35 F						
	3202 PURVA KISHOR DESHMUKH							, 71527501в				
SEM.:	1											
	SIGNALS & SYSTEMS	DD	100 40	28	15	43 F)					
	SIGNALS & SYSTEMS			20	13	19 F						
	ELECTRONIC DEVICES & CIRCUIT			31	30	61 F						
	ELECTRONIC DEVICES & CIRCUIT			71	30	34 F						
204183	NETWORK THEORY		100 40	27	23	50 F						
204183	NETWORK THEORY		25 10	_,	23	22 F						
204184	DATA STRUCTURES & ALGORITHMS			46	42	88 F						
204184				10	12	34 F						
204185	DIGITAL ELECTRONICS		100 40	33	18	51 F						
	DIGITAL ELECTRONICS			33	10	6 F						
	ELEX. MEASURING INSTRU & TOO					36 F						
201100	FIRST SEM TOTAL =/750	LJIW	30 20			50 1						
	33203 RACHIT KUMAR THAKUR							,71527503J				
SEM.:	1											
	SIGNALS & SYSTEMS	PP	100 40	30	27	57 F)					
	SIGNALS & SYSTEMS		25 10			21 F						
	ELECTRONIC DEVICES & CIRCUIT			46	15	61 F						
	ELECTRONIC DEVICES & CIRCUIT			-	-	38 F						
204183	NETWORK THEORY		100 40	33	33	66 F						
204183	NETWORK THEORY		25 10			22 F						
204184	DATA STRUCTURES & ALGORITHMS			40	34	74 F						
204184						36 F						
204185	DIGITAL ELECTRONICS			39	23	62 F						
204185	DIGITAL ELECTRONICS		50 20		-•	26 F						
	ELEX. MEASURING INSTRU & TOO					39 F						
	FIRST SEM TOTAL = $502/750$					'						

	: 25 JAN 2016 CEM							PAGE NO. 67
NOTE:	FIRST LINE : SEAT NO.,	NAME OF THE CANDID	OATE,	MOTHER	R, PERMANE	NT REG. NO., PREV	IOUS SEAT	NO., COLLEGE, SEAT NO. P/F:PASS/FAIL, C:PREV.CARRY OV
		·	•		•		•	
	3204 RAKSHE VEDANT SHA							
SEM.:	1							
204181	SIGNALS & SYSTEMS	PP 100 40	29	20	49 P			
204181	SIGNALS & SYSTEMS	TW 25 10			22 P			
204182	ELECTRONIC DEVICES & CI	IRCUITS PP 100 40	35	32	67 P			
204182	ELECTRONIC DEVICES & CI	IRCUITS PR 50 20			40 P			
204183	NETWORK THEORY	PP 100 40	31	23	54 P			
204183	NETWORK THEORY	TW 25 10			22 P			
204184	DATA STRUCTURES & ALGOR	RITHMS PP 100 40	37	46	83 P			
204184	DATA STRUCTURES & ALGOR	RITHMS OR 50 20			34 P			
204185	DIGITAL ELECTRONICS	PP 100 40	33	31	64 P			
204185	DIGITAL ELECTRONICS	PR 50 20			30 P			
	FIRST SEM TOTAL = 508/7	750			43 P			
	3205 RAMAIYA APURVA UN							,s140053205
SEM.:	1							
204181	SIGNALS & SYSTEMS	PP 100 40	23	17	40 P			
204181	SIGNALS & SYSTEMS	TW 25 10			24 P			
204182	ELECTRONIC DEVICES & C	IRCUITS PP 100 40	45	37	82 P			
204182	ELECTRONIC DEVICES & CI	IRCUITS PR 50 20			34 P			
204183	NETWORK THEORY	PP 100 40	29	32	61 P			
204183	NETWORK THEORY	TW 25 10			22 P			
204184	DATA STRUCTURES & ALGOR	RITHMS PP 100 40	31	42	73 P			
204184	DATA STRUCTURES & ALGOR	RITHMS OR 50 20			39 P			
204185	DIGITAL ELECTRONICS	PP 100 40	31	31	62 P			
204185	DIGITAL ELECTRONICS	PR 50 20			30 P			
204186	ELEX. MEASURING INSTRU FIRST SEM TOTAL = 512/7				45 P			
s14005	3206 RANADE TANVI UDAY			PURWA		,71527512н		,s140053206
SEM.:	1							
	SIGNALS & SYSTEMS	PP 100 40	32	32	64 P			
	SIGNALS & SYSTEMS	TW 25 10			19 P			
204182	ELECTRONIC DEVICES & CI		36		64 P			
	ELECTRONIC DEVICES & CI		-		35 P			
204183	NETWORK THEORY	PP 100 40	37	28	65 P			
204183	NETWORK THEORY	TW 25 10			20 P			
204184	DATA STRUCTURES & ALGOR		31	37	68 P			
204184	DATA STRUCTURES & ALGOR				37 P			
204185		PP 100 40	29	22	51 P			
204185		PR 50 20	-		30 P			
204186	ELEX. MEASURING INSTRU FIRST SEM TOTAL = 489/7	& TOOLSTW 50 20			36 P			

	6 S.E.(2014 PAT.)(ELECTRONICS			-	COMPU	TED T	ECHNOLOGY BU	IN E		DAGE NO. CO	
	: 25 JAN 2016 CENTRE: P										
	FIRST LINE : SEAT NO., NAME O										
OTHER LIN	IES: SUB.CODE, SUB.NAME, SUB.TY	PΕ,	MAX.M	ARKS, MI	N. PAS	S MAR	KS, OE MARKS,	TH MARKS,	OBTAINED,	P/F:PASS/FAIL	, C:PREV.CARRY OVER
S14005	3207 RANDAD PRATIK PRAKASH				SANG	ITA	,	71527513F	,PICT	,S14005	3207
SEM.:	1										
	SIGNALS & SYSTEMS				31	63	Р				
204181	SIGNALS & SYSTEMS	TW	25 1	0		18	Р				
204182	ELECTRONIC DEVICES & CIRCUITS	PP	100 4	0 37	31	68	Р				
204182	ELECTRONIC DEVICES & CIRCUITS					36					
204183	NETWORK THEORY				37	68					
204183	NETWORK THEORY					18					
204184	DATA STRUCTURES & ALGORITHMS				43	80					
204184	DATA STRUCTURES & ALGORITHMS					38					
204185	DIGITAL ELECTRONICS				32	61					
204185						32					
204186	ELEX. MEASURING INSTRU & TOOL	STW	50 20	0		35	Р				
	FIRST SEM TOTAL = 517/750										
	2200 - DANE WITTER WANGING										
514005	3208 RANE MITESH MANOHAR				PKADI	NYA	,	/122/212R	, PICI	,S14005	3208
CEM .	1										
SEM.:	SIGNALS & SYSTEMS	DD	100 4	n 28	21	59	D				
	SIGNALS & SYSTEMS				21	17					
	ELECTRONIC DEVICES & CIRCUITS				22	52					
	ELECTRONIC DEVICES & CIRCUITS				22	30					
204182			100 4		19	40					
204183	NETWORK THEORY		25 10		13	20					
204184					42	74					
204184					72	31					
204185				0 27	26	53					
204185					20	37					
	ELEX. MEASURING INSTRU & TOOL					31					
	FIRST SEM TOTAL = $444/750$		-			-					
	3209 RASIKA HEMANT KULKARNI							71527521G			
							•		•	,	
SEM.:	1										
204181	SIGNALS & SYSTEMS	PP	100 4	0 28	80		F				
204181	SIGNALS & SYSTEMS	TW	25 1	0		10	Р				
204182	ELECTRONIC DEVICES & CIRCUITS	PP	100 4	0 38	05		F				
204182	ELECTRONIC DEVICES & CIRCUITS	PR	50 2	0		25	Р				
204183	NETWORK THEORY	PP	100 4	0 27	15	42	Р				
204183	NETWORK THEORY	TW	25 1	0		19	Р				
204184	DATA STRUCTURES & ALGORITHMS	PP	100 4	0 27	37	64	Р				
204184	DATA STRUCTURES & ALGORITHMS	OR	50 2	0		30	Р				
204185	DIGITAL ELECTRONICS	PP	100 4	0 25	16	41	Р				
204185	DIGITAL ELECTRONICS	PR	50 2	0		5	F				
204186	ELEX. MEASURING INSTRU & TOOL	STW	50 2	0		27	Р				
	FIRST SEM TOTAL =/750										

	6 S.E.(2014 PAT.)(ELECTRONICS : 25 JAN 2016 CENTRE: P		-	UTE OF	COMPU	JTER TECHNO	OLOGY, PU	NE.		PAGE NO. 69	
NOTE:	FIRST LINE : SEAT NO., NAME OES: SUB.CODE, SUB.NAME, SUB.TY	 F T	HE CAND	 IDATE,	МОТН	IER, PERMAN	 NENT REG.	NO., PRE	 VIOUS SEAT	NO., COLLEGE,	SEAT NO.
S14005	3210 RATHOD AISHWARYA RAJESH				SUPR	RIYA	,	71633653н	,PICT	,S1400532	210
SEM.:											
	SIGNALS & SYSTEMS				15						
	SIGNALS & SYSTEMS				20	21 P					
204182	ELECTRONIC DEVICES & CIRCUITS				39	86 P					
204182	ELECTRONIC DEVICES & CIRCUITS				20	33 P					
204183	NETWORK THEORY				30	58 P					
204183	NETWORK THEORY				20	22 P					
204184	DATA STRUCTURES & ALGORITHMS				38	73 P					
204184	DATA STRUCTURES & ALGORITHMS DIGITAL ELECTRONICS				20	38 P					
204185					39	73 P 38 P					
204185	ELEX. MEASURING INSTRU & TOOL					36 P 43 P					
	FIRST SEM TOTAL = 529/750										
	3211 RATHOD SURAJ MOHAN										
CEM .	1										
SEM.:			100 40	2.5	10	42 p					
	SIGNALS & SYSTEMS				10						
	SIGNALS & SYSTEMS ELECTRONIC DEVICES & CIRCUITS				22	17 P 45 P					
	ELECTRONIC DEVICES & CIRCUITS				23	43 P 25 P					
204182	NETWORK THEORY		100 40		24	48 P					
204183	NETWORK THEORY		25 10		24	19 P					
204184	DATA STRUCTURES & ALGORITHMS				4 1	66 P					
204184	DATA STRUCTURES & ALGORITHMS				71	6 F					
204185			100 40		27	42 P					
204185						10 F					
	ELEX. MEASURING INSTRU & TOOL					38 P					
	FIRST SEM TOTAL =/750										
	3212 RAUT HRISHIKESH PURUSHO							71527530F			
SEM.:	1										
	SIGNALS & SYSTEMS	PР	100 40	33	32	65 P					
	SIGNALS & SYSTEMS		25 10			18 P					
	ELECTRONIC DEVICES & CIRCUITS				25	72 P					
	ELECTRONIC DEVICES & CIRCUITS					42 P					
204183			100 40		37	72 P					
204183	NETWORK THEORY		25 10			21 P					
204184					40	86 P					
204184						42 P					
204185	DIGITAL ELECTRONICS				33	73 P					
204185			50 20			40 P					
204186	ELEX. MEASURING INSTRU & TOOL	STW	50 20			41 P					
	FIRST SEM TOTAL = 572/750										

	L6 S.E.(2014 PAT.)(ELECTRONICS		-							
	E: 25 JAN 2016 CENTRE: PI									
	FIRST LINE : SEAT NO., NAME O									
	NES: SUB.CODE, SUB.NAME, SUB.TY			•		•	•		,	
	3213 RAUT RINKOO POPAT									
							,	, -	,	
SEM.:	1									
204181	SIGNALS & SYSTEMS	PP	100 40	23	17	40 P				
204181	SIGNALS & SYSTEMS	TW	25 10)		22 P				
204182	ELECTRONIC DEVICES & CIRCUITS	PP	100 40	29	29	58 P				
204182	ELECTRONIC DEVICES & CIRCUITS	PR	50 20)		32 P				
204183	NETWORK THEORY	PP	100 40	29	17	46 P				
204183	NETWORK THEORY	TW	25 10)		20 P				
204184	DATA STRUCTURES & ALGORITHMS	PP	100 40	13	36	F				
204184	DATA STRUCTURES & ALGORITHMS	OR	50 20)		32 P				
204185	DIGITAL ELECTRONICS	PP	100 40	26	21	47 P				
204185	DIGITAL ELECTRONICS	PR	50 20)		10 F				
204186	ELEX. MEASURING INSTRU & TOOLS	STW	50 20)		31 P				
	FIRST SEM TOTAL =/750									
S14005	33214 RAUTMARE VISHWAJIT LAXM:	IKA	NT		SHUB	HANGI	,71527532В	,PICT	,s14005321	4
SEM.:										
	SIGNALS & SYSTEMS					F				
	SIGNALS & SYSTEMS					16 P				
204182	ELECTRONIC DEVICES & CIRCUITS	PP	100 40	35	20	55 P				
	ELECTRONIC DEVICES & CIRCUITS					15 F				
204183	NETWORK THEORY		100 40		11	F				
204183	NETWORK THEORY		25 10			16 P				
204184					38	60 P				
204184						30 P				
204185			100 40		23	56 P				
204185						30 P				
204186	ELEX. MEASURING INSTRU & TOOLS	STW	50 20)		24 P				
	FIRST SEM TOTAL =/750									
		• • •								
S14005	33215 RAVIRAJ KUMAR				RAGI	NI	,71527534J	,PICT	,S14005321	5
C=::	.1									
SEM.:			100 40		10	47 5				
	SIGNALS & SYSTEMS				19	47 P				
			25 10		2.0	18 P				
	ELECTRONIC DEVICES & CIRCUITS				26	61 P				
	ELECTRONIC DEVICES & CIRCUITS				20	27 P				
204183			100 40		29	62 P				
204183			25 10		20	20 P				
204184					39	81 P				
204184					20	32 P				
204185			50 20		29	61 P				
204185						7 F				
204186	ELEX. MEASURING INSTRU & TOOLS FIRST SEM TOTAL =/750	W۱د	JU 20	,		35 P				
	FINST SEM TOTAL =//30									

	6 S.E.(2014 PAT.)(ELECTRONICS : 25 JAN 2016 CENTRE: PU			-	OF	COMPUTE	ER T	ECHNOLOGY,	PUNE.	,	PAGE N	0. 71	
NOTE:	FIRST LINE : SEAT NO., NAME OF	F TH	HE CA	NDIDA	TE,	MOTHER	R, P	PERMANENT RE	G. NO., PR	EVIOUS SEA	AT NO.,	COLLEGE,	SEAT NO.
	3217 ROKADE GURUPRASAD PRAKAS								,71527540C				
SEM.:	1												
204181	SIGNALS & SYSTEMS	PP	100	40	24	80		F					
204181	SIGNALS & SYSTEMS	TW	25	10			10	Р					
	ELECTRONIC DEVICES & CIRCUITS				30	15	45						
	ELECTRONIC DEVICES & CIRCUITS						31						
204183			100		19		44						
204183	NETWORK THEORY						18						
204184	DATA STRUCTURES & ALGORITHMS				28		55						
204184	DATA STRUCTURES & ALGORITHMS						7						
204185	DIGITAL ELECTRONICS				26		46						
204185	DIGITAL ELECTRONICS						30						
	FIRST SEM TOTAL =/750						20						
	3218 RUPAWATE RAHUL RAVINDRA												
	4												
SEM.:			100	40	2-	4.5	4.0	_					
	SIGNALS & SYSTEMS				25		40						
			25		20		12						
	ELECTRONIC DEVICES & CIRCUITS				29		44						
	ELECTRONIC DEVICES & CIRCUITS				21		15						
204183 204183	NETWORK THEORY NETWORK THEORY		100 25		21		46 18						
204183	DATA STRUCTURES & ALGORITHMS				25		53						
204184					23	20	21						
204185					23	17	40						
204185	DIGITAL ELECTRONICS				23		20						
	ELEX. MEASURING INSTRU & TOOLS						22						
201100	FIRST SEM TOTAL = $/750$	J 1 W	30	20				•					
	3219 RUSHABH UTTAM KAMBLE								,71527545D				
SEM.:			100	40	20	1.5		_					
	SIGNALS & SYSTEMS				29		44						
			25		2.5		18						
	ELECTRONIC DEVICES & CIRCUITS				35	15	50						
	ELECTRONIC DEVICES & CIRCUITS				21	10	30						
204183			100		21	ТЭ	40						
204183			25		20	20	20						
204184					29	30	59 24						
204184	DATA STRUCTURES & ALGORITHMS				22	26	24						
204185	DIGITAL ELECTRONICS				33	20	59						
204185	DIGITAL ELECTRONICS						35						
204100	FIRST SEM TOTAL = 417/750	3 I W	30	20			38	r					

	E: 25 JAN 2016 CENTRE:							PAGE NO. 72
NOTE: HER LIN	FIRST LINE : SEAT NO., NAME NES: SUB.CODE, SUB.NAME, SUB.T	OF THE CAND	IDATE, RKS, MI	MOTHI IN.PAS	ER, PERMANEN S MARKS, OE	T REG. NO., PREV	IOUS SEAT OBTAINED,	P/F:PASS/FAIL, C:PREV.CARRY OV
	3220 SACHIN PRAKASH				Α			,S140053220
SEM.:	1							
204181	SIGNALS & SYSTEMS	PP 100 40	19	10	F			
204181	SIGNALS & SYSTEMS	TW 25 10			14 P			
204182	ELECTRONIC DEVICES & CIRCUIT	S PP 100 40	24	07	F			
204182	ELECTRONIC DEVICES & CIRCUIT	S PR 50 20			23 P			
204183	NETWORK THEORY	PP 100 40	22	06	F			
204183	NETWORK THEORY	TW 25 10			11 P			
204184	DATA STRUCTURES & ALGORITHMS	PP 100 40	18	25	43 P			
204184	DATA STRUCTURES & ALGORITHMS				8 F			
204185		PP 100 40	24	09	F			
204185	DIGITAL ELECTRONICS	PR 50 20			7 F			
	ELEX. MEASURING INSTRU & TOO FIRST SEM TOTAL =/750				21 P			
	3221 SAKAT TANAYA VASANT				 DA			,s140053221
SEM.:	1							
	SIGNALS & SYSTEMS	PP 100 40	26	25	51 P			
		TW 25 10	20	23	20 P			
	ELECTRONIC DEVICES & CIRCUIT		39	28	67 P			
	ELECTRONIC DEVICES & CIRCUIT		33	20	32 P			
204183	NETWORK THEORY	PP 100 40	28	30	58 P			
204183	NETWORK THEORY	Tw 25 10	20	30	18 P			
204184	DATA STRUCTURES & ALGORITHMS		33	38	71 P			
204184			33	30	74 P			
204185	DIGITAL ELECTRONICS	PP 100 40	32	34	66 P			
	DIGITAL ELECTRONICS		32	31	5 F			
	ELEX. MEASURING INSTRU & TOO				28 P			
204100	FIRST SEM TOTAL = $/750$.E31W 30 20			20 1			
	33222 SAKET M SIDDHESHWAR				LI			,S140053222
SEM.:	1							
	SIGNALS & SYSTEMS	PP 100 40	23	26	49 P			
	SIGNALS & SYSTEMS		-	-	21 P			
	ELECTRONIC DEVICES & CIRCUIT		29	28	57 P			
	ELECTRONIC DEVICES & CIRCUIT		-	-	36 P			
204183	NETWORK THEORY	PP 100 40	21	19	40 P			
204183	NETWORK THEORY	Tw 25 10			22 P			
204184	DATA STRUCTURES & ALGORITHMS		29	35	64 P			
204184			_5	- -	23 P			
204185	DIGITAL ELECTRONICS		30	32	62 P			
204185	DIGITAL ELECTRONICS	PR 50 20	30	J_	30 P			
	ELEX. MEASURING INSTRU & TOO				38 P			
_0.100	FIRST SEM TOTAL = 442/750							

	6 S.E.(2014 PAT.)(ELECTRONICS : 25 JAN 2016 CENTRE: PU			-	OF.	СОМРИТ	ΓER	TECHNOLOGY,	PUNE.	,	PAGE NO	o. 73	
NOTE:	FIRST LINE : SEAT NO., NAME OF	F TH	IE CA	NDIDA	ΑΤE,	MOTHE	ΞR,	PERMANENT RE	EG. NO., PRE	VIOUS SEA	T NO.,	COLLEGE,	SEAT NO.
	3223 SAKSHI GUPTA												
SEM.:	1												
204181	SIGNALS & SYSTEMS	PP	100	40	22	20	42	Р					
204181	SIGNALS & SYSTEMS	TW	25	10			16	Р					
204182	ELECTRONIC DEVICES & CIRCUITS	PP	100	40	38	19	57	Р					
	ELECTRONIC DEVICES & CIRCUITS							Р					
204183			100		22	28		Р					
204183	NETWORK THEORY						18						
204184	DATA STRUCTURES & ALGORITHMS				22	42	64						
204184	DATA STRUCTURES & ALGORITHMS				2.0	2.5	22						
204185	DIGITAL ELECTRONICS				39	25	64						
204185	DIGITAL ELECTRONICS						7						
204166	FIRST SEM TOTAL =/750	SIW	30	20			23	P					
s14005	3224 SALI GANESH TUKARAM												
SEM.:	1												
	SIGNALS & SYSTEMS	DD	100	40	28	19	47	' D					
			25		20	19	22						
	ELECTRONIC DEVICES & CIRCUITS				37	19		P					
	ELECTRONIC DEVICES & CIRCUITS				31	13		P					
204183	NETWORK THEORY		100		18	23		. Р					
204183			25					Р					
204184	DATA STRUCTURES & ALGORITHMS				25	41		Р					
204184	DATA STRUCTURES & ALGORITHMS	OR	50	20			41	. Р					
204185	DIGITAL ELECTRONICS	PP	100	40	22	35	57	Р					
204185	DIGITAL ELECTRONICS	PR	50	20			36	Р					
204186	ELEX. MEASURING INSTRU & TOOLS	STW	50	20			40	Р					
	FIRST SEM TOTAL = 459/750												
	3225 SALUNKE RUTUJA DEELIP												
SEM.:	1												
	SIGNALS & SYSTEMS	PP	100	40	39	39	78	Р					
204181	SIGNALS & SYSTEMS	TW	25	10			21	. Р					
204182	ELECTRONIC DEVICES & CIRCUITS	PP	100	40	42	21	63	Р					
204182	ELECTRONIC DEVICES & CIRCUITS	PR	50	20			36	Р					
204183	NETWORK THEORY	PP	100	40	38	20	58	Р					
204183	NETWORK THEORY	TW	25	10			20	Р					
204184	DATA STRUCTURES & ALGORITHMS	PP	100	40	35	39	74	P					
204184	DATA STRUCTURES & ALGORITHMS	OR	50	20			43	Р					
204185	DIGITAL ELECTRONICS	PP	100	40	38	37	75	Р					
204185	DIGITAL ELECTRONICS	PR	50	20			40	Р					
204186	ELEX. MEASURING INSTRU & TOOLS FIRST SEM TOTAL = 547/750	STW	50	20			39	Р					

BRANCH: 16 S.E.(2014 PAT.)(FLECTRONICS &TELECOM)

	: : 25 JAN 2016 CENTRE								PAGE NO. 74	
NOTE:	FIRST LINE : SEAT NO., NAME	E OF THE CANDI	DATE,	MOTH	ER, PERMAN	NENT REG. NO	., PRE	VIOUS SEAT	NO., COLLEGE	, SEAT NO.
	IES: SUB.CODE, SUB.NAME, SUB.	•	-		•	•	-			
	3226 SALUNKHE PRASHANTI PR									
SEM.:	1									
204181	SIGNALS & SYSTEMS	PP 100 40	34	21	55 P					
204181	SIGNALS & SYSTEMS	TW 25 10			21 P					
204182	ELECTRONIC DEVICES & CIRCUI	ITS PP 100 40	39	27	66 P					
204182	ELECTRONIC DEVICES & CIRCUI	ITS PR 50 20			40 P					
204183	NETWORK THEORY	PP 100 40	21	27	48 P					
204183	NETWORK THEORY	TW 25 10			20 P					
204184	DATA STRUCTURES & ALGORITHM	MS PP 100 40	30	40	70 P					
204184	DATA STRUCTURES & ALGORITHM	MS OR 50 20			39 P					
204185	DIGITAL ELECTRONICS	PP 100 40	22	29	51 P					
204185	DIGITAL ELECTRONICS	PR 50 20			30 P					
	ELEX. MEASURING INSTRU & TO FIRST SEM TOTAL = 484/750				44 P					
	3227 SAMARTH SARANG PRAVIN				L				,s140053	
SEM.:	1									
204181	SIGNALS & SYSTEMS	PP 100 40	29	15	44 P					
204181	SIGNALS & SYSTEMS	TW 25 10			15 P					
204182	ELECTRONIC DEVICES & CIRCUI	ITS PP 100 40	32	15	47 P					
204182	ELECTRONIC DEVICES & CIRCUI	ITS PR 50 20			14 F					
204183	NETWORK THEORY	PP 100 40	19	21	40 P					
204183	NETWORK THEORY	TW 25 10			18 P					
204184	DATA STRUCTURES & ALGORITHM	MS PP 100 40	24	16	40 P					
204184	DATA STRUCTURES & ALGORITHM	MS OR 50 20			21 P					
204185	DIGITAL ELECTRONICS	PP 100 40	21	11	F					
204185	DIGITAL ELECTRONICS	PR 50 20			6 F					
204186	ELEX. MEASURING INSTRU & TO FIRST SEM TOTAL =/750	OOLSTW 50 20			33 P					
	3228 SAMDURKAR SHARDUL AND				 SHREE			pICT		
SEM.:		DD 100 40	22	20	60 5					
	SIGNALS & SYSTEMS		32	28	60 P					
	SIGNALS & SYSTEMS		40	/11	21 P					
	ELECTRONIC DEVICES & CIRCUI		40	41	81 P					
	ELECTRONIC DEVICES & CIRCUI		20	20	36 P					
	NETWORK THEORY	PP 100 40	30	39	69 P					
204183	NETWORK THEORY DATA STRUCTURES & ALGORITHM		21	27	20 P 68 P					
204184			31	37						
204184 204185	DATA STRUCTURES & ALGORITHM DIGITAL ELECTRONICS		27	35	36 P 67 P					
204185	DIGITAL ELECTRONICS DIGITAL ELECTRONICS		32	22	67 P 32 P					
	ELEX. MEASURING INSTRU & TO									
ZU4100	FIRST SEM TOTAL = 523/750	JULSIW 3U ZU			33 P					

	6 S.E.(2014 PAT.)(ELECTRONICS : 25 JAN 2016 CENTRE: P		-	ITE OF	COMPU [*]	TER TECHNOLOGY, P	UNE.		PAGE NO. 75	
NOTE:	FIRST LINE : SEAT NO., NAME O ES: SUB.CODE, SUB.NAME, SUB.TY	 F TH	HE CANDI	DATE,	МОТН	ER, PERMANENT REG		VIOUS SEAT	NO., COLLEGE,	SEAT NO.
		-		•		•		•		
	3229 SANAGAR SNEHA PRAMOD									
SEM.:	1 SIGNALS & SYSTEMS	D.D.	100 40	20	25	E4 D				
					23	54 P				
	SIGNALS & SYSTEMS				22	22 P				
204182				30	32	70 P				
204182 204183	ELECTRONIC DEVICES & CIRCUITS NETWORK THEORY			22	28	35 P 60 P				
	NETWORK THEORY			32	20					
204183	DATA STRUCTURES & ALGORITHMS			42	20	20 P				
204184				42	39	81 P				
204184 204185	DATA STRUCTURES & ALGORITHMS DIGITAL ELECTRONICS			26	22	44 P 69 P				
204185				30	33	40 P				
	ELEX. MEASURING INSTRU & TOOL:					31 P				
	FIRST SEM TOTAL = 526/750									
	3230 SANSARE TEJASVINI NANDK									
SEM.:						40 -				
	SIGNALS & SYSTEMS				20					
	SIGNALS & SYSTEMS					16 P				
	ELECTRONIC DEVICES & CIRCUITS			39	27	66 P				
	ELECTRONIC DEVICES & CIRCUITS					37 P				
204183	NETWORK THEORY		100 40	27	34	61 P				
204183	NETWORK THEORY		25 10			19 P				
204184	DATA STRUCTURES & ALGORITHMS			27	39	66 P				
204184						34 P				
204185			100 40	31	26	57 P				
204185						36 P				
204186	FIRST SEM TOTAL = 471/750	STW	50 20			31 P				
S14005	3231 SANYASI SARTHAK DILIP				SHUB	HANGI	,71527570E	,PICT	,S1400532	231
SEM.:	1									
204181	SIGNALS & SYSTEMS	PP	100 40	16	24	40 P				
204181	SIGNALS & SYSTEMS	TW	25 10			20 P				
204182	ELECTRONIC DEVICES & CIRCUITS	PP	100 40	35	26	61 P				
204182	ELECTRONIC DEVICES & CIRCUITS	PR	50 20			29 P				
204183	NETWORK THEORY	PP	100 40	31	30	61 P				
204183	NETWORK THEORY	TW	25 10			21 P				
204184	DATA STRUCTURES & ALGORITHMS	PP	100 40	33	38	71 P				
204184	DATA STRUCTURES & ALGORITHMS	OR	50 20			37 P				
204185	DIGITAL ELECTRONICS	PP	100 40	33	37	70 P				
204185	DIGITAL ELECTRONICS	PR	50 20			32 P				
204186	ELEX. MEASURING INSTRU & TOOL	STW	50 20			38 P				
	FIRST SEM TOTAL = 480/750									

	E: 25 JAN 2016 CENTRE:							PAGE NO. 76
NOTE: HER LIN	FIRST LINE : SEAT NO., NAME SES: SUB.CODE, SUB.NAME, SUB.T	OF THE CAND YPE, MAX.MA	IDATE, RKS, MI	MOTHE IN.PASS	ER, PERMANEN S MARKS, OE	T REG. NO., PREV MARKS, TH MARKS,	TIOUS SEAT OBTAINED,	P/F:PASS/FAIL, C:PREV.CARRY OV
	3232 SAOJI AADITI VIVEK							,s140053232
SEM.:	1							
204181	SIGNALS & SYSTEMS	PP 100 40	28	33	61 P			
204181	SIGNALS & SYSTEMS	TW 25 10			22 P			
204182	ELECTRONIC DEVICES & CIRCUIT	S PP 100 40	47	36	83 P			
204182	ELECTRONIC DEVICES & CIRCUIT	S PR 50 20			42 P			
204183	NETWORK THEORY	PP 100 40		42	86 P			
204183	NETWORK THEORY	TW 25 10			21 P			
204184	DATA STRUCTURES & ALGORITHMS	PP 100 40	39	41	80 P			
204184	DATA STRUCTURES & ALGORITHMS	OR 50 20			36 P			
204185	DIGITAL ELECTRONICS	PP 100 40	42	39	81 P			
204185	DIGITAL ELECTRONICS	PR 50 20			41 P			
204186	ELEX. MEASURING INSTRU & TOO FIRST SEM TOTAL = 580/750	LSTW 50 20			27 P			
								,s140053233
SEM.:	1							
	SIGNALS & SYSTEMS	PP 100 40	15	16	F			
		TW 25 10		10	19 P			
	ELECTRONIC DEVICES & CIRCUIT			19	53 P			
	ELECTRONIC DEVICES & CIRCUIT			13	30 P			
204183	NETWORK THEORY	PP 100 40		21	45 P			
204183	NETWORK THEORY	TW 25 10		2.1	19 P			
204184	DATA STRUCTURES & ALGORITHMS			32	56 P			
204184				32	33 P			
204185	DIGITAL ELECTRONICS	PP 100 40		34	55 P			
	DIGITAL ELECTRONICS			34	21 P			
	ELEX. MEASURING INSTRU & TOO				34 P			
204100	FIRST SEM TOTAL = $/750$	L31W 30 20			3 4 F			
	3234 SHAH YASH NARESH				КНА			,s140053234
SEM.:	1							
	SIGNALS & SYSTEMS	PP 100 40	38	35	73 P			
	SIGNALS & SYSTEMS			- -	19 P			
	ELECTRONIC DEVICES & CIRCUIT			24	65 P			
	ELECTRONIC DEVICES & CIRCUIT				47 P			
204183	NETWORK THEORY	PP 100 40		38	69 P			
204183	NETWORK THEORY	TW 25 10		- -	20 P			
204184	DATA STRUCTURES & ALGORITHMS			36	72 P			
204184				30	37 P			
204185	DIGITAL ELECTRONICS			27	60 P			
204185		PR 50 20			6 F			
	ELEX. MEASURING INSTRU & TOO				о г 39 Р			
∠U+10U	FIRST SEM TOTAL = $/750$	L31W 30 20			JJ F			

	6 S.E.(2014 PAT.)(ELECTRONICS : 25 JAN 2016 CENTRE: P			-	OF	COMPUT	ER T	ECHNOLOGY, PU	UNE.		PAGE NO). 77	
NOTE:	FIRST LINE : SEAT NO., NAME O	FT	HE CA	NDIDA	ΓЕ,	MOTHE	R, P	PERMANENT REG.	. NO., PRE	VIOUS SEAT	NO.,	COLLEGE,	SEAT NO.
OTHER LIN	ES: SUB.CODE, SUB.NAME, SUB.TY	PΕ,	MAX.	MARKS	, MI	N.PASS	MAR	KKS, OE MARKS,	, TH MARKS,	OBTAINED,	P/F:PA	ASS/FAIL,	C:PREV.CARRY OVER
S14005	3235 SHAIKH AMIR AQUEEL					IFFAT		,	,71527584E	,PICT		,s1400532	235
SEM.:													
	SIGNALS & SYSTEMS				26	40	66						
	SIGNALS & SYSTEMS						21						
	ELECTRONIC DEVICES & CIRCUITS				48	35	83						
	ELECTRONIC DEVICES & CIRCUITS						44						
204183	NETWORK THEORY				39	36	75						
204183	NETWORK THEORY						23						
204184	DATA STRUCTURES & ALGORITHMS				27	37	64						
204184	DATA STRUCTURES & ALGORITHMS						39						
204185	DIGITAL ELECTRONICS				39	30	69						
204185							35						
204186	ELEX. MEASURING INSTRU & TOOL	SIW	50	20			39	Р					
	FIRST SEM TOTAL = 558/750												
614005	2226 CHANIL KUMAR	• • •											
S14005	3236 SHANU KUMAR					MANJU	DEV	′Ι ,	,/152/59UK	, PICI		,51400532	230
SEM.:	1												
	SIGNALS & SYSTEMS	DD	100	40 :	26	10	0.1	D					
	SIGNALS & SYSTEMS SIGNALS & SYSTEMS				50	40	84 24						
	ELECTRONIC DEVICES & CIRCUITS				1.1	30	83						
	ELECTRONIC DEVICES & CIRCUITS				++	39	42						
204182	NETWORK THEORY		100		42	42	84						
204183	NETWORK THEORY		25		72	72	19						
204184	DATA STRUCTURES & ALGORITHMS				40	37	77						
204184	DATA STRUCTURES & ALGORITHMS				10	3,	40						
204185			100		40	36	76						
204185			50			30	31						
	ELEX. MEASURING INSTRU & TOOL						39						
	FIRST SEM TOTAL = 599/750							•					
	3237 SHASHANK KAILASH GAJBHI					MAHES			,71527594B			,S1400532	
								•	,	, -		,	
SEM.:	1												
204181	SIGNALS & SYSTEMS	PP	100	40 1	16	03		F					
204181	SIGNALS & SYSTEMS	TW	25	10			10	Р					
204182	ELECTRONIC DEVICES & CIRCUITS	PP	100	40 2	25	02		F					
204182	ELECTRONIC DEVICES & CIRCUITS	PR	50	20			33	Р					
204183	NETWORK THEORY	PP	100	40 2	20	08		F					
204183	NETWORK THEORY	TW	25	10			13	Р					
204184	DATA STRUCTURES & ALGORITHMS	PP	100	40 2	28	30	58	Р					
204184							22						
204185	DIGITAL ELECTRONICS		100		23	23	46						
204185	DIGITAL ELECTRONICS		50				21						
	ELEX. MEASURING INSTRU & TOOL	STW	50	20			20						
	FIRST SEM TOTAL =/750												
	•												

	6 S.E.(2014 PAT.)(ELECTRONICS : 25 JAN 2016 CENTRE: P		-		COMPU ¹	TER TECHNO	LOGY, PL	INE.		PAGE NO. 78	
NOTE:	FIRST LINE : SEAT NO., NAME O ES: SUB.CODE, SUB.NAME, SUB.TY	 F TI	HE CANI	 DIDATE,	MOTH	ER, PERMAN	ENT REG.	NO., PRE	vious seat	NO., COLLEGE,	SEAT NO.
S14005	3238 SHEFALI GARG				VAND	NA	,	71527595L	,PICT	,S140053	238
SEM.:	1										
	SIGNALS & SYSTEMS	PP	100 40	0 41	44	85 P					
204181	SIGNALS & SYSTEMS	TW	25 10	0		24 P					
204182	ELECTRONIC DEVICES & CIRCUITS	PP	100 40	0 40	37	77 P					
204182	ELECTRONIC DEVICES & CIRCUITS	PR	50 20	0		42 P					
204183	NETWORK THEORY	PP	100 40	0 37	42	79 P					
204183	NETWORK THEORY	TW	25 10	0		23 P					
204184	DATA STRUCTURES & ALGORITHMS	PP	100 40	0 44	38	82 P					
204184	DATA STRUCTURES & ALGORITHMS	OR	50 20	0		38 P					
204185	DIGITAL ELECTRONICS	PP	100 40	0 34	36	70 P					
204185	DIGITAL ELECTRONICS	PR	50 20	0		40 P					
	ELEX. MEASURING INSTRU & TOOL FIRST SEM TOTAL = 605/750					45 P					
	3239 SHEJULE RUPALI ARJUNRAO									,S140053	
SEM.:	1										
	- SIGNALS & SYSTEMS	PP	100 40	0 30	16	46 P					
	SIGNALS & SYSTEMS					22 P					
	ELECTRONIC DEVICES & CIRCUITS				29	57 P					
204182	ELECTRONIC DEVICES & CIRCUITS	PR	50 20	0		35 P					
204183	NETWORK THEORY	PP	100 40	0 21	31	52 P					
204183	NETWORK THEORY	TW	25 10	0		20 P					
204184	DATA STRUCTURES & ALGORITHMS	PP	100 40	0 27	31	58 P					
204184	DATA STRUCTURES & ALGORITHMS	OR	50 20	0		35 P					
204185	DIGITAL ELECTRONICS	PP	100 40	0 23	35	58 P					
204185	DIGITAL ELECTRONICS	PR	50 20	0		30 P					
204186	ELEX. MEASURING INSTRU & TOOL FIRST SEM TOTAL = 448/750	STW	50 20	0		35 P					
	3240 SHEJWAL SAYALI VINAYAK							 71633658J			
	1										
SEM.:			100 4	0 25	2.2	60 -					
	SIGNALS & SYSTEMS				33	68 P					
	SIGNALS & SYSTEMS		25 10		2.4	24 P					
	ELECTRONIC DEVICES & CIRCUITS				34	79 P					
	ELECTRONIC DEVICES & CIRCUITS				26	40 P					
204183	NETWORK THEORY		100 40		36	69 P					
204183	NETWORK THEORY		25 10		20	23 P					
204184					38	71 P					
204184					ס כ	46 P					
204185	DIGITAL ELECTRONICS		50 20		37	74 P 30 P					
204185 204186	DIGITAL ELECTRONICS ELEX. MEASURING INSTRU & TOOL					30 P 46 P					
2U4100	FIRST SEM TOTAL = 570/750	SIW	JU 2(J		40 P					

	.6 S.E.(2014 PAT.)(ELECTRONICS E: 25 JAN 2016 CENTRE: PI		-	E OF	СОМРИТ	ΓER ٦	TECHNOLOGY, PL	JNE.		PAGE NO. 79	
	FIRST LINE : SEAT NO., NAME OF			•		•		•		•	
	IES: SUB.CODE, SUB.NAME, SUB.TY										
	3241 SHELKE AMIT KIRAN										
SEM.:	1										
204181	SIGNALS & SYSTEMS	PP	100 40	17	30	47	Р				
204181	SIGNALS & SYSTEMS	TW	25 10			22	Р				
204182	ELECTRONIC DEVICES & CIRCUITS	PP	100 40	39	24	63	Р				
204182	ELECTRONIC DEVICES & CIRCUITS	PR	50 20			38	P				
204183	NETWORK THEORY	PP :	100 40	31	26	57	P				
204183	NETWORK THEORY	TW	25 10			22	Р				
204184	DATA STRUCTURES & ALGORITHMS	PP :	100 40	36	26	62	P				
204184	DATA STRUCTURES & ALGORITHMS	OR	50 20			34	Р				
204185	DIGITAL ELECTRONICS	PP	100 40	34	15	49	Р				
204185	DIGITAL ELECTRONICS	PR	50 20			39	P				
	ELEX. MEASURING INSTRU & TOOLS FIRST SEM TOTAL = 474/750					41					
	3242 SHELKE SHRIKRUSHNA DILI										
SEM.:	1										
	SIGNALS & SYSTEMS	PP :	100 40	18	07		F				
204181	SIGNALS & SYSTEMS	TW	25 10			13	Р				
204182	ELECTRONIC DEVICES & CIRCUITS	PP	100 40	37	23	60	Р				
204182	ELECTRONIC DEVICES & CIRCUITS	PR	50 20			24	Р				
204183	NETWORK THEORY	PP :	100 40	17	23	40	Р				
204183	NETWORK THEORY	TW	25 10			19	Р				
204184	DATA STRUCTURES & ALGORITHMS	PP	100 40	27	25	52	Р				
204184	DATA STRUCTURES & ALGORITHMS	OR	50 20			26	Р				
204185	DIGITAL ELECTRONICS	PP	100 40	34	15	49	Р				
204185	DIGITAL ELECTRONICS	PR	50 20			6	F				
204186	ELEX. MEASURING INSTRU & TOOLS	STW	50 20			33	Р				
	FIRST SEM TOTAL =/750										
	33243 SHETI SWAPNILKAILAS				PUSHF	PA	,	71527601J	,PICT	,S140053	243
SEM.:			400 -	. -		_					
	SIGNALS & SYSTEMS			26	28	54					
	SIGNALS & SYSTEMS		25 10			21					
	ELECTRONIC DEVICES & CIRCUITS			23	25	48					
	ELECTRONIC DEVICES & CIRCUITS					22					
204183			100 40	33	22	55					
204183			25 10			21					
204184	DATA STRUCTURES & ALGORITHMS			41	34	75					
204184						39					
204185	DIGITAL ELECTRONICS		100 40	30	29	59					
204185			50 20			38					
204186	ELEX. MEASURING INSTRU & TOOLS FIRST SEM TOTAL = 470/750	STW	50 20			38	Р				

BRANCH: 16 S.E.(2014 PAT.)(ELECTRONICS &TELECOM)

DATE	L6 S.E.(2014 PAT.)(ELECTRONICE: 25 JAN 2016 CENTRE:	PUNE	INSTITU				•		PAGE NO. 80
NOTE:	FIRST LINE : SEAT NO., NAME	OF TH	HE CANDI	DATE,	MOTH	IER, PERMANEN	NT REG. NO., PREV	IOUS SEAT	NO., COLLEGE, SEAT NO. P/F:PASS/FAIL, C:PREV.CARRY OVE
	3244 SHINDE PRANALI SURYAKA								,S140053244
SEM.:	1								
204181	SIGNALS & SYSTEMS	PP	100 40	26	15	41 P			
204181	SIGNALS & SYSTEMS	TW	25 10			21 P			
204182	ELECTRONIC DEVICES & CIRCUIT	S PP	100 40	37	27	64 P			
204182	ELECTRONIC DEVICES & CIRCUIT	S PR	50 20			41 P			
204183	NETWORK THEORY	PP	100 40	29	20	49 P			
204183	NETWORK THEORY	TW	25 10			20 P			
204184	DATA STRUCTURES & ALGORITHMS	PP	100 40	27	35	62 P			
204184	DATA STRUCTURES & ALGORITHMS	OR	50 20			43 P			
204185	DIGITAL ELECTRONICS	PP	100 40	32	30	62 P			
204185	DIGITAL ELECTRONICS	PR	50 20			35 P			
204186	ELEX. MEASURING INSTRU & TOO FIRST SEM TOTAL = 480/750	LSTW	50 20			42 P			
	33245 SHINDE SHAMAL VIKRAM					A			,s140053245
SEM.:	1								
204181	SIGNALS & SYSTEMS	PP	100 40	33	25	58 P			
204181	SIGNALS & SYSTEMS	TW	25 10			22 P			
204182	ELECTRONIC DEVICES & CIRCUIT	S PP	100 40	50	39	89 P			
204182	ELECTRONIC DEVICES & CIRCUIT	S PR	50 20			40 P			
204183	NETWORK THEORY	PP	100 40	38	35	73 P			
204183	NETWORK THEORY	TW	25 10			23 P			
204184	DATA STRUCTURES & ALGORITHMS	PP	100 40	28	31	59 P			
204184	DATA STRUCTURES & ALGORITHMS	OR	50 20			42 P			
204185	DIGITAL ELECTRONICS	PP	100 40	30	26	56 P			
204185	DIGITAL ELECTRONICS		50 20			35 P			
204186	ELEX. MEASURING INSTRU & TOO FIRST SEM TOTAL = 539/750	LSTW	50 20			42 P			
s14005	33246 SHINDE SNEHAL RAMESH				NAND		,71527604C	,PICT	,s140053246
SEM.:	1								
204181	SIGNALS & SYSTEMS	PP	100 40	32	33	65 P			
204181	SIGNALS & SYSTEMS	TW	25 10			21 P			
204182	ELECTRONIC DEVICES & CIRCUIT	S PP	100 40	46	28	74 P			
204182	ELECTRONIC DEVICES & CIRCUIT	S PR	50 20			32 P			
204183	NETWORK THEORY	PP	100 40	36	45	81 P			
204183	NETWORK THEORY	TW	25 10			22 P			
204184	DATA STRUCTURES & ALGORITHMS	PP	100 40	38	38	76 P			
204184	DATA STRUCTURES & ALGORITHMS	OR	50 20			41 P			
204185	DIGITAL ELECTRONICS	PP	100 40	35	34	69 P			
204185	DIGITAL ELECTRONICS	PR	50 20			40 P			
204186	ELEX. MEASURING INSTRU & TOO FIRST SEM TOTAL = 561/750	LSTW	50 20			40 P			

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLL	
OTHER LINES: SUB.CODE, SUB.NAME, SUB.TYPE, MAX.MARKS, MIN.PASS MARKS, OE MARKS, TH MARKS, OBTAINED, P/F:PASS/F	FAIL, C:PREV.CARRY OVER
S140053247 SHINDE SUMEET CHANDRAKANT ANITA ,71527606K ,PICT ,S14	40053247
SEM.:1	
204181 SIGNALS & SYSTEMS PP 100 40 31 15 46 P	
204181 SIGNALS & SYSTEMS TW 25 10 14 P	
204182 ELECTRONIC DEVICES & CIRCUITS PP 100 40 35 20 55 P	
204182 ELECTRONIC DEVICES & CIRCUITS PR 50 20 38 P 204183 NETWORK THEORY PP 100 40 29 22 51 P	
204183 NETWORK THEORY PP 100 40 29 22 51 P 204183 NETWORK THEORY TW 25 10 19 P	
204184 DATA STRUCTURES & ALGORITHMS PP 100 40 33 20 53 P	
204184 DATA STRUCTURES & ALGORITHMS OR 50 20 38 P	
204185 DIGITAL ELECTRONICS PP 100 40 33 22 55 P	
204185 DIGITAL ELECTRONICS PR 50 20 40 P	
204186 ELEX. MEASURING INSTRU & TOOLSTW 50 20 35 P	
FIRST SEM TOTAL = $444/750$	
S140053248 SHIRALKAR APURVA ASHOK SUNITA ,71633661J ,PICT ,S14	
SEM.:1	
204181 SIGNALS & SYSTEMS PP 100 40 25 21 46 P	
204181 SIGNALS & SYSTEMS TW 25 10 21 P	
204182 ELECTRONIC DEVICES & CIRCUITS PP 100 40 42 28 70 P	
204182 ELECTRONIC DEVICES & CIRCUITS PR 50 20 39 P	
204183 NETWORK THEORY PP 100 40 23 34 57 P	
204183 NETWORK THEORY TW 25 10 20 P	
204184 DATA STRUCTURES & ALGORITHMS PP 100 40 25 33 58 P	
204184 DATA STRUCTURES & ALGORITHMS OR 50 20 36 P	
204185 DIGITAL ELECTRONICS PP 100 40 25 17 42 P	
204185 DIGITAL ELECTRONICS PR 50 20 26 P	
204186 ELEX. MEASURING INSTRU & TOOLSTW 50 20 39 P	
FIRST SEM TOTAL = 454/750	
	40053249
SEM.:1	
204181 SIGNALS & SYSTEMS PP 100 40 26 18 44 P	
204181 SIGNALS & SYSTEMS TW 25 10 21 P	
204182 ELECTRONIC DEVICES & CIRCUITS PP 100 40 44 33 77 P	
204182 ELECTRONIC DEVICES & CIRCUITS PR 50 20 43 P	
204183 NETWORK THEORY PP 100 40 32 29 61 P	
204183 NETWORK THEORY TW 25 10 17 P	
204184 DATA STRUCTURES & ALGORITHMS PP 100 40 40 43 83 P	
204184 DATA STRUCTURES & ALGORITHMS OR 50 20 39 P	
204185 DIGITAL ELECTRONICS PP 100 40 40 35 75 P	
204185 DIGITAL ELECTRONICS PR 50 20 40 P	
204186 ELEX. MEASURING INSTRU & TOOLSTW 50 20 40 P FIRST SEM TOTAL = 540/750	

DATE	6 S.E.(2014 PAT.)(ELECTRONICS : 25 JAN 2016 CENTRE: P	UNE	INST	ITUTE				•		
NOTE:	FIRST LINE : SEAT NO., NAME O ES: SUB.CODE, SUB.NAME, SUB.TY	F TH	HE CA	NDIDA	ΛTE,	МОТН	IER, PERMANEI	NT REG. NO., PREV	IOUS SEAT N	O., COLLEGE, SEAT NO.
	3250 SHITOLE ANMOL SUNIL									
SEM.:	1									
	SIGNALS & SYSTEMS				24	16	40 P			
204181	SIGNALS & SYSTEMS	TW	25	10			21 P			
204182					39	26	65 P			
	ELECTRONIC DEVICES & CIRCUITS						33 P			
204183	NETWORK THEORY			_	22	28	50 P			
204183	NETWORK THEORY						21 P			
204184	DATA STRUCTURES & ALGORITHMS				23	39	62 P			
204184	DATA STRUCTURES & ALGORITHMS						42 P			
204185	DIGITAL ELECTRONICS				23	26	49 P			
204185	DIGITAL ELECTRONICS						21 P			
204186	ELEX. MEASURING INSTRU & TOOL FIRST SEM TOTAL = 448/750	STW	50	20			44 P			
	3251 SHIYALE SACHIT SHIYALE							,71633664C		
CEM .	1									
SEM.:		-	100	40	2.4	0.7	_			
	SIGNALS & SYSTEMS				24	07				
	SIGNALS & SYSTEMS				40	22	21 P			
	ELECTRONIC DEVICES & CIRCUITS ELECTRONIC DEVICES & CIRCUITS				40	22	62 P			
204182	NETWORK THEORY		100		25	22	33 P 47 P			
204183			25		23	22	47 P 19 P			
204183	NETWORK THEORY DATA STRUCTURES & ALGORITHMS				26	22	19 P 59 P			
					20	33				
204184 204185			100		26	26	35 P 52 P			
204185					20	20	32 P 25 P			
	DIGITAL ELECTRONICS ELEX. MEASURING INSTRU & TOOL						39 P			
204100	FIRST SEM TOTAL = $/750$.SIW	30	20			39 P			
	3252 SHREYAS HEMANT NAZARE						ANA			,s140053252
SEM.:	1									
		pр	100	40	38	46	84 P			
			25			. •	24 P			
	ELECTRONIC DEVICES & CIRCUITS				46	45	91 P			
	ELECTRONIC DEVICES & CIRCUITS				. 5	. 5	40 P			
204183	NETWORK THEORY		100		40	47	87 P			
204183			25		. 5	• •	23 P			
204184					39	41	80 P			
204184	DATA STRUCTURES & ALGORITHMS				_ 3		41 P			
204185	DIGITAL ELECTRONICS				43	37	80 P			
204185			50			51	30 P			
	ELEX. MEASURING INSTRU & TOOL						45 P			
	FIRST SEM TOTAL = 625/750		30							

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NOTE: THER LIN	FIRST LINE : SEAT NO., NAME (IES: SUB.CODE, SUB.NAME, SUB.T)	OF TH YPE,	HE CANDII	DATE, KS, MI	MOTHE IN.PASS	R, MA	PERMANEN RKS, OE	T REG. NO. MARKS, TH	, PRE	VIOUS SEAT OBTAINED,	NO., P/F:P	COLLEGE, ASS/FAIL,	SEAT NO. C:PREV.CARR	RY OVE
	3253 SHRIRAM ANUSHKA ANIL									,PICT				
SEM.:														
204181	SIGNALS & SYSTEMS	PP	100 40	12	06		F							
	SIGNALS & SYSTEMS					20	Р							
	ELECTRONIC DEVICES & CIRCUITS			28	80		-							
	ELECTRONIC DEVICES & CIRCUITS					10								
204183	NETWORK THEORY		100 40	20	03		-							
204183	NETWORK THEORY					18	Р							
204184	DATA STRUCTURES & ALGORITHMS			28	03		-							
204184	DATA STRUCTURES & ALGORITHMS					30	Р							
204185	DIGITAL ELECTRONICS			27	07		-							
204185	DIGITAL ELECTRONICS					10								
	ELEX. MEASURING INSTRU & TOOL FIRST SEM TOTAL =/750					22								
	3254 SHRUTI ATUL MALI									,PICT				
CEM .	1													
SEM.:	SIGNALS & SYSTEMS	DD	100 40	27	1 [42	В							
	SIGNALS & SYSTEMS			21	13	42 16								
	ELECTRONIC DEVICES & CIRCUITS			16	26	72								
	ELECTRONIC DEVICES & CIRCUITS			40	20	38								
204183	NETWORK THEORY		100 40	32	33	65								
204183	NETWORK THEORY		25 10	32	33	20								
204183	DATA STRUCTURES & ALGORITHMS			25	40	65								
204184				23	40	32								
204184	DIGITAL ELECTRONICS		100 40	27	25	62								
				37	23	21								
	DIGITAL ELECTRONICS ELEX. MEASURING INSTRU & TOOL					33								
204100	FIRST SEM TOTAL = 466/750	_51W	30 20			33	۲							
	3255 SHUBHAM JAKHOTIYA									,PICT				
SEM.:	1													
	SIGNALS & SYSTEMS	PP	100 40	22	11		F							
	SIGNALS & SYSTEMS		25 10		- -	19								
	ELECTRONIC DEVICES & CIRCUITS			27	15	42								
	ELECTRONIC DEVICES & CIRCUITS					30								
204183	NETWORK THEORY		100 40	27	15	42								
204183	NETWORK THEORY		25 10			18								
204184	DATA STRUCTURES & ALGORITHMS			34	32	66								
204184				٥.		38								
204185	DIGITAL ELECTRONICS			32	24	56								
204185	DIGITAL ELECTRONICS DIGITAL ELECTRONICS		50 20	32		21								
	ELEX. MEASURING INSTRU & TOOL					32								
_0.100	FIRST SEM TOTAL =/750	,	30 20			<i>52</i>	•							

HER LIN	FIRST LINE : SEAT NO., NAME (NES: SUB.CODE, SUB.NAME, SUB.TY	YPE,	MAX.MARK	OATE,	MOTHE IN.PASS	ER, PERMA 5 MARKS,	NENT REG. OE MARKS,	NO., PRETH MARKS,	VIOUS SEAT OBTAINED,	NO., P/F:P	COLLEGE, ASS/FAIL,	SEAT NO. C:PREV.CARRY (
	53256 SHUKLA MAMTA RAMENDRA											
SEM.:	:1											
204181	SIGNALS & SYSTEMS	PP	100 40	41	25	66 P						
204181	SIGNALS & SYSTEMS	TW	25 10			21 P						
	ELECTRONIC DEVICES & CIRCUITS			46	36	82 P						
	ELECTRONIC DEVICES & CIRCUITS					42 P						
204183	NETWORK THEORY		100 40	36	43	79 P						
204183	NETWORK THEORY					23 P						
204184	DATA STRUCTURES & ALGORITHMS			34	45	79 P						
204184	DATA STRUCTURES & ALGORITHMS					37 P						
204185	DIGITAL ELECTRONICS			38	36	74 P						
204185						36 P						
	ELEX. MEASURING INSTRU & TOOL FIRST SEM TOTAL = 579/750					40 P						
	53257 SHWETA IYER								,PICT			
SEM.:	:1											
	SIGNALS & SYSTEMS	PP	100 40	37	33	70 P						
	SIGNALS & SYSTEMS					24 P						
	ELECTRONIC DEVICES & CIRCUITS			47	37	84 P						
	ELECTRONIC DEVICES & CIRCUITS				3.	44 P						
204183	NETWORK THEORY		100 40	47	42	89 P						
204183	NETWORK THEORY		25 10			23 P						
204184	DATA STRUCTURES & ALGORITHMS			35	34	69 P						
204184				33	J 1	42 P						
204185			100 40	41	27	68 P						
	DIGITAL ELECTRONICS				_,	42 P						
	ELEX. MEASURING INSTRU & TOOL					45 P						
	FIRST SEM TOTAL = 600/750											
	33258 SINGH NAVIN BACHCHAN SI								,PICT			
SEM.:	:1											
	SIGNALS & SYSTEMS	PP	100 40	24	09	F						
	SIGNALS & SYSTEMS			- •	- -	18 P						
	ELECTRONIC DEVICES & CIRCUITS			33	16	49 P						
	ELECTRONIC DEVICES & CIRCUITS				-	32 P						
204183	NETWORK THEORY			32	27	59 P						
204183	NETWORK THEORY		25 10			19 P						
204184	DATA STRUCTURES & ALGORITHMS			34	25	59 P						
204184					- -	31 P						
204185	DIGITAL ELECTRONICS			33	20	53 P						
204185				33	_•	7 F						
	ELEX. MEASURING INSTRU & TOOL					7 г 31 Р						
_0.100	FIRST SEM TOTAL =/750		33 20			J± 1						

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NOTE: HER LIN	FIRST LINE : SEAT NO., NAME (ES: SUB.CODE, SUB.NAME, SUB.T	OF THE	CANDIDA X.MARKS	TE, , MI	MOTHE N.PASS	R, PE MARK	RMANENT REG S, OE MARKS	. NO., PRE , TH MARKS,	VIOUS SEAT OBTAINED,	NO., P/F:P	COLLEGE, ASS/FAIL,	SEAT N	NO. CARRY OV
	3259 SINGH VIKAS RAJESH												
SEM.:	1												
204181	SIGNALS & SYSTEMS	PP 10	00 40	16	26	42 P							
204181	SIGNALS & SYSTEMS	TW 2	25 10			20 P							
204182	ELECTRONIC DEVICES & CIRCUITS	S PP 10	00 40	42	33	75 P							
204182	ELECTRONIC DEVICES & CIRCUITS	S PR 5	50 20			34 P							
204183	NETWORK THEORY	PP 10		25	28	53 P							
204183	NETWORK THEORY					20 P							
204184	DATA STRUCTURES & ALGORITHMS	PP 10	00 40	22	31	53 P							
204184	DATA STRUCTURES & ALGORITHMS					9 F							
204185	DIGITAL ELECTRONICS	PP 10	00 40	29	24	53 P							
204185	DIGITAL ELECTRONICS	PR 5	50 20			27 P							
	FIRST SEM TOTAL =/750					42 P							
	3260 SOMVANSHI KETAN VISHVAN												
-	_												
SEM.:		10		2.0	4.7	70 -							
	SIGNALS & SYSTEMS			38	41	79 P							
	SIGNALS & SYSTEMS			4.5	4.6	20 P							
	ELECTRONIC DEVICES & CIRCUITS			45	46	91 P							
	ELECTRONIC DEVICES & CIRCUITS			4.5	47	42 P							
204183	NETWORK THEORY	PP 10		45	47	92 P							
204183	NETWORK THEORY	TW 2		2.2	4.1	20 P							
204184	DATA STRUCTURES & ALGORITHMS			33	41	74 P							
204184						37 P							
204185	DIGITAL ELECTRONICS	PP 10		38	28	66 P							
	DIGITAL ELECTRONICS					39 P							
204186	ELEX. MEASURING INSTRU & TOOI FIRST SEM TOTAL = 600/750	LSTW 5	50 20			40 P							
	3261 SREERADH SREEKUMAR							, 71527634E					
SEM.:	1												
	SIGNALS & SYSTEMS	pp 1∩	00 40	18	06	F							
	SIGNALS & SYSTEMS			_0		15 P							
	ELECTRONIC DEVICES & CIRCUITS			36	32	68 P							
	ELECTRONIC DEVICES & CIRCUITS			50	32	24 P							
204182	NETWORK THEORY	PP 10		32	16	48 P							
204183	NETWORK THEORY	TW 2		J <u>L</u>	10	46 P							
204183	DATA STRUCTURES & ALGORITHMS			36	30	66 P							
				20	30								
204184				22	1 [11 F							
204185	DIGITAL ELECTRONICS			3 3	15	48 P							
204185	DIGITAL ELECTRONICS	PR 5				25 P							
204186	ELEX. MEASURING INSTRU & TOOI FIRST SEM TOTAL =/750	LSTW 5	00 20			26 P							

	L6 S.E.(2014 PAT.)(ELECTRONICS		-		COMPLI	TED T	TECHNOLOGY DU	INIT		DACE NO. 96	
	E : 25 JAN 2016 CENTRE: P										
NOTE:	FIRST LINE : SEAT NO., NAME O	FT	HE CANE	DIDATE,	мотн	ER, P	PERMANENT REG.	NO., PRE	VIOUS SEAT	NO., COLLEGE	, SEAT NO.
OTHER LIN	NES: SUB.CODE, SUB.NAME, SUB.TY	PE,	MAX.MA	ARKS, MI	N.PAS	S MAR	KS, OE MARKS,	TH MARKS,	OBTAINED,	P/F:PASS/FAIL	, C:PREV.CARRY OVER
S14005	33262 SRINJAY SOUMITRA SARKAR				BHAK	TI	,	71527635C	,PICT	,514005	3262
SEM.:											
	SIGNALS & SYSTEMS				29	57					
	SIGNALS & SYSTEMS					13					
204182					32	77					
204182	ELECTRONIC DEVICES & CIRCUITS				2.2	30					
204183	NETWORK THEORY				22	57					
204183	NETWORK THEORY				2.4	16					
204184	DATA STRUCTURES & ALGORITHMS				34	62					
204184					20	34					
204185 204185					20	68 6					
	ELEX. MEASURING INSTRU & TOOL					31					
204180	FIRST SEM TOTAL =/750	SIW	30 20	J		ЭТ	r				
	FIRST SEM TOTAL =/750										
	33263 SURADKAR NAIRUTI MANOJ										
31.003	SOLVED IN THE TANK OF				D/ (ICS	11/ (14/ (,	713270101	,, 10,	,511003	3203
SEM.:	.1										
	SIGNALS & SYSTEMS	PP	100 40	35	26	61	Р				
	SIGNALS & SYSTEMS					15					
	ELECTRONIC DEVICES & CIRCUITS				34	81					
	ELECTRONIC DEVICES & CIRCUITS					36					
204183			100 40		24	49					
204183	NETWORK THEORY	TW	25 10)		18	Р				
204184	DATA STRUCTURES & ALGORITHMS	PP	100 40	36	34	70	Р				
204184	DATA STRUCTURES & ALGORITHMS	OR	50 20)		22	Р				
204185	DIGITAL ELECTRONICS	PP	100 40	28	29	57	Р				
204185	DIGITAL ELECTRONICS	PR	50 20)		22	P				
204186	ELEX. MEASURING INSTRU & TOOL	STW	50 20)		33	P				
	FIRST SEM TOTAL = 464/750										
s14005	33264 SURUDWAD MANOJKUMAR BAL	AJI	RAO		ARUN	ABAI	,	71527642F	,PICT	,S14005	3264
SEM.:											
	SIGNALS & SYSTEMS		100 40	24	19	43	P				
204181	SIGNALS & SYSTEMS	TW	25 10)		16	P				
	ELECTRONIC DEVICES & CIRCUITS				22	64					
	ELECTRONIC DEVICES & CIRCUITS					38					
204183					22	51					
204183	NETWORK THEORY		25 10			22					
204184					37	73					
204184					2.2	37					
204185					28	57					
204185			50 20			8					
204186		STW	50 20	J		33	Р				
	FIRST SEM TOTAL =/750										

	E: 25 JAN 2016 CENTRE:									PAGE NO			
NOTE: HER LIN	FIRST LINE : SEAT NO., NAME NES: SUB.CODE, SUB.NAME, SUB.T	OF TH YPE,	E CANDII	OATE,	MOTHI IN.PASS	ER, PERMAN S MARKS, O	NENT REG. NO DE MARKS, TH	O., PRE H MARKS,	EVIOUS SEAT OBTAINED,	NO., P/F:P/	COLLEGE, ASS/FAIL,	SEAT C:PREV.	NO. CARRY OV
	3265 SUSHIL PATIL						,71						
SEM.:	1												
204181	SIGNALS & SYSTEMS	PP	100 40	36	38	74 P							
204181	SIGNALS & SYSTEMS	TW	25 10			18 P							
204182	ELECTRONIC DEVICES & CIRCUIT	S PP	100 40	36	38	74 P							
204182	ELECTRONIC DEVICES & CIRCUIT	S PR	50 20			32 P							
204183	NETWORK THEORY	PP	100 40	37	44	81 P							
204183	NETWORK THEORY	TW	25 10			21 P							
204184	DATA STRUCTURES & ALGORITHMS	PP	100 40	39	40	79 P							
204184	DATA STRUCTURES & ALGORITHMS	OR	50 20			38 P							
204185	DIGITAL ELECTRONICS	PP	100 40	39	36	75 P							
204185	DIGITAL ELECTRONICS	PR	50 20			32 P							
	ELEX. MEASURING INSTRU & TOO FIRST SEM TOTAL = 560/750					36 P							
	3266 SUSHIL S. KHAIRNAR								,PICT				
SEM.:	1												
	SIGNALS & SYSTEMS	PР	100 40	36	48	84 P							
	SIGNALS & SYSTEMS			30	10	24 P							
	ELECTRONIC DEVICES & CIRCUIT			46	48	94 P							
	ELECTRONIC DEVICES & CIRCUIT			10	10	45 P							
204183	NETWORK THEORY		100 40	31	47	78 P							
204183	NETWORK THEORY		25 10	31	.,	23 P							
204184	DATA STRUCTURES & ALGORITHMS			31	46	77 P							
204184				31	10	41 P							
204185	DIGITAL ELECTRONICS		100 40	3.8	35	73 P							
	DIGITAL ELECTRONICS			30	33	75 T							
	ELEX. MEASURING INSTRU & TOO					44 P							
204180	FIRST SEM TOTAL = 622/750	LJIW	30 20			44 F							
	3267 SUTKAR ADITYA SUBHASH					нА			,PICT				
SEM.:	1												
	SIGNALS & SYSTEMS	PP	100 40	21	27	48 P							
	SIGNALS & SYSTEMS		25 10			21 P							
	ELECTRONIC DEVICES & CIRCUIT			35	29	64 P							
	ELECTRONIC DEVICES & CIRCUIT					34 P							
204183	NETWORK THEORY		100 40	29	33	62 P							
204183	NETWORK THEORY		25 10			20 P							
204184	DATA STRUCTURES & ALGORITHMS			37	43	80 P							
204184				٠.		40 P							
204185	DIGITAL ELECTRONICS			30	32	62 P							
204185	DIGITAL ELECTRONICS DIGITAL ELECTRONICS		50 20	30	32	25 P							
	ELEX. MEASURING INSTRU & TOO					41 P							
~04±00	FIRST SEM TOTAL = 497/750	∟ J I W	JU 20			7 4 F							

	6 S.E.(2014 PAT.)(ELECTRONICS : 25 JAN 2016 CENTRE: P		-		COMPU	ITER TE	ECHNOLOGY, PU	INE.		PAGE NO. 88	
NOTE:	FIRST LINE : SEAT NO., NAME O ES: SUB.CODE, SUB.NAME, SUB.TY	 F TI	HE CANI	 DIDATE,	МОТН	IER, PE	ERMANENT REG.	NO., PRE	vious seat	NO., COLLEGE,	SEAT NO.
S14005	3268 SUTRAVE SUSHANT SUDHIR				SARI	TA	,	71527646J	,PICT	,s140053	268
SEM.:	1										
	SIGNALS & SYSTEMS					47 F	•				
	SIGNALS & SYSTEMS					22 F					
204182					31	66 F					
204182	ELECTRONIC DEVICES & CIRCUITS					32 F					
204183	NETWORK THEORY				18	49 F					
204183	NETWORK THEORY					22 F					
204184	DATA STRUCTURES & ALGORITHMS				33	64 F					
204184	DATA STRUCTURES & ALGORITHMS					34 F					
204185	DIGITAL ELECTRONICS				22						
204185						32 F					
	ELEX. MEASURING INSTRU & TOOL FIRST SEM TOTAL = 467/750										
	3269 TAKALIKAR SUPRIYA SUBHA										
a	1										
SEM.:			400 4		2.0	-					
	SIGNALS & SYSTEMS				38	69 F					
	SIGNALS & SYSTEMS				20	21 F					
	ELECTRONIC DEVICES & CIRCUITS				39	83 F					
	ELECTRONIC DEVICES & CIRCUITS				20	36 F					
204183	NETWORK THEORY		100 40		38	81 F					
204183	NETWORK THEORY		25 10		40	19 F					
204184 204184	DATA STRUCTURES & ALGORITHMS DATA STRUCTURES & ALGORITHMS				40	69 F					
					20	37 F					
204185				0 41	29	70 F					
204185	DIGITAL ELECTRONICS ELEX. MEASURING INSTRU & TOOL					37 F 42 F					
204180	FIRST SEM TOTAL = 564/750	.51W	30 20	J		42 F					
	3270 TAMBADE PRANAV PRAFULLA							71527649C			
SEM.:	1										
204181	SIGNALS & SYSTEMS	PP	100 40	0 19	21	40 F	•				
204181	SIGNALS & SYSTEMS	TW	25 10	0		15 F	•				
204182	ELECTRONIC DEVICES & CIRCUITS	PP	100 40	0 27	28	55 F	•				
204182	ELECTRONIC DEVICES & CIRCUITS	PR	50 20	0		33 F	•				
204183	NETWORK THEORY	PP	100 40	0 33	22	55 F	•				
204183	NETWORK THEORY	TW	25 10	0		21 F	•				
204184	DATA STRUCTURES & ALGORITHMS	PP	100 40	0 15	37	52 F	•				
204184	DATA STRUCTURES & ALGORITHMS	OR	50 20	0		34 F	•				
204185	DIGITAL ELECTRONICS	PP	100 40	0 26	15	41 F	•				
204185	DIGITAL ELECTRONICS	PR	50 20	0		22 F	•				
204186	ELEX. MEASURING INSTRU & TOOL	.STW	50 20	0		34 F	•				
	FIRST SEM TOTAL = 402/750										

	: 25 JAN 2016 CENTRE: P							•				
NOTE:	FIRST LINE : SEAT NO., NAME O ES: SUB.CODE, SUB.NAME, SUB.TY	F TH	HE CA	ANDIDA	ΛTE,	мотні	ER, PER	MANENT RE	G. NO., PREV	IOUS SEAT	NO., COLLEGE,	SEAT NO.
	3271 TAMBE RUSHIKESH DILIP											
SEM.:	1											
	SIGNALS & SYSTEMS				34	32	66 P					
204181	SIGNALS & SYSTEMS	TW	25	10			24 P					
204182	ELECTRONIC DEVICES & CIRCUITS				47	32	79 P					
	ELECTRONIC DEVICES & CIRCUITS						45 P					
204183	NETWORK THEORY			_	35	43	78 P					
204183	NETWORK THEORY						23 P					
204184	DATA STRUCTURES & ALGORITHMS				33	33	66 P					
204184	DATA STRUCTURES & ALGORITHMS						40 P					
204185	DIGITAL ELECTRONICS				38	42	80 P					
204185	DIGITAL ELECTRONICS						39 P					
204186	ELEX. MEASURING INSTRU & TOOL FIRST SEM TOTAL = 582/750	STW	50	20			42 P					
	3272 TANAY SUDARSHAN CHOUGUL										,S1400532	
CEM .	1											
SEM.:		-	100	10	11	1 5	F.C. D.					
	SIGNALS & SYSTEMS				4 L	ТЭ	56 P					
	SIGNALS & SYSTEMS				27	27	12 P					
	ELECTRONIC DEVICES & CIRCUITS ELECTRONIC DEVICES & CIRCUITS				37	21	64 P 33 P					
204182	NETWORK THEORY		100		38	25	63 P					
204183			25		30	25	16 P					
204183	NETWORK THEORY DATA STRUCTURES & ALGORITHMS				40	22						
					40	22	62 P					
204184	DATA STRUCTURES & ALGORITHMS				35	22	31 P					
204185 204185	DIGITAL ELECTRONICS DIGITAL ELECTRONICS		100		33	23	58 P 37 P					
204186	ELEX. MEASURING INSTRU & TOOL FIRST SEM TOTAL = 455/750	SIW	30	20			23 P					
	3273 THAKUR NUPUR SHEKHAR								,71527665E			
SEM.:	1											
	SIGNALS & SYSTEMS	DD	100	40	37	42	79 P					
	SIGNALS & SYSTEMS		25		<i>31</i>	74	79 P 21 P					
	ELECTRONIC DEVICES & CIRCUITS				45	39	21 P 84 P					
	ELECTRONIC DEVICES & CIRCUITS ELECTRONIC DEVICES & CIRCUITS				7 J	39	39 P					
204182	NETWORK THEORY		100		41	40	39 P 81 P					
204183	NETWORK THEORY NETWORK THEORY		25		71	40	20 P					
204183 204184	DATA STRUCTURES & ALGORITHMS				40	3	20 P 78 P					
					40	38						
204184	DATA STRUCTURES & ALGORITHMS				20	26	44 P					
204185	DIGITAL ELECTRONICS				39	30	75 P					
204185	DIGITAL ELECTRONICS		50				38 P					
2U4186	ELEX. MEASURING INSTRU & TOOL FIRST SEM TOTAL = 595/750	51W	50	20			36 P					

DATE	L6 S.E.(2014 PAT.)(ELECTRONICS E: 25 JAN 2016 CENTRE:	PUNE	INST	ITUTE									
NOTE: OTHER LIN	FIRST LINE : SEAT NO., NAME (NES: SUB.CODE, SUB.NAME, SUB.T	OF T YPE,	HE CA MAX.	NDIDAT	ΓE, , MIN	MOTHEI N.PASS	R, P MAR	PERMANENT REG. KS, OE MARKS,	NO., PR	EVIOUS SEAT , OBTAINED,	T NO., , P/F:F	COLLEGE, PASS/FAIL,	SEAT NO. C:PREV.CARRY OVE
	33274 THATTE SNEHA UDAY							,					
SEM.:	1												
204181	SIGNALS & SYSTEMS	PP	100	40 3	36	43	79	P					
204181	SIGNALS & SYSTEMS	TW	25	10			21	P					
204182	ELECTRONIC DEVICES & CIRCUITS	S PP	100	40 4	45	35	80	P					
204182	ELECTRONIC DEVICES & CIRCUITS	S PR	50	20			42	P					
204183	NETWORK THEORY		100	-	29	35	64	P					
204183	NETWORK THEORY	TW	25	10			22	P					
204184	DATA STRUCTURES & ALGORITHMS	PP	100	40 3	30	34	64	P					
204184	DATA STRUCTURES & ALGORITHMS	OR	50	20			37	P					
204185	DIGITAL ELECTRONICS	PP	100	40 3	35	39	74	P					
204185	DIGITAL ELECTRONICS	PR	50	20			39	P					
	ELEX. MEASURING INSTRU & TOO FIRST SEM TOTAL = 559/750						37						
	3275 THOKE SHREYAS ANIRUDHA												
SEM.:	1												
	SIGNALS & SYSTEMS	DD	100	40 3	20	23	43	D					
	SIGNALS & SYSTEMS				_0	23	16						
	ELECTRONIC DEVICES & CIRCUITS				32	22	54						
	ELECTRONIC DEVICES & CIRCUITS				,,	22	24						
204183	NETWORK THEORY		100		20	20	40						
204183	NETWORK THEORY		25		-0	20	22						
204184	DATA STRUCTURES & ALGORITHMS				19	21	40						
	DATA STRUCTURES & ALGORITHMS				LJ	21	35						
	DIGITAL ELECTRONICS		100		30	24	54						
	DIGITAL ELECTRONICS		50		,,	24	40						
							26						
204100	FIRST SEM TOTAL = 394/750	LJIW	30	20			20	r					
	33276 THOMBARE SAYALI VIJAYK								,71527669н				
SEM.:	1												
	SIGNALS & SYSTEMS	PP	100	40 2	24	16	40	Р					
204181	SIGNALS & SYSTEMS	TW	25	10			17	Р					
204182	ELECTRONIC DEVICES & CIRCUITS	S PP	100	40 2	27	16	43	Р					
204182	ELECTRONIC DEVICES & CIRCUITS	S PR	50	20			24	Р					
204183	NETWORK THEORY	PP	100	40 (80	07		F					
204183	NETWORK THEORY	TW	25	10			20	Р					
204184	DATA STRUCTURES & ALGORITHMS	PP	100	40 2	22	23	45	Р					
204184	DATA STRUCTURES & ALGORITHMS	OR	50	20			28	Р					
204185	DIGITAL ELECTRONICS	PP	100	40 1	15	17		F					
	DIGITAL ELECTRONICS		50				38	Р					
204186	ELEX. MEASURING INSTRU & TOO	LSTW	50	20			35						
	FIRST SEM TOTAL =/750												

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NOTE: HER LIN	FIRST LINE : SEAT NO., NAME (IES: SUB.CODE, SUB.NAME, SUB.T	OF THE CAN YPE, MAX.M	DIDATE,	MOTH IN.PAS	HER, PERMANEN SS MARKS, OE	T REG. NO., PREV MARKS, TH MARKS,	IOUS SEAT OBTAINED,	NO., COLLEGE, SEAT NO. P/F:PASS/FAIL, C:PREV.CARRY OVE
	3277 THOMBRE UTKARSHA SANJA							,S140053277
SEM.:	1							
204181	SIGNALS & SYSTEMS	PP 100 4	0 22	18	40 P			
204181	SIGNALS & SYSTEMS	TW 25 1	.0		22 P			
	ELECTRONIC DEVICES & CIRCUITS			31	73 P			
	ELECTRONIC DEVICES & CIRCUITS				42 P			
204183	NETWORK THEORY	PP 100 4	-	24	51 P			
204183	NETWORK THEORY				22 P			
204184	DATA STRUCTURES & ALGORITHMS			38	62 P			
204184	DATA STRUCTURES & ALGORITHMS				45 P			
204185		PP 100 4		29				
204185	DIGITAL ELECTRONICS				31 P			
	ELEX. MEASURING INSTRU & TOOL FIRST SEM TOTAL = 492/750				45 P			
	3278 THORAT RUGVED DEVENDRA							,s140053278
SEM.:	1							
	SIGNALS & SYSTEMS	PP 100 4	.0 23	29	52 P			
	SIGNALS & SYSTEMS			23	14 P			
	ELECTRONIC DEVICES & CIRCUITS			20	53 P			
	ELECTRONIC DEVICES & CIRCUITS		-	20	36 P			
204183	NETWORK THEORY	PP 100 4		24	43 P			
204183	NETWORK THEORY	TW 25 1		- '	16 P			
204184	DATA STRUCTURES & ALGORITHMS			22	40 P			
204184					26 P			
204185	DIGITAL ELECTRONICS	PP 100 4		25	56 P			
		PR 50 2		23	30 P			
	ELEX. MEASURING INSTRU & TOOI				36 P			
	FIRST SEM TOTAL = 402/750							
	3279 TIWARI MITHUN RAMESHCH				OSH			,s140053279
SEM.:	1							
204181	SIGNALS & SYSTEMS	PP 100 4	0 28	35	63 P			
204181	SIGNALS & SYSTEMS	TW 25 1	.0		18 P			
204182	ELECTRONIC DEVICES & CIRCUITS	S PP 100 4	0 46	32	78 P			
204182	ELECTRONIC DEVICES & CIRCUITS	S PR 50 2	0		36 P			
204183	NETWORK THEORY	PP 100 4	0 37	32	69 P			
204183	NETWORK THEORY	TW 25 1	.0		20 P			
204184	DATA STRUCTURES & ALGORITHMS	PP 100 4	0 40	38	78 P			
204184	DATA STRUCTURES & ALGORITHMS	OR 50 2	0		38 P			
204185	DIGITAL ELECTRONICS	PP 100 4	0 28	33	61 P			
204185	DIGITAL ELECTRONICS	PR 50 2			29 P			
204186	ELEX. MEASURING INSTRU & TOOI	LSTW 50 2	0		38 P			
	FIRST SEM TOTAL = 528/750							

DATE	L6 S.E.(2014 PAT.)(ELECTRONICS E: 25 JAN 2016 CENTRE: PI	UNE	INST	ITUTE				-					
NOTE:	FIRST LINE : SEAT NO., NAME OF	F TH	IE CA	NDIDA	TE,	MOTHE	R, PER	MANENT RE	G. NO., PRE	/IOUS SEA	T NO.,	COLLEGE,	SEAT NO.
	33280 UDIT MENDIRATTA								,71527677〕				
SEM.:	1												
204181	SIGNALS & SYSTEMS	PP	100	40	24	34	58 P						
204181	SIGNALS & SYSTEMS	TW	25	10			17 P						
204182	ELECTRONIC DEVICES & CIRCUITS	PP	100	40	33	21	54 P						
204182	ELECTRONIC DEVICES & CIRCUITS	PR	50	20			31 P						
204183	NETWORK THEORY	PP	100	40	17	23	40 P						
204183	NETWORK THEORY	TW	25	10			21 P						
204184	DATA STRUCTURES & ALGORITHMS	PP	100	40	37	28	65 P						
204184	DATA STRUCTURES & ALGORITHMS	OR	50	20			35 P						
204185	DIGITAL ELECTRONICS	PP	100	40	21	28	49 P						
204185	DIGITAL ELECTRONICS	PR	50	20			35 P						
204186	ELEX. MEASURING INSTRU & TOOLS FIRST SEM TOTAL = 439/750	STW	50	20			34 P						
s14005	33281 UMEKAR PRANAV AMBADAS								,71527679E				
CEM .	.1												
SEM.:		-	100	40	22	10	40 5						
	SIGNALS & SYSTEMS				22	18	40 P						
			25		10	2.5	19 P						
	ELECTRONIC DEVICES & CIRCUITS				19	33	54 P						
	ELECTRONIC DEVICES & CIRCUITS		100		19	10	33 P						
204183 204183	NETWORK THEORY NETWORK THEORY		25		19	12	F 20 P						
204183	DATA STRUCTURES & ALGORITHMS				32	24	20 P						
204184	DATA STRUCTURES & ALGORITHMS DATA STRUCTURES & ALGORITHMS				32	24	31 P						
204185			100		32	20	52 P						
204185	DIGITAL ELECTRONICS				<i>3</i>	20	6 F						
	ELEX. MEASURING INSTRU & TOOLS						33 P						
204100	FIRST SEM TOTAL = $/750$	J 1 W	30	20			33 1						
S14005	53282 UTKARSH					SARIT	Α		,71527680J	,PICT		,S1400532	282
SEM.:	1												
204181	SIGNALS & SYSTEMS	PP	100	40	41	43	84 P						
204181	SIGNALS & SYSTEMS	TW	25	10			19 P						
204182	ELECTRONIC DEVICES & CIRCUITS	PP	100	40	43	28	71 P						
204182	ELECTRONIC DEVICES & CIRCUITS	PR	50	20			21 P						
204183	NETWORK THEORY	PP	100	40	30	29	59 P						
204183	NETWORK THEORY	TW	25	10			19 P						
204184	DATA STRUCTURES & ALGORITHMS	PP	100	40	38	34	72 P						
204184	DATA STRUCTURES & ALGORITHMS	OR	50	20			36 P						
204185	DIGITAL ELECTRONICS	PP	100	40	25	24	49 P						
204185	DIGITAL ELECTRONICS	PR	50	20			22 P						
204186	ELEX. MEASURING INSTRU & TOOLS FIRST SEM TOTAL = 487/750	STW	50	20			35 P						

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NOTE: HER LIN	FIRST LINE : SEAT NO., NAME (ES: SUB.CODE, SUB.NAME, SUB.T	OF TH	E CANDID MAX.MARK	ATE, S, M	MOTHE IN.PASS	ER, PI S MARI	ERMANENT REG KS, OE MARKS	. NO., PRE	VIOUS SEAT OBTAINED,	NO., P/F:P	COLLEGE, ASS/FAIL,	SEAT NO. C:PREV.CARRY O
	3283 VALECHA SAKSHI SATISH											
SEM.:	1											
204181	SIGNALS & SYSTEMS	PP :	100 40	22	27	49 ।						
	SIGNALS & SYSTEMS					22 1						
	ELECTRONIC DEVICES & CIRCUITS			42	24	66 1						
	ELECTRONIC DEVICES & CIRCUITS					35 I						
204183	NETWORK THEORY		100 40	21	26	47 I						
204183	NETWORK THEORY					21						
204184	DATA STRUCTURES & ALGORITHMS			36	33	69 1						
204184	DATA STRUCTURES & ALGORITHMS					38 ।	•					
204185	DIGITAL ELECTRONICS			22	29							
204185	DIGITAL ELECTRONICS					25 I						
	ELEX. MEASURING INSTRU & TOOK FIRST SEM TOTAL = 464/750					41						
	3284 VANAVE AKASH RAVSAHEB							,71633667н				
SEM.:	1											
	SIGNALS & SYSTEMS	PP '	100 40	16	10	1	=					
	SIGNALS & SYSTEMS				10	22 ו						
	ELECTRONIC DEVICES & CIRCUITS			35	26	61						
	ELECTRONIC DEVICES & CIRCUITS			33	20	35 1						
204183	NETWORK THEORY		100 40	33	27	60 1						
204183	NETWORK THEORY		25 10	33	_,	21						
204184	DATA STRUCTURES & ALGORITHMS			32	32	64 1						
204184				32	32	38 1						
204185	DIGITAL ELECTRONICS		100 40	36	30	66 1						
	DIGITAL ELECTRONICS			30	30	29 1						
	ELEX. MEASURING INSTRU & TOO					42 1						
201100	FIRST SEM TOTAL =/750	LJIW	30 20			12						
	3285 VANGARI MAMTA GOVIND			• • • •				,71633668F				
SEM.:	1											
204181	SIGNALS & SYSTEMS	PP :	100 40	33	15	48 ।	•					
	SIGNALS & SYSTEMS		25 10			22 1						
204182	ELECTRONIC DEVICES & CIRCUITS	S PP	100 40	39	31	70 I	•					
204182	ELECTRONIC DEVICES & CIRCUITS	S PR	50 20			42 1	•					
204183	NETWORK THEORY	PP :	100 40	25	32	57 I	•					
204183	NETWORK THEORY	TW	25 10			22	•					
204184	DATA STRUCTURES & ALGORITHMS			26	32	58 ו						
204184						39 ו						
204185	DIGITAL ELECTRONICS			24	28	52 1						
204185	DIGITAL ELECTRONICS		50 20			22 1						
	ELEX. MEASURING INSTRU & TOO					40 1						
	FIRST SEM TOTAL = 472/750		-									

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NOTE: THER LIN	FIRST LINE : SEAT NO., NAME (NES: SUB.CODE, SUB.NAME, SUB.T	OF THE CANDI YPE, MAX.MAR	DATE, KS, MI	MOTHI IN.PAS	ER, PERMANEN S MARKS, OE	T REG. NO., PREV	IOUS SEAT OBTAINED,	P/F:PASS/FAIL, C:PREV.CARRY OVE
	33286 VARDEKAR NEHA CHANDRAK							,s140053286
SEM.:	:1							
204181	SIGNALS & SYSTEMS	PP 100 40	26	20	46 P			
	SIGNALS & SYSTEMS				20 P			
	ELECTRONIC DEVICES & CIRCUIT		33	17	50 P			
					32 P			
204183	NETWORK THEORY	PP 100 40	29	15	44 P			
204183	NETWORK THEORY				20 P			
204184	DATA STRUCTURES & ALGORITHMS		35	32	67 P			
204184	DATA STRUCTURES & ALGORITHMS				37 P			
204185			29	21				
204185					35 P			
	ELEX. MEASURING INSTRU & TOO FIRST SEM TOTAL = 441/750				40 P			
								,s140053287
CEM •	. 1							
SEM.:	SIGNALS & SYSTEMS	DD 100 40	22	10	41 P			
		TW 25 10	22	19	41 P 19 P			
	ELECTRONIC DEVICES & CIRCUIT		21	25	56 P			
	ELECTRONIC DEVICES & CIRCUIT		31	23	30 P			
204183	NETWORK THEORY	PP 100 40	32	20	52 P			
204183	NETWORK THEORY	TW 25 10	32	20	20 P			
204184	DATA STRUCTURES & ALGORITHMS		32	35	67 P			
204184			32	33	38 P			
204185			25	29	54 P			
	DIGITAL ELECTRONICS		23	23	35 P			
	ELEX. MEASURING INSTRU & TOO				37 P			
	FIRST SEM TOTAL = 458/750							
	53288 VASAGADE SUMEDHA SUNIL				 TA			,s140053288
SEM.:	:1							
	SIGNALS & SYSTEMS	PP 100 40	27	28	55 P			
	SIGNALS & SYSTEMS				22 P			
204182	ELECTRONIC DEVICES & CIRCUIT	S PP 100 40	45	33	78 P			
204182	ELECTRONIC DEVICES & CIRCUIT	S PR 50 20			44 P			
204183	NETWORK THEORY	PP 100 40	36	33	69 P			
204183	NETWORK THEORY	TW 25 10			22 P			
204184	DATA STRUCTURES & ALGORITHMS	PP 100 40	31	23	54 P			
204184	DATA STRUCTURES & ALGORITHMS	OR 50 20			45 P			
204185	DIGITAL ELECTRONICS	PP 100 40	29	34	63 P			
204185	DIGITAL ELECTRONICS	PR 50 20			37 P			
204186	ELEX. MEASURING INSTRU & TOO	LSTW 50 20			40 P			
	FIRST SEM TOTAL = 529/750							

	: 25 JAN 2016 CENTRE: F									PAGE N		
NOTE: HER LIN	FIRST LINE : SEAT NO., NAME CES: SUB.CODE, SUB.NAME, SUB.TY)F TH ′PE,	IE CAND MAX.MA	IDATE, RKS, M	MOTH IN.PAS	IER, I	PERMANENT REG RKS, OE MARKS	. NO., PRE	VIOUS SEAT OBTAINED,	NO., P/F:P	COLLEGE, ASS/FAIL,	SEAT NO. C:PREV.CARRY OV
	3289 VELANKAR SIDDHI SANJAY											
SEM.:	1											
204181	SIGNALS & SYSTEMS	PP	100 40	26	29	55	P					
204181	SIGNALS & SYSTEMS	TW	25 10			20	P					
204182	ELECTRONIC DEVICES & CIRCUITS	PP.	100 40	34	25	59	P					
204182	ELECTRONIC DEVICES & CIRCUITS	PR	50 20			34						
204183	NETWORK THEORY	PP	100 40	27	29	56	P					
204183	NETWORK THEORY	TW	25 10			19	P					
204184	DATA STRUCTURES & ALGORITHMS	PP	100 40	33	30	63	P					
204184	DATA STRUCTURES & ALGORITHMS	OR	50 20			37	P					
204185	DIGITAL ELECTRONICS	PP	100 40	31	30	61	P					
204185	DIGITAL ELECTRONICS	PR	50 20			30	P					
204186	ELEX. MEASURING INSTRU & TOOL FIRST SEM TOTAL = 473/750	.STW	50 20			39	P					
	3290 WADIKAR PINAK SUHAS							,71527693L				
SEM.:	1											
	SIGNALS & SYSTEMS	PP	100 40	42	26	68	Р					
	SIGNALS & SYSTEMS					21						
	ELECTRONIC DEVICES & CIRCUITS				33	81						
204182	ELECTRONIC DEVICES & CIRCUITS	PR	50 20			43	Р					
204183			100 40		44	83						
204183	NETWORK THEORY		25 10			22						
204184	DATA STRUCTURES & ALGORITHMS				36	82						
204184	DATA STRUCTURES & ALGORITHMS					42						
204185	DIGITAL ELECTRONICS		100 40		28	67						
204185	DIGITAL ELECTRONICS					45						
	ELEX. MEASURING INSTRU & TOOL					44						
	FIRST SEM TOTAL = 598/750											
	3291 WAGHMARE ASHLESHA NAMDE							,71527696E				
SEM.:	1											
	SIGNALS & SYSTEMS	PP	100 40	17	23	40	Р					
	SIGNALS & SYSTEMS		25 10			20						
	ELECTRONIC DEVICES & CIRCUITS				29	76						
	ELECTRONIC DEVICES & CIRCUITS					36						
204183	NETWORK THEORY		100 40		26	55						
204183	NETWORK THEORY		25 10			20						
204184	DATA STRUCTURES & ALGORITHMS				23	49						
204184	DATA STRUCTURES & ALGORITHMS				=	38						
204185	DIGITAL ELECTRONICS				27	57						
204185	DIGITAL ELECTRONICS		50 20			31						
204186	ELEX. MEASURING INSTRU & TOOL					29						
- 1-00	FIRST SEM TOTAL = 451/750	- · ••	20 20									

BRANCH: 16 S.E.(2014 PAT.)(ELECTRONICS &TELECOM)

	: 25 JAN 2016 CENTRE: PU											
NOTE:	FIRST LINE : SEAT NO., NAME OF	TH	IE CAN	NDIDAT	Έ,	мотн	ER, PERMA	NENT REG.	NO., PRE	VIOUS SEAT	NO., COLLEGE,	SEAT NO.
	ES: SUB.CODE, SUB.NAME, SUB.TYP											
	3292 WAIGAONKAR SAKSHI RAJESH											
SEM.:	1											
	SIGNALS & SYSTEMS	PP	100 4	40 2	1	19	40 P					
	SIGNALS & SYSTEMS				_		19 P					
	ELECTRONIC DEVICES & CIRCUITS				5	28	63 P					
204182	ELECTRONIC DEVICES & CIRCUITS				_		32 P					
204183	NETWORK THEORY				2	24	56 P					
204183	NETWORK THEORY						21 P					
204184	DATA STRUCTURES & ALGORITHMS				4	28	62 P					
204184	DATA STRUCTURES & ALGORITHMS						27 P					
204185	DIGITAL ELECTRONICS				2	18	40 P					
204185							30 P					
	ELEX. MEASURING INSTRU & TOOLS FIRST SEM TOTAL = 425/750						35 P					
	3293 WAKODKAR VARAD SANJEEV											
SEM.:	1											
204181	SIGNALS & SYSTEMS	PP	100 4	40 2	9	37	66 P					
204181	SIGNALS & SYSTEMS	TW	25 1	10			21 P					
204182	ELECTRONIC DEVICES & CIRCUITS	PP	100 4	40 4	1	31	72 P					
204182	ELECTRONIC DEVICES & CIRCUITS	PR	50 2	20			32 P					
204183	NETWORK THEORY	PP	100 4	40 2	7	33	60 P					
204183	NETWORK THEORY	TW	25 1	10			22 P					
204184	DATA STRUCTURES & ALGORITHMS	PP	100 4	40 3	9	38	77 P					
204184	DATA STRUCTURES & ALGORITHMS	OR	50 2	20			39 P					
204185	DIGITAL ELECTRONICS	PP	100 4	40 3	6	32	68 P					
204185	DIGITAL ELECTRONICS	PR	50 2	20			27 P					
204186	ELEX. MEASURING INSTRU & TOOLS FIRST SEM TOTAL = 526/750	STW	50 2	20			42 P					
	3294 WIDHATE RAHUL								 71527705н			
C	1											
SEM.:		DD	100	10 1	6	24	40 p					
	SIGNALS & SYSTEMS SIGNALS & SYSTEMS				.0	44	40 P 15 P					
204181					a	15	13 P 44 P					
	ELECTRONIC DEVICES & CIRCUITS ELECTRONIC DEVICES & CIRCUITS				9	Τ)	44 P 22 P					
204182	NETWORK THEORY				.7	15	22 P F					
204183	NETWORK THEORY				. 1	Τ)	F 18 Р					
204183	DATA STRUCTURES & ALGORITHMS				.9	21	10 P 40 P					
204184	DATA STRUCTURES & ALGORITHMS DATA STRUCTURES & ALGORITHMS				. <u>J</u>	4	40 P 26 P					
204184	DIGITAL ELECTRONICS				1	10	26 P 40 P					
204185	DIGITAL ELECTRONICS DIGITAL ELECTRONICS				т	ТЭ	40 P 4 F					
204185							4 F 34 P					
∠∪ 1 100	FIRST SEM TOTAL =/750	∍ i VV	JU 2	20			J+ F					

	E: 25 JAN 2016 CENTRE:							PAGE NO. 97
NOTE: HER LIN	FIRST LINE : SEAT NO., NAME (NES: SUB.CODE, SUB.NAME, SUB.T	OF THE CA YPE, MAX.	NDIDATE, MARKS, M	MOTH	HER, PERMANEN SS MARKS, OE	T REG. NO., PREV MARKS, TH MARKS,	IOUS SEAT OBTAINED,	NO., COLLEGE, SEAT NO. P/F:PASS/FAIL, C:PREV.CARRY OV
	3295 YENGE SAURAV NIVRUTTI							,s140053295
SEM.:	1							
204181	SIGNALS & SYSTEMS	PP 100	40 17	23	40 P			
204181	SIGNALS & SYSTEMS	TW 25	10		16 P			
204182	ELECTRONIC DEVICES & CIRCUIT	S PP 100	40 31	18	49 P			
204182	ELECTRONIC DEVICES & CIRCUIT	S PR 50	20		32 P			
204183	NETWORK THEORY	PP 100		21	44 P			
204183	NETWORK THEORY				21 P			
204184	DATA STRUCTURES & ALGORITHMS	PP 100	40 25	29	54 P			
204184	DATA STRUCTURES & ALGORITHMS	OR 50			38 P			
204185		PP 100		22	51 P			
204185	DIGITAL ELECTRONICS	PR 50	20		36 P			
204186	ELEX. MEASURING INSTRU & TOO FIRST SEM TOTAL = 419/750	LSTW 50	20		38 P			
								,s140053296
SEM.:	1							
	SIGNALS & SYSTEMS	PP 100	40 29	17	46 P			
	SIGNALS & SYSTEMS			_,	18 P			
	ELECTRONIC DEVICES & CIRCUIT			27	63 P			
	ELECTRONIC DEVICES & CIRCUIT			_,	33 P			
204183	NETWORK THEORY	PP 100		32	70 P			
204183	NETWORK THEORY	TW 25		32	21 P			
204184	DATA STRUCTURES & ALGORITHMS			24	65 P			
204184				۷.	43 P			
204185	DIGITAL ELECTRONICS	PP 100		26	51 P			
	DIGITAL ELECTRONICS			20	35 P			
	ELEX. MEASURING INSTRU & TOO				34 P			
	FIRST SEM TOTAL = 479/750							
	33297 ZAWARE PRASAD ASHOK				HURI			,s140053297
SEM.:	1							
204181	SIGNALS & SYSTEMS	PP 100	40 29	18	47 P			
204181	SIGNALS & SYSTEMS	TW 25	10		22 P			
204182	ELECTRONIC DEVICES & CIRCUIT	S PP 100	40 37	24	61 P			
204182	ELECTRONIC DEVICES & CIRCUIT	S PR 50	20		37 P			
204183	NETWORK THEORY	PP 100	40 25	15	40 P			
204183	NETWORK THEORY	TW 25	10		21 P			
204184	DATA STRUCTURES & ALGORITHMS	PP 100	40 27	27	54 P			
204184	DATA STRUCTURES & ALGORITHMS	OR 50	20		40 P			
204185	DIGITAL ELECTRONICS	PP 100	40 27	34	61 P			
204185	DIGITAL ELECTRONICS	PR 50			36 P			
	ELEX. MEASURING INSTRU & TOO				41 P			
	FIRST SEM TOTAL = 460/750							

SAVITRIBAI PHULE PUNE UNIVERSITY ,S.E.(2014 COURSE)EXAMINATION,OCT 2015

BRANCH: 19 S.E.(2014 PAT.)(COMPUTER)

DATE: 25 JAN 2016 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 97

NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: SUB.CODE, SUB.NAME, SUB.TYPE, MAX.MARKS, MIN.PASS MARKS, OE MARKS, TH MARKS, OBTAINED, P/F:PASS/FAIL, C:PREV.CARRY OVER

	: 25 JAN 2016 CENTRE: P	UNE INSTITUT	E OF	COMPUTE	ER TECHNOLOGY,	PUNE.	PAGE	NO. 98	
	FIRST LINE : SEAT NO., NAME O								
	ES: SUB.CODE, SUB.NAME, SUB.TY								
	4201 ABHALE BHAGYASHRI SHANT.								• • •
SEM.:	1								
210241	DISCRETE STRUCTURE	PP 100 40	36	26	62 P				
210242	DATA STRUCTURE & PROB SOLVING	PP 100 40	42	23	65 P				
210242	DATA STRUCTURE & PROB SOLVING	PR 50 20			38 P				
210243	DIGITAL ELEX. & LOGIC DESIGN	PP 100 40	34	41	75 P				
210243	DIGITAL ELEX. & LOGIC DESIGN	TW 25 10			20 P				
210244	OPERATING SYSTEM & ADMIN	PP 100 40	37	26	63 P				
210244	OPERATING SYSTEM & ADMIN	TW 25 10			20 P				
210244	OPERATING SYSTEM & ADMIN	PR 50 20			40 P				
210245	MICROPROCESSOR ARCHITECTURE	PP 100 40	38	33	71 P				
210245	MICROPROCESSOR ARCHITECTURE	TW 25 10			20 P				
210245	MICROPROCESSOR ARCHITECTURE	OR 50 20			42 P				
210246	SOFT SKILLS	TW 25 10			20 P				
	FIRST SEM TOTAL = $536/750$								
S14005	4202 ABHYANKAR ADYA ANAND			AVANEE		,71527084C	,PICT	,S140054202	
	_								
SEM.:		100 10	4.5	4-	00 -				
	DISCRETE STRUCTURE		45		90 P				
	DATA STRUCTURE & PROB SOLVING		33		60 P				
	DATA STRUCTURE & PROB SOLVING		2.4		36 P				
	DIGITAL ELEX. & LOGIC DESIGN		34		74 P				
210243 210244	DIGITAL ELEX. & LOGIC DESIGN OPERATING SYSTEM & ADMIN	PP 100 40	40		20 P 60 P				
	OPERATING SYSTEM & ADMIN	TW 25 10	40		22 P				
	OPERATING SYSTEM & ADMIN	PR 50 20			30 P				
	MICROPROCESSOR ARCHITECTURE	PP 100 40	37		70 P				
	MICROPROCESSOR ARCHITECTURE	TW 25 10	31		21 P				
		OR 50 20			42 P				
		TW 25 10			23 P				
2102.0	FIRST SEM TOTAL = 548/750	25 16							
	4203 ADITYA SINGH			BANDAN	IA	.71527085M	.PICT	,S140054203	
						•	•	,	
SEM.:	1								
210241	DISCRETE STRUCTURE	PP 100 40	37	22	59 P				
210242	DATA STRUCTURE & PROB SOLVING	PP 100 40	29	23	52 P				
210242	DATA STRUCTURE & PROB SOLVING	PR 50 20			AA F				
210243	DIGITAL ELEX. & LOGIC DESIGN	PP 100 40	29	22	51 P				
210243	DIGITAL ELEX. & LOGIC DESIGN	TW 25 10			10 P				
210244	OPERATING SYSTEM & ADMIN	PP 100 40	39	20	59 P				
210244	OPERATING SYSTEM & ADMIN	TW 25 10			10 P				
210244	OPERATING SYSTEM & ADMIN	PR 50 20			AA F				
210245	MICROPROCESSOR ARCHITECTURE	PP 100 40	33	27	60 P				
210245	MICROPROCESSOR ARCHITECTURE	TW 25 10			10 P				

210245 MICROPROCESSOR ARCHITECTURE OR 50 20 8 F
210246 SOFT SKILLS TW 25 10 10 P
FIRST SEM TOTAL = --/750

DATE	19 S.E.(2014 PAT.)(COMPUTER) E: 25 JAN 2016 CENTRE: P						•		PAGE NO. 99
NOTE:	FIRST LINE : SEAT NO., NAME O	F THE CA	NDIDA	ΓЕ,	МОТН	ER, PERMANEN	IT REG. NO., PREV	COUS SEAT	 Γ NO., COLLEGE, SEAT NO. , P/F:PASS/FAIL, C:PREV.CARRY OVI
	 54204 ADITYA SURESH WADEKAR					 KSHI			, s140054204
SEM.:		DD 100	40	4 E	22	70 p			
210241				45 30		78 P			
210242 210242				50	19	49 P 41 P			
210242				31	21	41 P			
210243	DIGITAL ELEX. & LOGIC DESIGN) 1	J 4	19 P			
210243		PP 100		31	12	49 P			
210244		TW 25) <u>T</u>	10	21 P			
210244		PR 50				30 P			
210245		PP 100		40	22	62 P			
		TW 25		10		20 P			
		OR 50				45 P			
	SOFT SKILLS					20 P			
	FIRST SEM TOTAL = 499/750								
	54205 ADNAN AHMAD					ASHAN			,s140054205
SEM.:	:1								
210241	DISCRETE STRUCTURE	PP 100	40	41	36	77 P			
210242	DATA STRUCTURE & PROB SOLVING	PP 100	40	43	26	69 P			
210242	DATA STRUCTURE & PROB SOLVING	PR 50	20			40 P			
210243	DIGITAL ELEX. & LOGIC DESIGN	PP 100	40	35	30	65 P			
210243	DIGITAL ELEX. & LOGIC DESIGN	TW 25	10			18 P			
210244	OPERATING SYSTEM & ADMIN	PP 100	40	42	29	71 P			
210244	OPERATING SYSTEM & ADMIN	TW 25	10			20 P			
210244	OPERATING SYSTEM & ADMIN	PR 50				36 P			
210245		PP 100		40	29	69 P			
		TW 25				19 P			
		OR 50				45 P			
210246	SOFT SKILLS FIRST SEM TOTAL = 551/750	TW 25	10			22 P			
	54206 ADSUL NIRANJAN LAXMAN								,s140054206
SEM.:	•1								
	DISCRETE STRUCTURE	pp 1∩∩	40	44	30	83 P			
210241					33	72 P			
	DATA STRUCTURE & PROB SOLVING			, ,	,,	72 P 38 P			
210242				39	32	71 P			
210243		TW 25			J.L	71 P 20 P			
210243		PP 100		35	24	59 P			
210244		TW 25				19 P			
210244		PR 50				31 P			
210244		PP 100		41	30	71 P			
		TW 25		-		19 P			

210245 MICROPROCESSOR ARCHITECTURE OR 50 20 46 P
210246 SOFT SKILLS TW 25 10 19 P
FIRST SEM TOTAL = 548/750

	: 25 JAN 2016 CEN	-	E IN	STITU	TE OF	COMPUT	ER TE	ECHNOL	OGY, PUI	NE.		PAGE N	0. 100	
	FIRST LINE : SEAT NO.,													
OTHER LIN	ES: SUB.CODE, SUB.NAME,	SUB.TYPE	, МА	X.MAR	KS, M	IN.PASS	MARI	KS, OE	MARKS,	TH MARKS	, OBTAINED,	P/F:P	ASS/FAIL,	C:PREV.CARRY OVER
	4207 AGARWAL SAHIL SAN					ASHA					,PICT			
SEM.:	1													
210241	DISCRETE STRUCTURE	Р	Р 10	0 40	43	39	82 F	Р						
210242	DATA STRUCTURE & PROB S	SOLVING P	Р 10	0 40	37	31	68 F	Р						
210242	DATA STRUCTURE & PROB S	SOLVING P	R 5	0 20			42 F	Р						
210243	DIGITAL ELEX. & LOGIC D	DESIGN P	Р 10	0 40	39	37	76 F	Р						
210243	DIGITAL ELEX. & LOGIC D	DESIGN T	w 2	5 10			19 F	Р						
210244	OPERATING SYSTEM & ADMI	IN P	P 10	0 40	34	24	58 F	Р						
210244	OPERATING SYSTEM & ADMI	IN T	w 2	5 10			21 F	Р						
210244	OPERATING SYSTEM & ADMI	IN P	R 5	0 20			33 F	Р						
210245	MICROPROCESSOR ARCHITEC	CTURE P	P 10	0 40	35	32	67 F	Р						
210245	MICROPROCESSOR ARCHITEC	CTURE T	w 2	5 10			18 F	Р						
210245	MICROPROCESSOR ARCHITEC	CTURE 0	R 5	0 20			45 F	Р						
210246	SOFT SKILLS	Т	w 2	5 10			19 F	Ρ						
	FIRST SEM TOTAL = 548/7	750												
S14005	4208 AGRAWAL DISHA DIN	NESH				MANIS	HA		,	71527091F	,PICT		,S1400542	208
SEM.:	1													
210241	DISCRETE STRUCTURE	Р	Р 10	0 40	41	45	86 F	Р						
210242	DATA STRUCTURE & PROB S	SOLVING P	Р 10	0 40	42	35	77 F	Р						
210242	DATA STRUCTURE & PROB S	SOLVING P	R 5	0 20			42 F	Р						
210243	DIGITAL ELEX. & LOGIC D	DESIGN P	P 10	0 40	39	45	84 F	Р						
210243	DIGITAL ELEX. & LOGIC D	DESIGN T	w 2	5 10			19 F	Р						
210244	OPERATING SYSTEM & ADMI	IN P	P 10	0 40	39	25	64 F	Р						
210244	OPERATING SYSTEM & ADMI	IN T	w 2	5 10			20 F	Р						
210244	OPERATING SYSTEM & ADMI	IN P	R 5	0 20			40 F	Р						
210245	MICROPROCESSOR ARCHITEC	CTURE P	P 10	0 40	39	27	66 I	Р						
210245	MICROPROCESSOR ARCHITEC	CTURE T	W 2	5 10			20 F	Р						
210245	MICROPROCESSOR ARCHITEC	CTURE 0	R 5	0 20			44 1	Р						
210246	SOFT SKILLS	Т	w 2	5 10			19 F	Р						
	FIRST SEM TOTAL = 581/7	750												
	4209 AGRAWAL SUMEET SH					SONAL					,PICT			
511003	UU / MONTANA SUMEET SI	. = 1317/313				JUIAL	_		, '	. 10210071	,, 101		, 51 100542	
SEM.:	1													
210241	DISCRETE STRUCTURE	Р	P 10	0 40	36	40	76 F	Р						
210242	DATA STRUCTURE & PROB S	SOLVING P	P 10	0 40	38	29	67 F	Р						
210242	DATA STRUCTURE & PROB S	SOLVING P	R 5	0 20			32 F	Р						
210243	DIGITAL ELEX. & LOGIC D	DESIGN P	P 10	0 40	32	32	64 F	Р						
210243	DIGITAL ELEX. & LOGIC D	DESIGN T	w 2	5 10			19 F	Р						
210244	OPERATING SYSTEM & ADMI	IN P	P 10	0 40	32	25	57 F	Р						
210244	OPERATING SYSTEM & ADMI	IN T	w 2	5 10			21 F	Р						
210244	OPERATING SYSTEM & ADMI	IN P	R 5	0 20			32 F	Р						
210245	MICROPROCESSOR ARCHITEC	CTURE P	P 10	0 40	37	28	65 F	Р						
210245	MICROPROCESSOR ARCHITEC	CTURE T	W 2	5 10			19 F	Р						

210245 MICROPROCESSOR ARCHITECTURE OR 50 20 38 P
210246 SOFT SKILLS TW 25 10 20 P
FIRST SEM TOTAL = 510/750

	: 25 JAN 2016 CENTRE: PI	UNE	INST	ITUTE	E OF	COMPU	TER	TECHNO	DLOGY, F	PUNE.			PAGE N	o. 101	
	FIRST LINE : SEAT NO., NAME OF														
	ES: SUB.CODE, SUB.NAME, SUB.TY														
	4210 AHIRE SUSHILKUMAR SOMNA														
SEM.:	1														
210241	DISCRETE STRUCTURE	PP	100	40	17	07		F							
210242	DATA STRUCTURE & PROB SOLVING	PP	100	40	17	10		F							
210242	DATA STRUCTURE & PROB SOLVING	PR	50	20			AA	F							
210243	DIGITAL ELEX. & LOGIC DESIGN	PP	100	40	13	13		F							
210243	DIGITAL ELEX. & LOGIC DESIGN	TW	25	10			11	Р							
210244	OPERATING SYSTEM & ADMIN	PP	100	40	21	12		F							
210244	OPERATING SYSTEM & ADMIN	TW	25	10			15	Р							
210244	OPERATING SYSTEM & ADMIN	PR	50	20			34	Р							
210245	MICROPROCESSOR ARCHITECTURE	PP	100	40	16	24	40	Р							
210245	MICROPROCESSOR ARCHITECTURE	TW	25	10			10	Р							
210245	MICROPROCESSOR ARCHITECTURE	OR	50	20			7	F							
210246	SOFT SKILLS	TW	25	10			10	Р							
	FIRST SEM TOTAL = $/750$														
S14005	4211 AMBRE TEJASWI SURESH					MINA	KSHI			,715271	L04M	,PICT		,S140054	211
SEM.:	1														
210241	DISCRETE STRUCTURE	PP	100	40	41	43	84	Р							
210242	DATA STRUCTURE & PROB SOLVING	PP	100	40	38	32	70	Р							
210242	DATA STRUCTURE & PROB SOLVING	PR	50	20			10	F							
210243	DIGITAL ELEX. & LOGIC DESIGN	PP	100	40	33	29	62	Р							
210243	DIGITAL ELEX. & LOGIC DESIGN	TW	25	10			20	Р							
210244	OPERATING SYSTEM & ADMIN		100		38	19	57								
210244	OPERATING SYSTEM & ADMIN						20								
210244	OPERATING SYSTEM & ADMIN		50				33	Р							
			100		33	23	56								
210245	MICROPROCESSOR ARCHITECTURE	TW	25	10			20	Р							
210245	MICROPROCESSOR ARCHITECTURE	OR	50	20			36	Р							
210246	SOFT SKILLS	TW	25	10			21	Р							
	FIRST SEM TOTAL = $/750$														
s14005	4212 AMBRE VIKRAM BALASAHEB					SHAR	MILA			,715271	L05K	,PICT		,S140054	212
SEM.:						a -									
	DISCRETE STRUCTURE					27									
	DATA STRUCTURE & PROB SOLVING				33	19	52								
	DATA STRUCTURE & PROB SOLVING				2.5	24	25								
	DIGITAL ELEX. & LOGIC DESIGN				25	3 T	56								
	DIGITAL ELEX. & LOGIC DESIGN				20	20	10								
	OPERATING SYSTEM & ADMIN		100		29	20	49								
			25				10								
	OPERATING SYSTEM & ADMIN		50		22	10	AA								
	MICROPROCESSOR ARCHITECTURE		100		22	19	41								
210245	MICROPROCESSOR ARCHITECTURE	I W	25	ΤO			10	٢							

210245 MICROPROCESSOR ARCHITECTURE OR 50 20 AA F
210246 SOFT SKILLS TW 25 10 10 P
FIRST SEM TOTAL = --/750

	9 S.E.(2014 PAT.)(COMPUTER) : 25 JAN 2016 CENTRE: PU	JNE	INSTIT	UTE OF	COM	IPUTER TECHNOLOGY	, PUNE.	PAGE	NO. 102
NOTE:	FIRST LINE : SEAT NO., NAME OF	F TH	HE CAND	DIDATE,	MC	THER, PERMANENT	REG. NO., PREVI	OUS SEAT NO.,	COLLEGE, SEAT NO.
OTHER LIN	ES: SUB.CODE, SUB.NAME, SUB.TY	PΕ,	MAX.MA	ARKS, M	IIN.F	PASS MARKS, OE MA	RKS, TH MARKS, C	BTAINED, P/F:	PASS/FAIL, C:PREV.CARRY OVER
314003	4213 AMBULKAK SATALI SHKIKANI	J			L	LENA	,71327100H	, PICI	,3140034213
SEM.:	1								
210241	DISCRETE STRUCTURE	PP	100 40	31	35	66 P			
210242	DATA STRUCTURE & PROB SOLVING	PP	100 40	33	15	6 48 P			
210242	DATA STRUCTURE & PROB SOLVING	PR	50 20)		28 P			
210243	DIGITAL ELEX. & LOGIC DESIGN	PP	100 40	32	23	3 55 P			
210243	DIGITAL ELEX. & LOGIC DESIGN	TW	25 10)		20 P			
210244	OPERATING SYSTEM & ADMIN		100 40		15				
210244			25 10			18 P			
210244	OPERATING SYSTEM & ADMIN		50 20			30 P			
210245			100 40		16				
210245			25 10			19 P			
210245			50 20			40 P			
210246		I W	25 10)		21 P			
	FIRST SEM TOTAL = 445/750								
	4214 AMOL SOLEY					RUDULA		,PICT	
SEM.:									
	DISCRETE STRUCTURE		100 40						
	DATA STRUCTURE & PROB SOLVING				27				
						38 P			
	DIGITAL ELEX. & LOGIC DESIGN				30				
210243	DIGITAL ELEX. & LOGIC DESIGN				4.5	17 P			
210244	OPERATING SYSTEM & ADMIN		100 40		15				
210244			25 10			19 P			
210244 210245	OPERATING SYSTEM & ADMIN MICROPROCESSOR ARCHITECTURE		50 20 100 40		29	43 P 60 P			
			25 10		23	17 P			
210245			50 20			33 P			
210243			25 10			17 P			
210240	FIRST SEM TOTAL = 502/750	1 VV	23 10	,		17 1			
S14005	4215 ANISHA RAM GUNJAL				SA	ANDHYA	,71527112в	,PICT	,S140054215
CEM .	1								
SEM.:		DD	100 40	11	16	: 00 p			
210241	DISCRETE STRUCTURE DATA STRUCTURE & PROB SOLVING		100 40						
210242	DATA STRUCTURE & PROB SOLVING DATA STRUCTURE & PROB SOLVING				20	39 P			
210242	DIGITAL ELEX. & LOGIC DESIGN				37				
210243	DIGITAL ELEX. & LOGIC DESIGN				57	19 P			
210243	OPERATING SYSTEM & ADMIN		100 40		27				
210244			25 10		۷.	19 P			
210244	OPERATING SYSTEM & ADMIN		50 20			40 P			
210245			100 40		32				
			25 10			19 P			

210245 MICROPROCESSOR ARCHITECTURE OR 50 20 45 P
210246 SOFT SKILLS TW 25 10 21 P
FIRST SEM TOTAL = 578/750

	9 S.E.(2014 PAT.)(COMPUTER) : 25 JAN 2016 CENTRE: PU	UNE	INST:	ITUTI	E OF	COMPUT	TER ⁻	ΓΕCHNO	OLOGY, F	PUNE.		PAGE NO	o. 103	
NOTE:	FIRST LINE : SEAT NO., NAME OF	F TH	IE CAI	NDID	ATE,	MOTHE	ER, I	PERMAN	NENT REG	. NO., PREV	IOUS SEA	T NO.,	COLLEGE, SEAT	NO.
OTHER LIN	ES: SUB.CODE, SUB.NAME, SUB.TY	PΕ,	MAX.	MARK:	S, MI	N.PASS	S MAI	RKS, C	DE MARKS	S, TH MARKS,	OBTAINED	, P/F:P/	ASS/FAIL, C:PREV	CARRY OVER
	4216 ANKTT CHOUDINDY													
514005	4216 ANKIT CHOUDHURY					SHILL	P1			,71527114J	, PICI		,5140054216	
SEM.:	1													
210241	DISCRETE STRUCTURE	PP	100	40	28	29	57	Р						
210242	DATA STRUCTURE & PROB SOLVING	PP	100	40	23	17	40	Р						
210242	DATA STRUCTURE & PROB SOLVING	PR	50	20			10	F						
210243	DIGITAL ELEX. & LOGIC DESIGN	PP	100	40	32	19	51	Р						
210243	DIGITAL ELEX. & LOGIC DESIGN	TW	25	10			10	Р						
210244	OPERATING SYSTEM & ADMIN	PP	100	40	30	15	45	Р						
210244	OPERATING SYSTEM & ADMIN	TW	25	10			10	Р						
210244	OPERATING SYSTEM & ADMIN	PR	50	20			30	Р						
210245	MICROPROCESSOR ARCHITECTURE	PP	100	40	32	28	60							
210245			25				10							
210245			50				12							
210246		TW	25	10			10	Р						
	FIRST SEM TOTAL =/750													
	4217 ANKUR MUNDRA									,71527115G				
										,	,		,	
SEM.:	1													
210241	DISCRETE STRUCTURE	PP	100	40	46	40	86	Р						
210242	DATA STRUCTURE & PROB SOLVING	PP	100	40	29	26	55	Р						
210242	DATA STRUCTURE & PROB SOLVING	PR	50	20			40	Р						
210243	DIGITAL ELEX. & LOGIC DESIGN	PP	100	40	40	39	79	Р						
210243	DIGITAL ELEX. & LOGIC DESIGN	TW	25	10			18	Р						
210244	OPERATING SYSTEM & ADMIN	PP	100	40	42	15	57	Р						
210244	OPERATING SYSTEM & ADMIN	TW	25	10			19	Р						
210244	OPERATING SYSTEM & ADMIN	PR	50	20			31	Р						
210245	MICROPROCESSOR ARCHITECTURE		100		39	27	66							
210245			25				19							
			50				32							
210246	SOFT SKILLS	TW	25	10			21	Р						
	FIRST SEM TOTAL = 523/750													
\$14005	4218 ANSUL GOENKA									,71527116E				
311003	TETS ANSOE GOLINA					JUNI	17			,713271102	,1 101		,5110051210	
SEM.:	1													
		PP	100	40	35	31	66	Р						
210242	DATA STRUCTURE & PROB SOLVING	PP	100	40	34	22	56	Р						
210242	DATA STRUCTURE & PROB SOLVING	PR	50	20			42							
210243	DIGITAL ELEX. & LOGIC DESIGN	PP	100	40	34	36	70	Р						
210243	DIGITAL ELEX. & LOGIC DESIGN	TW	25	10			16	Р						
210244	OPERATING SYSTEM & ADMIN	PP	100	40	36	17	53	Р						
210244	OPERATING SYSTEM & ADMIN	TW	25	10			17	Р						
210244	OPERATING SYSTEM & ADMIN	PR	50	20			37	Р						
210245	MICROPROCESSOR ARCHITECTURE	PP	100	40	40	23	63	Р						
210245	MICROPROCESSOR ARCHITECTURE	TW	25	10			16	Р						

210245 MICROPROCESSOR ARCHITECTURE OR 50 20 28 P
210246 SOFT SKILLS TW 25 10 19 P
FIRST SEM TOTAL = 483/750

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE OTHER LINES: SUB.CODE, SUB.NAME, SUB.TYPE, MAX.MARKS, MIN.PASS MARKS, OE MARKS, TH MARKS, OBTAINED, P/F:PASS/FAIL S140054219 ANUBHAV GOEL RASHI ,71527117C ,PICT ,S14005 SEM.:1 210241 DISCRETE STRUCTURE PP 100 40 30 39 69 P 210242 DATA STRUCTURE & PROB SOLVING PP 100 40 36 23 59 P 210242 DATA STRUCTURE & PROB SOLVING PR 50 20 30 P 210243 DIGITAL ELEX. & LOGIC DESIGN PP 100 40 39 33 72 P 210243 DIGITAL ELEX. & LOGIC DESIGN TW 25 10 18 P	
S140054219 ANUBHAV GOEL RASHI ,71527117C ,PICT ,S14005 SEM.:1 210241 DISCRETE STRUCTURE PP 100 40 30 39 69 P 210242 DATA STRUCTURE & PROB SOLVING PP 100 40 36 23 59 P 210242 DATA STRUCTURE & PROB SOLVING PR 50 20 30 P 210243 DIGITAL ELEX. & LOGIC DESIGN PP 100 40 39 33 72 P	, SEAT NO.
210241 DISCRETE STRUCTURE PP 100 40 30 39 69 P 210242 DATA STRUCTURE & PROB SOLVING PP 100 40 36 23 59 P 210242 DATA STRUCTURE & PROB SOLVING PR 50 20 30 P 210243 DIGITAL ELEX. & LOGIC DESIGN PP 100 40 39 33 72 P	
210241 DISCRETE STRUCTURE PP 100 40 30 39 69 P 210242 DATA STRUCTURE & PROB SOLVING PP 100 40 36 23 59 P 210242 DATA STRUCTURE & PROB SOLVING PR 50 20 30 P 210243 DIGITAL ELEX. & LOGIC DESIGN PP 100 40 39 33 72 P	
210242 DATA STRUCTURE & PROB SOLVING PR 50 20 30 P 210243 DIGITAL ELEX. & LOGIC DESIGN PP 100 40 39 33 72 P	
210243 DIGITAL ELEX. & LOGIC DESIGN PP 100 40 39 33 72 P	
210243 DIGITAL ELEX. & LOGIC DESIGN TW 25 10 18 P	
210244 OPERATING SYSTEM & ADMIN PP 100 40 43 15 58 P	
210244 OPERATING SYSTEM & ADMIN TW 25 10 20 P	
210244 OPERATING SYSTEM & ADMIN PR 50 20 38 P	
210245 MICROPROCESSOR ARCHITECTURE PP 100 40 31 26 57 P	
210245 MICROPROCESSOR ARCHITECTURE TW 25 10 18 P	
210245 MICROPROCESSOR ARCHITECTURE OR 50 20 32 P	
210246 SOFT SKILLS TW 25 10 20 P	
FIRST SEM TOTAL = 491/750	
S140054220 ARNAV SHRIVASTAVA ASHA ,71527119K ,PICT ,S14005	
SEM.:1	
210241 DISCRETE STRUCTURE PP 100 40 38 37 75 P	
210242 DATA STRUCTURE & PROB SOLVING PP 100 40 41 19 60 P	
210242 DATA STRUCTURE & PROB SOLVING PR 50 20 35 P	
210243 DIGITAL ELEX. & LOGIC DESIGN PP 100 40 31 32 63 P	
210243 DIGITAL ELEX. & LOGIC DESIGN TW 25 10 18 P	
210244 OPERATING SYSTEM & ADMIN PP 100 40 40 17 57 P	
210244 OPERATING SYSTEM & ADMIN TW 25 10 20 P	
210244 OPERATING SYSTEM & ADMIN PR 50 20 30 P	
210245 MICROPROCESSOR ARCHITECTURE PP 100 40 34 22 56 P	
210245 MICROPROCESSOR ARCHITECTURE TW 25 10 19 P	
210245 MICROPROCESSOR ARCHITECTURE OR 50 20 42 P	
210246 SOFT SKILLS TW 25 10 20 P FIRST SEM TOTAL = 495/750	
S140054221 ARORA AMANJOTSINGH SATPALSINGH SWARANKAUR ,71633672D ,PICT ,S14005	
SEM.:1	
210241 DISCRETE STRUCTURE PP 100 40 34 31 65 P	
210241 DISCRETE STRUCTURE & PROB SOLVING PP 100 40 34 23 57 P	
210242 DATA STRUCTURE & PROB SOLVING PR 50 20 40 P	
210243 DIGITAL ELEX. & LOGIC DESIGN PP 100 40 32 32 64 P	
210243 DIGITAL ELEX. & LOGIC DESIGN TW 25 10 20 P	
210244 OPERATING SYSTEM & ADMIN PP 100 40 34 22 56 P	
210244 OPERATING SYSTEM & ADMIN TW 25 10 20 P	
210244 OPERATING SYSTEM & ADMIN PR 50 20 36 P	
210245 MICROPROCESSOR ARCHITECTURE PP 100 40 33 31 64 P	
210245 MICROPROCESSOR ARCHITECTURE TW 25 10 20 P	

210245 MICROPROCESSOR ARCHITECTURE OR 50 20 22 P
210246 SOFT SKILLS TW 25 10 19 P
FIRST SEM TOTAL = 483/750

	9 S.E.(2014 PAT.)(COMPUTER) : 25 JAN 2016 CENTRE: PU	UNE	INSTI	TUTE O	F CO	MPUTER	۲	ECHNOL	OGY, F	UNE.		PAGE NO	o. 105	
NOTE:	FIRST LINE : SEAT NO., NAME OF	F TH	IE CANI	DIDATE	, M	OTHER,	, Р	ERMANE	ENT REG	. NO., PREV	/IOUS S	EAT NO.,	COLLEGE,	SEAT NO.
OTHER LIN	ES: SUB.CODE, SUB.NAME, SUB.TYI	PΕ,	MAX.MA	ARKS,	MIN.	PASS M	1AR	KS, OE	MARKS	, TH MARKS,	OBTAIN	ED, P/F:PA	ASS/FAIL,	C:PREV.CARRY OVER
\$14005	4222 ARUNDHATI PALSULE				M	EGHA				,71527123н	,PIC	Γ	,51400542	22
SEM.:	1													
210241	DISCRETE STRUCTURE	PP	100 40	31	2	2 5	53	Р						
210242	DATA STRUCTURE & PROB SOLVING	PP	100 40	38	2	0 5	8	Р						
210242	DATA STRUCTURE & PROB SOLVING	PR	50 20	0		4	12	Р						
210243	DIGITAL ELEX. & LOGIC DESIGN	PP	100 40	0 34	2	8 6	52	Р						
210243	DIGITAL ELEX. & LOGIC DESIGN	TW	25 10	0		1	L7	Р						
210244	OPERATING SYSTEM & ADMIN	PP	100 40	0 28	2	0 4	18	Р						
210244	OPERATING SYSTEM & ADMIN	TW	25 10	0		2	20	Р						
210244	OPERATING SYSTEM & ADMIN	PR	50 20	0		3	31	Р						
210245	MICROPROCESSOR ARCHITECTURE	PP	100 40	0 31	1	.7 4	18	Р						
210245	MICROPROCESSOR ARCHITECTURE	TW	25 10	0		1	L9	Р						
210245	MICROPROCESSOR ARCHITECTURE	OR	50 20	0		3	30	Р						
210246		TW	25 10	0		1	L8	Р						
	FIRST SEM TOTAL = 446/750													
	4223 ASHUTOSH UPADHYAY									,71527126в				
										,	,		,	.
SEM.:	1													
210241	DISCRETE STRUCTURE	PP	100 40	0 32	3	8 7	70	Р						
210242	DATA STRUCTURE & PROB SOLVING	PP	100 40	0 34	2	2 5	6	Р						
210242	DATA STRUCTURE & PROB SOLVING	PR	50 20	0		4	AΑ	F						
210243	DIGITAL ELEX. & LOGIC DESIGN	PP	100 40	0 30	2	8 5	8	Р						
210243	DIGITAL ELEX. & LOGIC DESIGN	TW	25 10	0		1	LO	Р						
210244	OPERATING SYSTEM & ADMIN	PP	100 40	0 28	1	.5 4	13	Р						
210244	OPERATING SYSTEM & ADMIN	TW	25 10	0		1	LO	Р						
210244	OPERATING SYSTEM & ADMIN	PR	50 20	0		A	AΑ	F						
			100 40		2		13							
210245	MICROPROCESSOR ARCHITECTURE	TW	25 10	0			LO							
			50 20				λA							
210246	SOFT SKILLS	TW	25 10	0		1	LO	Р						
	FIRST SEM TOTAL =/750													
\$14005	4224 ATTARDE MOHIT SHRIRAM									,71527127L				
311003	TEET ATTAKE MOTET STREET				J	AVIIA				,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,	,110	•	,51100512	.2 1
SEM.:	1													
		PP	100 40	0 29	2	3 5	52	Р						
210242	DATA STRUCTURE & PROB SOLVING	PP	100 40	0 26	2	3 4	19	Р						
210242	DATA STRUCTURE & PROB SOLVING	PR	50 20	0		3	32	Р						
210243	DIGITAL ELEX. & LOGIC DESIGN	PP	100 40	0 37	2	3 6	50	Р						
210243	DIGITAL ELEX. & LOGIC DESIGN	TW	25 10	0		1	L7	Р						
210244	OPERATING SYSTEM & ADMIN	PP	100 40	0 37	1	.7 5	54	Р						
210244	OPERATING SYSTEM & ADMIN	TW	25 10	0		2	20	Р						
210244	OPERATING SYSTEM & ADMIN	PR	50 20	0		3	36	Р						
210245	MICROPROCESSOR ARCHITECTURE	PP	100 40	0 34	2	3 5	57	Р						
210245	MICROPROCESSOR ARCHITECTURE	TW	25 10	0		2	20	Р						

210245 MICROPROCESSOR ARCHITECTURE OR 50 20 22 P
210246 SOFT SKILLS TW 25 10 18 P
FIRST SEM TOTAL = 437/750

	: 25 JAN 2016 CENTRE: P	UNE	INST	ITUTE	OF	COMPUTI	ER -	TECHNOLOG	Y, PU	NE.		PAGE N	o. 106	
	FIRST LINE : SEAT NO., NAME O													
	ES: SUB.CODE, SUB.NAME, SUB.TY													
	4225 AVHAD ATUL TULSHIDAS													
SEM.:	1													
210241	DISCRETE STRUCTURE	PP	100	40	44	42	86	Р						
210242	DATA STRUCTURE & PROB SOLVING	PP	100	40	39	23	62	Р						
210242	DATA STRUCTURE & PROB SOLVING	PR	50	20			36	Р						
210243	DIGITAL ELEX. & LOGIC DESIGN	PP	100	40	34	35	69	Р						
210243	DIGITAL ELEX. & LOGIC DESIGN	TW	25	10			19	Р						
210244	OPERATING SYSTEM & ADMIN	PP	100	40	31	23	54	Р						
210244	OPERATING SYSTEM & ADMIN	TW	25	10			21	Р						
210244	OPERATING SYSTEM & ADMIN	PR	50	20			40	Р						
210245	MICROPROCESSOR ARCHITECTURE	PP	100	40	42	29	71	Р						
210245	MICROPROCESSOR ARCHITECTURE	TW	25	10			19	Р						
210245	MICROPROCESSOR ARCHITECTURE	OR	50	20			22	Р						
210246		TW	25	10			19	Р						
	FIRST SEM TOTAL = $518/750$													
S14005	4226 AVINASH GOEN					ANITA			,	71527130L	,PICT		,S1400542	226
SEM.:			100	40	2.0	2.2		_						
	DISCRETE STRUCTURE		100	-	_	22	51							
_	DATA STRUCTURE & PROB SOLVING			-	26	05								
	DATA STRUCTURE & PROB SOLVING				20	1 5	22							
	DIGITAL ELEX. & LOGIC DESIGN DIGITAL ELEX. & LOGIC DESIGN				29	15	44 17	•						
210243 210244	OPERATING SYSTEM & ADMIN		100		34	08	17 							
-			25		34	08	22							
	OPERATING SYSTEM & ADMIN		50				36							
			100		27	15	42							
			25		_,	13	19							
			50				20							
			25				19							
2202.0	FIRST SEM TOTAL =/750							•						
	4227 BARBIND MAYURESH MUKUND									71527144L				227
									,		•		•	
SEM.:	1													
210241	DISCRETE STRUCTURE	PP	100	40	34	27	61	Р						
210242	DATA STRUCTURE & PROB SOLVING	PP	100	40	37	21	58	Р						
210242	DATA STRUCTURE & PROB SOLVING	PR	50	20			38	Р						
210243	DIGITAL ELEX. & LOGIC DESIGN	PP	100	40	35	26	61	Р						
210243	DIGITAL ELEX. & LOGIC DESIGN	TW	25	10			10	Р						
210244	OPERATING SYSTEM & ADMIN	PP	100	40	41	25	66	Р						
210244	OPERATING SYSTEM & ADMIN	TW	25	10			20	Р						
210244	OPERATING SYSTEM & ADMIN	PR	50	20			32	Р						
210245	MICROPROCESSOR ARCHITECTURE	PP	100	40	36	26	62	Р						
210245	MICROPROCESSOR ARCHITECTURE	TW	25	10			20	Р						

210245 MICROPROCESSOR ARCHITECTURE OR 50 20 28 P
210246 SOFT SKILLS TW 25 10 21 P
FIRST SEM TOTAL = 477/750

	:: 25 JAN 2016 CE	-	NE I	NSTIT	UTE OF	COMPU	ITER 7	ГЕСН	NOLOGY,	PUNE.			PAGE N	10. 107	
	FIRST LINE : SEAT NO.,														
	IES: SUB.CODE, SUB.NAME,				-		-				•		•	-	
	4228 BARGAL PRADNYA U													,S1400542	
SEM.:	1														
	DISCRETE STRUCTURE	Ī	PP 1	.00 40	38	40	78	Р							
210242	DATA STRUCTURE & PROB					29	62								
210242	DATA STRUCTURE & PROB						35								
210243	DIGITAL ELEX. & LOGIC	DESIGN I	PP 1	.00 40	42	29	71	Р							
210243	DIGITAL ELEX. & LOGIC	DESIGN -	TW	25 10			18	Р							
210244	OPERATING SYSTEM & ADM	IN I	PP 1	.00 40	40	22	62	Р							
210244	OPERATING SYSTEM & ADM	IN -	TW	25 10			20	Р							
210244	OPERATING SYSTEM & ADM	IN I	PR	50 20			39	Р							
210245	MICROPROCESSOR ARCHITE	CTURE I	PP 1	.00 40	42	30	72	Р							
210245	MICROPROCESSOR ARCHITE	CTURE -	TW	25 10			20	Р							
210245	MICROPROCESSOR ARCHITE	CTURE (OR	50 20			43	Р							
210246	SOFT SKILLS	-	TW	25 10			21	Р							
	FIRST SEM TOTAL = 541/	750													
S14005	4229 BATWAL OMKAR SAN	DEEP				VAIS	HALI			,71527	7146G	,PICT		,S1400542	229
SEM.:	1														
	DISCRETE STRUCTURE	ı	DD 1	.00 40	42	45	87	D							
	DATA STRUCTURE & PROB					19	53								
	DATA STRUCTURE & PROB					13	32								
	DIGITAL ELEX. & LOGIC					30	63								
210243	DIGITAL ELEX. & LOGIC	DESIGN	TW	25 10			20								
210244	OPERATING SYSTEM & ADM					18	55								
210244	OPERATING SYSTEM & ADM			25 10			21								
210244	OPERATING SYSTEM & ADM	IN I	PR	50 20			37	Р							
210245	MICROPROCESSOR ARCHITE	CTURE I	PP 1	.00 40	33	30	63	Р							
210245	MICROPROCESSOR ARCHITE	CTURE -	TW	25 10			21	Р							
210245	MICROPROCESSOR ARCHITE	CTURE (OR	50 20			38	Р							
210246	SOFT SKILLS	-	TW	25 10			20	Р							
	FIRST SEM TOTAL = 510/	750													
S14005	4230 BHADALE APOORVA	SURYAKAN	Τ			KIRT	I			,71527	7148C	,PICT		,S1400542	230
SEM.:	1														
	DISCRETE STRUCTURE	ı	pp 1	00 40	36	44	80	Р							
	DATA STRUCTURE & PROB					33	76								
210242	DATA STRUCTURE & PROB					33	40								
						39	84								
210243	DIGITAL ELEX. & LOGIC						23								
210244	OPERATING SYSTEM & ADM					29	74								
210244	OPERATING SYSTEM & ADM			25 10			19								
210244	OPERATING SYSTEM & ADM			50 20			36								
210245	MICROPROCESSOR ARCHITE	CTURE I	PP 1	.00 40	42	26	68	Р							
210245	MICROPROCESSOR ARCHITE	CTURE -	TW	25 10			21	Р							

210245 MICROPROCESSOR ARCHITECTURE OR 50 20 46 P
210246 SOFT SKILLS TW 25 10 22 P
FIRST SEM TOTAL = 589/750

	E: 25 JAN 2016 CENTRE: PL	JNE IN	STITU	TE OF	COMPUT	TER TEC	HNOLOGY, PU	JNE.	ı	PAGE NO. 108	
	FIRST LINE : SEAT NO., NAME O										
	NES: SUB.CODE, SUB.NAME, SUB.TYI			-		•		•		•	
	54231 BHADANGE AKSHAY GANESH										
SEM.:	:1										
210241	DISCRETE STRUCTURE	PP 10	0 40	30	20	50 P					
210242	DATA STRUCTURE & PROB SOLVING	PP 10	0 40	29	24	53 P					
210242	DATA STRUCTURE & PROB SOLVING	PR 5	0 20			45 P					
210243	DIGITAL ELEX. & LOGIC DESIGN	PP 10	0 40	32	32	64 P					
210243	DIGITAL ELEX. & LOGIC DESIGN	TW 2	5 10			20 P					
210244	OPERATING SYSTEM & ADMIN	PP 10	0 40	22	34	56 P					
210244	OPERATING SYSTEM & ADMIN	TW 2	5 10			20 P					
210244	OPERATING SYSTEM & ADMIN	PR 5	0 20			35 P					
210245	MICROPROCESSOR ARCHITECTURE	PP 10	0 40	32	27	59 P					
210245	MICROPROCESSOR ARCHITECTURE	TW 2	5 10			21 P					
210245	MICROPROCESSOR ARCHITECTURE	OR 5	0 20			38 P					
210246	SOFT SKILLS	TW 2	5 10			21 P					
	FIRST SEM TOTAL = 482/750										
	74222 PUALEDAO CAMEUT DAMAKAN										
\$14005	54232 BHALERAO SAKSHI RAMAKAN	l			VAISE	HALI	:	,/152/15UE	, PICI	, \$14005	4232
SEM.:	.1										
	.ı DISCRETE STRUCTURE	DD 10	0 40	41	41	82 P					
-	DATA STRUCTURE & PROB SOLVING	_				62 P					
	DATA STRUCTURE & PROB SOLVING			31	23	37 P					
210242	DIGITAL ELEX. & LOGIC DESIGN			41	23	64 P					
210243					23	19 P					
210244	OPERATING SYSTEM & ADMIN	PP 10		41	33	74 P					
210244		TW 2				21 P					
210244		PR 5				34 P					
		PP 10		32	28	60 P					
210245		TW 2				21 P					
210245	MICROPROCESSOR ARCHITECTURE	OR 5	0 20			37 P					
210246	SOFT SKILLS	TW 2	5 10			20 P					
	FIRST SEM TOTAL = 531/750										
S14005	54233 BHANGE ASHWINI GOVIND				JIJAE	BAI	;	,71633674L	,PICT	, \$14005	4233
SEM.:											
	DISCRETE STRUCTURE					47 P					
210242				26	AA	F -					
210242						5 F					
210243				26	24	50 P					
210243	DIGITAL ELEX. & LOGIC DESIGN			2.5	20	20 P					
210244		PP 10		35	29	64 P					
210244		TW 2				18 P					
		PR 5		2.4	2.0	22 P					
		PP 10		24	28	52 P					
210245	MICROPROCESSOR ARCHITECTURE	TW 2	2 TO			20 P					

210245 MICROPROCESSOR ARCHITECTURE OR 50 20 21 P 210246 SOFT SKILLS TW 25 10 21 P FIRST SEM TOTAL = --/750

	9	UNE	INST:	ITUTI	E OF	COMPUT	TER T	ГЕСНИС	OLOGY. F	PUNE.		PAGE NO	o. 109	
	DATE: 25 JAN 2016 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 109													
NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.														
	ES: SUB.CODE, SUB.NAME, SUB.TY													
S14005	4234 BHIDE PARITOSH RAJESH					TEJAS	SHREE	Ξ		,71527153К	,PICT		,S140054234	
SEM.:			100	40	40	2.5	0.4	_						
			100		49		84							
210242	DATA STRUCTURE & PROB SOLVING				38	30	68							
	DATA STRUCTURE & PROB SOLVING				22	20	33							
210243 210243	DIGITAL ELEX. & LOGIC DESIGN DIGITAL ELEX. & LOGIC DESIGN				32	29	61 24							
210243	OPERATING SYSTEM & ADMIN		100		43	33	76							
210244			25		43	33	23							
-			50				38							
210244			100		38	30	68							
210245			25		30	30	22							
			50				45							
			25				20							
	FIRST SEM TOTAL = 562/750													
s14005	4235 BHOKARE MRUNAL DILIPRAJ					SWAT	I			,71527157в	,PICT		,S140054235	
SEM.:	1													
210241	DISCRETE STRUCTURE	PP	100	40	33	27	60	Р						
210242	DATA STRUCTURE & PROB SOLVING	PP	100	40	25	27	52	Р						
210242	DATA STRUCTURE & PROB SOLVING	PR	50	20			21	Р						
210243	DIGITAL ELEX. & LOGIC DESIGN	PP	100	40	29	25	54	Р						
210243	DIGITAL ELEX. & LOGIC DESIGN						19							
	OPERATING SYSTEM & ADMIN		100		29	34	63							
			25				22							
	OPERATING SYSTEM & ADMIN		50				30							
210245			100		26	29	55							
			25				19							
			50				22							
210246	SOFT SKILLS	I W	25 .	10			19	Р						
	FIRST SEM TOTAL = 436/750													
\$14005	4236 BHOKRE MADHURA JAYANT												,s140054236	
311003	TESC BROKKE PARBHOTOL SYTTAIN					317 (10				,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,	,, 10,		,5110031230	
SEM.:	1													
		PP	100	40	41	48	89	Р						
210242	DATA STRUCTURE & PROB SOLVING	PP	100	40	39	32	71							
210242	DATA STRUCTURE & PROB SOLVING	PR	50	20			42	Р						
210243	DIGITAL ELEX. & LOGIC DESIGN	PP	100	40	39	32	71	Р						
210243	DIGITAL ELEX. & LOGIC DESIGN	TW	25	10			19	Р						
210244	OPERATING SYSTEM & ADMIN	PP	100	40	36	29	65	Р						
210244	OPERATING SYSTEM & ADMIN	TW	25	10			21	Р						
210244	OPERATING SYSTEM & ADMIN	PR	50	20			35	Р						
210245	MICROPROCESSOR ARCHITECTURE	PP	100	40	38	31	69	Р						
210245	MICROPROCESSOR ARCHITECTURE	TW	25	10			22	Р						

210245 MICROPROCESSOR ARCHITECTURE OR 50 20 36 P
210246 SOFT SKILLS TW 25 10 21 P
FIRST SEM TOTAL = 561/750

DATE	19 S.E.(2014 PAT.)(COMPUTER) E: 25 JAN 2016 CENTRE: P						•		PAGE NO. 110
NOTE:	FIRST LINE : SEAT NO., NAME O	F THE CA	NDIDA	TE,	МОТН	ER, PERMANEN	IT REG. NO., PREV	IOUS SEAT	Γ NO., COLLEGE, SEAT NO. , P/F:PASS/FAIL, C:PREV.CARRY OV
	 54237 BORA ANMOL RAVINDRA								, s140054237
SEM.:	:1 DISCRETE STRUCTURE	DD 100	40	45	15	60 P			
	DATA STRUCTURE & PROB SOLVING			4 3 32	_	56 P			
	DATA STRUCTURE & PROB SOLVING			<i>J</i>	24	25 P			
210243				33	25	58 P			
210243	DIGITAL ELEX. & LOGIC DESIGN					18 P			
210244		PP 100		31	30	61 P			
		TW 25		_		19 P			
210244		PR 50				28 P			
210245	MICROPROCESSOR ARCHITECTURE	PP 100	40	38	27	65 P			
210245	MICROPROCESSOR ARCHITECTURE	TW 25	10			18 P			
210245	MICROPROCESSOR ARCHITECTURE	OR 50	20			35 P			
210246	SOFT SKILLS	TW 25	10			20 P			
	FIRST SEM TOTAL = 463/750								
									,s140054238
SEM.:	:1								
210241	DISCRETE STRUCTURE	PP 100	40	27	26	53 P			
	DATA STRUCTURE & PROB SOLVING			24	21	45 P			
	DATA STRUCTURE & PROB SOLVING					8 F			
210243	DIGITAL ELEX. & LOGIC DESIGN	PP 100	40	31	24	55 P			
210243	DIGITAL ELEX. & LOGIC DESIGN	TW 25	-			21 P			
210244		PP 100		33	32	65 P			
210244		TW 25				21 P			
210244		PR 50		. .	2.5	31 P			
210245		PP 100		34	25	59 P			
210245		TW 25				21 P			
210245		OR 50				38 P			
210246	SOFT SKILLS FIRST SEM TOTAL =/750	IW 25	10			20 P			
	54239 BUDHKAR AISHWARYA SHISH					LI			,s140054239
SEM.:	:1								
	DISCRETE STRUCTURE	PP 100	40	35	41	76 P			
210242	DATA STRUCTURE & PROB SOLVING	PP 100	40	40	31	71 P			
210242	DATA STRUCTURE & PROB SOLVING	PR 50	20			40 P			
210243	DIGITAL ELEX. & LOGIC DESIGN	PP 100	40	34	29	63 P			
210243	DIGITAL ELEX. & LOGIC DESIGN	TW 25	10			22 P			
210244	OPERATING SYSTEM & ADMIN	PP 100	40	47	35	82 P			
210244	OPERATING SYSTEM & ADMIN	TW 25	10			20 P			
210244	OPERATING SYSTEM & ADMIN	PR 50	20			35 P			
210245	MICROPROCESSOR ARCHITECTURE	PP 100	40	42	30	72 P			
210245	MICROPROCESSOR ARCHITECTURE	TW 25	10			22 P			

210245 MICROPROCESSOR ARCHITECTURE OR 50 20 26 P
210246 SOFT SKILLS TW 25 10 23 P
FIRST SEM TOTAL = 552/750

	: 25 JAN 2016 CENTRE: F	PUNE	INSTIT	UTE OF	COMPU	TER T	ECHNOLOG	Y, PUN	IE.		PAGE N	10. 111	
	FIRST LINE : SEAT NO., NAME O												
	ES: SUB.CODE, SUB.NAME, SUB.TY			•		-			-		-	•	
S14005	4240 CHALAVADI DURGESH MAHAD)EV			ANUSA	AYA		, 7	1633676G	,PICT		,S1400542	40
SEM.:	1												
210241	DISCRETE STRUCTURE	PP	100 40	27	19	46 F	•						
210242	DATA STRUCTURE & PROB SOLVING	j PP	100 40	23	23	46 F	•						
210242	DATA STRUCTURE & PROB SOLVING	3 PR	50 20			35 F	•						
210243	DIGITAL ELEX. & LOGIC DESIGN	PP	100 40	31	21	52 F	•						
210243	DIGITAL ELEX. & LOGIC DESIGN					20 F	•						
210244	OPERATING SYSTEM & ADMIN		100 40		25	58 F							
210244	OPERATING SYSTEM & ADMIN		25 10			21 F							
210244	OPERATING SYSTEM & ADMIN		50 20			32 F							
210245			100 40		23	58 F							
210245	MICROPROCESSOR ARCHITECTURE		25 10			21 1							
210245		_	50 20			38 F							
210246	SOFT SKILLS	I W	25 10			21 1	,						
	FIRST SEM TOTAL = 448/750												
\$14005	4241 CHANDAK ANUSHKA MAHESHK				JAYA							,s1400542	
314003	1211 CHANDAK ANOSHKA MAHESHK	COMA	N.		JAIA			, /	13271730	, PICI		,31400342	41
SEM.:	1												
	DISCRETE STRUCTURE	PP	100 40	42	37	79 i	o						
	DATA STRUCTURE & PROB SOLVING					69 i							
	DATA STRUCTURE & PROB SOLVING					40 F							
	DIGITAL ELEX. & LOGIC DESIGN				26	68 i	o						
210243	DIGITAL ELEX. & LOGIC DESIGN	TW	25 10			18 F	o						
210244	OPERATING SYSTEM & ADMIN	PP	100 40	40	29	69 F	o						
210244	OPERATING SYSTEM & ADMIN	TW	25 10			21 F	•						
210244	OPERATING SYSTEM & ADMIN	PR	50 20			38 F	•						
210245	MICROPROCESSOR ARCHITECTURE	PP	100 40	39	26	65 F	•						
210245	MICROPROCESSOR ARCHITECTURE	TW	25 10			18 F	•						
210245	MICROPROCESSOR ARCHITECTURE	OR	50 20			23 F	•						
210246	SOFT SKILLS	TW	25 10			18 F	•						
	FIRST SEM TOTAL = $526/750$												
S14005	4242 CHARLES K MATHEW				SKI	MATHEV	V	,7	'1527175L	,PICT		,S1400542	42
SEM.:			100 40	43	42	٥.	_						
	DISCRETE STRUCTURE				42	85 F							
	DATA STRUCTURE & PROB SOLVING				27	63 F							
210242	DATA STRUCTURE & PROB SOLVING				26	36 F							
210243 210243	DIGITAL ELEX. & LOGIC DESIGN DIGITAL ELEX. & LOGIC DESIGN				26	59 i 18 i							
210243	OPERATING SYSTEM & ADMIN				32	72 F							
210244	OPERATING SYSTEM & ADMIN		25 10		32	17 F							
	OPERATING SYSTEM & ADMIN		50 20			31 F							
210244			100 40		26	57 F							
	MICROPROCESSOR ARCHITECTURE		25 10		20	17 F							
		. **	_3 _10			±, ,							

210245 MICROPROCESSOR ARCHITECTURE OR 50 20 40 P
210246 SOFT SKILLS TW 25 10 19 P
FIRST SEM TOTAL = 514/750

	: 25 JAN 2016 CENTRE:		E INST	TITUTE	E OF	COMPUTE	R TEC	HNOLOGY, PU	JNE.	1	PAGE NO	. 112	
	FIRST LINE : SEAT NO., NAME ES: SUB.CODE, SUB.NAME, SUB.				-				•		•	•	
s14005	4243 CHAVAN RUTUJA RAJENDF	RA				RUTIKA		,	,71633677E	,PICT		,s1400542	243
CEM .	1												
SEM.: 210241	DISCRETE STRUCTURE	ρI	2 100	40	35	25	50 P						
	DATA STRUCTURE & PROB SOLVI				31		58 P						
	DATA STRUCTURE & PROB SOLVI				31		11 P						
210243	DIGITAL ELEX. & LOGIC DESIG				28		59 P						
210243	DIGITAL ELEX. & LOGIC DESIG						20 P						
210244	OPERATING SYSTEM & ADMIN	PI	100	40	37	26	63 P						
210244	OPERATING SYSTEM & ADMIN	ΤV	v 25	10		2	21 P						
210244	OPERATING SYSTEM & ADMIN	PF	R 50	20		2	28 P						
210245	MICROPROCESSOR ARCHITECTURE	E PI	100	40	34	32	66 P						
210245	MICROPROCESSOR ARCHITECTURE	E TV	v 25	10		2	20 P						
210245	MICROPROCESSOR ARCHITECTURE	E OF	R 50	20		3	33 P						
210246	SOFT SKILLS	T۷	v 25	10		2	20 P						
	FIRST SEM TOTAL = $489/750$												
S14005	4244 CHETNA SURESH BERI					RUPINDI	ER	,	,71527181E	,PICT		,s1400542	244
SEM.:			400	4.0		5.4							
	DISCRETE STRUCTURE				33		67 P						
	DATA STRUCTURE & PROB SOLVI				34		55 P						
210242	DATA STRUCTURE & PROB SOLVI				20		38 P						
	DIGITAL ELEX. & LOGIC DESIGNATION OF STATE OF ST				39		51 P						
210243 210244	DIGITAL ELEX. & LOGIC DESIGNO OPERATING SYSTEM & ADMIN		v 25 P 100		36		19 P 55 P						
210244	OPERATING SYSTEM & ADMIN		v 25		30		19 P						
210244	OPERATING SYSTEM & ADMIN		R 50				30 P						
210244	MICROPROCESSOR ARCHITECTURE		> 100		35		57 P						
210245	MICROPROCESSOR ARCHITECTURE		v 25		33		18 P						
	MICROPROCESSOR ARCHITECTURE		· 50				34 P						
	SOFT SKILLS		v 25				20 P						
	FIRST SEM TOTAL = 483/750												
S14005	4245 CHIMTE ANKITA GAJANA					MANGAL		,	,71633678C	,PICT		,s1400542	245
SEM.:	1												
210241	DISCRETE STRUCTURE	PI	100	40	26	16	42 P						
210242	DATA STRUCTURE & PROB SOLVI	NG P	100	40	16	29	45 P						
210242	DATA STRUCTURE & PROB SOLVI	NG P	R 50	20		!	5 F						
210243	DIGITAL ELEX. & LOGIC DESIG				26		50 P						
210243	DIGITAL ELEX. & LOGIC DESIG						20 P						
210244	OPERATING SYSTEM & ADMIN				34		54 P						
210244	OPERATING SYSTEM & ADMIN		v 25				20 P						
210244	OPERATING SYSTEM & ADMIN		50				24 P						
210245	MICROPROCESSOR ARCHITECTURE		25		22		47 P						
210245	MICROPROCESSOR ARCHITECTURE	<u>.</u> TV	v 25	ΤO		2	20 P						

210245 MICROPROCESSOR ARCHITECTURE OR 50 20 9 F
210246 SOFT SKILLS TW 25 10 20 P
FIRST SEM TOTAL = --/750

	.9	UNE	INSTI	TUTE O)F (OMPUT	ER -	TECHNO	OLOGY, F	PUNE.		PAGE NO	o. 113	
	•••••••••••••••••••••••••••••••••													
NOTE:	NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.													
OTHER LIN	IES: SUB.CODE, SUB.NAME, SUB.TYI	PΕ,	MAX.M	ARKS,	MIN	I.PASS	MAI	RKS, (OE MARKS	S, TH MARKS,	OBTAINE	D, P/F:PA	ASS/FAIL, C:P	REV.CARRY OVER
	4246 CHINAWALKAR MOSAMI TUSHA													
SEM.:	1													
	DISCRETE STRUCTURE	PP	100 40	0 40)	41	81	Р						
210242	DATA STRUCTURE & PROB SOLVING	PP	100 40	0 34	Ļ	30	64	Р						
210242	DATA STRUCTURE & PROB SOLVING	PR	50 20	0			30	Р						
210243	DIGITAL ELEX. & LOGIC DESIGN	PP	100 40	0 35	;	34	69	Р						
210243	DIGITAL ELEX. & LOGIC DESIGN	TW	25 10	0			22	Р						
210244	OPERATING SYSTEM & ADMIN	PP	100 40	0 36	5	34	70	Р						
210244	OPERATING SYSTEM & ADMIN	TW	25 10	0			21	Р						
210244	OPERATING SYSTEM & ADMIN	PR	50 20	0			32	Р						
210245	MICROPROCESSOR ARCHITECTURE	PP	100 40	0 41	-	31	72	Р						
210245	MICROPROCESSOR ARCHITECTURE	TW	25 10	0			23	Р						
210245	MICROPROCESSOR ARCHITECTURE	OR	50 20	0			42	Р						
210246	SOFT SKILLS	TW	25 10	0			21	Р						
	FIRST SEM TOTAL = 547/750													
	4247 CHUDEKAR NEHA DILEEP									,71633679м				
										•	,		•	
SEM.:	1													
210241	DISCRETE STRUCTURE	PP	100 40	0 32		19	51	Р						
210242	DATA STRUCTURE & PROB SOLVING	PP	100 40	0 34	ļ	29	63	Р						
210242	DATA STRUCTURE & PROB SOLVING	PR	50 20	0			36	Р						
210243	DIGITAL ELEX. & LOGIC DESIGN	PP	100 40	0 34		21	55	Р						
210243							21							
	OPERATING SYSTEM & ADMIN		100 40		5	32	68							
			25 10				19							
			50 20			~=	20							
			100 40)	27	63							
			25 10				20							
			50 20				39							
210246	SOFT SKILLS FIRST SEM TOTAL = 475/750	IW	25 10	J			20	Р						
	·													
S14005	4248 DASGAONKAR SHUBHADA RAMI	DAS				RAJAS	HREI	E		,71633680E	,PICT	-	,S140054248	
SEM.:			100 4			20		_						
			100 40			20	50							
	DATA STRUCTURE & PROB SOLVING				5	30	58							
	DATA STRUCTURE & PROB SOLVING					22	22							
					•	4	47							
210243	DIGITAL ELEX. & LOGIC DESIGN				,	2 [20							
	OPERATING SYSTEM & ADMIN		100 40)	35	65							
			25 10 50 20				20							
210244	OPERATING SYSTEM & ADMIN				ļ	24	30 58							
			100 40 25 10		•	4	20							
210243	MICROPROCESSOR ARCHITECTURE	ı W	23 II	J			20	٢						

210245 MICROPROCESSOR ARCHITECTURE OR 50 20 21 P
210246 SOFT SKILLS TW 25 10 21 P
FIRST SEM TOTAL = 432/750

	: 25 JAN 2016 CENTRE	-	NE INS	STITUT	E OF	COMPU	TER T	ΓECHNOLO(GY, PU	INE.	1	PAGE N	0. 114	
	FIRST LINE : SEAT NO., NAM													
	ES: SUB.CODE, SUB.NAME, SUB													
	4249 DEDAGE SHRAVANI CHAN													
SEM.:	1													
210241	DISCRETE STRUCTURE	F	PP 100	40	26	29	55	Р						
210242	DATA STRUCTURE & PROB SOLV	ING F	PP 100	40	26	25	51	Р						
210242	DATA STRUCTURE & PROB SOLV	ING F	PR 50	20			30	Р						
210243	DIGITAL ELEX. & LOGIC DESI	GN F	PP 100	40	31	34	65	Р						
210243	DIGITAL ELEX. & LOGIC DESI	GN 7	TW 25	5 10			21	Р						
210244	OPERATING SYSTEM & ADMIN	F	PP 100	40	35	30	65	Р						
210244	OPERATING SYSTEM & ADMIN	٦	TW 25	5 10			20	Р						
210244	OPERATING SYSTEM & ADMIN	F	PR 50	20			29	Р						
210245	MICROPROCESSOR ARCHITECTUR	E F	PP 100	40	41	31	72	Р						
210245	MICROPROCESSOR ARCHITECTUR	E 7	TW 25	5 10			21	Р						
210245	MICROPROCESSOR ARCHITECTUR	E C	OR 50	20			33	Р						
210246	SOFT SKILLS	٦	TW 25	5 10			22	Р						
	FIRST SEM TOTAL = $484/750$													
S14005	4250 DEDANIYA KEVIN ASHVI	NBHA]				SHOBI	HNABE	ΞN	,	71527192L	,PICT		,S1400542	250
SEM.:		_	- 100		20	4.5	4.5	_						
	DISCRETE STRUCTURE		PP 100			15	45							
	DATA STRUCTURE & PROB SOLV				33	15	48							
	DATA STRUCTURE & PROB SOLV				2.5	21	20							
210243					25	21	46	·						
210243 210244	DIGITAL ELEX. & LOGIC DESI OPERATING SYSTEM & ADMIN		PP 100		22	27	18 59							
	OPERATING SYSTEM & ADMIN		TW 25		32	21	19							
	OPERATING SYSTEM & ADMIN		PR 50				32							
	MICROPROCESSOR ARCHITECTUR		PP 100		29	22	51							
210245	MICROPROCESSOR ARCHITECTUR		TW 25		23	22	18							
	MICROPROCESSOR ARCHITECTUR		OR 50				21							
	SOFT SKILLS		TW 25				18							
210210	FIRST SEM TOTAL = 395/750	•		, 10										
	4251 DEODHAR SHAUNAK HEMA									71527193〕				251
									ŕ		•		,	
SEM.:	1													
210241	DISCRETE STRUCTURE	F	PP 100	40	34	38	72	Р						
210242	DATA STRUCTURE & PROB SOLV	ING F	PP 100	40	33	29	62	Р						
210242	DATA STRUCTURE & PROB SOLV	ING F	PR 50	20			36	Р						
210243	DIGITAL ELEX. & LOGIC DESI	GN F	PP 100	40	34	25	59	Р						
210243	DIGITAL ELEX. & LOGIC DESI	GN 7	TW 25	5 10			20	Р						
210244	OPERATING SYSTEM & ADMIN	F	PP 100	40	32	24	56	Р						
210244	OPERATING SYSTEM & ADMIN	7	TW 25	5 10			19	Р						
210244	OPERATING SYSTEM & ADMIN	F	PR 50	20			35	Р						
210245	MICROPROCESSOR ARCHITECTUR	E F	PP 100	40	33	20	53	Р						
210245	MICROPROCESSOR ARCHITECTUR	Е 7	TW 25	5 10			20	Р						

210245 MICROPROCESSOR ARCHITECTURE OR 50 20 40 P
210246 SOFT SKILLS TW 25 10 21 P
FIRST SEM TOTAL = 493/750

	9 S.E.(2014 PAT.)(COMPUTER) : 25 JAN 2016 CENTRE: PI	UNE	INSTIT	UTE OF	COMPU	TER TECHNOL	.OGY, PUNE.	Р	PAGE NO. 115
	FIRST LINE : SEAT NO., NAME OF ES: SUB.CODE, SUB.NAME, SUB.TY			•		•	•		NO., COLLEGE, SEAT NO. P/F:PASS/FAIL, C:PREV.CARRY OVER
									c1400F42F2
514003	4252 DESAI RATHIN SAMEER				KOHI	.INI	,71527195E	,PICI	,5140034232
SEM.:	1								
210241	DISCRETE STRUCTURE	PP	100 40	37	33	70 P			
210242	DATA STRUCTURE & PROB SOLVING	PP	100 40	38	25	63 P			
210242	DATA STRUCTURE & PROB SOLVING	PR	50 20			25 P			
	DIGITAL ELEX. & LOGIC DESIGN				26	61 P			
210243	DIGITAL ELEX. & LOGIC DESIGN					19 P			
210244			100 40		26	65 P			
210244			25 10			20 P			
210244 210245			50 20 100 40		19	30 P 49 P			
210245			25 10		13	49 P 18 P			
210245			50 20			22 P			
			25 10			18 P			
	FIRST SEM TOTAL = 460/750								
S14005	4253 DESALE AKSHAY RAMCHANDRA	A			VIDY	Ά	,71527196C	,PICT	,s140054253
6514	4								
SEM.:		-	100 40	20	20	70 D			
	DISCRETE STRUCTURE DATA STRUCTURE & PROB SOLVING				39 32				
	DATA STRUCTURE & PROB SOLVING DATA STRUCTURE & PROB SOLVING				32	67 P 38 P			
	DIGITAL ELEX. & LOGIC DESIGN				25	65 P			
210243	DIGITAL ELEX. & LOGIC DESIGN				23	18 P			
210244			100 40		28	67 P			
210244			25 10			19 P			
210244	OPERATING SYSTEM & ADMIN	PR	50 20			38 P			
210245	MICROPROCESSOR ARCHITECTURE	PP	100 40	38	25	63 P			
210245	MICROPROCESSOR ARCHITECTURE	TW	25 10			19 P			
210245	MICROPROCESSOR ARCHITECTURE	OR	50 20			35 P			
210246	SOFT SKILLS	TW	25 10			20 P			
	FIRST SEM TOTAL = 527/750								
614005							715271074		
S14005	4254 DESHMUKH MOUSAMI RAJESH				PRAN	IOTI	,/152/19/M	, PICI	,S140054254
SEM.:	1								
	DISCRETE STRUCTURE	PP	100 40	33	43	76 P			
	DATA STRUCTURE & PROB SOLVING				20	41 P			
210242	DATA STRUCTURE & PROB SOLVING					25 P			
210243	DIGITAL ELEX. & LOGIC DESIGN	PP	100 40	26	17	43 P			
210243	DIGITAL ELEX. & LOGIC DESIGN	TW	25 10			18 P			
210244	OPERATING SYSTEM & ADMIN	PP	100 40	31	24	55 P			
210244	OPERATING SYSTEM & ADMIN	TW	25 10			20 P			
210244	OPERATING SYSTEM & ADMIN	PR	50 20			39 P			
210245	MICROPROCESSOR ARCHITECTURE	PP	100 40	39	26	65 P			
210245	MICROPROCESSOR ARCHITECTURE	TW	25 10			17 P			

210245 MICROPROCESSOR ARCHITECTURE OR 50 20 34 P
210246 SOFT SKILLS TW 25 10 20 P
FIRST SEM TOTAL = 453/750

	9 S.E.(2014 PAT.)(COMPUTER) : 25 JAN 2016 CENTRE: PI	JNE	INSTIT	UTE OF	COMPU	TER T	ECHNOLOGY, PL	JNE.	PAGE N	0. 116
	FIRST LINE : SEAT NO., NAME OF ES: SUB.CODE, SUB.NAME, SUB.TY			-		-		•	•	·
		-		-			,	•	•	
S14005	4255 DESHPANDE ASHWIN AJAY				ANUJA	А	,	71527200E	,PICT	, \$140054255
SEM.:	1									
210241	DISCRETE STRUCTURE	PP	100 40	37	34	71	Р			
210242	DATA STRUCTURE & PROB SOLVING	PP	100 40	45	31	76	Р			
210242	DATA STRUCTURE & PROB SOLVING	PR	50 20)		40	Р			
210243	DIGITAL ELEX. & LOGIC DESIGN	PP	100 40	38	30	68	Р			
210243	DIGITAL ELEX. & LOGIC DESIGN	TW	25 10)		20				
210244	OPERATING SYSTEM & ADMIN		100 40		35	74				
210244			25 10			19				
210244	OPERATING SYSTEM & ADMIN		50 20		2.1	38				
210245			100 40		31	72				
210245			25 10			20				
210245			50 20			39				
210240	SOFT SKILLS FIRST SEM TOTAL = 558/750	I W	25 10	•		21	P			
	FIRST SEM TOTAL - 336/730									
	4256 DESHPANDE PRANAV SHAILEI								,PICT	
SEM.:	1									
210241	DISCRETE STRUCTURE	PP	100 40	19	42	61	Р			
210242	DATA STRUCTURE & PROB SOLVING	PP	100 40	31	29	60	Р			
210242	DATA STRUCTURE & PROB SOLVING	PR	50 20	1		10	F			
210243	DIGITAL ELEX. & LOGIC DESIGN	PP	100 40	34	22	56				
210243	DIGITAL ELEX. & LOGIC DESIGN					20				
210244	OPERATING SYSTEM & ADMIN		100 40		38	76				
210244			25 10			19				
210244	OPERATING SYSTEM & ADMIN		50 20		20	32				
210245	MICROPROCESSOR ARCHITECTURE		100 40		30	61				
			25 10			20				
210245 210246	MICROPROCESSOR ARCHITECTURE SOFT SKILLS		50 20			38 23				
210240	FIRST SEM TOTAL =/750	I W	23 10			23	۲			
	·									
s14005	4257 DESHPANDE SAGAR SHIRISH				ANAGI	НА	,	71633683K	,PICT	,s140054257
SEM.:	1									
210241	DISCRETE STRUCTURE	PP	100 40	38	33	71	Р			
210242	DATA STRUCTURE & PROB SOLVING				25	68				
210242	DATA STRUCTURE & PROB SOLVING					42				
210243	DIGITAL ELEX. & LOGIC DESIGN				30	62				
210243	DIGITAL ELEX. & LOGIC DESIGN				24	21				
210244	OPERATING SYSTEM & ADMIN		100 40		31	75				
210244			25 10			20				
210244	OPERATING SYSTEM & ADMIN		50 20		2.2	40				
210245			100 40		23	60				
Z1UZ45	MICROPROCESSOR ARCHITECTURE	ΙW	25 10	1		20	۲			

210245 MICROPROCESSOR ARCHITECTURE OR 50 20 31 P
210246 SOFT SKILLS TW 25 10 22 P
FIRST SEM TOTAL = 532/750

	: 25 JAN 2016 CENTRE: PI	UNE INSTITU	TE OF	COMPU	TER TECHNOLO	DGY, PUNE.	F	PAGE NO. 117	
	FIRST LINE : SEAT NO., NAME OF ES: SUB.CODE, SUB.NAME, SUB.TY		-		•	•			
S14005	4258 DESHPANDE SHARDUL SHRIN:	IWAS		SHAR	MILA	,71527204н	,PICT	,S1400542	258
SEM.:	1								
		PP 100 40	41	42	83 P				
210242	DATA STRUCTURE & PROB SOLVING	PP 100 40	40	33	73 P				
210242	DATA STRUCTURE & PROB SOLVING	PR 50 20			43 P				
210243	DIGITAL ELEX. & LOGIC DESIGN	PP 100 40	38	34	72 P				
210243	DIGITAL ELEX. & LOGIC DESIGN	TW 25 10			19 P				
210244	OPERATING SYSTEM & ADMIN	PP 100 40	45	31	76 P				
210244	OPERATING SYSTEM & ADMIN	TW 25 10			20 P				
210244	OPERATING SYSTEM & ADMIN	PR 50 20			35 P				
210245	MICROPROCESSOR ARCHITECTURE	PP 100 40	45	28	73 P				
210245	MICROPROCESSOR ARCHITECTURE	TW 25 10			19 P				
210245	MICROPROCESSOR ARCHITECTURE	OR 50 20			41 P				
210246	SOFT SKILLS	TW 25 10			19 P				
	FIRST SEM TOTAL = 573/750								
S14005	4259 DESHPANDE SHUBHANKAR VII	VAY		MANJ	IRI	,71527205F	,PICT	,S1400542	259
CEM .	1								
SEM.:		PP 100 40	20	12	82 P				
	DATA STRUCTURE & PROB SOLVING			43 33	66 P				
	DATA STRUCTURE & PROB SOLVING DATA STRUCTURE & PROB SOLVING		33	33	38 P				
	DIGITAL ELEX. & LOGIC DESIGN		36	30	66 P				
	DIGITAL ELEX. & LOGIC DESIGN		30	30	19 P				
	OPERATING SYSTEM & ADMIN	PP 100 40	33	32	19 P 65 P				
		TW 25 10	,,,	32	22 P				
	OPERATING SYSTEM & ADMIN	PR 50 20			40 P				
		PP 100 40	33	27	40 P				
		TW 25 10	33	21	19 P				
		OR 50 20			43 P				
		TW 25 10			20 P				
210240	FIRST SEM TOTAL = 540/750	TW 23 10			20 1				
S14005	4260 DEVKATE AKSHAY AJINATH			CHHA	ΥA	,71527211L	,PICT	, \$1400542	260
CEM .	1								
SEM.:		DD 100 40	22	20	CO D				
				28	60 P				
	DATA STRUCTURE & PROB SOLVING		41	33	74 P				
	DATA STRUCTURE & PROB SOLVING		20	2.5	25 P				
	DIGITAL ELEX. & LOGIC DESIGN		30	25	55 P				
			3 F	26	20 P				
210244		PP 100 40	35	26	61 P				
		TW 25 10			20 P				
	OPERATING SYSTEM & ADMIN	PR 50 20	20	2.4	30 P				
		PP 100 40	28	24	52 P				
210245	MICROPROCESSOR ARCHITECTURE	TW 25 10			20 P				

210245 MICROPROCESSOR ARCHITECTURE OR 50 20 32 P
210246 SOFT SKILLS TW 25 10 21 P
FIRST SEM TOTAL = 470/750

	9 S.E.(2014 PAT.)(COMPUTER) : 25 JAN 2016 CENTRE: PI	UNE INSTIT	UTE OF	COMPL	JTER TECHNOLO	OGY, PUNE.	Р	AGE NO. 118			
	······································										
	FIRST LINE : SEAT NO., NAME OF		•		-	•		NO., COLLEGE, SEAT NO. P/F:PASS/FAIL, C:PREV.CARRY OVER			
	·	•	-		•		•				
s14005	4261 DHATRAK SWAPNIL VINAYAK			SHOE	вна	,71633684н	,PICT	,s140054261			
SEM.:	1										
210241	DISCRETE STRUCTURE	PP 100 40	25	15	40 P						
210242	DATA STRUCTURE & PROB SOLVING	PP 100 40	31	20	51 P						
210242	DATA STRUCTURE & PROB SOLVING	PR 50 20			5 F						
210243	DIGITAL ELEX. & LOGIC DESIGN	PP 100 40	26	15	41 P						
210243	DIGITAL ELEX. & LOGIC DESIGN	TW 25 10			20 P						
210244	OPERATING SYSTEM & ADMIN	PP 100 40	27	24	51 P						
210244	OPERATING SYSTEM & ADMIN	TW 25 10			20 P						
210244	OPERATING SYSTEM & ADMIN	PR 50 20			32 P						
210245	MICROPROCESSOR ARCHITECTURE	PP 100 40	29	24	53 P						
210245	MICROPROCESSOR ARCHITECTURE	TW 25 10			20 P						
		OR 50 20			32 P						
210246	SOFT SKILLS	TW 25 10			21 P						
	FIRST SEM TOTAL =/750										
	4262 DHONDKAR SUYASH JALANDAI							,s140054262			
SEM.:	1										
	DISCRETE STRUCTURE	PP 100 40		30	59 P						
210242	DATA STRUCTURE & PROB SOLVING	PP 100 40	32	21	53 P						
	DATA STRUCTURE & PROB SOLVING				35 P						
	DIGITAL ELEX. & LOGIC DESIGN			15							
210243	DIGITAL ELEX. & LOGIC DESIGN			2=	14 P						
	OPERATING SYSTEM & ADMIN	PP 100 40		27	50 P						
		TW 25 10			18 P						
	OPERATING SYSTEM & ADMIN	PR 50 20		2.5	33 P						
	MICROPROCESSOR ARCHITECTURE	PP 100 40		25	46 P						
		TW 25 10 OR 50 20			16 P						
	MICROPROCESSOR ARCHITECTURE SOFT SKILLS				23 P 16 P						
210240	FIRST SEM TOTAL = 404/750	TW 23 10			10 P						
S14005	4263 DHRUV SINGH NUNIA			SAVI	TA	,71527216М	,PICT	,s140054263			
SEM.:											
	DISCRETE STRUCTURE				55 P						
				06	F						
	DATA STRUCTURE & PROB SOLVING			1 -	6 F						
	DIGITAL ELEX. & LOGIC DESIGN			15	40 P						
210243	DIGITAL ELEX. & LOGIC DESIGN			ΛĒ	14 P						
210244	OPERATING SYSTEM & ADMIN	PP 100 40 TW 25 10		05	F 10 p						
	OPERATING SYSTEM & ADMIN OPERATING SYSTEM & ADMIN	PR 50 20			18 P 33 P						
210244		PP 100 40		03	ээ Р F						
		TW 25 10		03	г 17 Р						
210273	MICKOT ROCESSON ARCHITECTURE	144 23 10			±, 1						

210245 MICROPROCESSOR ARCHITECTURE OR 50 20 5 F
210246 SOFT SKILLS TW 25 10 15 P
FIRST SEM TOTAL = --/750

	9 S.E.(2014 PAT.)(COMPUTER) : 25 JAN 2016 CENTRE: PU	UNE	INSTITU	TE OF	COMPU	TER TE	CHNOLOGY, P	UNE.	PAGE N	0. 119
NOTE:	FIRST LINE : SEAT NO., NAME OF	F T	HE CANDI	DATE,	MOTH	ER, PE	RMANENT REG	. NO., PREVI	OUS SEAT NO.,	COLLEGE, SEAT NO.
OTHER LIN	ES: SUB.CODE, SUB.NAME, SUB.TY	PΕ,	MAX.MAR	KS, MI	N.PAS	S MARK	S, OE MARKS	, TH MARKS, C	BTAINED, P/F:P	ASS/FAIL, C:PREV.CARRY OVER
	4264									
S14005	4264 DIXIT HEMANT VIJAY				PRAG	YA		,/152/218H	,PICT	,5140034264
SEM.:	1									
210241	DISCRETE STRUCTURE	PP	100 40	35	39	74 P				
210242	DATA STRUCTURE & PROB SOLVING	PP	100 40	36	31	67 P				
210242	DATA STRUCTURE & PROB SOLVING	PR	50 20			40 P				
210243	DIGITAL ELEX. & LOGIC DESIGN	PP	100 40	38	22	60 P				
210243	DIGITAL ELEX. & LOGIC DESIGN	TW	25 10			16 P				
210244	OPERATING SYSTEM & ADMIN	PP	100 40	32	18	50 P				
210244			25 10			19 P				
210244	OPERATING SYSTEM & ADMIN		50 20			34 P				
210245			100 40	31	23	54 P				
210245			25 10			16 P				
210245			50 20			20 P				
210246		ΙW	25 10			17 P				
	FIRST SEM TOTAL = 467/750									
	4265 DOIJAD SHUBHAM SHAILESH								,PICT	
								,	,	,
SEM.:	1									
210241	DISCRETE STRUCTURE	PP	100 40	27	33	60 P				
210242	DATA STRUCTURE & PROB SOLVING	PP	100 40	31	32	63 P				
210242	DATA STRUCTURE & PROB SOLVING	PR	50 20			15 F				
210243	DIGITAL ELEX. & LOGIC DESIGN	PP	100 40	26	15	41 P				
210243	DIGITAL ELEX. & LOGIC DESIGN	TW	25 10			13 P				
210244	OPERATING SYSTEM & ADMIN	PP	100 40	25	15	40 P				
210244			25 10			18 P				
210244	OPERATING SYSTEM & ADMIN		50 20			35 P				
210245	MICROPROCESSOR ARCHITECTURE		100 40	13	09	F				
			25 10			16 P				
210245			50 20			10 F				
210246	SOFT SKILLS	ΙW	25 10			16 P				
	FIRST SEM TOTAL =/750									
\$14005	4266 DUMBARE SHUBHAM DIPAK	• • •							,PICT	
521003	TEGO BONDANCE ONOBINAN BENTAN					10.00		,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,	,. 20.	,51.003.200
SEM.:	1									
210241	DISCRETE STRUCTURE	PP	100 40	40	34	74 P				
210242	DATA STRUCTURE & PROB SOLVING	PP	100 40	23	08	F				
210242	DATA STRUCTURE & PROB SOLVING	PR	50 20			15 F				
210243	DIGITAL ELEX. & LOGIC DESIGN	PP	100 40	26	15	41 P				
210243	DIGITAL ELEX. & LOGIC DESIGN	TW	25 10			19 P				
210244	OPERATING SYSTEM & ADMIN	PP	100 40	15	11	F				
210244	OPERATING SYSTEM & ADMIN	TW	25 10			19 P				
210244	OPERATING SYSTEM & ADMIN	PR	50 20			36 P				
210245			100 40	29	07	F				
210245	MICROPROCESSOR ARCHITECTURE	TW	25 10			18 P				

210245 MICROPROCESSOR ARCHITECTURE OR 50 20 24 P
210246 SOFT SKILLS TW 25 10 20 P
FIRST SEM TOTAL = --/750

	9 S.E.(2014 PAT.)(COMPUTER) : 25 JAN 2016 CENTRE: PI	JNE	INSTITU	JTE OF	COMPL	JTER TECHNOL	OGY, PUNE.	F	PAGE NO. 120
	FIRST LINE : SEAT NO., NAME OF ES: SUB.CODE, SUB.NAME, SUB.TY			-		•	•		NO., COLLEGE, SEAT NO. P/F:PASS/FAIL, C:PREV.CARRY OVER
S14005	4267 GADEKAR MAYURESH RAJESH				REKH	ΗA	,71633685F	,PICT	, S140054267
SEM.:	1								
210241	DISCRETE STRUCTURE	PP	100 40	37	15	52 P			
210242	DATA STRUCTURE & PROB SOLVING	PP	100 40	22	19	41 P			
210242	DATA STRUCTURE & PROB SOLVING	PR	50 20			5 F			
210243	DIGITAL ELEX. & LOGIC DESIGN	PP	100 40	31	26	57 P			
210243	DIGITAL ELEX. & LOGIC DESIGN	TW	25 10			20 P			
210244	OPERATING SYSTEM & ADMIN		100 40	37	21	58 P			
210244			25 10			19 P			
210244	OPERATING SYSTEM & ADMIN		50 20	20	2.5	32 P			
210245 210245			100 40	28	25	53 P 20 P			
210245			25 10 50 20			20 P 22 P			
			25 10			22 P 20 P			
210240	FIRST SEM TOTAL =/750	1 44	23 10			20 1			
	·								
S14005	4268 GAIKWAD AISHWARYA AJIT				VAIS	SHALI	,71527223D	,PICT	,s140054268
SEM.:									
	DISCRETE STRUCTURE		100 40						
	DATA STRUCTURE & PROB SOLVING			18	36	54 P			
	DATA STRUCTURE & PROB SOLVING			20	16	25 P 54 P			
210243	DIGITAL ELEX. & LOGIC DESIGN DIGITAL ELEX. & LOGIC DESIGN			36	10	18 P			
210243	OPERATING SYSTEM & ADMIN		100 40	24	17	16 P 41 P			
210244			25 10	24	Τ,	19 P			
210244	OPERATING SYSTEM & ADMIN		50 20			35 P			
210245	MICROPROCESSOR ARCHITECTURE		100 40	34	19	53 P			
210245	MICROPROCESSOR ARCHITECTURE	TW	25 10			17 P			
210245	MICROPROCESSOR ARCHITECTURE	OR	50 20			28 P			
210246	SOFT SKILLS	TW	25 10			17 P			
	FIRST SEM TOTAL = 413/750								
S14005	4269 GANDHI RUTU RAHUL				MADH	HURI	,71527225L	,PICT	, s140054269
SEM.:	1								
	DISCRETE STRUCTURE	PР	100 40	34	37	71 P			
210242	DATA STRUCTURE & PROB SOLVING			36	21	57 P			
210242	DATA STRUCTURE & PROB SOLVING					36 P			
210243	DIGITAL ELEX. & LOGIC DESIGN			43	20	63 P			
210243	DIGITAL ELEX. & LOGIC DESIGN					20 P			
210244	OPERATING SYSTEM & ADMIN	PP	100 40	40	26	66 P			
210244	OPERATING SYSTEM & ADMIN	TW	25 10			20 P			
210244	OPERATING SYSTEM & ADMIN	PR	50 20			38 P			
210245	MICROPROCESSOR ARCHITECTURE	PP	100 40	35	31	66 P			
210245	MICROPROCESSOR ARCHITECTURE	TW	25 10			20 P			

210245 MICROPROCESSOR ARCHITECTURE OR 50 20 33 P
210246 SOFT SKILLS TW 25 10 20 P
FIRST SEM TOTAL = 510/750

	: 25 JAN 2016 CENTRE: P	UNE	INST	ΓΙΤUΤ	E OF	COMPU	TER	TECHNOLOG	Y, PU	NE.		PAGE N	0. 121	
	FIRST LINE : SEAT NO., NAME O													
	ES: SUB.CODE, SUB.NAME, SUB.TY													
	4270 GANGURDE NIDHI MILIND													
SEM.:	1													
210241	DISCRETE STRUCTURE	PP	100	40	33	24	57	Р						
210242	DATA STRUCTURE & PROB SOLVING	PP	100	40	27	22	49	Р						
210242	DATA STRUCTURE & PROB SOLVING	PR	50	20			22	Р						
210243	DIGITAL ELEX. & LOGIC DESIGN	PP	100	40	30	22	52	Р						
210243	DIGITAL ELEX. & LOGIC DESIGN	TW	25	10			20	Р						
210244	OPERATING SYSTEM & ADMIN	PP	100	40	27	20	47	Р						
210244	OPERATING SYSTEM & ADMIN	TW	25	10			19	Р						
210244	OPERATING SYSTEM & ADMIN	PR	50	20			32	Р						
210245	MICROPROCESSOR ARCHITECTURE	PP	100	40	35	29	64	Р						
210245	MICROPROCESSOR ARCHITECTURE	TW	25	10			19	Р						
210245	MICROPROCESSOR ARCHITECTURE	OR	50	20			12	F						
210246	SOFT SKILLS	TW	25	10			20	Р						
	FIRST SEM TOTAL = $/750$													
S14005	4271 GARGI SANDEEP VIPRA					BHAV	ANA		,	71527230G	,PICT		,S1400542	271
SEM.:	1													
210241	DISCRETE STRUCTURE	PP	100	40	40	44	84	Р						
	DATA STRUCTURE & PROB SOLVING				34	38	72							
210242	DATA STRUCTURE & PROB SOLVING	PR	50	20			25	Р						
210243	DIGITAL ELEX. & LOGIC DESIGN				32	22	54	•						
210243	DIGITAL ELEX. & LOGIC DESIGN						22							
210244	OPERATING SYSTEM & ADMIN		100		42	27	69							
	OPERATING SYSTEM & ADMIN		25				22							
	OPERATING SYSTEM & ADMIN		50				34							
			100		40	31	71							
			25				22							
			50				40							
210246		TW	25	10			22	Р						
	FIRST SEM TOTAL = 537/750													
S14005	4272 GATHANI SNEHA DHARMESH					JYOT	I		,	71527232C	,PICT		,S1400542	272
	1													
SEM.:		D D	100	40	2.0	47	0.5	Б						
	DISCRETE STRUCTURE		100			47	85							
	DATA STRUCTURE & PROB SOLVING				33	37	70							
	DATA STRUCTURE & PROB SOLVING				2.0	21	25							
	DIGITAL ELEX. & LOGIC DESIGN				38	31	69							
	DIGITAL ELEX. & LOGIC DESIGN				27	2.0	21							
	OPERATING SYSTEM & ADMIN		100		37	26	63							
	OPERATING SYSTEM & ADMIN		25				21							
	OPERATING SYSTEM & ADMIN		50		11	27	35							
	MICROPROCESSOR ARCHITECTURE		100		41	37	78							
210245	MICROPROCESSOR ARCHITECTURE	I W	25	ΤÜ			20	۲						

210245 MICROPROCESSOR ARCHITECTURE OR 50 20 43 P
210246 SOFT SKILLS TW 25 10 17 P
FIRST SEM TOTAL = 547/750

	9 S.E.(2014 PAT.)(COMPUTER) : 25 JAN 2016 CENTRE: PU	UNE	INST	ITUTE	E OF	COMPUT	TER ⁻	TECHNO	OLOGY, F	PUNE.		PAGE NO	o. 122	
	FIRST LINE : SEAT NO., NAME OF ES: SUB.CODE, SUB.NAME, SUB.TY													
	4273 GAURAV JADHAV									,71527233M				
SEM.:	1													
		PP	100	40	36	45	81	Р						
210242	DATA STRUCTURE & PROB SOLVING				41	37	78							
210242	DATA STRUCTURE & PROB SOLVING						40							
210243	DIGITAL ELEX. & LOGIC DESIGN				38	31	69							
210243	DIGITAL ELEX. & LOGIC DESIGN	TW	25	10			18	Р						
210244	OPERATING SYSTEM & ADMIN	PP	100	40	40	28	68	Р						
210244	OPERATING SYSTEM & ADMIN	TW	25	10			22	Р						
210244	OPERATING SYSTEM & ADMIN	PR	50	20			38	Р						
210245	MICROPROCESSOR ARCHITECTURE	PP	100	40	35	29	64	Р						
210245	MICROPROCESSOR ARCHITECTURE	TW	25	10			20	Р						
210245	MICROPROCESSOR ARCHITECTURE	OR	50	20			39	Р						
210246	SOFT SKILLS	TW	25	10			19	Р						
	FIRST SEM TOTAL = 556/750													
	4274 GEDAM RAJAT RAJENDRA		••••							,71527237D				
CEM .	1													
SEM.:	DISCRETE STRUCTURE	DD	100 4	40	3/1	33	67	D						
	DATA STRUCTURE & PROB SOLVING			-	21	21	42							
	DATA STRUCTURE & PROB SOLVING				21	21	10							
	DIGITAL ELEX. & LOGIC DESIGN				34	16	50							
210243	_				J.		15							
			100		21	19	40							
			25				16							
			50				30							
210245	MICROPROCESSOR ARCHITECTURE	PP	100	40	31	19	50	Р						
210245	MICROPROCESSOR ARCHITECTURE	TW	25	10			18	Р						
210245	MICROPROCESSOR ARCHITECTURE	OR	50	20			5	F						
210246	SOFT SKILLS	TW	25	10			21	Р						
	FIRST SEM TOTAL =/750													
	4275 - CUNITY OF CHILD CONTY													
S14005	4275 GHANEKAR OMKAR SADASHIV					MADHU	UKI			,71527238в	, PICI		,5140054275	
SEM.:	1													
		PP	100	40	36	29	65	Р						
	DATA STRUCTURE & PROB SOLVING				41	34	75							
	DATA STRUCTURE & PROB SOLVING						38							
210243	DIGITAL ELEX. & LOGIC DESIGN	PP	100	40	35	21	56	Р						
210243	DIGITAL ELEX. & LOGIC DESIGN	TW	25	10			20	Р						
210244	OPERATING SYSTEM & ADMIN	PP	100	40	34	20	54	Р						
210244	OPERATING SYSTEM & ADMIN	TW	25	10			19	Р						
210244	OPERATING SYSTEM & ADMIN	PR	50	20			33	Р						
210245	MICROPROCESSOR ARCHITECTURE	PP	100	40	33	32	65	Р						
210245	MICROPROCESSOR ARCHITECTURE	TW	25	10			18	Р						

210245 MICROPROCESSOR ARCHITECTURE OR 50 20 27 P
210246 SOFT SKILLS TW 25 10 17 P
FIRST SEM TOTAL = 487/750

	9 S.E.(2014 PAT.)(COMPUTER) : 25 JAN 2016 CENTRE: PI	JNE	INSTITU	JTE OF	COMPL	JTER TECHNOLO	OGY, PUNE.	Р	PAGE NO. 123
NOTE:	FIRST LINE : SEAT NO., NAME OF	F TH	HE CANDI	DATE,	MOTH	HER, PERMANE	NT REG. NO., PREV	IOUS SEAT	NO., COLLEGE, SEAT NO.
OTHER LIN	ES: SUB.CODE, SUB.NAME, SUB.TY	PΕ,	MAX.MAF	RKS, MI	IN.PAS	SS MARKS, OE	MARKS, TH MARKS,	OBTAINED,	P/F:PASS/FAIL, C:PREV.CARRY OVER
									6140054276
S14005	4276 GHANGARE KARTIK DINESH				VIJA	AYA	,/152/239L	,PICI	,5140034276
SEM.:	1								
210241	DISCRETE STRUCTURE	PP	100 40	30	37	67 P			
210242	DATA STRUCTURE & PROB SOLVING	PP	100 40	26	07	F			
210242	DATA STRUCTURE & PROB SOLVING	PR	50 20			5 F			
210243	DIGITAL ELEX. & LOGIC DESIGN	PP	100 40	27	15	42 P			
210243	DIGITAL ELEX. & LOGIC DESIGN	TW	25 10			17 P			
210244	OPERATING SYSTEM & ADMIN	PP	100 40	34	15	49 P			
210244			25 10			17 P			
210244	OPERATING SYSTEM & ADMIN		50 20			40 P			
210245			100 40	23	17	40 P			
210245			25 10			17 P			
			50 20			11 F			
210246		IW	25 10			15 P			
	FIRST SEM TOTAL =/750								
	4277 GODBOLE SHREYAS KEDAR					HURI			,S140054277
SEM.:	1								
210241	DISCRETE STRUCTURE	PP	100 40	29	24	53 P			
210242	DATA STRUCTURE & PROB SOLVING	PP	100 40	35	29	64 P			
210242	DATA STRUCTURE & PROB SOLVING	PR	50 20			25 P			
210243	DIGITAL ELEX. & LOGIC DESIGN	PP	100 40	36	23	59 P			
210243	DIGITAL ELEX. & LOGIC DESIGN	TW	25 10			20 P			
210244	OPERATING SYSTEM & ADMIN		100 40	41	31	72 P			
210244	OPERATING SYSTEM & ADMIN		25 10			20 P			
210244	OPERATING SYSTEM & ADMIN		50 20	24	a =	23 P			
210245	MICROPROCESSOR ARCHITECTURE		100 40	3 L	27	58 P			
210245			25 10			19 P			
210245			50 20			38 P			
210246	SOFT SKILLS FIRST SEM TOTAL = 473/750	I W	25 10			22 P			
	•								
s14005	4278 GODSE NEHA ATUL					PALI	,71527241B		
							, , _ , _ , _ , _ ,	,	,
SEM.:	1								
210241	DISCRETE STRUCTURE	PP	100 40	44	45	89 P			
210242	DATA STRUCTURE & PROB SOLVING	PP	100 40	40	33	73 P			
210242	DATA STRUCTURE & PROB SOLVING	PR	50 20			43 P			
210243	DIGITAL ELEX. & LOGIC DESIGN	PP	100 40	40	24	64 P			
210243	DIGITAL ELEX. & LOGIC DESIGN	TW	25 10			22 P			
210244	OPERATING SYSTEM & ADMIN	PP	100 40	43	31	74 P			
210244			25 10			23 P			
210244	OPERATING SYSTEM & ADMIN		50 20			35 P			
210245			100 40	45	32	77 P			
210245	MICROPROCESSOR ARCHITECTURE	TW	25 10			20 P			

210245 MICROPROCESSOR ARCHITECTURE OR 50 20 38 P
210246 SOFT SKILLS TW 25 10 20 P
FIRST SEM TOTAL = 578/750

	9 S.E.(2014 PAT.)(COMPUTER) : 25 JAN 2016 CENTRE: PU	UNE	INSTITU	JTE OF	COMP	UTER TECHNOLO	GY, PUNE.	Р	AGE NO. 124
	FIRST LINE : SEAT NO., NAME OF			-		-	•		NO., COLLEGE, SEAT NO. P/F:PASS/FAIL, C:PREV.CARRY OVER
	,	,		•		•	,	•	
	4279 GOGATE ADVAIT SUHAS						,71527242L		
SEM.:	1								
210241	DISCRETE STRUCTURE	PP	100 40	30	38	68 P			
210242	DATA STRUCTURE & PROB SOLVING	PP	100 40	41	15	56 P			
210242	DATA STRUCTURE & PROB SOLVING	PR	50 20			22 P			
210243	DIGITAL ELEX. & LOGIC DESIGN	PP	100 40	25	15	40 P			
210243	DIGITAL ELEX. & LOGIC DESIGN	TW	25 10			21 P			
210244	OPERATING SYSTEM & ADMIN		100 40	34	19	53 P			
210244			25 10			22 P			
210244	OPERATING SYSTEM & ADMIN		50 20	2.0	20	32 P			
210245			100 40	28	20	48 P			
210245			25 10			21 P			
210245			50 20 25 10			21 P 21 P			
210240	SOFT SKILLS FIRST SEM TOTAL = 425/750	I VV	23 10			21 P			
	·								
	4280 GOPALGHARE PRIYANKA PRAI								,5140054280
SEM.:	1								
	DISCRETE STRUCTURE		100 40						
	DATA STRUCTURE & PROB SOLVING			40	38	78 P			
	DATA STRUCTURE & PROB SOLVING					41 P			
	DIGITAL ELEX. & LOGIC DESIGN			37	26				
210243	DIGITAL ELEX. & LOGIC DESIGN			42	21	21 P			
210244	OPERATING SYSTEM & ADMIN		100 40	42	31	73 P 20 P			
210244 210244	OPERATING SYSTEM & ADMIN OPERATING SYSTEM & ADMIN		25 10 50 20			20 P 43 P			
210244	MICROPROCESSOR ARCHITECTURE		100 40	39	35	74 P			
			25 10	33	33	20 P			
210245			50 20			42 P			
			25 10			20 P			
	FIRST SEM TOTAL = 561/750								
S14005	4281 GORE RAHUL RAVI				PRA	GATI	,71527243〕	,PICT	,S140054281
SEM.:			100 10			-0			
	DISCRETE STRUCTURE		100 40			78 P			
210242	DATA STRUCTURE & PROB SOLVING			37	20	57 P			
210242	DATA STRUCTURE & PROB SOLVING			22	10	38 P			
210243	DIGITAL ELEX. & LOGIC DESIGN			32	18	50 P			
210243 210244	DIGITAL ELEX. & LOGIC DESIGN OPERATING SYSTEM & ADMIN		100 40	32	19	10 P 51 P			
210244			25 10	32	ТЭ	31 P 19 P			
210244	OPERATING SYSTEM & ADMIN		50 20			30 P			
210244			100 40	33	22	55 P			
			25 10	,,,		16 P			
		. **	_3 _0						

210245 MICROPROCESSOR ARCHITECTURE OR 50 20 11 F 210246 SOFT SKILLS TW 25 10 19 P FIRST SEM TOTAL = --/750

	9 S.E.(2014 PAT.)(COMPUTER) : 25 JAN 2016 CENTRE: PU	UNE	INST	ITUT	E OF	COMPUT	ΓER ⁻	TECHNOL	.OGY, P	UNE.		PAGE N	0. 125	
NOTE:	FIRST LINE : SEAT NO., NAME OF	F TI	HE CA	NDID	ATE,	MOTHE	ER, I	PERMANE	NT REG	. NO., PREV	/IOUS SEA	T NO.,	COLLEGE,	SEAT NO.
OTHER LIN	ES: SUB.CODE, SUB.NAME, SUB.TY	PΕ,	MAX.	MARK	S, MI	N.PASS	S MAI	RKS, OE	MARKS	, TH MARKS,	OBTAINED	, P/F:P/	ASS/FAIL, C	::PREV.CARRY OVER
S14005	4282 GOTE ISHAN PRASANNA					ROHIN	ΝI			,71527244G	,PICT		,S14005428	2
SEM.:	1													
	DISCRETE STRUCTURE	DD	100	40	32	30	71	D						
	DATA STRUCTURE & PROB SOLVING				34	29	63							
	DATA STRUCTURE & PROB SOLVING				J 1	23	40							
	DIGITAL ELEX. & LOGIC DESIGN				35	23	58							
210243	DIGITAL ELEX. & LOGIC DESIGN				33	23	16							
210244	OPERATING SYSTEM & ADMIN		100		38	28	66							
210244			25		30	20	18							
210244	OPERATING SYSTEM & ADMIN		50				39							
210245			100		34	28	62							
210245			25		•		16							
			50				26							
			25				18							
	FIRST SEM TOTAL = 493/750													
S14005	4283 GUJARE SURABHI NITIN					SANG]	ΙΤΑ			,71527245E	,PICT		,S14005428	3
SEM.:	1													
210241	DISCRETE STRUCTURE	PP	100	40	38	41	79	Р						
210242	DATA STRUCTURE & PROB SOLVING	PP	100	40	35	27	62	Р						
210242	DATA STRUCTURE & PROB SOLVING	PR	50	20			22	Р						
210243	DIGITAL ELEX. & LOGIC DESIGN	PP	100	40	41	26	67	Р						
210243	DIGITAL ELEX. & LOGIC DESIGN	TW	25	10			21	Р						
210244	OPERATING SYSTEM & ADMIN	PP	100	40	38	18	56	Р						
210244	OPERATING SYSTEM & ADMIN	TW	25	10			20	Р						
210244	OPERATING SYSTEM & ADMIN	PR	50	20			30	Р						
210245	MICROPROCESSOR ARCHITECTURE	PP	100	40	41	31	72	Р						
210245	MICROPROCESSOR ARCHITECTURE	TW	25	10			19	Р						
210245			50				35							
210246	SOFT SKILLS	TW	25	10			20	Р						
	FIRST SEM TOTAL = 503/750													
-14005				• • • •										
S14005	4284 GUPTA ASMITA JITENDRA					JY0T]	Ι			,71527246C	,PICT		,514005428	4
65.4 -	1													
SEM.:		-	100	40	27	40	77	5						
			100				77							
	DATA STRUCTURE & PROB SOLVING				35	26	61							
	DATA STRUCTURE & PROB SOLVING				20	1.0	20							
210243 210243	DIGITAL ELEX. & LOGIC DESIGN DIGITAL ELEX. & LOGIC DESIGN				38	τo	54 20							
210243	OPERATING SYSTEM & ADMIN		100		39	24	63							
			25		צנ	۷4	21							
	OPERATING SYSTEM & ADMIN		50				39							
210244			100		29	29	58							
			25		23	<i>L J</i>	20							
210243	MICKOT ROCESSON ARCHITECTURE	1 44	۷.	10			20	•						

210245 MICROPROCESSOR ARCHITECTURE OR 50 20 21 P
210246 SOFT SKILLS TW 25 10 20 P
FIRST SEM TOTAL = 474/750

	9 S.E.(2014 PAT.)(COMPUTER) : 25 JAN 2016 CENTRE: PI	JNE	INSTITU	TE OF	COMPU	TER TECHNOLO	OGY, PUNE.	Р	AGE NO. 126
	FIRST LINE : SEAT NO., NAME OF			-		-	•		
	·	-		-		•		-	P/F:PASS/FAIL, C:PREV.CARRY OVER
	4286 HARIDAS RUTUJA SANJEEV						,71527250м		,s140054286
SEM.:	1								
210241	DISCRETE STRUCTURE	PP	100 40	20	10	F			
210242	DATA STRUCTURE & PROB SOLVING	PP	100 40	18	07	F			
210242	DATA STRUCTURE & PROB SOLVING	PR	50 20			6 F			
210243	DIGITAL ELEX. & LOGIC DESIGN	PP	100 40	21	07	F			
210243	DIGITAL ELEX. & LOGIC DESIGN	TW	25 10			19 P			
210244	OPERATING SYSTEM & ADMIN		100 40	23	17	40 P			
210244			25 10			19 P			
210244	OPERATING SYSTEM & ADMIN		50 20			31 P			
210245			100 40	30	06	F			
210245			25 10			19 P			
210245			50 20			12 F			
210246		I W	25 10			18 P			
	FIRST SEM TOTAL =/750								
	4287 HIWALE AKSHAY RAJARAM					TA			,\$140054287
SEM.:	1								
210241	DISCRETE STRUCTURE	PP	100 40	34	46	80 P			
210242	DATA STRUCTURE & PROB SOLVING	PP	100 40	34	38	72 P			
	DATA STRUCTURE & PROB SOLVING					43 P			
210243	DIGITAL ELEX. & LOGIC DESIGN			37	27	64 P			
210243	DIGITAL ELEX. & LOGIC DESIGN					14 P			
210244	OPERATING SYSTEM & ADMIN		100 40	46	29	75 P			
210244			25 10			22 P			
210244	OPERATING SYSTEM & ADMIN		50 20	43	22	40 P			
210245	MICROPROCESSOR ARCHITECTURE		100 40	42	32	74 P			
			25 10			18 P			
210245			50 20			35 P			
210246	SOFT SKILLS FIRST SEM TOTAL = 556/750	I W	23 10			19 P			
	•								
s14005	4288 HUMANE ANIRUDDHA SANJAY				SAND	HYA	,71527253F	,PICT	,S140054288
SEM.:									
	DISCRETE STRUCTURE			37		72 P			
210242	DATA STRUCTURE & PROB SOLVING			41	31	72 P			
210242	DATA STRUCTURE & PROB SOLVING					38 P			
210243	DIGITAL ELEX. & LOGIC DESIGN			33	28	61 P			
210243	DIGITAL ELEX. & LOGIC DESIGN			2 =	2.5	19 P			
210244	OPERATING SYSTEM & ADMIN		100 40	37	25	62 P			
210244			25 10			18 P			
210244	OPERATING SYSTEM & ADMIN		50 20	A A	2.5	32 P			
210245			100 40	44	35	79 P			
Z1UZ45	MICROPROCESSOR ARCHITECTURE	ΙW	25 10			17 P			

210245 MICROPROCESSOR ARCHITECTURE OR 50 20 39 P
210246 SOFT SKILLS TW 25 10 17 P
FIRST SEM TOTAL = 526/750

	9	UNE	INST	ITUT	E OF	COMPU ⁻	TER ⁻	TECHNO	OLOGY, F	PUNE.		PAGE NO	o. 127	
									•					
NOTE:	FIRST LINE : SEAT NO., NAME OF	F TH	HE CA	NDID	ATE,	мотні	ER,	PERMAN	NENT REG	. NO., PRE\	/IOUS SEA	T NO.,	COLLEGE, SE	EAT NO.
OTHER LIN	ES: SUB.CODE, SUB.NAME, SUB.TYI	PΕ,	MAX.	MARK	S, MI	N.PAS	S MA	RKS, C	OE MARKS	, TH MARKS,	OBTAINED	, P/F:P/	ASS/FAIL, C:PR	REV.CARRY OVER
\$14005	4289 INGALE KHUSHAL DINKAR					SUNI	TA			,/152/254D	, PICT		,S140054289	
SEM.:	1													
210241	DISCRETE STRUCTURE	PP	100	40	29	32	61	Р						
210242	DATA STRUCTURE & PROB SOLVING	PP	100	40	30	20	50	Р						
210242	DATA STRUCTURE & PROB SOLVING	PR	50	20			5	F						
210243	DIGITAL ELEX. & LOGIC DESIGN	PP	100	40	23	23	46	Р						
210243	DIGITAL ELEX. & LOGIC DESIGN	TW	25	10			12	Р						
210244	OPERATING SYSTEM & ADMIN	PP	100	40	33	18	51	Р						
210244	OPERATING SYSTEM & ADMIN	TW	25	10			12	Р						
210244	OPERATING SYSTEM & ADMIN	PR	50	20			29	Р						
210245	MICROPROCESSOR ARCHITECTURE	PP	100	40	30	25	55	Р						
210245	MICROPROCESSOR ARCHITECTURE	TW	25	10			10	Р						
210245	MICROPROCESSOR ARCHITECTURE	OR	50	20			20	Р						
210246	SOFT SKILLS	TW	25	10			15	Р						
	FIRST SEM TOTAL =/750													
	4290 ISAVE AISHWARYA BHAGWAN									,71527256L				
314003	4290 ISAVE AISHWARTA BHAGWAN					SUKE	ΝΠΑ			,71327230L	, PICI		,3140034290	
SEM.:	1													
		PP	100	40	34	41	75	Р						
	DATA STRUCTURE & PROB SOLVING						76							
	DATA STRUCTURE & PROB SOLVING						23							
	DIGITAL ELEX. & LOGIC DESIGN				44	29	73	Р						
210243	DIGITAL ELEX. & LOGIC DESIGN	TW	25	10			20	Р						
210244	OPERATING SYSTEM & ADMIN	PP	100	40	30	24	54	Р						
210244	OPERATING SYSTEM & ADMIN	TW	25	10			21	Р						
210244	OPERATING SYSTEM & ADMIN	PR	50	20			34	Р						
210245	MICROPROCESSOR ARCHITECTURE	PP	100	40	41	30	71	Р						
210245	MICROPROCESSOR ARCHITECTURE	TW	25	10			18	Р						
210245	MICROPROCESSOR ARCHITECTURE	OR	50	20			38	Р						
210246	SOFT SKILLS	TW	25	10			19	Р						
	FIRST SEM TOTAL = $522/750$													
S14005	4291 JADHAV KAUSTUBH ULHAS					MANG	ALA			,71633689〕	,PICT		,S140054291	
O.E.W	1													
SEM.:		-	100	40	20	1.0	4 5	_						
			100				45 74							
	DATA STRUCTURE & PROB SOLVING				40	34	74							
	DATA STRUCTURE & PROB SOLVING				21	22	44 54							
210243 210243	DIGITAL ELEX. & LOGIC DESIGN DIGITAL ELEX. & LOGIC DESIGN				31	23	54 19							
210243	OPERATING SYSTEM & ADMIN		100		34	15	19 49							
			25		J4	τ)	49 21							
	OPERATING SYSTEM & ADMIN		50				40							
210244			100		33	25	58							
			25		55	_ ,	20							
			_ ,				_0	•						

210245 MICROPROCESSOR ARCHITECTURE OR 50 20 35 P
210246 SOFT SKILLS TW 25 10 20 P
FIRST SEM TOTAL = 479/750

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	: 25 JAN 2016 CENTRE: P	UNE	INSTITU	TE OF	COMPUT	ER T	TECHNOLOG	Y, PU	NE.	1	PAGE N	0. 128	
	FIRST LINE : SEAT NO., NAME O			-		•			-		•	-	
	ES: SUB.CODE, SUB.NAME, SUB.TY												
	4292 JADHAV MALVIKA RANJITSI												
SEM.:	1												
	DISCRETE STRUCTURE	PP	100 40	35	29	64	Р						
	DATA STRUCTURE & PROB SOLVING			30		50							
	DATA STRUCTURE & PROB SOLVING					38							
210243	DIGITAL ELEX. & LOGIC DESIGN			34	19	53							
210243	DIGITAL ELEX. & LOGIC DESIGN	TW	25 10			16	Р						
210244	OPERATING SYSTEM & ADMIN	PP	100 40	27	18	45	Р						
210244	OPERATING SYSTEM & ADMIN	TW	25 10			19	Р						
210244	OPERATING SYSTEM & ADMIN	PR	50 20			29	P						
210245	MICROPROCESSOR ARCHITECTURE	PP	100 40	32	25	57	P						
210245	MICROPROCESSOR ARCHITECTURE	TW	25 10			17	Р						
210245	MICROPROCESSOR ARCHITECTURE	OR	50 20			39	Р						
210246	SOFT SKILLS	TW	25 10			19	P						
	FIRST SEM TOTAL = $446/750$												
S14005	4293 JADHAV SANKET SANTOSHRA	0			LEENA	١.		,	71527261G	,PICT		,S1400542	293
SEM.:			100 10										
	DISCRETE STRUCTURE			37		76							
	DATA STRUCTURE & PROB SOLVING			32	21	53							
210242	DATA STRUCTURE & PROB SOLVING			20	0.7	5							
	DIGITAL ELEX. & LOGIC DESIGN			30	07	10	•						
210243 210244	DIGITAL ELEX. & LOGIC DESIGN OPERATING SYSTEM & ADMIN		100 40	25	1 5	18 40							
210244	OPERATING SYSTEM & ADMIN		25 10	23	13	20							
210244	OPERATING SYSTEM & ADMIN		50 20			35							
210244			100 40	30	15	45							
210245	MICROPROCESSOR ARCHITECTURE		25 10	30	13	19							
			50 20			22							
			25 10			18							
2202.0	FIRST SEM TOTAL =/750		23 20				•						
	4294 JADHAV SAURABH KAILAS				BEBI				71527262E				294
SEM.:	1												
210241	DISCRETE STRUCTURE	PP	100 40	26	15	41	Р						
210242	DATA STRUCTURE & PROB SOLVING	PP	100 40	27	15	42	Р						
210242	DATA STRUCTURE & PROB SOLVING	PR	50 20			38	Р						
210243	DIGITAL ELEX. & LOGIC DESIGN	PP	100 40	19	06		F						
210243	DIGITAL ELEX. & LOGIC DESIGN	TW	25 10			18	Р						
210244	OPERATING SYSTEM & ADMIN	PP	100 40	15	25	40	P						
210244	OPERATING SYSTEM & ADMIN	TW	25 10			19	P						
210244	OPERATING SYSTEM & ADMIN	PR	50 20			34	P						
210245	MICROPROCESSOR ARCHITECTURE	PP	100 40	21	19	40	P						
210245	MICROPROCESSOR ARCHITECTURE	TW	25 10			18	Р						

210245 MICROPROCESSOR ARCHITECTURE OR 50 20 11 F 210246 SOFT SKILLS TW 25 10 19 P FIRST SEM TOTAL = --/750

	9 S.E.(2014 PAT.)(COMPUTER) : 25 JAN 2016 CENTRE: PU	UNE	INSTIT	UTE OF	- COM	PUTER	TECHNO	DLOGY, P	PUNE.		PAGE NO	0. 129	
NOTE:	FIRST LINE : SEAT NO., NAME OF	F TH	E CAND	DIDATE,	, MO	THER,	PERMAN	NENT REG	. NO., PREV	'IOUS SEAT	NO.,	COLLEGE, SE	AT NO.
OTHER LIN	ES: SUB.CODE, SUB.NAME, SUB.TY	PΕ,	MAX.MA	ARKS, M	IIN.P	ASS MA	RKS, C	DE MARKS	, TH MARKS,	OBTAINED,	P/F:P/	ASS/FAIL, C:PR	EV.CARRY OVER
S14005	4295 JADHAV TEJAL RAVINDRA				KI	RAN			,71527264м	,PICT		,s140054295	
SEM.:	1												
	DISCRETE STRUCTURE	PP	100 40) 26	20	46	Б Р						
	DATA STRUCTURE & PROB SOLVING						B P						
	DATA STRUCTURE & PROB SOLVING						P						
210243	DIGITAL ELEX. & LOGIC DESIGN	PP	100 40	28	18	46	6 P						
210243	DIGITAL ELEX. & LOGIC DESIGN	TW	25 10)		18	ВР						
210244	OPERATING SYSTEM & ADMIN	PP	100 40	32	19	51	. P						
210244	OPERATING SYSTEM & ADMIN	TW	25 10)		22	? P						
210244	OPERATING SYSTEM & ADMIN	PR	50 20)		36	5 P						
210245	MICROPROCESSOR ARCHITECTURE	PP	100 40	35	22	57	' P						
210245	MICROPROCESSOR ARCHITECTURE	TW	25 10)		20) P						
210245	MICROPROCESSOR ARCHITECTURE	OR	50 20)		41	. P						
210246	SOFT SKILLS	TW	25 10)		18	ВР						
	FIRST SEM TOTAL = $430/750$												
	4206												
514005	4296 JADHAV VINOD NARAYAN				Su	NTIA			,71527265К	, PICI		,5140054296	
SEM.:	1												
	DISCRETE STRUCTURE	DD	100 40) 31	16	77	' P						
	DATA STRUCTURE & PROB SOLVING						Р						
	DATA STRUCTURE & PROB SOLVING				30		- ') P						
	DIGITAL ELEX. & LOGIC DESIGN				22	_	, . 5 P						
210243	_						3 P						
	OPERATING SYSTEM & ADMIN		100 40		21	_	B P						
			25 10) P						
210244	OPERATING SYSTEM & ADMIN	PR	50 20)		30) P						
210245	MICROPROCESSOR ARCHITECTURE	PP	100 40	30	21	51	. P						
210245	MICROPROCESSOR ARCHITECTURE	TW	25 10)		20) P						
210245	MICROPROCESSOR ARCHITECTURE	OR	50 20)		37	' P						
210246	SOFT SKILLS	TW	25 10)		21	. P						
	FIRST SEM TOTAL = 483/750												
S14005	4297 JAGDALE PRANJALI BALASO				JA	YASHRE	E		,71527266н	,PICT		,S140054297	
SEM.:													
	DISCRETE STRUCTURE				42) P						
	DATA STRUCTURE & PROB SOLVING				31		B P						
	DATA STRUCTURE & PROB SOLVING						P						
	DIGITAL ELEX. & LOGIC DESIGN				29) P						
210243	DIGITAL ELEX. & LOGIC DESIGN				2.0		? P						
210244	OPERATING SYSTEM & ADMIN		100 40		26		l P						
			25 10				? P						
	OPERATING SYSTEM & ADMIN		50 20		27) Р						
210245			100 40		27		' P						
210243	MICROPROCESSOR ARCHITECTURE	ı W	25 10	,		19) P						

210245 MICROPROCESSOR ARCHITECTURE OR 50 20 35 P
210246 SOFT SKILLS TW 25 10 20 P
FIRST SEM TOTAL = 484/750

	9 S.E.(2014 PAT.)(COMPUTER) : 25 JAN 2016 CENTRE: PU	UNE	INSTIT	UTE OF	COMPU	TER TECHN	IOLOGY, PU	NE.	1	PAGE NO. 130
	FIRST LINE : SEAT NO., NAME OF			-		•		-		NO., COLLEGE, SEAT NO. P/F:PASS/FAIL, C:PREV.CARRY OVER
	·	-		-		•	·	-	•	
	4298 JAIL SIDHARTH ABHAY									, \$140054298
SEM.:	1									
210241	DISCRETE STRUCTURE	PP	100 40	41	30	71 P				
210242	DATA STRUCTURE & PROB SOLVING	PP	100 40	36	20	56 P				
210242	DATA STRUCTURE & PROB SOLVING	PR	50 20)		40 P				
210243	DIGITAL ELEX. & LOGIC DESIGN	PP	100 40	34	20	54 P				
210243	DIGITAL ELEX. & LOGIC DESIGN	TW	25 10)		23 P				
210244			100 40		18	59 P				
210244			25 10			20 P				
210244			50 20			45 P				
210245			100 40		18	57 P				
210245			25 10			20 P				
			50 20			46 P				
210246		I W	25 10)		20 P				
	FIRST SEM TOTAL = 511/750									
	4299 JAIN RAVI SURESHCHANDRA					ALADEVI				,s140054299
SEM.:										
	DISCRETE STRUCTURE									
	DATA STRUCTURE & PROB SOLVING				32	66 P				
	DATA STRUCTURE & PROB SOLVING				1.0	20 P				
	DIGITAL ELEX. & LOGIC DESIGN DIGITAL ELEX. & LOGIC DESIGN				10	52 P				
210243 210244	OPERATING SYSTEM & ADMIN				28	20 P 66 P				
210244			25 10		20	19 P				
210244			50 20			32 P				
210244			100 40		29	65 P				
			25 10		23	19 P				
			50 20			12 F				
	SOFT SKILLS					20 P				
	FIRST SEM TOTAL =/750									
S14005	4300 JAIN RUSHABHA RAJENDRA				KAMAI	LA	,	71527270F	,PICT	,S140054300
SEM.:										
	DISCRETE STRUCTURE				41	77 P				
	DATA STRUCTURE & PROB SOLVING				23	61 P				
	DATA STRUCTURE & PROB SOLVING				17	40 P				
	DIGITAL ELEX. & LOGIC DESIGN				17	46 P				
	DIGITAL ELEX. & LOGIC DESIGN				10	20 P				
210244	OPERATING SYSTEM & ADMIN				19	56 P				
210244			25 10			22 P				
210244			50 20		26	30 P 56 P				
210245			100 40 25 10		20	36 Р 20 Р				
Z1UZ43	MITCHOLUGESON ANCHITECTURE	1 VV	23 IL	,		20 P				

210245 MICROPROCESSOR ARCHITECTURE OR 50 20 41 P
210246 SOFT SKILLS TW 25 10 20 P
FIRST SEM TOTAL = 489/750

	: 25 JAN 2016 CENTRE: P	UNE	INSTIT	TUTE OF	COMPU	TER TEC	CHNOLOGY	/, PUNE.			PAGE N	0. 131	
	FIRST LINE : SEAT NO., NAME O												
	ES: SUB.CODE, SUB.NAME, SUB.TY			•		•			•		•	-	
S14005	4301 JAIN SARTHAK PRAVEEN KU	MAR			SEEM	A		,715	27271D	,PICT		,S1400543	301
SEM.:	1												
	DISCRETE STRUCTURE	PP	100 40	31	34	65 P							
210242	DATA STRUCTURE & PROB SOLVING				32	67 P							
210242	DATA STRUCTURE & PROB SOLVING					40 P							
210243	DIGITAL ELEX. & LOGIC DESIGN	PP	100 40) 29	24	53 P							
210243	DIGITAL ELEX. & LOGIC DESIGN	TW	25 10)		18 P							
210244	OPERATING SYSTEM & ADMIN	PP	100 40	31	24	55 P							
210244	OPERATING SYSTEM & ADMIN	TW	25 10)		19 P							
210244	OPERATING SYSTEM & ADMIN	PR	50 20)		38 P							
210245	MICROPROCESSOR ARCHITECTURE	PP	100 40	35	24	59 P							
210245	MICROPROCESSOR ARCHITECTURE	TW	25 10)		17 P							
210245	MICROPROCESSOR ARCHITECTURE	OR	50 20)		20 P							
210246	SOFT SKILLS	TW	25 10)		19 P							
	FIRST SEM TOTAL = $470/750$												
S14005	4302 JOPULE PRIYANKA ARUN				SUNA	NDA		,714	00894L	,PICT		,S1400543	302
SEM.:													
	DISCRETE STRUCTURE		100 40		00	F							
	DATA STRUCTURE & PROB SOLVING				13	F							
	DATA STRUCTURE & PROB SOLVING				10	10 F							
	DIGITAL ELEX. & LOGIC DESIGN				10	F							
210243	DIGITAL ELEX. & LOGIC DESIGN OPERATING SYSTEM & ADMIN		100 40		18	10 P 40 P							
	OPERATING SYSTEM & ADMIN		25 10		10	40 P							
210244	OPERATING SYSTEM & ADMIN		50 20			8 F							
210244			100 40		06	F							
			25 10		00	13 P							
210245			50 20			11 F							
210246			25 10			18 P							
2202.0	FIRST SEM TOTAL =/750		23 20			10 .							
S14005	4303 KADAM SAINATH HANMANTH				PRAY	AG		,715	27280c	,PICT		,S1400543	303
SEM.:	1												
210241	DISCRETE STRUCTURE	PP	100 40) AA	AA	F							
210242	DATA STRUCTURE & PROB SOLVING	PP	100 40) AA	AA	F							
210242	DATA STRUCTURE & PROB SOLVING	PR	50 20)		AA F							
210243	DIGITAL ELEX. & LOGIC DESIGN				AA	F							
210243	DIGITAL ELEX. & LOGIC DESIGN					10 P							
210244	OPERATING SYSTEM & ADMIN		100 40		AA	F							
210244	OPERATING SYSTEM & ADMIN		25 10			16 P							
210244	OPERATING SYSTEM & ADMIN		50 20			AA F							
			100 40		AA	F							
210245	MICROPROCESSOR ARCHITECTURE	TW	25 10)		10 P							

210245 MICROPROCESSOR ARCHITECTURE OR 50 20 AA F
210246 SOFT SKILLS TW 25 10 14 P
FIRST SEM TOTAL = --/750

	9 S.E.(2014 PAT.)(COMPUTER) : 25 JAN 2016 CENTRE: PU	JNE	INSTIT	UTE OF	COMPL	JTER TECHNOLO	GY, PUNE.	Р	PAGE NO. 132
	FIRST LINE : SEAT NO., NAME OF			-		•	•		·
	·	-		-		•		-	P/F:PASS/FAIL, C:PREV.CARRY OVER
	4304 KAGADE NIKITA SANJIVAN								
SEM.:	1								
210241	DISCRETE STRUCTURE	PP	100 40	41	45	86 P			
210242	DATA STRUCTURE & PROB SOLVING	PP	100 40	42	42	84 P			
210242	DATA STRUCTURE & PROB SOLVING	PR	50 20			40 P			
210243	DIGITAL ELEX. & LOGIC DESIGN	PP	100 40	29	38	67 P			
210243	DIGITAL ELEX. & LOGIC DESIGN	TW	25 10			20 P			
210244	OPERATING SYSTEM & ADMIN		100 40		29	57 P			
210244			25 10			20 P			
210244	OPERATING SYSTEM & ADMIN		50 20			40 P			
210245			100 40		30	66 P			
210245			25 10			20 P			
210245			50 20			38 P			
210246		I W	25 10			20 P			
	FIRST SEM TOTAL = 558/750								
						VINI			,S140054305
SEM.:									
	DISCRETE STRUCTURE		100 40						
	DATA STRUCTURE & PROB SOLVING				07	F			
	DATA STRUCTURE & PROB SOLVING				0.7	5 F			
	DIGITAL ELEX. & LOGIC DESIGN				07	-			
210243	DIGITAL ELEX. & LOGIC DESIGN				1 [16 P			
210244	OPERATING SYSTEM & ADMIN		100 40		15	49 P			
210244 210244	OPERATING SYSTEM & ADMIN OPERATING SYSTEM & ADMIN		25 10 50 20			12 P 29 P			
210244	MICROPROCESSOR ARCHITECTURE		100 40		16	48 P			
			25 10		10	46 P			
210245			50 20			28 P			
210246	SOFT SKILLS					19 P			
210210	FIRST SEM TOTAL =/750		25 10			13 1			
S14005	4306 KALANI SAHIL HARESH				REEN	NA	,71633691L	,PICT	,s140054306
CEM .	1								
SEM.:		D D	100 40	22	2.4	57 D			
	DISCRETE STRUCTURE		100 40		24	57 P			
210242 210242	DATA STRUCTURE & PROB SOLVING DATA STRUCTURE & PROB SOLVING				21	57 P 30 P			
210242					19	50 P 55 P			
210243	DIGITAL ELEX. & LOGIC DESIGN DIGITAL ELEX. & LOGIC DESIGN				19	33 P 21 P			
210243	OPERATING SYSTEM & ADMIN		100 40		20	56 P			
210244			25 10		20	30 P 21 P			
210244	OPERATING SYSTEM & ADMIN		50 20			38 P			
210244			100 40		23	62 P			
			25 10		23	21 P			
			_5 _5			•			

210245 MICROPROCESSOR ARCHITECTURE OR 50 20 22 P
210246 SOFT SKILLS TW 25 10 16 P
FIRST SEM TOTAL = 456/750

	9	UNE	INSTIT	TUTE O	F CO	MPUTER	TECHNOL	OGY, PUNE.		i	PAGE NO	o. 133	
								•					
NOTE:	FIRST LINE : SEAT NO., NAME OF	F TH	IE CANE	DIDATE	, M	OTHER,	PERMANE	ENT REG. NO.,	PREVIO	US SEAT	NO.,	COLLEGE, SE	AT NO.
OTHER LIN	ES: SUB.CODE, SUB.NAME, SUB.TY	PΕ,	MAX.MA	ARKS, I	MIN.	PASS MA	RKS, O	MARKS, TH MA	RKS, OB	TAINED,	P/F:P/	ASS/FAIL, C:PR	EV.CARRY OVER
S14005	4307 KALE JANHAVI RAJENDRA				S	HRADDHA		,715272	82K	,PICT		,S140054307	
SEM.:	1												
210241	DISCRETE STRUCTURE	PP	100 40) 40	4	7 87	' P						
210242	DATA STRUCTURE & PROB SOLVING	PP	100 40	23	3	6 59	P						
210242	DATA STRUCTURE & PROB SOLVING	PR	50 20)		38	Р						
210243	DIGITAL ELEX. & LOGIC DESIGN	PP	100 40	34	2	3 57	' P						
210243	DIGITAL ELEX. & LOGIC DESIGN	TW	25 10)		21	. Р						
210244	OPERATING SYSTEM & ADMIN	PP	100 40	39	2	0 59	P						
210244	OPERATING SYSTEM & ADMIN	TW	25 10)		21	. Р						
210244	OPERATING SYSTEM & ADMIN	PR	50 20)		30	P						
210245	MICROPROCESSOR ARCHITECTURE	PP	100 40) 42	2	8 70	P						
210245	MICROPROCESSOR ARCHITECTURE	TW	25 10)		21	. P						
210245	MICROPROCESSOR ARCHITECTURE	OR	50 20)		21	. Р						
210246	SOFT SKILLS	TW	25 10)		22	Р						
	FIRST SEM TOTAL = 506/750												
	4308 KALE SANGRAM AGRAM					AYASHRE						,s140054308	
314003	4300 RALE SANGRAM AGRAM				J	ATASHKE	. C	,713272	озп	, PICI		,3140034308	
SEM.:	1												
		PP	100 40) 24	2	2 46	i P						
	DATA STRUCTURE & PROB SOLVING) P						
	DATA STRUCTURE & PROB SOLVING					10							
210243	DIGITAL ELEX. & LOGIC DESIGN	PP	100 40) 27	1	.5 42	Р						
210243	DIGITAL ELEX. & LOGIC DESIGN	TW	25 10)		16	P						
210244	OPERATING SYSTEM & ADMIN	PP	100 40	36	2	2 58	Р						
210244	OPERATING SYSTEM & ADMIN	TW	25 10)		18	Р						
210244	OPERATING SYSTEM & ADMIN	PR	50 20)		32	Р						
210245	MICROPROCESSOR ARCHITECTURE	PP	100 40	30	2	1 51	. Р						
210245	MICROPROCESSOR ARCHITECTURE	TW	25 10)		17	' P						
210245	MICROPROCESSOR ARCHITECTURE	OR	50 20)		15	F						
210246	SOFT SKILLS	TW	25 10)		20	P						
	FIRST SEM TOTAL = $/750$												
S14005	4309 KALKAR SHRUTI KISHOR				K	AVITA		,716336	92〕	,PICT		,s140054309	
CEM .	1												
SEM.:		DD	100 40) 20	2	6 75	Ъ						
	DISCRETE STRUCTURE DATA STRUCTURE & PROB SOLVING		100 40		_	6 75	' P						
	DATA STRUCTURE & PROB SOLVING				2		P						
	DIGITAL ELEX. & LOGIC DESIGN				2		P						
210243	DIGITAL ELEX. & LOGIC DESIGN				_		. P						
210243	OPERATING SYSTEM & ADMIN		100 40		2		. г ' Р						
			25 10		_		Р Р						
	OPERATING SYSTEM & ADMIN		50 20				P						
210245			100 40		3		Р						
			25 10		,		. Р						
		-				- -							

210245 MICROPROCESSOR ARCHITECTURE OR 50 20 25 P
210246 SOFT SKILLS TW 25 10 21 P
FIRST SEM TOTAL = 521/750

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	: : 25 JAN 2016 CENTRE: PI	UNE	INSTI	TUTE 0	F C	OMPUTER	TECH	NOLOGY,	PUNE.			PAGE N	0. 134	
	ETDET LINE + CEAT NO NAME OF													
	FIRST LINE : SEAT NO., NAME OF SUB.CODE, SUB.NAME, SUB.TY				•	•				•		•	-	
S14005	4310 KANGLE SHRUTI SANJAY				A	ANUPAMA			,7152	7292G	,PICT		,S1400543	310
SEM.:	1													
210241	DISCRETE STRUCTURE	PP	100 4	0 44	1 3	31 7	5 P							
210242	DATA STRUCTURE & PROB SOLVING	PP	100 4	0 42	2 3	35 7	7 P							
210242	DATA STRUCTURE & PROB SOLVING	PR	50 2	0		40	0 P							
210243	DIGITAL ELEX. & LOGIC DESIGN	PP	100 4	0 35	5 2	29 6	4 P							
210243	DIGITAL ELEX. & LOGIC DESIGN	TW	25 1	0		18	8 P							
210244	OPERATING SYSTEM & ADMIN	PP	100 4	0 33	3 2	24 5	7 P							
210244	OPERATING SYSTEM & ADMIN	TW	25 1	0		2	3 P							
210244	OPERATING SYSTEM & ADMIN	PR	50 2	0		40	0 P							
210245	MICROPROCESSOR ARCHITECTURE	PP	100 4	0 40) 3	31 7	1 P							
210245	MICROPROCESSOR ARCHITECTURE	TW	25 1	0		19	9 P							
			50 2				3 P							
210246		TW	25 1	0		18	8 P							
	FIRST SEM TOTAL = 545/750													
	4244		• • • • •											
S14005	4311 KAPADIA SHABBIR ANJUM				F	RUBINA			,7152	/295M	,PICT		,5140054	311
CEM .	1													
SEM.:		D.D.	100 4	n 22	, -	21 6	4 5							
	DISCRETE STRUCTURE		100 4		_		4 P							
	DATA STRUCTURE & PROB SOLVING) 4		0 P							
	DATA STRUCTURE & PROB SOLVING DIGITAL ELEX. & LOGIC DESIGN						8 Р 0 Р							
	DIGITAL ELEX. & LOGIC DESIGN			-			0 Р 7 Р							
			100 4		L 2		7 P							
			25 10				2 F 9 P							
	OPERATING SYSTEM & ADMIN		50 20				2 P							
			100 4		, 2		2 · 0 Р							
			25 1		-		8 P							
			50 2				3 P							
			25 1				8 P							
	FIRST SEM TOTAL = 431/750													
S14005	4312 KARANVEER JHALA				ŀ	KALPANA			,7152	7298F	,PICT		,S1400543	312
SEM.:	1													
210241	DISCRETE STRUCTURE	PP	100 4	0 31	L ()4 -	- F							
210242	DATA STRUCTURE & PROB SOLVING	PP	100 4	0 29) [L5 4	4 P							
210242	DATA STRUCTURE & PROB SOLVING	PR	50 2	0		5	F							
210243	DIGITAL ELEX. & LOGIC DESIGN	PP	100 4	0 22	2 ()7 -	- F							
210243							3 P							
210244			100 4		5 _	L7 4	2 P							
210244			25 1				7 P							
	OPERATING SYSTEM & ADMIN		50 2				1 P							
			100 4		3 [8 P							
210245	MICROPROCESSOR ARCHITECTURE	TW	25 1	0		1	7 P							

210245 MICROPROCESSOR ARCHITECTURE OR 50 20 11 F 210246 SOFT SKILLS TW 25 10 19 P FIRST SEM TOTAL = --/750

	: 25 JAN 2016 CENTRE: P	UNE	INSTI	TUTE OF	СОМРИТ	TER TE	ECHNOLOGY, F	PUNE.	ı	PAGE NO). 135	
	FIRST LINE : SEAT NO., NAME O			•		-		-		•	-	
	ES: SUB.CODE, SUB.NAME, SUB.TY	-		-			•		•	-		
	4313 KASABE SRUSHTI SURYAKAN											
SEM.:	1											
	DISCRETE STRUCTURE	PP	100 4	0 32	22	54 F	P					
	DATA STRUCTURE & PROB SOLVING				28	57 F						
210242	DATA STRUCTURE & PROB SOLVING	i PR	50 2	0		15 F	F					
210243	DIGITAL ELEX. & LOGIC DESIGN	PP	100 4	0 36	24	60 F	P					
210243	DIGITAL ELEX. & LOGIC DESIGN	TW	25 1	0		22 F	P					
210244	OPERATING SYSTEM & ADMIN	PP	100 4	0 38	22	60 F	P					
210244	OPERATING SYSTEM & ADMIN	TW	25 1	0		20 F	P					
210244	OPERATING SYSTEM & ADMIN	PR	50 2	0		30 F	P					
210245	MICROPROCESSOR ARCHITECTURE	PP	100 4	0 42	26	68 F	P					
210245	MICROPROCESSOR ARCHITECTURE	TW	25 1	0		21 F	P					
210245	MICROPROCESSOR ARCHITECTURE	OR	50 2	0		29 F						
210246	SOFT SKILLS	TW	25 1	0		22 F	P					
	FIRST SEM TOTAL =/750											
	4214											
S14005	4314 KASHETTIWAR KSHITIJ MIL	IND			SAND	HYA		,71527301к	,PICT		,S1400543	314
CEM .	1											
SEM.:		DD	100 4	n 25	37	72 F	D					
	DISCRETE STRUCTURE DATA STRUCTURE & PROB SOLVING				28	68 F						
210242	DATA STRUCTURE & PROB SOLVING				20	40 F						
-	DIGITAL ELEX. & LOGIC DESIGN				20	59 F						
210243	DIGITAL ELEX. & LOGIC DESIGN				20	19 F						
210244	OPERATING SYSTEM & ADMIN		100 4		21	62 F						
210244	OPERATING SYSTEM & ADMIN		25 1			19 F						
210244	OPERATING SYSTEM & ADMIN	PR	50 2	0		40 F						
210245	MICROPROCESSOR ARCHITECTURE	PP	100 4	0 41	25	66 F	P					
210245	MICROPROCESSOR ARCHITECTURE	TW	25 1	0		17 F	P					
210245	MICROPROCESSOR ARCHITECTURE	OR	50 2	0		28 F	P					
210246	SOFT SKILLS	TW	25 1	0		20 F	P					
	FIRST SEM TOTAL = $510/750$											
S14005	4315 KASTURKAR VAISHNAVI MOH	IAN			ANJAL	_I		,71527303F	,PICT		,S1400543	315
SEM.:												
	DISCRETE STRUCTURE		100 4		45	85 F						
210242	DATA STRUCTURE & PROB SOLVING				27	63 F						
210242	DATA STRUCTURE & PROB SOLVING				2.6	40 F						
210243	DIGITAL ELEX. & LOGIC DESIGN				26	65 F						
210243	DIGITAL ELEX. & LOGIC DESIGN				10	22 i 49 i						
210244 210244	OPERATING SYSTEM & ADMIN OPERATING SYSTEM & ADMIN		25 1		18	49 i						
210244	OPERATING SYSTEM & ADMIN OPERATING SYSTEM & ADMIN		50 2			22 i						
210244	MICROPROCESSOR ARCHITECTURE		100 4		28	65 F						
	MICROPROCESSOR ARCHITECTURE		25 1		20	22 1						
		. ••	_J I	-		'	•					

210245 MICROPROCESSOR ARCHITECTURE OR 50 20 40 P
210246 SOFT SKILLS TW 25 10 20 P
FIRST SEM TOTAL = 521/750

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	: 25 JAN 2016 CENTRE: P	UNE	INST	TITUTE	E OF	COMPUT	ER -	TECHNOLOGY,	PUNE.		PAGE N	0. 136	
	FIRST LINE : SEAT NO., NAME O												
	ES: SUB.CODE, SUB.NAME, SUB.TY	-			•			•	·		-		
	4316 KESHAV JOGLEKAR												
SEM.:	1												
210241	DISCRETE STRUCTURE	PP	100	40	37	43	80	Р					
210242	DATA STRUCTURE & PROB SOLVING	PP	100	40	30	33	63	Р					
210242	DATA STRUCTURE & PROB SOLVING	PR	50	20			38	Р					
210243	DIGITAL ELEX. & LOGIC DESIGN	PP	100	40	39	19	58	Р					
210243	DIGITAL ELEX. & LOGIC DESIGN	TW	25	10			16	Р					
210244	OPERATING SYSTEM & ADMIN	PP	100	40	37	23	60	Р					
210244	OPERATING SYSTEM & ADMIN	TW	25	10			20	Р					
210244	OPERATING SYSTEM & ADMIN	PR	50	20			38	Р					
210245	MICROPROCESSOR ARCHITECTURE	PP	100	40	31	27	58	Р					
210245	MICROPROCESSOR ARCHITECTURE	TW	25	10			16	Р					
210245	MICROPROCESSOR ARCHITECTURE	OR	50	20			23	Р					
210246	SOFT SKILLS	TW	25	10			17	Р					
	FIRST SEM TOTAL = $487/750$												
s14005	4317 KESWANI ANUSHI VEERAL					SHABE	ETA		,71527309E	,PICT		,S1400543	317
SEM.:	1												
210241	DISCRETE STRUCTURE	PP	100	40	40	32	72	Р					
210242	DATA STRUCTURE & PROB SOLVING	PP	100	40	35	32	67	Р					
210242	DATA STRUCTURE & PROB SOLVING	PR	50	20			40	Р					
210243	DIGITAL ELEX. & LOGIC DESIGN	PP	100	40	27	29	56	Р					
210243	DIGITAL ELEX. & LOGIC DESIGN	TW	25	10			19	Р					
210244	OPERATING SYSTEM & ADMIN		100		38	22	60						
210244	OPERATING SYSTEM & ADMIN		25				18						
210244	OPERATING SYSTEM & ADMIN		50				35	Р					
			100		36	27	63						
210245	MICROPROCESSOR ARCHITECTURE	TW	25	10			18	Р					
210245	MICROPROCESSOR ARCHITECTURE	OR	50	20			42	Р					
210246		TW	25	10			19	Р					
	FIRST SEM TOTAL = $509/750$												
s14005	4318 KHANDVE MAITHILEE DEEPA	K				JYOTI			,71527313C	,PICT		,S1400543	318
SEM.:													
	DISCRETE STRUCTURE		100			25	59						
	DATA STRUCTURE & PROB SOLVING				35	32	67						
	DATA STRUCTURE & PROB SOLVING						24						
	DIGITAL ELEX. & LOGIC DESIGN				29	19	48						
	DIGITAL ELEX. & LOGIC DESIGN				2.2	10	19						
	OPERATING SYSTEM & ADMIN		100		33	19	52						
	OPERATING SYSTEM & ADMIN		25				18						
	OPERATING SYSTEM & ADMIN		50		2.2	22	30						
	MICROPROCESSOR ARCHITECTURE		100		33	22	55						
210245	MICROPROCESSOR ARCHITECTURE	I W	25	ΤO			18	۲					

210245 MICROPROCESSOR ARCHITECTURE OR 50 20 22 P
210246 SOFT SKILLS TW 25 10 17 P
FIRST SEM TOTAL = 429/750

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	9 S.E.(2014 PAT.)(COMPUTER) : 25 JAN 2016 CENTRE: PU	UNE	INST	TTUT	E OF	COMPUT	ER :	TECHNO	LOGY, P	UNE.		PAGE N	0. 137	
NOTE:	FIRST LINE : SEAT NO., NAME OF	F TI	HE CA	NDID	ATE,	MOTHE	R,	PERMAN	IENT REG	. NO., PREV	IOUS SEA	T NO.,	COLLEGE, S	EAT NO.
OTHER LIN	ES: SUB.CODE, SUB.NAME, SUB.TY	PΕ,	MAX.	MARK	S, MI	N.PASS	MA	RKS, C	E MARKS	, TH MARKS,	OBTAINED	, P/F:P	ASS/FAIL, C:P	REV.CARRY OVER
S14005	4319 KHANKALE SHUBHAM DEEPAK					SULBF	łΑ			,71633694E	,PICT		,S140054319	
SEM.:	1													
210241	DISCRETE STRUCTURE	PP	100	40	34	21	55	Р						
210242	DATA STRUCTURE & PROB SOLVING	PP	100	40	29	32	61	Р						
210242	DATA STRUCTURE & PROB SOLVING	PR	50	20			38	Р						
210243	DIGITAL ELEX. & LOGIC DESIGN	PP	100	40	35	26	61	Р						
210243	DIGITAL ELEX. & LOGIC DESIGN	TW	25	10			20	Р						
210244	OPERATING SYSTEM & ADMIN	PP	100	40	39	27	66	Р						
210244	OPERATING SYSTEM & ADMIN	TW	25	10			23	Р						
210244	OPERATING SYSTEM & ADMIN	PR	50	20			36	Р						
210245	MICROPROCESSOR ARCHITECTURE	PP	100	40	38	28	66	Р						
210245	MICROPROCESSOR ARCHITECTURE	TW	25	10			21	Р						
210245	MICROPROCESSOR ARCHITECTURE	OR	50	20			27	Р						
210246	SOFT SKILLS	TW	25	10			21	Р						
	FIRST SEM TOTAL = 495/750													
	4320 KHARE MRUNMAYEE AJAY												,s140054320	
314003	TOZO KITAKE MKONMATEL AJAT					ANOFA	AIVI/A			, / 132/ 3140	, , , , ,		,3140034320	
SEM.:	1													
	DISCRETE STRUCTURE	PP	100	40	34	42	76	Р						
	DATA STRUCTURE & PROB SOLVING			-			66							
	DATA STRUCTURE & PROB SOLVING						40							
210243	DIGITAL ELEX. & LOGIC DESIGN	PP	100	40	36	25	61	Р						
210243	DIGITAL ELEX. & LOGIC DESIGN	TW	25	10			21	Р						
210244	OPERATING SYSTEM & ADMIN	PP	100	40	39	26	65	Р						
210244	OPERATING SYSTEM & ADMIN	TW	25	10			20	Р						
210244	OPERATING SYSTEM & ADMIN	PR	50	20			40	Р						
210245	MICROPROCESSOR ARCHITECTURE	PP	100	40	31	27	58	Р						
210245	MICROPROCESSOR ARCHITECTURE	TW	25	10			20	Р						
210245	MICROPROCESSOR ARCHITECTURE	OR	50	20			38	Р						
210246	SOFT SKILLS	TW	25	10			20	Р						
	FIRST SEM TOTAL = $525/750$													
S14005	4321 KHEDKAR RAHUL ASHOK					USHA				,/152/316Н	,PICT		,S140054321	
CEM .	1													
SEM.:		DD	100	40	11	24	75	ь						
	DATA STRUCTURE & PROB SOLVING		100		41 36	34 38	75 74							
	DATA STRUCTURE & PROB SOLVING				30	30	35							
210242	DIGITAL ELEX. & LOGIC DESIGN				41	30	71							
210243	DIGITAL ELEX. & LOGIC DESIGN				r T	50	16							
210243	OPERATING SYSTEM & ADMIN		100		45	15	60							
	OPERATING SYSTEM & ADMIN		25		. 3		20							
	OPERATING SYSTEM & ADMIN		50				31							
210245			100		42	28	70							
			25				19							
· ·•				-										

210245 MICROPROCESSOR ARCHITECTURE OR 50 20 30 P
210246 SOFT SKILLS TW 25 10 18 P
FIRST SEM TOTAL = 519/750

	.9 S.E.(2014 PAT.)(COMPUTER) E: 25 JAN 2016 CENTRE: P						•		PAGE NO. 138
NOTE:	FIRST LINE : SEAT NO., NAME O	F THE	E CAND	IDATE,	MOTH	IER, PERMANEN	NT REG. NO., PREV	IOUS SEAT	Γ NO., COLLEGE, SEAT NO. , P/F:PASS/FAIL, C:PREV.CARRY OVE
									6140054222
514005	i4322 KINJAL JITENDRA SANGHVI				PKII	ΓI	,/132/3198	, PICI	,S140054322
SEM.:	1								
210241	DISCRETE STRUCTURE	PP 1	L00 40	29	41	70 P			
210242	DATA STRUCTURE & PROB SOLVING	PP 1	L00 40	39	38	77 P			
210242	DATA STRUCTURE & PROB SOLVING	PR	50 20	1		38 P			
210243	DIGITAL ELEX. & LOGIC DESIGN	PP 1	L00 40	31	36	67 P			
210243	DIGITAL ELEX. & LOGIC DESIGN	TW	25 10	1		18 P			
210244	OPERATING SYSTEM & ADMIN	PP 1	L00 40	34	26	60 P			
210244	OPERATING SYSTEM & ADMIN	TW	25 10	ı		21 P			
210244	OPERATING SYSTEM & ADMIN	PR	50 20			32 P			
210245	MICROPROCESSOR ARCHITECTURE	PP 1	L00 40	36	28	64 P			
			25 10			18 P			
			50 20			37 P			
210246	SOFT SKILLS	TW	25 10	1		18 P			
	FIRST SEM TOTAL = 520/750								
	64323 KOLHE SHUBHAM PRASHANT								,s140054323
SEM.:	1								
210241	DISCRETE STRUCTURE	PP 1	L00 40	38	42	80 P			
210242	DATA STRUCTURE & PROB SOLVING	PP 1	L00 40	28	37	65 P			
210242	DATA STRUCTURE & PROB SOLVING	PR	50 20	1		38 P			
210243	DIGITAL ELEX. & LOGIC DESIGN	PP 1	L00 40	32	30	62 P			
210243	DIGITAL ELEX. & LOGIC DESIGN	TW	25 10	1		18 P			
210244	OPERATING SYSTEM & ADMIN	PP 1	L00 40	34	18	52 P			
210244	OPERATING SYSTEM & ADMIN	TW	25 10	1		21 P			
210244	OPERATING SYSTEM & ADMIN	PR	50 20	1		30 P			
210245	MICROPROCESSOR ARCHITECTURE	PP 1	L00 40	28	34	62 P			
210245	MICROPROCESSOR ARCHITECTURE	TW	25 10	1		19 P			
210245	MICROPROCESSOR ARCHITECTURE	OR	50 20	ı		36 P			
210246	SOFT SKILLS FIRST SEM TOTAL = 505/750	TW	25 10			22 P			
	64324 KOMAL PREET HORA					PENDER			,s140054324
SEM.:	1								
	DISCRETE STRUCTURE	DD 1	L00 40	39	45	84 P			
					39	64 P 77 P			
	DATA STRUCTURE & PROB SOLVING				פנ	77 P 40 P			
210242	DIGITAL ELEX. & LOGIC DESIGN				42	77 P			
210243			25 10		74	77 P 21 P			
210243	OPERATING SYSTEM & ADMIN		23 10 100 40		26	66 P			
210244	OPERATING SYSTEM & ADMIN		25 10		20	23 P			
210244			50 20			34 P			
210244	MICROPROCESSOR ARCHITECTURE		100 40		35	76 P			
/ 4 >				71	<i>J J</i>	, U I			

210245 MICROPROCESSOR ARCHITECTURE OR 50 20 39 P
210246 SOFT SKILLS TW 25 10 22 P
FIRST SEM TOTAL = 580/750

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	9	UNE I	NSTITU	TE OF	COMPUT	ER T	ECHNOL	LOGY. PL	JNE.		PAGE NO	o. 139	
NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.													
OTHER LIN	ES: SUB.CODE, SUB.NAME, SUB.TY	PE, M	MAX.MAR	KS, MI	N. PASS	MAF	RKS, OE	E MARKS,	TH MARKS,	OBTAINED,	P/F:P/	ASS/FAIL, C:PREV.CA	RRY OVER
S14005	4326 KOTAK NIKITA NARENDRA				DAXA			,	71527326E	,PICT		,S140054326	
SEM.:		DD 1	00 40	41	4.4	0.5	5						
			.00 40	41		85							
210242 210242	DATA STRUCTURE & PROB SOLVING DATA STRUCTURE & PROB SOLVING			45	34	79 40							
210242	DIGITAL ELEX. & LOGIC DESIGN			38	30	77							
210243	DIGITAL ELEX. & LOGIC DESIGN			30	33	24							
210244	OPERATING SYSTEM & ADMIN		.00 40	40	17	57							
210244			25 10			22							
210244			50 20			42							
210245	MICROPROCESSOR ARCHITECTURE	PP 1	.00 40	42	30	72	Р						
210245	MICROPROCESSOR ARCHITECTURE	TW	25 10			23	Р						
210245	MICROPROCESSOR ARCHITECTURE	OR	50 20			41	Р						
210246	SOFT SKILLS	TW	25 10			23	Р						
	FIRST SEM TOTAL = 585/750												
S14005	4327 KOTAWAR VAISHNAVI VINOD				RASHM	ΙI		,	71527327C	,PICT		,S140054327	
SEM.:													
			-00 40			70							
	DATA STRUCTURE & PROB SOLVING			31	34	65							
	DATA STRUCTURE & PROB SOLVING			2.4	2.5	42							
	DIGITAL ELEX. & LOGIC DESIGN			34	25	59	-						
210243			25 10	22	20	20 53							
	OPERATING SYSTEM & ADMIN OPERATING SYSTEM & ADMIN		25 10	33	20	19							
			50 20			26							
			.00 40	34	31	65							
			25 10	34	31	19							
			50 20			28							
	SOFT SKILLS					18							
	FIRST SEM TOTAL = 484/750						•						
S14005	4328 KOTHARI RANJEET ANUP				ARCHA	NA		3	71527328м	,PICT		,s140054328	
SEM.:	1												
210241	DISCRETE STRUCTURE	PP 1	.00 40	36	40	76	Р						
210242	DATA STRUCTURE & PROB SOLVING	PP 1	.00 40	25	27	52	Р						
210242	DATA STRUCTURE & PROB SOLVING	PR	50 20			5	F						
210243	DIGITAL ELEX. & LOGIC DESIGN	PP 1	.00 40	22	18	40	Р						
210243	DIGITAL ELEX. & LOGIC DESIGN					10							
210244	OPERATING SYSTEM & ADMIN		.00 40	30	15	45							
			25 10			15							
	OPERATING SYSTEM & ADMIN		50 20			29							
210245			.00 40	27	22	49							
210245	MICROPROCESSOR ARCHITECTURE	TW	25 10			10	Р						

210245 MICROPROCESSOR ARCHITECTURE OR 50 20 11 F 210246 SOFT SKILLS TW 25 10 12 P FIRST SEM TOTAL = --/750

	: 25 JAN 2016 CENTRE: P	UNE]	NSTI	TUTE 0	F COM	IPUTER	TECHNO	LOGY, Pl	UNE.	I	PAGE NO	o. 140	
	FIRST LINE : SEAT NO., NAME O												
	ES: SUB.CODE, SUB.NAME, SUB.TY												
	4329 KSHITIJA S WAGHURDEKAR												
SEM.:	1												
210241	DISCRETE STRUCTURE	PP 1	L00 4	0 38	33	71	. P						
210242	DATA STRUCTURE & PROB SOLVING	PP 1	L00 4	0 41	. 29	70) P						
210242	DATA STRUCTURE & PROB SOLVING	PR	50 2	0		41	. P						
210243	DIGITAL ELEX. & LOGIC DESIGN	PP 1	L00 4	0 34	35	69) P						
210243	DIGITAL ELEX. & LOGIC DESIGN	TW	25 1	0		20) P						
210244	OPERATING SYSTEM & ADMIN	PP 1	L00 4	0 33	29	62	? P						
210244	OPERATING SYSTEM & ADMIN	TW	25 1	0		19) P						
210244	OPERATING SYSTEM & ADMIN	PR	50 2	0		32	? P						
210245	MICROPROCESSOR ARCHITECTURE	PP 1	L00 4	0 44	28	72	? P						
210245	MICROPROCESSOR ARCHITECTURE	TW	25 1	0		20) P						
210245	MICROPROCESSOR ARCHITECTURE	OR	50 2	0		40) P						
210246	SOFT SKILLS	TW	25 1	0		22	? P						
	FIRST SEM TOTAL = $538/750$												
s14005	4330 KULKARNI ADITYA PADMAKA	R			SH	IUBHADA	١.	;	,71527333н	,PICT		,S1400543	330
SEM.:	1												
210241	DISCRETE STRUCTURE	PP 1	L00 4	0 36	33	69) P						
210242	DATA STRUCTURE & PROB SOLVING	PP 1	L00 4	0 35	33	68	ВР						
210242	DATA STRUCTURE & PROB SOLVING	PR	50 2	0		40) P						
210243	DIGITAL ELEX. & LOGIC DESIGN	PP 1	L00 4	0 38	34	72	. P						
210243	DIGITAL ELEX. & LOGIC DESIGN	TW	25 1	0			. P						
210244	OPERATING SYSTEM & ADMIN		L00 4		18		B P						
			25 1) P						
210244	OPERATING SYSTEM & ADMIN		50 2				Б Р						
			L00 4		25		' P						
210245			25 1			20) P						
			50 2				. P						
210246		TW	25 1	0		20) P						
	FIRST SEM TOTAL = 498/750												
S14005	4331 KULKARNI ATHARVA SUDHIR				SA	NJIVAN	II	:	,71527334F	,PICT		,S1400543	331
SEM.:			.00 4	0 25									
	DISCRETE STRUCTURE			0 25			. Р						
	DATA STRUCTURE & PROB SOLVING				. 23		Р						
	DATA STRUCTURE & PROB SOLVING						Б Р 						
	DIGITAL ELEX. & LOGIC DESIGN				24		Р						
	DIGITAL ELEX. & LOGIC DESIGN						5 P						
	OPERATING SYSTEM & ADMIN		L00 4		15) P						
	OPERATING SYSTEM & ADMIN		25 1				B P						
	OPERATING SYSTEM & ADMIN		50 2) P						
	MICROPROCESSOR ARCHITECTURE		L00 4		23		i P						
210245	MICROPROCESSOR ARCHITECTURE	1 W	25 1	U		18	3 P						

210245 MICROPROCESSOR ARCHITECTURE OR 50 20 8 F
210246 SOFT SKILLS TW 25 10 18 P
FIRST SEM TOTAL = --/750

	: : 25 JAN 2016 CENTRE: PU	JNE INSTI	TUTE OF	COMPU	TER TECHNOLO	OGY, PUNE.	Р	AGE NO. 141	
	FIRST LINE : SEAT NO., NAME OF								
	FIRST LINE : SEAT NO., NAME OF		-		•	•		•	
S14005	4332 KULKARNI OMKAR CHANDRAKA	ANT		MANJ	USHA	,71527338j	,PICT	,s1400543	32
SEM.:	1								
210241	DISCRETE STRUCTURE	PP 100 4	0 39	30	69 P				
210242	DATA STRUCTURE & PROB SOLVING	PP 100 4	0 33	28	61 P				
210242	DATA STRUCTURE & PROB SOLVING	PR 50 2	0		33 P				
210243	DIGITAL ELEX. & LOGIC DESIGN	PP 100 4	0 40	34	74 P				
210243	DIGITAL ELEX. & LOGIC DESIGN	TW 25 1	.0		19 P				
210244	OPERATING SYSTEM & ADMIN	PP 100 4	0 37	18	55 P				
210244	OPERATING SYSTEM & ADMIN	TW 25 1	.0		22 P				
210244	OPERATING SYSTEM & ADMIN	PR 50 2	0		33 P				
210245	MICROPROCESSOR ARCHITECTURE	PP 100 4	0 39	23	62 P				
210245	MICROPROCESSOR ARCHITECTURE	TW 25 1	.0		20 P				
210245	MICROPROCESSOR ARCHITECTURE	OR 50 2	0		21 P				
210246	SOFT SKILLS	TW 25 1	.0		19 P				
	FIRST SEM TOTAL = 488/750								
	4222								
\$14005	4333 KUMBHAR ADITYA SANJAY			SUNI	TA	,/152/343E	,PICT	,S1400543	33
CEM .	1								
SEM.:		PP 100 4	0 21	42	73 P				
	DATA STRUCTURE & PROB SOLVING			24					
	DATA STRUCTURE & PROB SOLVING DATA STRUCTURE & PROB SOLVING			24	55 P 38 P				
	DIGITAL ELEX. & LOGIC DESIGN			29	63 P				
	DIGITAL ELEX. & LOGIC DESIGN		-	29	18 P				
		PP 100 4		15	51 P				
_		TW 25 1		13	19 P				
	OPERATING SYSTEM & ADMIN	PR 50 2			30 P				
		PP 100 4		24	55 P				
		TW 25 1			18 P				
		OR 50 2			21 P				
		TW 25 1			19 P				
	FIRST SEM TOTAL = 460/750								
S14005	4334 LAKHE NITEEN SUDIP			MEEN	ABAI	,71527349D	,PICT	,s1400543	34
SEM.:	1								
210241	DISCRETE STRUCTURE	PP 100 4	0 35	40	75 P				
210242	DATA STRUCTURE & PROB SOLVING	PP 100 4	0 42	24	66 P				
	DATA STRUCTURE & PROB SOLVING				35 P				
	DIGITAL ELEX. & LOGIC DESIGN			27	65 P				
	DIGITAL ELEX. & LOGIC DESIGN				18 P				
210244		PP 100 4		19	55 P				
		TW 25 1			20 P				
	OPERATING SYSTEM & ADMIN	PR 50 2		2.2	32 P				
		PP 100 4		22	56 P				
210245	MICROPROCESSOR ARCHITECTURE	TW 25 1	.U		19 P				

210245 MICROPROCESSOR ARCHITECTURE OR 50 20 35 P
210246 SOFT SKILLS TW 25 10 19 P
FIRST SEM TOTAL = 495/750

	:: 25 JAN 2016 CENTRE: P	UNE	INST	ITUTE	OF	СОМРИТ	ER -	TECHNOLOG	Y, PUN	Ε.		PAGE N	IO. 142	
	ETROT LINE - COAT NO NAME O													
	FIRST LINE : SEAT NO., NAME O IES: SUB.CODE, SUB.NAME, SUB.TY						-			•		•	-	
S14005	4335 LOKWANI ROHIT KANHAIYA					MANIS	SHA		, 7	1527352D	,PICT		,S1400543	335
SEM.:	1													
	DISCRETE STRUCTURE	PP	100	40	40	45	85	Р						
210242	DATA STRUCTURE & PROB SOLVING	PP	100	40	36	36	72	Р						
210242	DATA STRUCTURE & PROB SOLVING	PR	50	20			30	Р						
210243	DIGITAL ELEX. & LOGIC DESIGN	PP	100	40	36	38	74	Р						
210243	DIGITAL ELEX. & LOGIC DESIGN	TW	25	10			20	Р						
210244	OPERATING SYSTEM & ADMIN	PP	100	40	34	18	52	Р						
210244	OPERATING SYSTEM & ADMIN	TW	25	10			19	Р						
210244	OPERATING SYSTEM & ADMIN	PR	50	20			30	Р						
210245	MICROPROCESSOR ARCHITECTURE	PP	100	40	40	29	69	Р						
210245	MICROPROCESSOR ARCHITECTURE	TW	25	10			19	Р						
210245	MICROPROCESSOR ARCHITECTURE	OR	50	20			29	Р						
210246	SOFT SKILLS	TW	25	10			17	Р						
	FIRST SEM TOTAL = $516/750$													
S14005	4336 LONDHE GAURAV TULSHIRAM					KALPA	NA		, 7	1633696м	,PICT		,S1400543	336
SEM.:	1													
	DISCRETE STRUCTURE	PP	100	40	34	41	75	Р						
	DATA STRUCTURE & PROB SOLVING				39	38	77							
	DATA STRUCTURE & PROB SOLVING						42							
	DIGITAL ELEX. & LOGIC DESIGN				38	33	71							
210243	DIGITAL ELEX. & LOGIC DESIGN	TW	25	10			20	Р						
	OPERATING SYSTEM & ADMIN		100		42	27	69							
210244	OPERATING SYSTEM & ADMIN	TW	25	10			22	Р						
210244	OPERATING SYSTEM & ADMIN	PR	50	20			42	Р						
210245	MICROPROCESSOR ARCHITECTURE	PP	100	40	44	30	74	Р						
210245	MICROPROCESSOR ARCHITECTURE	TW	25	10			20	Р						
210245	MICROPROCESSOR ARCHITECTURE	OR	50	20			34	Р						
210246	SOFT SKILLS	TW	25	10			20	Р						
	FIRST SEM TOTAL = 566/750													
S14005	4337 LONDHE PRIYANKA MARUTI					SULBH	łΑ		,7	1527354L	,PICT		,S1400543	337
SEM.:	1													
	DISCRETE STRUCTURE	PР	100	40	35	29	64	P						
	DATA STRUCTURE & PROB SOLVING				41	34	75							
	DATA STRUCTURE & PROB SOLVING					5.	40							
	DIGITAL ELEX. & LOGIC DESIGN				30	39	69							
	DIGITAL ELEX. & LOGIC DESIGN						23							
210243			100		44	25	69							
-			25		•	_5	21							
	OPERATING SYSTEM & ADMIN		50				40							
			100		32	28	60							
	MICROPROCESSOR ARCHITECTURE		25			-	22							
			-				_							

210245 MICROPROCESSOR ARCHITECTURE OR 50 20 36 P
210246 SOFT SKILLS TW 25 10 23 P
FIRST SEM TOTAL = 542/750

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DATE	<pre>L9 S.E.(2014 PAT.)(COMPUTER) E : 25 JAN 2016</pre>											PAGE N		
NOTE:	FIRST LINE : SEAT NO., NAME ONES: SUB.CODE, SUB.NAME, SUB.TY	F TH	IE CA	NDID	ATE,	МОТН	HER, F	PERMANE	ENT REG.	NO., PRE	VIOUS SEAT	Γ NO.,	COLLEGE,	SEAT NO.
	54338 LOTLIKAR SUMEDH RAMCHAN													
SEM.:	:1													
210241	DISCRETE STRUCTURE	PP	100	40	42	43	85	Р						
210242	DATA STRUCTURE & PROB SOLVING	PP	100	40	35	37	72	Р						
210242	DATA STRUCTURE & PROB SOLVING	PR	50	20			41	Р						
210243	DIGITAL ELEX. & LOGIC DESIGN	PP	100	40	43	41	84	Р						
210243	DIGITAL ELEX. & LOGIC DESIGN	TW	25	10			22	Р						
210244	OPERATING SYSTEM & ADMIN	PP	100	40	40	24	64	Р						
210244	OPERATING SYSTEM & ADMIN	TW	25	10			23	Р						
210244	OPERATING SYSTEM & ADMIN	PR	50	20			35	Р						
210245	MICROPROCESSOR ARCHITECTURE	PP	100	40	39	29	68	Р						
210245	MICROPROCESSOR ARCHITECTURE	TW	25	10			21	Р						
	MICROPROCESSOR ARCHITECTURE						30							
210246	SOFT SKILLS FIRST SEM TOTAL = 566/750	TW	25	10			21	Р						
	54339 MAGAI SHRUTI SATISH				• • • •					71527360E				
SEM.:	:1													
210241	DISCRETE STRUCTURE	PP	100	40	41	34	75	Р						
210242	DATA STRUCTURE & PROB SOLVING	PP	100	40	34	34	68	Р						
	DATA STRUCTURE & PROB SOLVING						40	-						
	DIGITAL ELEX. & LOGIC DESIGN				43	41								
210243	DIGITAL ELEX. & LOGIC DESIGN		25	-			21							
	OPERATING SYSTEM & ADMIN		100		40	27	67							
			25				19							
210244			50		4.4	2.2	35							
210245			100		41	33	74							
	MICROPROCESSOR ARCHITECTURE		25				20							
			50				39							
210246	SOFT SKILLS FIRST SEM TOTAL = 562/750	ıw	23	10			20	Р						
	54340 MAKHEJA YASH SUNDERLAL							:		71527366D				
SEM.:	:1													
210241	DISCRETE STRUCTURE	PP	100	40	41	42	83	Р						
210242	DATA STRUCTURE & PROB SOLVING	PP	100	40	40	27	67	Р						
210242	DATA STRUCTURE & PROB SOLVING	PR	50	20			40	Р						
210243	DIGITAL ELEX. & LOGIC DESIGN	PP	100	40	36	25	61	Р						
210243	DIGITAL ELEX. & LOGIC DESIGN	TW	25	10			18	Р						
210244	OPERATING SYSTEM & ADMIN	PP	100	40	36	20	56	Р						
210244	OPERATING SYSTEM & ADMIN	TW	25	10			18	Р						
210244	OPERATING SYSTEM & ADMIN	PR	50	20			36	Р						
210245	MICROPROCESSOR ARCHITECTURE	PP	100	40	45	26	71	Р						
210245	MICROPROCESSOR ARCHITECTURE	TW	25	10			19	Р						

210245 MICROPROCESSOR ARCHITECTURE OR 50 20 22 P
210246 SOFT SKILLS TW 25 10 21 P
FIRST SEM TOTAL = 512/750

BRANCH: 19 S.E.(2014 PAT.)(COMPUTER) DATE: 25 JAN 2016 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 144										
NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.										
OTHER LIN	ES: SUB.CODE, SUB.NAME, SUB.TY	PE, MAX.MA	ARKS, M	IN.PAS	SS MARKS, OE	MARKS, TH MARKS, C	BTAINED,	P/F:PASS/FAIL, C:PREV.CARRY OVER		
\$14005	4341 MALGAUNDE KAVITA TATYAS	AHEB		SUNI	TA	,/163369/K	,PICT	,5140054341		
SEM.:	1									
210241	DISCRETE STRUCTURE	PP 100 40	38	20	58 P					
210242	DATA STRUCTURE & PROB SOLVING	PP 100 40	34	29	63 P					
210242	DATA STRUCTURE & PROB SOLVING	PR 50 20)		38 P					
210243	DIGITAL ELEX. & LOGIC DESIGN	PP 100 40	30	33	63 P					
210243	DIGITAL ELEX. & LOGIC DESIGN	TW 25 10)		21 P					
210244	OPERATING SYSTEM & ADMIN	PP 100 40) 44	22	66 P					
210244	OPERATING SYSTEM & ADMIN	TW 25 10			21 P					
		PR 50 20			37 P					
210245		PP 100 40		28	60 P					
210245		TW 25 10			21 P					
		OR 50 20			27 P					
210246		TW 25 10)		19 P					
	FIRST SEM TOTAL = 494/750									
	4342 MALPANI BHARAT KAMALKISI							,s140054342		
3000				٠, <u>-</u>		,	,	,02.000.0.2		
SEM.:	1									
210241	DISCRETE STRUCTURE	PP 100 40) 42	31	73 P					
210242	DATA STRUCTURE & PROB SOLVING	PP 100 40	37	33	70 P					
210242	DATA STRUCTURE & PROB SOLVING	PR 50 20)		22 P					
210243	DIGITAL ELEX. & LOGIC DESIGN	PP 100 40	39	34	73 P					
210243	DIGITAL ELEX. & LOGIC DESIGN	TW 25 10)		21 P					
210244	OPERATING SYSTEM & ADMIN	PP 100 40) 29	15	44 P					
210244	OPERATING SYSTEM & ADMIN	TW 25 10)		21 P					
	OPERATING SYSTEM & ADMIN	PR 50 20			39 P					
		PP 100 40		31	63 P					
		TW 25 10			20 P					
		OR 50 20			23 P					
210246	SOFT SKILLS	TW 25 10)		22 P					
	FIRST SEM TOTAL = 491/750									
\$14005	4343 MALU ANUJ ANIL				IIVANI			,s140054343		
314003	TOTO MALO ANOS ANIL			SANS	TVANI	,713273093	, PICI	,3140034343		
SEM.:	1									
		PP 100 40) 46	37	83 P					
				35	72 P					
210242	DATA STRUCTURE & PROB SOLVING	PR 50 20)		37 P					
	DIGITAL ELEX. & LOGIC DESIGN			38	72 P					
210243	DIGITAL ELEX. & LOGIC DESIGN	TW 25 10)		19 P					
210244	OPERATING SYSTEM & ADMIN	PP 100 40	33	22	55 P					
210244	OPERATING SYSTEM & ADMIN	TW 25 10)		20 P					
210244	OPERATING SYSTEM & ADMIN	PR 50 20)		40 P					
210245	MICROPROCESSOR ARCHITECTURE	PP 100 40	36	29	65 P					
210245	MICROPROCESSOR ARCHITECTURE	TW 25 10)		18 P					

210245 MICROPROCESSOR ARCHITECTURE OR 50 20 37 P
210246 SOFT SKILLS TW 25 10 20 P
FIRST SEM TOTAL = 538/750

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	9 S.E.(2014 PAT.)(COMPUTER) : 25 JAN 2016 CENTRE: PU	UNE	INSTI	TUTE (OF (COMPUTE	R -	ΓECHNOLOGY	Y, PUN	NE.			PAGE N	o. 145	
	FIRST LINE : SEAT NO., NAME OF				•		-			•			•	•	
	ES: SUB.CODE, SUB.NAME, SUB.TY	-		-				•	-	•			-		
	4344 MANE ABHISHEK PRAVIN													,s140054344	
SEM.:	1														
210241	DISCRETE STRUCTURE	PP	100 4	0 29	9	39	68	Р							
210242	DATA STRUCTURE & PROB SOLVING	PP	100 4	0 37	7	28	65	Р							
210242	DATA STRUCTURE & PROB SOLVING	PR	50 2	0			36	Р							
210243	DIGITAL ELEX. & LOGIC DESIGN	PP	100 4	0 39	9	21	60	P							
210243	DIGITAL ELEX. & LOGIC DESIGN	TW	25 1	0			21	Р							
210244	OPERATING SYSTEM & ADMIN	PP	100 4	0 38	3		57								
210244			25 1				22								
210244	OPERATING SYSTEM & ADMIN		50 2		_		36								
210245			100 4		7		64								
210245			25 1				21								
			50 2				13								
210246		IW	25 1	U			21	Р							
	FIRST SEM TOTAL =/750														
	4345 MANGAM DARSHAN BHAGWAN													,s140054345	
SEM.:															
	DISCRETE STRUCTURE		100 4				50								
	DATA STRUCTURE & PROB SOLVING				9		59								
	DATA STRUCTURE & PROB SOLVING						5								
	DIGITAL ELEX. & LOGIC DESIGN)	-	60	-							
210243	DIGITAL ELEX. & LOGIC DESIGN				,		20								
210244	OPERATING SYSTEM & ADMIN		100 4 25 1		_		40								
210244 210244	OPERATING SYSTEM & ADMIN OPERATING SYSTEM & ADMIN		50 2				2032								
210244	MICROPROCESSOR ARCHITECTURE		100 4		5		50								
			25 1		,		20								
210245			50 2				11								
210246			25 1				22								
210210	FIRST SEM TOTAL =/750		23 1	Ü				•							
S14005	4346 MANISH BODHI					KAVITA	L		,	71527378н	, 1	PICT		,s140054346	
SEM.:	1														
	DISCRETE STRUCTURE	PР	100 4	0 4	L	42	83	Þ							
	DATA STRUCTURE & PROB SOLVING						79								
210242	DATA STRUCTURE & PROB SOLVING						42								
210242	DIGITAL ELEX. & LOGIC DESIGN				9		77								
210243	DIGITAL ELEX. & LOGIC DESIGN				-		23								
210244	OPERATING SYSTEM & ADMIN		100 4		5		55								
210244			25 1				21								
210244	OPERATING SYSTEM & ADMIN		50 2				34								
210245			100 4		2		72								
210245		TW	25 1	0			22	Р							

210245 MICROPROCESSOR ARCHITECTURE OR 50 20 36 P
210246 SOFT SKILLS TW 25 10 22 P
FIRST SEM TOTAL = 566/750

	9 S.E.(2014 PAT.)(COMPUTER) : 25 JAN 2016 CENTRE: PI	JNE	INSTI	TUTE O	F C	OMPUTER	TECHNOLOGY,	PUNE.	PAGE	NO. 146
NOTE:	FIRST LINE : SEAT NO., NAME OF	F TI	HE CAN	DIDATE	., N	OTHER,	PERMANENT RI	EG. NO., PREV	IOUS SEAT NO.,	COLLEGE, SEAT NO.
OTHER LIN	ES: SUB.CODE, SUB.NAME, SUB.TY	PΕ,	MAX.M	ARKS,	MIN	.PASS MA	ARKS, OE MARI	KS, TH MARKS, (OBTAINED, P/F:	PASS/FAIL, C:PREV.CARRY OVER
	4247									
S14005	4347 MANISH DIGAMBAR GAWALI				ı	_AIA		,/152/3/9F	,PICT	,5140054347
SEM.:	1									
210241	DISCRETE STRUCTURE	PP	100 4	0 39) 4	17 86	5 P			
210242	DATA STRUCTURE & PROB SOLVING	PP	100 4	0 41		33 74	ŀ P			
210242	DATA STRUCTURE & PROB SOLVING	PR	50 2	0		38	B P			
210243	DIGITAL ELEX. & LOGIC DESIGN	PP	100 4	0 40)	37 77	' P			
210243	DIGITAL ELEX. & LOGIC DESIGN	TW	25 1	0		18	B P			
210244	OPERATING SYSTEM & ADMIN	PP	100 4	0 34	. 7	20 54	P P			
210244			25 1) P			
210244	OPERATING SYSTEM & ADMIN		50 2				3 P			
210245			100 4				' P			
210245			25 1				3 P			
			50 2			21				
210246		IW	25 1	U		17	Р			
	FIRST SEM TOTAL = 528/750									
	10.10								,PICT	
SEM.:	1									
210241	DISCRETE STRUCTURE	PP	100 4	0 35		33 68	B P			
210242	DATA STRUCTURE & PROB SOLVING	PP	100 4	0 35		30 65	i P			
210242	DATA STRUCTURE & PROB SOLVING	PR	50 2	0		40) P			
210243	DIGITAL ELEX. & LOGIC DESIGN	PP	100 4	0 38		35 73	3 P			
210243	DIGITAL ELEX. & LOGIC DESIGN			•			. P			
210244	OPERATING SYSTEM & ADMIN		100 4				' P			
210244			25 1				. P			
210244	OPERATING SYSTEM & ADMIN		50 2				5 P			
210245	MICROPROCESSOR ARCHITECTURE		100 4				5 P			
			25 1) P			
210245			50 2				. P			
210246	SOFT SKILLS FIRST SEM TOTAL = 509/750	ıw	25 1	U		21	. P			
s14005	4349 MASTUD SNEHA SUGRIV								,PICT	
SEM.:	1									
210241	DISCRETE STRUCTURE	PP	100 4	0 33		23 56	5 P			
210242	DATA STRUCTURE & PROB SOLVING	PP	100 4	0 30) 4	10 70) P			
210242	DATA STRUCTURE & PROB SOLVING	PR	50 2	0		28	3 P			
210243	DIGITAL ELEX. & LOGIC DESIGN						. P			
210243	DIGITAL ELEX. & LOGIC DESIGN) P			
210244	OPERATING SYSTEM & ADMIN		100 4		• .		. P			
210244			25 1				. P			
210244	OPERATING SYSTEM & ADMIN		50 2) P			
210245			100 4				. P			
210245	MICROPROCESSOR ARCHITECTURE	TW	25 1	U		20) P			

210245 MICROPROCESSOR ARCHITECTURE OR 50 20 27 P
210246 SOFT SKILLS TW 25 10 22 P
FIRST SEM TOTAL = 477/750

	:: 25 JAN 2016 CENTRE: P	UNE	INST	ΓΙΤUΤΙ	E OF	COMPUTI	ER '	TECHNOLOGY,	, PUN	NE.		PAGE N	0. 147	
	FIRST LINE : SEAT NO., NAME O													
	IES: SUB.CODE, SUB.NAME, SUB.TY													
	4350 MASURKAR RAJAT PRASHANT									71527387G				
SEM.:	1													
210241	DISCRETE STRUCTURE	PP	100	40	32	36	68	Р						
210242	DATA STRUCTURE & PROB SOLVING	PP	100	40	37	25	62	Р						
210242	DATA STRUCTURE & PROB SOLVING	PR	50	20			38	Р						
210243	DIGITAL ELEX. & LOGIC DESIGN	PP	100	40	30	29	59	Р						
210243	DIGITAL ELEX. & LOGIC DESIGN	TW	25	10			18	Р						
210244	OPERATING SYSTEM & ADMIN	PP	100	40	24	16	40	Р						
210244	OPERATING SYSTEM & ADMIN	TW	25	10			19	Р						
210244	OPERATING SYSTEM & ADMIN	PR	50	20			36	Р						
210245	MICROPROCESSOR ARCHITECTURE	PP	100	40	29	20	49	Р						
210245	MICROPROCESSOR ARCHITECTURE	TW	25	10			19	Р						
210245	MICROPROCESSOR ARCHITECTURE	OR	50	20			24	Р						
210246		TW	25	10			20	Р						
	FIRST SEM TOTAL = 452/750													
	4254	• • •												
S14005	4351 MAYANK GUPTA					RAJNI	GU	PTA	, 7	71527388E	,PICT		,S140054	351
O.E.M .	1													
SEM.:			100	40	41	20		_						
	DISCRETE STRUCTURE		100	_	41		80							
	DATA STRUCTURE & PROB SOLVING				43	32	75 42							
	DATA STRUCTURE & PROB SOLVING DIGITAL ELEX. & LOGIC DESIGN				38	34	42 72							
210243	DIGITAL ELEX. & LOGIC DESIGN				36	34	18							
210243	OPERATING SYSTEM & ADMIN		100		33	27	60							
-	OPERATING SYSTEM & ADMIN		25		33	21	19							
	OPERATING SYSTEM & ADMIN		50				30							
			100		35	27	62							
			25		33	_,	19							
			50				22							
			25				18							
	FIRST SEM TOTAL = 517/750							•						
	4352 MD SHAHZAD ALAM									71527390G				352
SEM.:	1													
210241	DISCRETE STRUCTURE	PP	100	40	43	22	65	Р						
210242	DATA STRUCTURE & PROB SOLVING	PP	100	40	38	23	61	P						
210242	DATA STRUCTURE & PROB SOLVING	PR	50	20			40	Р						
210243	DIGITAL ELEX. & LOGIC DESIGN	PP	100	40	25	28	53	Р						
210243	DIGITAL ELEX. & LOGIC DESIGN	TW	25	10			16	Р						
210244	OPERATING SYSTEM & ADMIN	PP	100	40	44	23	67	Р						
210244	OPERATING SYSTEM & ADMIN	TW	25	10			20	Р						
210244	OPERATING SYSTEM & ADMIN	PR	50	20			40	Р						
210245	MICROPROCESSOR ARCHITECTURE	PP	100	40	36	25	61	P						
210245	MICROPROCESSOR ARCHITECTURE	TW	25	10			17	Р						

210245 MICROPROCESSOR ARCHITECTURE OR 50 20 28 P
210246 SOFT SKILLS TW 25 10 17 P
FIRST SEM TOTAL = 485/750

	: 25 JAN 2016 CENTRE: P	UNE I	NSTIT	TUTE OF	СОМР	UTER T	TECHNOLOGY, F	PUNE.	F	PAGE NO	. 148	
	FIRST LINE : SEAT NO., NAME O											
OTHER LIN	ES: SUB.CODE, SUB.NAME, SUB.TY	PE, M	MAX.MA	ARKS, M	IIN.PAS	SS MAR	RKS, OE MARKS	S, TH MARKS,	OBTAINED,	P/F:PA	SS/FAIL,	C:PREV.CARRY OVER
	4353 MEHENDALE ESHAN ANAND											
SEM.:	1											
210241	DISCRETE STRUCTURE	PP 1	L00 40	30	29	59	Р					
210242	DATA STRUCTURE & PROB SOLVING	PP 1	L00 40	35	23	58	Р					
210242	DATA STRUCTURE & PROB SOLVING	PR	50 20)		27	Р					
210243	DIGITAL ELEX. & LOGIC DESIGN	PP 1	LOO 40	39	20	59	Р					
210243	DIGITAL ELEX. & LOGIC DESIGN	TW	25 10)		19	Р					
210244	OPERATING SYSTEM & ADMIN	PP 1	L00 40	39	20	59	Р					
210244	OPERATING SYSTEM & ADMIN	TW	25 10)		19	Р					
210244	OPERATING SYSTEM & ADMIN	PR	50 20)		31	Р					
210245	MICROPROCESSOR ARCHITECTURE	PP 1	LOO 40	34	23	57	P					
210245	MICROPROCESSOR ARCHITECTURE	TW	25 10)		19	Р					
210245	MICROPROCESSOR ARCHITECTURE	OR	50 20)		9	F					
210246	SOFT SKILLS	TW	25 10)		19	Р					
	FIRST SEM TOTAL = $/750$											
s14005	4354 MEHENDARGE SHIVANI SUNI	L			SUCI	HITA		,71527393м	,PICT		,S1400543	54
SEM.:												
	DISCRETE STRUCTURE		L00 40			64						
	DATA STRUCTURE & PROB SOLVING				28	61						
	DATA STRUCTURE & PROB SOLVING				2-	40						
					27	66	-					
210243	DIGITAL ELEX. & LOGIC DESIGN				1.0	23						
210244	OPERATING SYSTEM & ADMIN		LOO 40		18	49						
			25 1050 20			21 30						
	OPERATING SYSTEM & ADMIN		100 40		25	60						
			25 10		23	21						
			50 20			31						
			25 10			22						
210240	FIRST SEM TOTAL = 488/750	I VV	23 10	,		22	r					
	4355 MEMANE SHRADDHA MAHESH							,71633700c				.55
314003	1999 PIERWAL SHADDIA PIANESH				··/ AIN -	-11		,, 1000,	,, 101		, 51 100575	
SEM.:	1											
	DISCRETE STRUCTURE	PP 1	LOO 40) 25	20	45	Р					
	DATA STRUCTURE & PROB SOLVING				23	47						
	DATA STRUCTURE & PROB SOLVING					20						
	DIGITAL ELEX. & LOGIC DESIGN				28	54						
	DIGITAL ELEX. & LOGIC DESIGN				-	19						
	OPERATING SYSTEM & ADMIN		L00 40		26	63						
	OPERATING SYSTEM & ADMIN		25 10		-	20						
	OPERATING SYSTEM & ADMIN		50 20			35						
210245	MICROPROCESSOR ARCHITECTURE	PP 1	LOO 40	29	27	56	Р					
210245	MICROPROCESSOR ARCHITECTURE	TW	25 10)		20	Р					

210245 MICROPROCESSOR ARCHITECTURE OR 50 20 40 P
210246 SOFT SKILLS TW 25 10 21 P
FIRST SEM TOTAL = 440/750

	E: 25 JAN 2016 CENTRE: P						•			PAGE NO.		
NOTE:	FIRST LINE : SEAT NO., NAME O	F TH	IE CANDI	DATE,	мотн	ER, PERMA	NENT REG.	NO., PRE	VIOUS SEAT	NO., C	OLLEGE,	SEAT NO.
	IES: SUB.CODE, SUB.NAME, SUB.TY	•		•		ŕ	•	ŕ	•	•		
S14005	4356 MENDRE RUCHA VILAS				ASHW	INI	,	71527397D	,PICT	,	S1400543	56
SEM.:	1											
210241	DISCRETE STRUCTURE	PP	100 40	36	26	62 P						
210242	DATA STRUCTURE & PROB SOLVING	PP	100 40	38	23	61 P						
210242	DATA STRUCTURE & PROB SOLVING	PR	50 20			40 P						
210243	DIGITAL ELEX. & LOGIC DESIGN	PP	100 40	41	23	64 P						
210243	DIGITAL ELEX. & LOGIC DESIGN	TW	25 10			21 P						
210244	OPERATING SYSTEM & ADMIN	PP	100 40	35	31	66 P						
210244	OPERATING SYSTEM & ADMIN	TW	25 10			19 P						
210244	OPERATING SYSTEM & ADMIN	PR	50 20			34 P						
210245	MICROPROCESSOR ARCHITECTURE	PP	100 40	36	30	66 P						
210245	MICROPROCESSOR ARCHITECTURE	TW	25 10			19 P						
210245	MICROPROCESSOR ARCHITECTURE	OR	50 20			37 P						
210246	SOFT SKILLS	TW	25 10			20 P						
	FIRST SEM TOTAL = 509/750											
	A257 MITKART ARTTVA RUALCUAN											
514003	i4357 mitkari aditya bhalchan	DKA			VAIS	HALI	,	/132/400G	,PICT	,	S1400543	57
SEM.:	1											
210241	DISCRETE STRUCTURE	PP	100 40	26	20	46 P						
210242	DATA STRUCTURE & PROB SOLVING	PP	100 40	27	15	42 P						
210242	DATA STRUCTURE & PROB SOLVING	PR	50 20			38 P						
210243	DIGITAL ELEX. & LOGIC DESIGN	PP	100 40	32	17	49 P						
210243	DIGITAL ELEX. & LOGIC DESIGN	TW	25 10			18 P						
210244	OPERATING SYSTEM & ADMIN	PP	100 40	32	21	53 P						
210244	OPERATING SYSTEM & ADMIN	TW	25 10			21 P						
210244	OPERATING SYSTEM & ADMIN	PR	50 20			42 P						
210245	MICROPROCESSOR ARCHITECTURE	PP	100 40	33	20	53 P						
210245	MICROPROCESSOR ARCHITECTURE	TW	25 10			18 P						
210245	MICROPROCESSOR ARCHITECTURE	OR	50 20			27 P						
210246	SOFT SKILLS	TW	25 10			19 P						
	FIRST SEM TOTAL = 426/750											
	34358 MOKASHI ASHISH ARVIND					A			,PICT		s1400543	
SEM.:		-	100 40	43	42	05 -						
	DISCRETE STRUCTURE					85 P						
210242				32	32	64 P						
210242				2.2	10	42 P						
210243				32	Т9	51 P						
210243					. .	19 P						
210244				40	24	64 P						
210244			25 10			19 P						
210244			50 20			38 P						
			100 40	38	23	61 P						
210245	MICROPROCESSOR ARCHITECTURE	TW	25 10			19 P						

210245 MICROPROCESSOR ARCHITECTURE OR 50 20 35 P
210246 SOFT SKILLS TW 25 10 21 P
FIRST SEM TOTAL = 518/750

	9 S.E.(2014 PAT.)(COMPUTER) :: 25 JAN 2016	UNE	INST	ITUTE	OF	COMPUT	ER	TECHNO	DLOGY, P	PUNE.		PAGE N	o. 150	
NOTE:	FIRST LINE : SEAT NO., NAME O	F TH	HE CA	NDIDA	ΛTE,	MOTHE	R,	PERMAN	NENT REG	. NO., PREV	IOUS SEA	T NO.,	COLLEGE,	SEAT NO.
OTHER LIN	ES: SUB.CODE, SUB.NAME, SUB.TY	PΕ,	MAX.	MARKS	, MI	N.PASS	MA	RKS, C	E MARKS	, TH MARKS,	OBTAINED	, P/F:P/	ASS/FAIL, C:	PREV.CARRY OVER
S14005	4359 MORE SONALI SHANKAR					ANITA				,71633701M	,PICT		,s140054359	
SEM.:	1													
	DISCRETE STRUCTURE	PP	100	40	29	43	72	Р						
210242	DATA STRUCTURE & PROB SOLVING	PP	100	40	32	26	58	Р						
210242	DATA STRUCTURE & PROB SOLVING	PR	50	20			40	Р						
210243	DIGITAL ELEX. & LOGIC DESIGN	PP	100	40	38	20	58	Р						
210243	DIGITAL ELEX. & LOGIC DESIGN	TW	25	10			21	Р						
210244	OPERATING SYSTEM & ADMIN	PP	100	40	46	23	69	Р						
210244	OPERATING SYSTEM & ADMIN	TW	25	10			20	Р						
210244	OPERATING SYSTEM & ADMIN	PR	50	20			35	Р						
210245	MICROPROCESSOR ARCHITECTURE	PP	100	40	25	25	50	Р						
210245	MICROPROCESSOR ARCHITECTURE	TW	25	10			20	Р						
210245	MICROPROCESSOR ARCHITECTURE	OR	50	20			35	Р						
210246	SOFT SKILLS	TW	25	10			22	Р						
	FIRST SEM TOTAL = $500/750$													
	4260													
\$14005	4360 MRIDUL PANKAJ KHANNA					ARADH	ANA			,71527415F	, PICT		,\$140054360	
SEM.:	1													
	DISCRETE STRUCTURE	DD	100	40	33	41	74	D						
	DATA STRUCTURE & PROB SOLVING					34	77							
	DATA STRUCTURE & PROB SOLVING				73	34	39							
	DIGITAL ELEX. & LOGIC DESIGN				34	31	65							
210243	DIGITAL ELEX. & LOGIC DESIGN				<i>3</i>	31	20	-						
	OPERATING SYSTEM & ADMIN		100		33	16	49							
	OPERATING SYSTEM & ADMIN		25				21							
	OPERATING SYSTEM & ADMIN		50				29							
210245	MICROPROCESSOR ARCHITECTURE		100		32	26	58							
210245	MICROPROCESSOR ARCHITECTURE	TW	25	10			19	Р						
210245	MICROPROCESSOR ARCHITECTURE	OR	50	20			25	Р						
210246	SOFT SKILLS	TW	25	10			21	Р						
	FIRST SEM TOTAL = 497/750													
S14005	4361 MRUNAL DHARMRAJ KHANDAG	ALE				SAVIT	Α			,71527416D	,PICT		,S140054361	
SEM.:														
	DISCRETE STRUCTURE				38	24	62							
	DATA STRUCTURE & PROB SOLVING				45	29	74							
	DATA STRUCTURE & PROB SOLVING						34							
210243	DIGITAL ELEX. & LOGIC DESIGN				35	29	64							
210243	DIGITAL ELEX. & LOGIC DESIGN				4-	20	22							
210244	OPERATING SYSTEM & ADMIN		100		45	29	74							
			25				21							
	OPERATING SYSTEM & ADMIN		50			27	35							
210245			100		44	27	71							
210245	MICROPROCESSOR ARCHITECTURE	١W	25	ΤO			21	Ч						

210245 MICROPROCESSOR ARCHITECTURE OR 50 20 42 P
210246 SOFT SKILLS TW 25 10 22 P
FIRST SEM TOTAL = 542/750

	: 25 JAN 2016 CENTRE: P	UNE INS	STITUTI	E OF	COMPUT	ER T	ECHNOLOGY, PL	JNE.	F	PAGE NO	. 151	
	FIRST LINE : SEAT NO., NAME O											
	ES: SUB.CODE, SUB.NAME, SUB.TY											
	4362 MUNAGEKAR ABHISHEK VILA											
SEM.:	1											
210241	DISCRETE STRUCTURE	PP 100	40	41	38	79	Р					
210242	DATA STRUCTURE & PROB SOLVING	PP 100	40	33	36	69	Р					
210242	DATA STRUCTURE & PROB SOLVING	PR 50	20			38	Р					
210243	DIGITAL ELEX. & LOGIC DESIGN	PP 100	40	40	34	74	Р					
210243	DIGITAL ELEX. & LOGIC DESIGN	TW 25	5 10			21	Р					
210244	OPERATING SYSTEM & ADMIN	PP 100	40	39	26	65	Р					
210244	OPERATING SYSTEM & ADMIN	TW 25	5 10			23	Р					
210244	OPERATING SYSTEM & ADMIN	PR 50	20			32	Р					
210245	MICROPROCESSOR ARCHITECTURE	PP 100	40	41	23	64	Р					
210245	MICROPROCESSOR ARCHITECTURE	TW 25	5 10			21	Р					
210245	MICROPROCESSOR ARCHITECTURE	OR 50	20			39	Р					
210246	SOFT SKILLS	TW 25	5 10			21	Р					
	FIRST SEM TOTAL = $546/750$											
S14005	4363 MUNDHADA DHANANJAY GOPA	L			PRAVI	NA	,	,71527420в	,PICT		,S1400543	63
SEM.:	1											
210241	DISCRETE STRUCTURE	PP 100) 40	44	43	87	Р					
	DATA STRUCTURE & PROB SOLVING			40	41	81						
210242	DATA STRUCTURE & PROB SOLVING	PR 50	20			37	Р					
210243	DIGITAL ELEX. & LOGIC DESIGN			38	35	73						
210243	DIGITAL ELEX. & LOGIC DESIGN					23						
210244	OPERATING SYSTEM & ADMIN	PP 100		37	29	66						
		TW 25				21						
	OPERATING SYSTEM & ADMIN	PR 50				30						
		PP 100		34	27	61						
		TW 25				21						
		OR 50				43						
210246		TW 25	5 10			20	Р					
	FIRST SEM TOTAL = 563/750											
	42C4 MUNICUT ATHARYA CHATTANY											
514005	4364 MUNSHI ATHARVA CHAITANY	Д			APARN	A	,	,71527421L	, PICI		,51400543	04
CEM .	1											
SEM.:		DD 100	. 40	11	20	71	D					
	DISCRETE STRUCTURE			41	30	71						
	DATA STRUCTURE & PROB SOLVING DATA STRUCTURE & PROB SOLVING			35	33	68 20						
				22	27							
	DIGITAL ELEX. & LOGIC DESIGN DIGITAL ELEX. & LOGIC DESIGN			33	<i>L1</i>	60 21						
	OPERATING SYSTEM & ADMIN	PP 100		31	20	51						
		TW 25		JΤ	20	21						
	OPERATING SYSTEM & ADMIN	PR 50				21						
	MICROPROCESSOR ARCHITECTURE	PR 30		36	22	58						
		TW 25		50	<i>- L</i>	20						
210243	MICHOI NOCESSON ANCHITECTURE	1 VV	, 10			20	•					

210245 MICROPROCESSOR ARCHITECTURE OR 50 20 27 P
210246 SOFT SKILLS TW 25 10 19 P
FIRST SEM TOTAL = 463/750

	9 S.E.(2014 PAT.)(COMPUTER) : 25 JAN 2016 CENTRE: PU	UNE	INSTI	TUTE (OF (COMPUT	ER -	ГЕСНИ	OLOGY, F	PUNE.			PAGE N	0. 152	
	FIRST LINE : SEAT NO., NAME OF ES: SUB.CODE, SUB.NAME, SUB.TY														
		-		•				•		•	-	•	-		
S14005	4365 MURTARKAR ANAGHA SANJAY					SHWET	ГА			,71527422〕	I	,PICT		,S140054365	
SEM.:	1														
210241	DISCRETE STRUCTURE	PP	100 40	0 32	2	36	68	Р							
210242	DATA STRUCTURE & PROB SOLVING	PP	100 40	0 32	2	26	58	Р							
210242	DATA STRUCTURE & PROB SOLVING	PR	50 20	0			22	Р							
210243	DIGITAL ELEX. & LOGIC DESIGN	PP	100 40	0 3	1	33	64	Р							
210243	DIGITAL ELEX. & LOGIC DESIGN	TW	25 10	0			19	Р							
210244	OPERATING SYSTEM & ADMIN	PP	100 40	0 34	4	23	57	Р							
210244	OPERATING SYSTEM & ADMIN	TW	25 10	0			19	Р							
210244	OPERATING SYSTEM & ADMIN	PR	50 20	0			27	Р							
210245	MICROPROCESSOR ARCHITECTURE	PP	100 40	0 34	4	26	60								
210245			25 10				18								
			50 20				41								
210246		TW	25 10	0			19	Р							
	FIRST SEM TOTAL = 472/750														
	40.00													,S140054366	
SEM.:	1														
210241	DISCRETE STRUCTURE	PP	100 40	0 3	7	36	73	Р							
210242	DATA STRUCTURE & PROB SOLVING	PP	100 40	0 34	4	30	64	Р							
210242	DATA STRUCTURE & PROB SOLVING	PR	50 20	0			21	Р							
210243	DIGITAL ELEX. & LOGIC DESIGN	PP	100 40	0 39	9	33	72	Р							
210243	DIGITAL ELEX. & LOGIC DESIGN	TW	25 10	0			22	Р							
210244	OPERATING SYSTEM & ADMIN	PP	100 40	0 34	4	27	61	Р							
210244	OPERATING SYSTEM & ADMIN	TW	25 10	0			20	Р							
210244	OPERATING SYSTEM & ADMIN		50 20				29								
		PP	100 40	0 34	4	28	62								
210245			25 10				21								
			50 20				21								
210246	SOFT SKILLS FIRST SEM TOTAL = 487/750	TW	25 10	0			21	Р							
S14005	4367 NAIK AISHWARYA CHANDRAKA	ANT				SEEMA	A			,71527426M	1	,PICT		,s140054367	
SEM.:	1														
		DD	100 40	n 3 [.]	1	21	52	D							
	DATA STRUCTURE & PROB SOLVING					18	46								
	DATA STRUCTURE & PROB SOLVING				0	10	20								
210242	DIGITAL ELEX. & LOGIC DESIGN				6	17	43								
210243	DIGITAL ELEX. & LOGIC DESIGN				-		19								
210244	OPERATING SYSTEM & ADMIN		100 40		0	16	46								
			25 10			-	19								
	OPERATING SYSTEM & ADMIN		50 20				26								
210245			100 40		7	16	43								
			25 10				19								

210245 MICROPROCESSOR ARCHITECTURE OR 50 20 20 P
210246 SOFT SKILLS TW 25 10 20 P
FIRST SEM TOTAL = 373/750

	: 25 JAN 2016 CENTRE: F	PUNE	INSTIT	UTE OF	COMPUTE	R TE	CHNOLOGY, PU	INE.	Ī	PAGE NO). 153	
	FIRST LINE : SEAT NO., NAME (•		-		-		•	•	
	ES: SUB.CODE, SUB.NAME, SUB.TY											
	4368 NAIKNAVARE SANJIVANI HA											
SEM.:	1											
	DISCRETE STRUCTURE	PP	100 40	36	32	68 P	•					
210242	DATA STRUCTURE & PROB SOLVING	G PP	100 40	39	32	71 P	•					
210242	DATA STRUCTURE & PROB SOLVING	G PR	50 20			36 P	•					
210243	DIGITAL ELEX. & LOGIC DESIGN	PP	100 40	37	29	66 P	•					
210243	DIGITAL ELEX. & LOGIC DESIGN	TW	25 10			22 P	•					
210244	OPERATING SYSTEM & ADMIN	PP	100 40	41	24	65 P	•					
210244	OPERATING SYSTEM & ADMIN	TW	25 10			20 P	•					
210244	OPERATING SYSTEM & ADMIN	PR	50 20			16 F	:					
210245	MICROPROCESSOR ARCHITECTURE	PP	100 40	32	26	58 P	•					
210245	MICROPROCESSOR ARCHITECTURE	TW	25 10			21 P						
210245	MICROPROCESSOR ARCHITECTURE	OR	50 20			39 P						
210246	SOFT SKILLS	TW	25 10			20 P	•					
	FIRST SEM TOTAL =/750											
S14005	4369 NAKUL SUBODH SARWATE				SHWETA	١	,	71527428н	,PICT		,S1400543	369
	•											
SEM.:			100 40	4.5	20	04 -						
	DISCRETE STRUCTURE					84 P						
-	DATA STRUCTURE & PROB SOLVING			_		70 P						
	DATA STRUCTURE & PROB SOLVING					38 P						
210243 210243	DIGITAL ELEX. & LOGIC DESIGN DIGITAL ELEX. & LOGIC DESIGN			37	24	61 P						
210243	OPERATING SYSTEM & ADMIN		100 40	30		51 P						
210244	OPERATING SYSTEM & ADMIN		25 10			19 P						
210244	OPERATING SYSTEM & ADMIN		50 20			39 P						
210245			100 40			67 P						
210245	MICROPROCESSOR ARCHITECTURE		25 10			19 P						
			50 20			22 P						
	SOFT SKILLS		25 10			23 P						
	FIRST SEM TOTAL = 512/750											
s14005	4370 NALE PALLAVI BAPURAO				SUNITA	\	,	71527430к	,PICT		,S1400543	370
SEM.:	1											
210241	DISCRETE STRUCTURE	PP	100 40	38	36	74 P	•					
210242	DATA STRUCTURE & PROB SOLVING	G PP	100 40	37	24	61 P	•					
210242	DATA STRUCTURE & PROB SOLVING	G PR	50 20			25 P	•					
210243	DIGITAL ELEX. & LOGIC DESIGN	PP	100 40	28	20	48 P						
210243	DIGITAL ELEX. & LOGIC DESIGN	TW	25 10			19 P						
210244	OPERATING SYSTEM & ADMIN	PP	100 40	35	20	55 P						
210244	OPERATING SYSTEM & ADMIN	TW	25 10			20 P						
210244	OPERATING SYSTEM & ADMIN	PR	50 20			26 P	•					
210245			100 40			52 P						
210245	MICROPROCESSOR ARCHITECTURE	TW	25 10			18 P	•					

210245 MICROPROCESSOR ARCHITECTURE OR 50 20 21 P
210246 SOFT SKILLS TW 25 10 18 P
FIRST SEM TOTAL = 437/750

	9 S.E.(2014 PAT.)(COMPUTER) : 25 JAN 2016 CENTRE: PU	JNE	INST	TUT	E OF	COMPU	TER ⁻	TECHNO	OLOGY, F	PUNE.		PAGE NO	o. 154	
									•					
NOTE:	FIRST LINE : SEAT NO., NAME OF	F TH	HE CA	NDID	ATE,	мотн	ER,	PERMAN	NENT REG	. NO., PREV	IOUS SEA	T NO.,	COLLEGE, S	EAT NO.
OTHER LIN	ES: SUB.CODE, SUB.NAME, SUB.TYI	PΕ,	MAX.	MARK	S, MI	N.PAS	S MAI	RKS, C	OE MARKS	, TH MARKS,	OBTAINED	, P/F:P/	ASS/FAIL, C:P	REV.CARRY OVER
	4371 NALHE PRABHAV OMPRAKASH									, 71527431н				
314003	43/1 NALHE PRABHAV UMPRARASH					KAKUI	NA			,71327431H	, PICI		,3140034371	
SEM.:	1													
210241	DISCRETE STRUCTURE	PP	100	40	35	29	64	Р						
210242	DATA STRUCTURE & PROB SOLVING	PP	100	40	37	22	59	Р						
210242	DATA STRUCTURE & PROB SOLVING	PR	50	20			31	Р						
210243	DIGITAL ELEX. & LOGIC DESIGN	PP	100	40	45	20	65	Р						
210243	DIGITAL ELEX. & LOGIC DESIGN	TW	25	10			20	Р						
210244	OPERATING SYSTEM & ADMIN	PP	100	40	32	22	54	Р						
210244	OPERATING SYSTEM & ADMIN	TW	25	10			22	Р						
210244	OPERATING SYSTEM & ADMIN	PR	50	20			25	Р						
210245	MICROPROCESSOR ARCHITECTURE	PP	100	40	30	19	49	Р						
210245	MICROPROCESSOR ARCHITECTURE	TW	25	10			19	Р						
210245	MICROPROCESSOR ARCHITECTURE	OR	50	20			26	Р						
210246	SOFT SKILLS	TW	25	10			19	Р						
	FIRST SEM TOTAL = $453/750$													
	4272 NANDITA VELAMANGUILI													
514005	4372 NANDITA YELAMANCHILI					BHARA	AIHI			,71527432F	, PICI		,5140054372	
SEM.:	1													
		DD	100	40	43	44	87	D						
	DATA STRUCTURE & PROB SOLVING						64							
	DATA STRUCTURE & PROB SOLVING				31	21	36							
	DIGITAL ELEX. & LOGIC DESIGN				3.8	25	63							
210243	DIGITAL ELEX. & LOGIC DESIGN				30	23	18	-						
	OPERATING SYSTEM & ADMIN		100		36	24	60							
			25				20							
	OPERATING SYSTEM & ADMIN		50				30							
			100		38	28	66							
			25				19							
			50				29							
	SOFT SKILLS						22							
	FIRST SEM TOTAL = 514/750													
S14005	4373 NAPHADE TEJASHREE ASHOK					CHHA	ΥA			,71527434в	,PICT		,s140054373	
SEM.:	1													
210241	DISCRETE STRUCTURE	PP	100	40	45	41	86	Р						
210242	DATA STRUCTURE & PROB SOLVING	PP	100	40	28	29	57	Р						
210242	DATA STRUCTURE & PROB SOLVING	PR	50	20			39	Р						
210243	DIGITAL ELEX. & LOGIC DESIGN				32	35	67							
210243	DIGITAL ELEX. & LOGIC DESIGN						18							
210244	OPERATING SYSTEM & ADMIN		100		26	19	45							
			25				20							
	OPERATING SYSTEM & ADMIN		50				30							
210245			100		30	28	58							
210245	MICROPROCESSOR ARCHITECTURE	TW	25	10			19	Р						

210245 MICROPROCESSOR ARCHITECTURE OR 50 20 37 P
210246 SOFT SKILLS TW 25 10 20 P
FIRST SEM TOTAL = 496/750

	9 S.E.(2014 PAT.)(COMPUTER) :: 25 JAN 2016	UNE :	INSTIT	UTE OF	СОМРИТ	ΓER 1	ECHNOI	LOGY, PL	INE.		PAGE NO	o. 155	
	FIRST LINE : SEAT NO., NAME OF												
	4374 NIDHI RAMANATHAN											,S140054374	
SEM.:	1												
		PP :	100 40	28	30	58	Р						
210242	DATA STRUCTURE & PROB SOLVING				22	53							
210242	DATA STRUCTURE & PROB SOLVING					41							
210243	DIGITAL ELEX. & LOGIC DESIGN	PP :	100 40	34	22	56	Р						
210243	DIGITAL ELEX. & LOGIC DESIGN	TW	25 10			19	Р						
210244	OPERATING SYSTEM & ADMIN	PP :	100 40	33	22	55	Р						
210244	OPERATING SYSTEM & ADMIN	TW	25 10			19	Р						
210244	OPERATING SYSTEM & ADMIN	PR	50 20			32	Р						
210245	MICROPROCESSOR ARCHITECTURE	PP :	100 40	41	22	63	Р						
210245	MICROPROCESSOR ARCHITECTURE	TW	25 10			19	Р						
210245	MICROPROCESSOR ARCHITECTURE	OR	50 20			29	Р						
210246	SOFT SKILLS	TW	25 10			22	Р						
	FIRST SEM TOTAL = 466/750												
	4375 NIKITA CHINCHWADE											,S140054375	
SEM.:	1												
210241	DISCRETE STRUCTURE	PP :	100 40	39	42	81	Р						
210242	DATA STRUCTURE & PROB SOLVING	PP :	100 40	41	27	68	Р						
210242	DATA STRUCTURE & PROB SOLVING	PR	50 20			43	Р						
210243	DIGITAL ELEX. & LOGIC DESIGN	PP :	100 40	35	29	64	Р						
210243	DIGITAL ELEX. & LOGIC DESIGN	TW	25 10			21	Р						
210244	OPERATING SYSTEM & ADMIN	PP :	100 40	38	28	66	Р						
210244	OPERATING SYSTEM & ADMIN	TW	25 10			21	Р						
210244	OPERATING SYSTEM & ADMIN		50 20			34							
210245	MICROPROCESSOR ARCHITECTURE		100 40		25	69							
210245			25 10			21							
			50 20			41							
210246	SOFT SKILLS FIRST SEM TOTAL = 552/750	TW	25 10			23	Р						
S14005	4376 NIKITA JYOTI				VINEE	ETA		,	71527443M	,PICT		,s140054376	
SEM.:	1												
		DD ²	100 40	42	22	75	В						
210241			100 40			75 57							
-					25	35							
210242	DIGITAL ELEX. & LOGIC DESIGN				21	56							
210243	DIGITAL ELEX. & LOGIC DESIGN				<u> </u>	19							
210243	OPERATING SYSTEM & ADMIN		23 10 100 40		26	58							
			25 10		20	20							
	OPERATING SYSTEM & ADMIN		50 20			32							
210244			100 40		26	65							
			25 10		_0	20							
		. ••	_3 _10			-0	•						

210245 MICROPROCESSOR ARCHITECTURE OR 50 20 21 P
210246 SOFT SKILLS TW 25 10 20 P
FIRST SEM TOTAL = 478/750

	E: 25 JAN 2016 CENTRE: P	UNE IN	STITU	TE OF	СОМРИТ	ΓER Τ	TECHNOLOGY, P	UNE.	F	PAGE NO	. 156	
	NES: SUB.CODE, SUB.NAME, SUB.TY			-		-		-		•	•	
	54377 NIMBALKAR SHUBHAM RAJEN											
SEM.:	:1											
210241	DISCRETE STRUCTURE	PP 10	0 40	28	25	53	Р					
210242	DATA STRUCTURE & PROB SOLVING	PP 10	0 40	31	15	46	Р					
210242	DATA STRUCTURE & PROB SOLVING	PR 5	0 20			40	Р					
210243	DIGITAL ELEX. & LOGIC DESIGN	PP 10	0 40	37	17	54	Р					
210243	DIGITAL ELEX. & LOGIC DESIGN	TW 2	5 10			13	Р					
210244	OPERATING SYSTEM & ADMIN	PP 10	0 40	38	15	53	Р					
210244	OPERATING SYSTEM & ADMIN	TW 2	5 10			18	Р					
210244	OPERATING SYSTEM & ADMIN	PR 5	0 20			32	Р					
210245	MICROPROCESSOR ARCHITECTURE	PP 10	0 40	29	21	50	Р					
210245	MICROPROCESSOR ARCHITECTURE	TW 2	5 10			19	Р					
210245	MICROPROCESSOR ARCHITECTURE	OR 5	0 20			22						
210246	SOFT SKILLS	TW 2	5 10			21	Р					
	FIRST SEM TOTAL = 421/750											
\$14005	54378 OAK RAJVARDHAN VIRENDRA				RADH1	ľΚΑ		,71527446F	,PICT	!	, \$1400543	/8
CEM .	.1											
SEM.:		DD 10	0 40	26	11	77	D					
	DISCRETE STRUCTURE DATA STRUCTURE & PROB SOLVING			36 36	41 29	77 65						
	DATA STRUCTURE & PROB SOLVING DATA STRUCTURE & PROB SOLVING			30	29	40						
210242				40	31	71						
210243				40	31	23	-					
210244		PP 10		37	29	66						
210244		TW 2		3,	23	23						
210244		PR 5				42						
		PP 10		44	29	73						
210245		TW 2				23						
210245		OR 5	0 20			45	Р					
		TW 2				22	Р					
	FIRST SEM TOTAL = 570/750											
S14005	54379 OMIK GOKUL MAHAJAN				ASHA			,71527448в	,PICT	:	,s1400543	79
SEM.:	:1											
210241	DISCRETE STRUCTURE	PP 10		45	35	80						
210242				39	29	68						
210242				s =		41						
210243				41	28	69						
210243					2.4	19						
210244		PP 10		37	24	61						
210244		TW 2				20						
	OPERATING SYSTEM & ADMIN	PR 5		2 =	27	26						
	MICROPROCESSOR ARCHITECTURE	PP 10		37	27	64						
210245	MICROPROCESSOR ARCHITECTURE	TW 2	2 TO			19	۲					

210245 MICROPROCESSOR ARCHITECTURE OR 50 20 43 P
210246 SOFT SKILLS TW 25 10 20 P
FIRST SEM TOTAL = 530/750

	9 S.E.(2014 PAT.)(COMPUTER) : 25 JAN 2016 CENTRE: PI	JNE	INSTI	TUTE	OF	COMPUTE	ER -	TECHNOLOGY,	PUNE.		Р	AGE N	0. 157	
NOTE:	FIRST LINE : SEAT NO., NAME OF	F TI	HE CAN	IDIDAT	E,	MOTHER	≀, Γ	PERMANENT R	EG. NO., PR	REVIC	OUS SEAT	NO.,	COLLEGE, SEAT NO.	
OTHER LIN	ES: SUB.CODE, SUB.NAME, SUB.TY	PΕ,	MAX.M	IARKS,	ΜI	N.PASS	MAF	RKS, OE MAR	KS, TH MARKS	5, OE	BTAINED,	P/F:P	ASS/FAIL, C:PREV.CARRY	OVER
	4200				• • •									
S14005	4380 PACHKAWADE ISHA BABAN					PKITY			,/152/4518	3	,PICI		,S140054380	
SEM.:	1													
210241	DISCRETE STRUCTURE	PP	100 4	0 3	6	38	74	Р						
210242	DATA STRUCTURE & PROB SOLVING	PP	100 4	0 4	1	21	62	Р						
210242	DATA STRUCTURE & PROB SOLVING	PR	50 2	20			20	Р						
210243	DIGITAL ELEX. & LOGIC DESIGN	PP	100 4	0 3	7	31	68	Р						
210243	DIGITAL ELEX. & LOGIC DESIGN	TW	25 1	.0			22	Р						
210244	OPERATING SYSTEM & ADMIN	PP	100 4	0 3	8	22	60	Р						
210244			25 1	_			19							
210244	OPERATING SYSTEM & ADMIN		50 2				28							
210245			100 4		.0		71							
210245			25 1				21							
210245			50 2				39							
210246		ΙW	25 1	.0			21	Р						
	FIRST SEM TOTAL = 505/750													
	4381 PALASH ASHOK SHAH												,s140054381	
									·					
SEM.:	1													
210241	DISCRETE STRUCTURE	PP	100 4	0 3	0	36	66	Р						
210242	DATA STRUCTURE & PROB SOLVING	PP	100 4	0 2	6	22	48	Р						
210242	DATA STRUCTURE & PROB SOLVING	PR	50 2	20			22	Р						
210243	DIGITAL ELEX. & LOGIC DESIGN	PP	100 4	0 3	6	18	54	Р						
210243	DIGITAL ELEX. & LOGIC DESIGN	TW	25 1	.0			19	Р						
210244	OPERATING SYSTEM & ADMIN	PP	100 4	0 2	7		42							
210244			25 1				21							
210244	OPERATING SYSTEM & ADMIN		50 2				14							
210245	MICROPROCESSOR ARCHITECTURE		100 4		.1		62							
			25 1				19							
210245			50 2				42							
210246	SOFT SKILLS FIRST SEM TOTAL =/750	I W	25 1	.0			19	Р						
	·													
s14005	4382 PALLOD RITESH RAMNARAYAI												,s140054382	
									,		,		,	
SEM.:	1													
210241	DISCRETE STRUCTURE	PP	100 4	0 4	4	37	81	Р						
210242	DATA STRUCTURE & PROB SOLVING	PP	100 4	0 3	8	32	70	Р						
210242	DATA STRUCTURE & PROB SOLVING	PR	50 2	20			34	Р						
210243	DIGITAL ELEX. & LOGIC DESIGN	PP	100 4	0 3	6	30	66	P						
210243	DIGITAL ELEX. & LOGIC DESIGN	TW	25 1	.0			20	P						
210244	OPERATING SYSTEM & ADMIN	PP	100 4	0 3	9	24	63	P						
210244			25 1				19							
210244	OPERATING SYSTEM & ADMIN		50 2				30							
210245			100 4		0		56							
210245	MICROPROCESSOR ARCHITECTURE	TW	25 1	.0			19	Р						

210245 MICROPROCESSOR ARCHITECTURE OR 50 20 37 P
210246 SOFT SKILLS TW 25 10 21 P
FIRST SEM TOTAL = 516/750

	9 S.E.(2014 PAT.)(COMPUTER) : 25 JAN 2016 CENTRE: PI	JNE	INSTITU	TE OF	COMPL	JTER TECHNOL	OGY, PUNE.	Р	AGE NO. 158
NOTE:	FIRST LINE : SEAT NO., NAME OF	F TI	HE CANDI	DATE,	MOTH	HER, PERMANE	ENT REG. NO., PRE	VIOUS SEAT	NO., COLLEGE, SEAT NO.
OTHER LIN	ES: SUB.CODE, SUB.NAME, SUB.TY	PΕ,	MAX.MAR	KS, M	IN.PAS	SS MARKS, OE	MARKS, TH MARKS,	OBTAINED,	P/F:PASS/FAIL, C:PREV.CARRY OVER
									6140054202
S14005	4383 PANDAV SAGAR RAJENDRA				PKA	ITRHA	,71527457М	, PICI	,5140054383
SEM.:	1								
210241	DISCRETE STRUCTURE	PP	100 40	43	35	78 P			
210242	DATA STRUCTURE & PROB SOLVING	PP	100 40	38	29	67 P			
210242	DATA STRUCTURE & PROB SOLVING	PR	50 20			30 P			
210243	DIGITAL ELEX. & LOGIC DESIGN	PP	100 40	39	27	66 P			
210243	DIGITAL ELEX. & LOGIC DESIGN	TW	25 10			18 P			
210244	OPERATING SYSTEM & ADMIN	PP	100 40	36	21	57 P			
210244			25 10			21 P			
210244	OPERATING SYSTEM & ADMIN		50 20			23 P			
210245			100 40	22	28	50 P			
210245			25 10			20 P			
			50 20			9 F			
210246		ΙW	25 10			21 P			
	FIRST SEM TOTAL =/750								
	4384 PANDILWAR AADITYA PRADEI					RMILA			,s140054384
321003	7,4,522,7,4,6,7,6,521,7,7,1,1,6,521		.0		J.,,		,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,	,. 20.	,52.003.501
SEM.:	1								
210241	DISCRETE STRUCTURE	PP	100 40	33	40	73 P			
210242	DATA STRUCTURE & PROB SOLVING	PP	100 40	37	31	68 P			
210242	DATA STRUCTURE & PROB SOLVING	PR	50 20			39 P			
210243	DIGITAL ELEX. & LOGIC DESIGN	PP	100 40	36	24	60 P			
210243	DIGITAL ELEX. & LOGIC DESIGN	TW	25 10			20 P			
210244	OPERATING SYSTEM & ADMIN	PP	100 40	36	21	57 P			
210244	OPERATING SYSTEM & ADMIN	TW	25 10			20 P			
210244	OPERATING SYSTEM & ADMIN		50 20			26 P			
210245	MICROPROCESSOR ARCHITECTURE		100 40	41	28	69 P			
			25 10			20 P			
210245			50 20			39 P			
210246		TW	25 10			19 P			
	FIRST SEM TOTAL = 510/750								
\$14005	4385 PARASKAR VAISHNAVI PRAKA								,s140054385
314003	TOO FARASKAR VAISHNAVI FRAK	1311			NTK	MALA	, 7 ±327 400M	,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,	,3140034303
SEM.:	1								
	DISCRETE STRUCTURE	PP	100 40	40	36	76 P			
210242	DATA STRUCTURE & PROB SOLVING			29	25	54 P			
210242	DATA STRUCTURE & PROB SOLVING	PR	50 20			15 F			
210243	DIGITAL ELEX. & LOGIC DESIGN	PP	100 40	31	25	56 P			
210243	DIGITAL ELEX. & LOGIC DESIGN	TW	25 10			19 P			
210244	OPERATING SYSTEM & ADMIN	PP	100 40	22	19	41 P			
210244	OPERATING SYSTEM & ADMIN	TW	25 10			20 P			
210244	OPERATING SYSTEM & ADMIN	PR	50 20			17 F			
210245	MICROPROCESSOR ARCHITECTURE	PP	100 40	33	24	57 P			
210245	MICROPROCESSOR ARCHITECTURE	TW	25 10			19 P			
41043	MITCHOLUGESON ANCHITECTURE	1 VV	73 TO			13 F			

210245 MICROPROCESSOR ARCHITECTURE OR 50 20 30 P
210246 SOFT SKILLS TW 25 10 19 P
FIRST SEM TOTAL = --/750

	9	UNE	INSTI	TUTE	OF	COMPU	TER ⁻	TECHNO	OLOGY, I	PUNE.		PAGE N	0. 159	
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NOTE:	FIRST LINE : SEAT NO., NAME OF	F TH	IE CAN	DIDA	ΓЕ,	МОТН	ER, I	PERMAN	NENT REG	G. NO., PRE	/IOUS SEA	T NO.,	COLLEGE, SEAT	NO.
OTHER LIN	ES: SUB.CODE, SUB.NAME, SUB.TY	PΕ,	MAX.M	IARKS	, MI	N.PAS	S MAI	RKS, C	OE MARKS	S, TH MARKS,	OBTAINED	, P/F:P	ASS/FAIL, C:PREV	.CARRY OVER
	4386 PARDESHI TANVI SUDHIR													
314003	4300 PARDESHI TANVI SUDHIR					VANI	IA			,71327402H	, PICI		,5140034360	
SEM.:	1													
210241	DISCRETE STRUCTURE	PP	100 4	0 3	38	40	78	Р						
210242	DATA STRUCTURE & PROB SOLVING	PP	100 4	.0 4	41	37	78	Р						
210242	DATA STRUCTURE & PROB SOLVING	PR	50 2	0			40	Р						
210243	DIGITAL ELEX. & LOGIC DESIGN	PP	100 4	0 3	33	28	61	Р						
210243	DIGITAL ELEX. & LOGIC DESIGN	TW	25 1	.0			22	Р						
210244	OPERATING SYSTEM & ADMIN	PP	100 4	0 3	34	31	65	Р						
210244	OPERATING SYSTEM & ADMIN	TW	25 1	.0			21	Р						
210244	OPERATING SYSTEM & ADMIN	PR	50 2	0			43	Р						
210245	MICROPROCESSOR ARCHITECTURE	PP	100 4	0 3	35	38	73	Р						
210245	MICROPROCESSOR ARCHITECTURE	TW	25 1	.0			22	Р						
210245	MICROPROCESSOR ARCHITECTURE	OR	50 2	0			41	Р						
210246	SOFT SKILLS	TW	25 1	.0			23	Р						
	FIRST SEM TOTAL = 567/750													
S140054387 PATEL AKSHAY CHIMANLAL MADHU										,71527464D				
314003	TATLE ANSHAT CHIMANEAL					MADIN	OBLIV			, 1 1321 4040	,1101		,5140054507	
SEM.:	1													
210241	DISCRETE STRUCTURE	PP	100 4	0 3	37	41	78	Р						
210242	DATA STRUCTURE & PROB SOLVING	PP	100 4	.0 4	43	27	70	Р						
210242	DATA STRUCTURE & PROB SOLVING	PR	50 2	0			39	Р						
210243	DIGITAL ELEX. & LOGIC DESIGN	PP	100 4	.0 4	42	32	74	Р						
210243	DIGITAL ELEX. & LOGIC DESIGN	TW	25 1	.0			21	Р						
210244	OPERATING SYSTEM & ADMIN	PP	100 4	.0 4	41	26	67	Р						
210244	OPERATING SYSTEM & ADMIN	TW	25 1	.0			22	Р						
210244	OPERATING SYSTEM & ADMIN	PR	50 2	0			35	Р						
210245	MICROPROCESSOR ARCHITECTURE	PP	100 4	0 3	38	33	71	Р						
210245	MICROPROCESSOR ARCHITECTURE	TW	25 1	.0			20	Р						
210245	MICROPROCESSOR ARCHITECTURE	OR	50 2	0			40	Р						
210246	SOFT SKILLS	TW	25 1	.0			20	Р						
	FIRST SEM TOTAL = $557/750$													
S14005	4388 PATEL NITIN MULCHAND					SAVI	TA			,71527465В	,PICT		,S140054388	
CEM .	1													
SEM.:		D D	100 4	^		26	40	D						
			100 4				49							
					25	17	42 37							
	DATA STRUCTURE & PROB SOLVING				26	22								
210243 210243	DIGITAL ELEX. & LOGIC DESIGN DIGITAL ELEX. & LOGIC DESIGN				26	<i>L L</i>	48 18							
210243	OPERATING SYSTEM & ADMIN		100 4		35	24	59							
			25 1		ر ر	۷4	59 18							
	OPERATING SYSTEM & ADMIN		50 2				39							
210244			100 4		39	29	68							
			25 1		, ,	23	18							
210243	MICKOT ROCESSON ARCHITECTURE	1 44	_ J _ L				Τ0	•						

210245 MICROPROCESSOR ARCHITECTURE OR 50 20 44 P
210246 SOFT SKILLS TW 25 10 19 P
FIRST SEM TOTAL = 459/750

	: 25 JAN 2016 CENTRE: P	UNE	INST	TITUTE	OF	COMPUT	ER	TECHNOLOGY,	PUNE.		PAGE N	0. 160	
	FIRST LINE : SEAT NO., NAME O												
	ES: SUB.CODE, SUB.NAME, SUB.TY												
	4389 PATIL AARYA ANIL									7J ,PIC			
SEM.:	1												
210241	DISCRETE STRUCTURE	PP	100	40	43	37	80	Р					
210242	DATA STRUCTURE & PROB SOLVING	PP	100	40	39	35	74	Р					
210242	DATA STRUCTURE & PROB SOLVING	PR	50	20			35	Р					
210243	DIGITAL ELEX. & LOGIC DESIGN	PP	100	40	38	26	64	P					
210243	DIGITAL ELEX. & LOGIC DESIGN	TW	25	10			19	Р					
210244	OPERATING SYSTEM & ADMIN	PP	100	40	34	31	65	Р					
210244	OPERATING SYSTEM & ADMIN	TW	25	10			20	Р					
210244	OPERATING SYSTEM & ADMIN	PR	50	20			45	Р					
210245	MICROPROCESSOR ARCHITECTURE	PP	100	40	46	38	84	P					
210245	MICROPROCESSOR ARCHITECTURE	TW	25	10			19	P					
210245	MICROPROCESSOR ARCHITECTURE	OR	50	20			44	P					
210246	SOFT SKILLS	TW	25	10			23	P					
	FIRST SEM TOTAL = $572/750$												
s14005	4390 PATIL BHUSHAN SANJIV					LALIT	Д		,7152746	8G ,PIC	Γ	,S140054	390
SEM.:													
	DISCRETE STRUCTURE		100	-	39		76						
_	DATA STRUCTURE & PROB SOLVING			-	37	29	66						
	DATA STRUCTURE & PROB SOLVING				22	2.4	30						
	DIGITAL ELEX. & LOGIC DESIGN				32	24	56	-					
210243	DIGITAL ELEX. & LOGIC DESIGN				21	20	18						
210244	OPERATING SYSTEM & ADMIN		100		31	30	61						
	OPERATING SYSTEM & ADMIN OPERATING SYSTEM & ADMIN		25 50				20 33						
			100		36	22	68						
			25		30	32	18						
			50				33						
			25				20						
210240	FIRST SEM TOTAL = 499/750	I VV	23	10			20	r					
	4391 PATIL DHANANJAY SHARAD									9E ,PIC			391
32.003	1331 171112 2111110113711 211111012					3007111			,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,	,,,,,	•	,0210051.	551
SEM.:	1												
	DISCRETE STRUCTURE	PP	100	40	43	32	75	Р					
	DATA STRUCTURE & PROB SOLVING				34		55						
	DATA STRUCTURE & PROB SOLVING						29						
	DIGITAL ELEX. & LOGIC DESIGN				36	20	56						
	DIGITAL ELEX. & LOGIC DESIGN						20						
	OPERATING SYSTEM & ADMIN		100		39	32	71						
	OPERATING SYSTEM & ADMIN		25				20						
210244	OPERATING SYSTEM & ADMIN	PR	50	20			29	Р					
210245	MICROPROCESSOR ARCHITECTURE	PP	100	40	29	23	52	Р					
210245	MICROPROCESSOR ARCHITECTURE	TW	25	10			20	Р					

210245 MICROPROCESSOR ARCHITECTURE OR 50 20 30 P
210246 SOFT SKILLS TW 25 10 18 P
FIRST SEM TOTAL = 475/750

	9 S.E.(2014 PAT.)(COMPUTER) : 25 JAN 2016 CENTRE: PU	UNE	INST	ITUTI	E OF	СОМРИТ	ER -	TECHNO	OLOGY, F	PUNE.		PAGE NO	o. 161	
NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: SUB.CODE, SUB.NAME, SUB.TYPE, MAX.MARKS, MIN.PASS MARKS, OE MARKS, TH MARKS, OBTAINED, P/F:PASS/FAIL, C:PREV.CARRY OVER														
	,	•			•			•		,		, ,	•	
	4392 PATIL NIKITA SHANTILAL													
SEM.:	1													
210241	DISCRETE STRUCTURE	PP	100	40	42	25	67	Р						
210242	DATA STRUCTURE & PROB SOLVING	PP	100	40	33	15	48	Р						
210242	DATA STRUCTURE & PROB SOLVING	PR	50	20			AA	F						
210243	DIGITAL ELEX. & LOGIC DESIGN	PP	100	40	38	17	55	Р						
210243	DIGITAL ELEX. & LOGIC DESIGN	TW	25	10			12	Р						
210244	OPERATING SYSTEM & ADMIN	PP	100	40	35	16	51	Р						
210244			25				13							
210244	OPERATING SYSTEM & ADMIN		50				AA							
210245			100		37	21	58							
210245			25				12							
			50				AA							
210246	SOFT SKILLS	IW	25	10			12	Р						
	FIRST SEM TOTAL =/750													
	4393 PATIL SANTOSH NANA									,71527474M				
										,	, -		,	
SEM.:	1													
210241	DISCRETE STRUCTURE	PP	100	40	37	44	81	Р						
210242	DATA STRUCTURE & PROB SOLVING	PP	100	40	33	31	64	Р						
210242	DATA STRUCTURE & PROB SOLVING	PR	50	20			29	Р						
210243	DIGITAL ELEX. & LOGIC DESIGN	PP	100	40	38	32	70	Р						
210243	DIGITAL ELEX. & LOGIC DESIGN	TW	25	10			15	Р						
210244	OPERATING SYSTEM & ADMIN	PP	100	40	30	22	52	Р						
210244	OPERATING SYSTEM & ADMIN	TW	25	10			18	Р						
210244	OPERATING SYSTEM & ADMIN		50				39							
			100		40	35	75							
			25				20							
			50				39							
210246	SOFT SKILLS	TW	25	10			19	Р						
	FIRST SEM TOTAL = 521/750													
\$14005	4394 PATIL SHRADDHA KISHOR												,s140054394	
314003	4394 FATTE SHRADDHA RISHOR					эпоыг	IANG.	L		,713274738	, PICI		,3140034334	
SEM.:	1													
		PP	100	40	31	29	60	Р						
	DATA STRUCTURE & PROB SOLVING				28	31	59							
	DATA STRUCTURE & PROB SOLVING					-	35							
	DIGITAL ELEX. & LOGIC DESIGN				43	30	73							
210243	DIGITAL ELEX. & LOGIC DESIGN						19							
210244	OPERATING SYSTEM & ADMIN		100		29	24	53							
210244	OPERATING SYSTEM & ADMIN	TW	25	10			20	Р						
210244	OPERATING SYSTEM & ADMIN	PR	50	20			41	Р						
210245	MICROPROCESSOR ARCHITECTURE	PP	100	40	41	29	70	Р						
210245	MICROPROCESSOR ARCHITECTURE	TW	25	10			18	Р						

210245 MICROPROCESSOR ARCHITECTURE OR 50 20 28 P
210246 SOFT SKILLS TW 25 10 18 P
FIRST SEM TOTAL = 494/750

	: 25 JAN 2016 CENTRE: P	UNE	INST	ГІТИТ	E OF	COMPU	TER -	TECHNOLOGY,	, PUNE			PAGE N	0. 162	
	FIRST LINE : SEAT NO., NAME O													
	ES: SUB.CODE, SUB.NAME, SUB.TY													
	4395 PATIL TANMAY BABASAHEB													
SEM.:	1													
210241	DISCRETE STRUCTURE	PP	100	40	32	31	63	Р						
210242	DATA STRUCTURE & PROB SOLVING	PP	100	40	28	20	48	Р						
210242	DATA STRUCTURE & PROB SOLVING	PR	50	20			23	Р						
210243	DIGITAL ELEX. & LOGIC DESIGN	PP	100	40	40	29	69	Р						
210243	DIGITAL ELEX. & LOGIC DESIGN	TW	25	10			12	Р						
210244	OPERATING SYSTEM & ADMIN	PP	100	40	31	21	52	Р						
210244	OPERATING SYSTEM & ADMIN	TW	25	10			18	Р						
210244	OPERATING SYSTEM & ADMIN	PR	50	20			23	Р						
210245	MICROPROCESSOR ARCHITECTURE	PP	100	40	35	27	62	Р						
210245	MICROPROCESSOR ARCHITECTURE	TW	25	10			15	Р						
210245	MICROPROCESSOR ARCHITECTURE	OR	50	20			20	Р						
210246	SOFT SKILLS	TW	25	10			20	Р						
	FIRST SEM TOTAL = $425/750$													
s14005	4396 PAWAR NAMRATA RAJENDRA					SUNI	TΑ		,71	633702K	,PICT		,S1400543	396
SEM.:	1													
210241	DISCRETE STRUCTURE	PP	100	40	25	21	46	Р						
210242	DATA STRUCTURE & PROB SOLVING	PP	100	40	34	36	70	Р						
210242	DATA STRUCTURE & PROB SOLVING	PR	50	20			27	Р						
210243	DIGITAL ELEX. & LOGIC DESIGN	PP	100	40	29	20	49	Р						
210243	DIGITAL ELEX. & LOGIC DESIGN	TW	25	10			21	Р						
210244	OPERATING SYSTEM & ADMIN		100		39	34	73							
	OPERATING SYSTEM & ADMIN		25				20	Р						
210244	OPERATING SYSTEM & ADMIN		50				41	Р						
			100		34	36	70							
210245	MICROPROCESSOR ARCHITECTURE	TW	25	10			20	Р						
210245	MICROPROCESSOR ARCHITECTURE	OR	50	20			21	Р						
210246		TW	25	10			21	Р						
	FIRST SEM TOTAL = $479/750$													
s14005	4397 PAWAR NEELAM VIJAY					RENU	KA		,71	527481D	,PICT		,S1400543	397
SEM.:														
	DISCRETE STRUCTURE		100			21	58							
	DATA STRUCTURE & PROB SOLVING				25	21	46							
210242	DATA STRUCTURE & PROB SOLVING						25							
210243	DIGITAL ELEX. & LOGIC DESIGN				30	19	49							
210243	DIGITAL ELEX. & LOGIC DESIGN				2.0	2.5	18							
	OPERATING SYSTEM & ADMIN		100		30	21	51							
210244	OPERATING SYSTEM & ADMIN		25				21							
	OPERATING SYSTEM & ADMIN		50		24	2.2	29							
	MICROPROCESSOR ARCHITECTURE		100		31	22	53							
210245	MICROPROCESSOR ARCHITECTURE	ΙW	25	ΤO			20	۲						

210245 MICROPROCESSOR ARCHITECTURE OR 50 20 26 P
210246 SOFT SKILLS TW 25 10 20 P
FIRST SEM TOTAL = 416/750

	9 S.E.(2014 PAT.)(COMPUTER) :: 25 JAN 2016	UNE	INSTIT	TUTE OF	COMP	UTER TECHNO	OLOGY, PUI	NE.		PAGE NO. 163	
NOTE:	FIRST LINE : SEAT NO., NAME O	F TI	HE CAND	DIDATE,	MOT	HER, PERMAN	NENT REG.	NO., PREV	'IOUS SEAT	NO., COLLEGE, SEAT NO.	
OTHER LIN	ES: SUB.CODE, SUB.NAME, SUB.TY	PΕ,	MAX.MA	ARKS, M	IIN.PA	SS MARKS, (DE MARKS,	TH MARKS,	OBTAINED,	P/F:PASS/FAIL, C:PREV.CARRY OVER	
										6140054700	
S14005	4398 PAWAR PRIYANKA DASHARATI	Н			LAI	А	•	/152/483L	, PICI	,S140054398	
SEM.:	1										
210241	DISCRETE STRUCTURE	PP	100 40	28	19	47 P					
210242	DATA STRUCTURE & PROB SOLVING	PP	100 40	30	17	47 P					
210242	DATA STRUCTURE & PROB SOLVING	PR	50 20)		6 F					
210243	DIGITAL ELEX. & LOGIC DESIGN	PP	100 40	20	20	40 P					
210243	DIGITAL ELEX. & LOGIC DESIGN	TW	25 10)		17 P					
210244	OPERATING SYSTEM & ADMIN	PP	100 40	35	17	52 P					
210244		TW	25 10)		19 P					
210244	OPERATING SYSTEM & ADMIN		50 20			29 P					
210245			100 40		25	42 P					
210245			25 10			20 P					
210245			50 20			20 P					
210246		IW	25 10)		19 P					
	FIRST SEM TOTAL =/750										
	4399 PAWAR SONALI SAKHARAM					ASHRI				,s140054399	
							,		,	,	
SEM.:	1										
210241	DISCRETE STRUCTURE	PP	100 40	28	21	49 P					
210242	DATA STRUCTURE & PROB SOLVING	PP	100 40	33	25	58 P					
210242	DATA STRUCTURE & PROB SOLVING	PR	50 20)		30 P					
210243	DIGITAL ELEX. & LOGIC DESIGN	PP	100 40	24	24	48 P					
210243	DIGITAL ELEX. & LOGIC DESIGN	TW	25 10)		20 P					
210244	OPERATING SYSTEM & ADMIN	PP	100 40	37	26	63 P					
210244			25 10			20 P					
210244	OPERATING SYSTEM & ADMIN		50 20			17 F					
210245	MICROPROCESSOR ARCHITECTURE		100 40		33	61 P					
			25 10			20 P					
210245			50 20			20 P					
210246		TW	25 10)		20 P					
	FIRST SEM TOTAL =/750										
\$14005	4400 PAWAR VINITA VIJAY					LA				,s140054400	
314003	TAWAK VINITA VIJAT					LA	y •	7 10557 041	,1 101	,3140034400	
SEM.:	1										
	DISCRETE STRUCTURE	PP	100 40	30	16	46 P					
210242	DATA STRUCTURE & PROB SOLVING	PP	100 40	37	26	63 P					
210242	DATA STRUCTURE & PROB SOLVING	PR	50 20)		35 P					
210243	DIGITAL ELEX. & LOGIC DESIGN	PP	100 40	32	20	52 P					
210243	DIGITAL ELEX. & LOGIC DESIGN	TW	25 10)		20 P					
210244	OPERATING SYSTEM & ADMIN	PP	100 40	38	28	66 P					
210244	OPERATING SYSTEM & ADMIN	TW	25 10)		21 P					
210244	OPERATING SYSTEM & ADMIN	PR	50 20)		41 P					
210245	MICROPROCESSOR ARCHITECTURE	PP	100 40	35	27	62 P					
210245	MICROPROCESSOR ARCHITECTURE	TW	25 10)		21 P					

210245 MICROPROCESSOR ARCHITECTURE OR 50 20 21 P
210246 SOFT SKILLS TW 25 10 22 P
FIRST SEM TOTAL = 470/750

	.9	UNE	INSTI	TUTE	0F	СОМРИТ	ΓER ⁻	TECHNO	OLOGY, I	PUNE.		PAGE N	0. 164	
									-					
NOTE:	FIRST LINE : SEAT NO., NAME OF	F TH	E CAN	IDIDAT	E,	MOTHE	ER,	PERMAI	NENT REC	G. NO., PRE	VIOUS SE	AT NO.,	COLLEGE, S	EAT NO.
OTHER LIN	ES: SUB.CODE, SUB.NAME, SUB.TYI	PΕ,	MAX.M	IARKS ,	MI	N.PASS	S MAI	RKS, (OE MARKS	S, TH MARKS,	OBTAINE	D, P/F:P	ASS/FAIL, C:P	REV.CARRY OVER
	4401 - DUUL 5 DDA JAKTA CANADAT													
S14005	4401 PHULE PRAJAKTA GANAPAT					VIJAY	ſΑ			,716337050	, PICI		,5140054401	
SEM.:	1													
210241	DISCRETE STRUCTURE	PP	100 4	10 3	31	31	62	Р						
210242	DATA STRUCTURE & PROB SOLVING	PP	100 4	10 3	35	27	62	Р						
210242	DATA STRUCTURE & PROB SOLVING	PR	50 2	20			22	Р						
210243	DIGITAL ELEX. & LOGIC DESIGN	PP	100 4	10 3	30	33	63	Р						
210243	DIGITAL ELEX. & LOGIC DESIGN	TW	25 1	_0			21	Р						
210244	OPERATING SYSTEM & ADMIN	PP	100 4	10 4	14	27	71	Р						
210244	OPERATING SYSTEM & ADMIN		25 1				21							
			50 2				21							
210245			100 4		33	30	63							
210245			25 1				21							
			50 2				10							
210246	SOFT SKILLS	I W	25 1	_0			23	Р						
	FIRST SEM TOTAL =/750													
													,s140054402	
										,	,		,	
SEM.:	1													
210241	DISCRETE STRUCTURE	PP	100 4	10 3	88	35	73	Р						
210242	DATA STRUCTURE & PROB SOLVING	PP	100 4	10 3	30	20	50	Р						
210242	DATA STRUCTURE & PROB SOLVING	PR	50 2	20			27	Р						
210243	DIGITAL ELEX. & LOGIC DESIGN	PP	100 4	10 2	29	23	52	Р						
210243	DIGITAL ELEX. & LOGIC DESIGN	TW	25 1	_0			24	Р						
210244	OPERATING SYSTEM & ADMIN	PP	100 4	10 3	37	31	68	Р						
			25 1				22							
	OPERATING SYSTEM & ADMIN		50 2				41							
210245	MICROPROCESSOR ARCHITECTURE		100 4		32	32	64							
			25 1				21							
			50 2				41							
210246	SOFT SKILLS	I W	25 1	_0			23	Р						
	FIRST SEM TOTAL = 506/750													
s14005	4403 PRACHI TOMAR												,s140054403	
311003	THOUSE THE TOP ARE					G01137				,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,	,, 10,		,5110051105	
SEM.:	1													
	DISCRETE STRUCTURE	PP	100 4	10 3	30	29	59	Р						
210242	DATA STRUCTURE & PROB SOLVING	PP	100 4	10 2	27	21	48	Р						
210242	DATA STRUCTURE & PROB SOLVING	PR	50 2	20			34	Р						
210243	DIGITAL ELEX. & LOGIC DESIGN	PP	100 4	10 2	25	23	48	Р						
210243	DIGITAL ELEX. & LOGIC DESIGN	TW	25 1	-0			17	Р						
210244	OPERATING SYSTEM & ADMIN	PP	100 4	10 3	34	26	60	Р						
210244	OPERATING SYSTEM & ADMIN	TW	25 1	_0			18	Р						
210244	OPERATING SYSTEM & ADMIN	PR	50 2	20			32	Р						
210245	MICROPROCESSOR ARCHITECTURE	PP	100 4	10 2	29	28	57	Р						
210245	MICROPROCESSOR ARCHITECTURE	TW	25 1	-0			17	Р						

210245 MICROPROCESSOR ARCHITECTURE OR 50 20 18 F
210246 SOFT SKILLS TW 25 10 22 P
FIRST SEM TOTAL = --/750

	: 25 JAN 2016 CENTRE: P	UNE	INST	ITUTE	OF	COMPUT	ER	TECHNOLO	GY, PU	INE.	J	PAGE N	o. 165	
	FIRST LINE : SEAT NO., NAME O													
	ES: SUB.CODE, SUB.NAME, SUB.TY													
	4404 PRANAY KUMAR KUNDU									71527493н				
SEM.:	1													
210241	DISCRETE STRUCTURE	PP	100	40	33	33	66	Р						
210242	DATA STRUCTURE & PROB SOLVING	PP	100	40	38	27	65	Р						
210242	DATA STRUCTURE & PROB SOLVING	PR	50	20			39	Р						
210243	DIGITAL ELEX. & LOGIC DESIGN	PP	100	40	39	24	63	Р						
210243	DIGITAL ELEX. & LOGIC DESIGN	TW	25	10			15	Р						
210244	OPERATING SYSTEM & ADMIN	PP	100	40	42	34	76	Р						
210244	OPERATING SYSTEM & ADMIN	TW	25	10			20	Р						
210244	OPERATING SYSTEM & ADMIN	PR	50	20			23	Р						
210245	MICROPROCESSOR ARCHITECTURE	PP	100	40	43	33	76	Р						
210245	MICROPROCESSOR ARCHITECTURE	TW	25	10			17	Р						
210245	MICROPROCESSOR ARCHITECTURE	OR	50	20			40	Р						
210246	SOFT SKILLS	TW	25	10			18	Р						
	FIRST SEM TOTAL = $518/750$													
s14005	4405 PRATIK MANGTANI					JAYA			,	71527495D	,PICT		,S1400544	105
SEM.:	1													
210241	DISCRETE STRUCTURE	PP	100	40	37	35	72	Р						
210242	DATA STRUCTURE & PROB SOLVING	PP	100	40	38	27	65	Р						
210242	DATA STRUCTURE & PROB SOLVING	PR	50	20			20	Р						
210243	DIGITAL ELEX. & LOGIC DESIGN	PP	100	40	25	31	56	Р						
210243	DIGITAL ELEX. & LOGIC DESIGN	TW	25	10			19	Р						
210244	OPERATING SYSTEM & ADMIN		100		34	29	63	Р						
210244	OPERATING SYSTEM & ADMIN						21							
210244	OPERATING SYSTEM & ADMIN		50				27							
			100		36	30	66							
210245			25				18	Р						
210245	MICROPROCESSOR ARCHITECTURE	OR	50	20			35	Р						
210246	SOFT SKILLS	TW	25	10			22	Р						
	FIRST SEM TOTAL = $484/750$													
							• • •							
s14005	4406 PSK SAUMYA P S SASTRY					MAHAL	AXM:	I	,	71527497L	,PICT		,S1400544	106
SEM.:														
	DISCRETE STRUCTURE		100			35	78							
	DATA STRUCTURE & PROB SOLVING				39	30	69							
							42							
	DIGITAL ELEX. & LOGIC DESIGN				31	29	60							
210243	DIGITAL ELEX. & LOGIC DESIGN				2.0	~-	18							
	OPERATING SYSTEM & ADMIN		100		38	27	65							
			25				21							
	OPERATING SYSTEM & ADMIN		50		a =	2.4	42							
	MICROPROCESSOR ARCHITECTURE		100		37	34	71							
210245	MICROPROCESSOR ARCHITECTURE	١W	25	ΤΩ			19	Р						

210245 MICROPROCESSOR ARCHITECTURE OR 50 20 43 P
210246 SOFT SKILLS TW 25 10 20 P
FIRST SEM TOTAL = 548/750

	:: 25 JAN 2016 CENTRE: PI	UNE I	NSTITUT	TE OF	COMPU	TER T	ECHNOLOGY,	PUNE.			PAGE NO). 166	
	FIRST LINE : SEAT NO., NAME OF SUB.CODE, SUB.NAME, SUB.TY			-		-		•			•	-	
S14005	4407 PUGALIA PRATIK PAWAN				PRIY	A		,71527	⁷ 498J	,PICT		,S1400544	.07
SEM.:	1												
210241	DISCRETE STRUCTURE	PP 1	.00 40	42	27	69	Р						
210242	DATA STRUCTURE & PROB SOLVING	PP 1	.00 40	39	31	70	Р						
210242	DATA STRUCTURE & PROB SOLVING	PR	50 20			39	Р						
210243	DIGITAL ELEX. & LOGIC DESIGN	PP 1	.00 40	39	19	58	Р						
210243	DIGITAL ELEX. & LOGIC DESIGN	TW	25 10			16	Р						
210244	OPERATING SYSTEM & ADMIN	PP 1	00 40	31	19	50	P						
210244	OPERATING SYSTEM & ADMIN	TW	25 10			19	Р						
210244	OPERATING SYSTEM & ADMIN	PR	50 20			17	F						
210245	MICROPROCESSOR ARCHITECTURE	PP 1	00 40	33	25	58	Р						
210245	MICROPROCESSOR ARCHITECTURE	TW	25 10			17	Р						
210245	MICROPROCESSOR ARCHITECTURE	OR	50 20			40	P						
210246	SOFT SKILLS	TW	25 10			19	P						
	FIRST SEM TOTAL = $/750$												
S14005	4408 RACHIT ABHAY JOSHI				VAIJ	AYANT	Ί	,71527	7502L	,PICT		,S1400544	.08
SEM.:		_											
	DISCRETE STRUCTURE		.00 40		16	44							
	DATA STRUCTURE & PROB SOLVING			36	18	54							
	DATA STRUCTURE & PROB SOLVING			24	4.5	35							
	DIGITAL ELEX. & LOGIC DESIGN			31	15	46							
	DIGITAL ELEX. & LOGIC DESIGN			2.2	2.5	17							
	OPERATING SYSTEM & ADMIN		00 40	33	25	58							
			25 10			20							
	OPERATING SYSTEM & ADMIN		50 20	20	20	22							
	MICROPROCESSOR ARCHITECTURE			38	28	66 17							
			25 10 50 20										
			25 10			21 19							
210240	FIRST SEM TOTAL = 419/750	I W	23 10			19	۲						
	FIRST SEM TOTAL = 419/730												
								,		,		,	
SEM.:	1												
210241	DISCRETE STRUCTURE	PP 1	.00 40	23	19	42	Р						
210242	DATA STRUCTURE & PROB SOLVING	PP 1	00 40	13	06		F						
210242	DATA STRUCTURE & PROB SOLVING	PR	50 20			AA	F						
210243	DIGITAL ELEX. & LOGIC DESIGN	PP 1	00 40	16	24	40	Р						
210243	DIGITAL ELEX. & LOGIC DESIGN	TW	25 10			10	Р						
210244	OPERATING SYSTEM & ADMIN	PP 1	.00 40	25	16	41	Р						
210244	OPERATING SYSTEM & ADMIN	TW	25 10			10	Р						
210244	OPERATING SYSTEM & ADMIN	PR	50 20			AA	F						
210245	MICROPROCESSOR ARCHITECTURE	PP 1	.00 40	23	07		F						
210245	MICROPROCESSOR ARCHITECTURE	TW	25 10			10	Р						

210245 MICROPROCESSOR ARCHITECTURE OR 50 20 AA F
210246 SOFT SKILLS TW 25 10 10 P
FIRST SEM TOTAL = --/750

	:: 25 JAN 2016 CENTR	-	E INS	TITUT	E OF	COMPUT	ER T	ECHN	OLOGY, I	PUNE.			PAGE N	10. 167		
	FIRST LINE : SEAT NO., NA															• •
	IES: SUB.CODE, SUB.NAME, SU				-		-				•		•	-		VER
	4410 RAPELLI YOGESH SHRI													,S140054		
SEM.:	1															
210241	DISCRETE STRUCTURE	Р	P 100	40	35	19	54	Р								
210242	DATA STRUCTURE & PROB SOL	VING P	P 100	40	28	15	43	Р								
210242	DATA STRUCTURE & PROB SOL	VING P	R 50	20			32	Р								
210243	DIGITAL ELEX. & LOGIC DES	IGN P	P 100	40	31	21	52	Р								
210243	DIGITAL ELEX. & LOGIC DES	IGN T	N 25	10			20	Р								
210244	OPERATING SYSTEM & ADMIN	Р	P 100	40	26	23	49	Р								
210244	OPERATING SYSTEM & ADMIN	T	N 25	10			20	Р								
210244	OPERATING SYSTEM & ADMIN	Р	R 50	20			23	Р								
210245	MICROPROCESSOR ARCHITECTU	RE P	P 100	40	25	23	48	Р								
210245	MICROPROCESSOR ARCHITECTU	RE T	N 25	10			20	Р								
210245	MICROPROCESSOR ARCHITECTU	RE O	R 50	20			23	Р								
210246	SOFT SKILLS	T	N 25	10			21	Р								
	FIRST SEM TOTAL = $405/750$															
S14005	4411 RASHI BISEN					RACHN	A			,7152	7517յ	,PICT		,S140054	411	
SEM.:	1															
210241	DISCRETE STRUCTURE	Р	P 100	40	38	40	78	Р								
210242	DATA STRUCTURE & PROB SOL	VING P	P 100	40	43	34	77	Р								
210242	DATA STRUCTURE & PROB SOL	VING P	R 50	20			34	Р								
210243	DIGITAL ELEX. & LOGIC DES	IGN P	P 100	40	39	32	71	Р								
210243	DIGITAL ELEX. & LOGIC DES	IGN T	N 25	10			20	Р								
210244	OPERATING SYSTEM & ADMIN	Р	P 100	40	32	27	59	Р								
210244	OPERATING SYSTEM & ADMIN	T	N 25	10			22	Р								
210244	OPERATING SYSTEM & ADMIN	Р	R 50	20			41	Р								
210245	MICROPROCESSOR ARCHITECTU	RE P	P 100	40	40	37	77	Р								
210245	MICROPROCESSOR ARCHITECTU	RE T	N 25	10			19	Р								
210245	MICROPROCESSOR ARCHITECTU		R 50				41									
210246	SOFT SKILLS		w 25	10			20	Р								
	FIRST SEM TOTAL = 559/750															
	4412 RASHI GOYAL					SIMI						,PICT				• •
SEM.:																
	DISCRETE STRUCTURE				37	41	78									
					34	34	68									
210242	DATA STRUCTURE & PROB SOL						40									
	DIGITAL ELEX. & LOGIC DES				25	33	58									
210243	DIGITAL ELEX. & LOGIC DES				3 -	2.	21									
210244	OPERATING SYSTEM & ADMIN		P 100		35	34	69									
210244	OPERATING SYSTEM & ADMIN		N 25				22									
210244	OPERATING SYSTEM & ADMIN		R 50		20	2.0	27									
210245	MICROPROCESSOR ARCHITECTU		P 100		39	36	75									
210245	MICROPROCESSOR ARCHITECTU	KE T	N 25	ΤÜ			20	Р								

210245 MICROPROCESSOR ARCHITECTURE OR 50 20 45 P
210246 SOFT SKILLS TW 25 10 23 P
FIRST SEM TOTAL = 546/750

	: 25 JAN 2016 CENTRE: PI	UNE	INSTITU	TE OF	COMPUT	ER 7	ΓΕCHNOLOGY,	PUNE.			PAGE NO	o. 168	
	FIRST LINE : SEAT NO., NAME OF ES: SUB.CODE, SUB.NAME, SUB.TY			-		-		•	•		•	•	
S14005	4413 RASHINKAR VAIDEHI SANJA	Y			SUVAR	NA		,71527	7519E	,PICT		,S1400544	13
SEM.:	1												
		PP	100 40	29	31	60	Р						
210242	DATA STRUCTURE & PROB SOLVING		100 40	22	24	46							
210242	DATA STRUCTURE & PROB SOLVING	PR	50 20			8	F						
210243	DIGITAL ELEX. & LOGIC DESIGN	PP	100 40	28	15	43	Р						
210243	DIGITAL ELEX. & LOGIC DESIGN	TW	25 10			10	Р						
210244	OPERATING SYSTEM & ADMIN	PP	100 40	18	22	40	Р						
210244	OPERATING SYSTEM & ADMIN	TW	25 10			12	Р						
210244	OPERATING SYSTEM & ADMIN	PR	50 20			23	Р						
210245	MICROPROCESSOR ARCHITECTURE	PP	100 40	30	15	45	Р						
210245	MICROPROCESSOR ARCHITECTURE	TW	25 10			10	Р						
210245	MICROPROCESSOR ARCHITECTURE	OR	50 20			9	F						
210246	SOFT SKILLS	TW	25 10			10	Р						
	FIRST SEM TOTAL =/750												
S14005	4414 RATHOD GAURAV SUBHASH				NISHA			,71527	7523C	,PICT		,S1400544	14
CEM .	1												
SEM.:		DD	100 40	22	20	43	D						
	DATA STRUCTURE & PROB SOLVING				20 19								
	DATA STRUCTURE & PROB SOLVING DATA STRUCTURE & PROB SOLVING			12	19	0							
	DIGITAL ELEX. & LOGIC DESIGN			22	11								
	DIGITAL ELEX. & LOGIC DESIGN			22	11	 17							
			100 40	34	1 Ω	52							
			25 10	74	10	17							
	OPERATING SYSTEM & ADMIN		50 20			23							
			100 40	21	26	47							
			25 10		20	16							
	MICROPROCESSOR ARCHITECTURE					12							
	SOFT SKILLS					18							
210210	FIRST SEM TOTAL =/750		23 10			10	•						
S14005	4415 RATHOD NIRANJAN VINAYAK				USHA			,71527	7524M	,PICT		,S1400544	15
CEM -	1												
SEM.:		-	100 40	20	2.0	7.4	Б						
			100 40	38		74							
	DATA STRUCTURE & PROB SOLVING			29	31	60							
	DATA STRUCTURE & PROB SOLVING			2.5	2.4	40							
	DIGITAL ELEX. & LOGIC DESIGN			35	4	59 16							
				40	27	16 67							
210244			100 40	40	27	67							
	OPERATING SYSTEM & ADMIN					19							
	OPERATING SYSTEM & ADMIN		50 20	21	2.2	30							
			100 40	31	55	64							
210245	MICROPROCESSOR ARCHITECTURE	ı W	25 10			19	۲						

210245 MICROPROCESSOR ARCHITECTURE OR 50 20 42 P
210246 SOFT SKILLS TW 25 10 18 P
FIRST SEM TOTAL = 508/750

	: : 25 JAN 2016 CENTRE: PI	UNE INSTITU	JTE OF	COMPU	TER TECHNOL	OGY, PUNE.	ı	PAGE NO. 169	
	ETROT LINE - COAT NO NAME O								
	FIRST LINE : SEAT NO., NAME OF		-		•	•			
		•	-		•	•			
S14005	4416 RATNAPARKHI VISHAKHA PR	ASHANT		REKH	Α	,7152752	28D ,PICT	,S140054	116
SEM.:	1								
	DISCRETE STRUCTURE	PP 100 40	33	38	71 P				
210242	DATA STRUCTURE & PROB SOLVING	PP 100 40	34	32	66 P				
210242	DATA STRUCTURE & PROB SOLVING	PR 50 20			38 P				
210243	DIGITAL ELEX. & LOGIC DESIGN	PP 100 40	38	25	63 P				
210243	DIGITAL ELEX. & LOGIC DESIGN	TW 25 10			19 P				
210244	OPERATING SYSTEM & ADMIN	PP 100 40	32	26	58 P				
210244	OPERATING SYSTEM & ADMIN	TW 25 10			22 P				
210244	OPERATING SYSTEM & ADMIN	PR 50 20			30 P				
210245	MICROPROCESSOR ARCHITECTURE	PP 100 40	36	38	74 P				
210245	MICROPROCESSOR ARCHITECTURE	TW 25 10			18 P				
210245	MICROPROCESSOR ARCHITECTURE	OR 50 20			40 P				
210246	SOFT SKILLS	TW 25 10			19 P				
	FIRST SEM TOTAL = 518/750								
	4417								
514005	4417 RAUNAK MAHESHWARI			VARS	НА	,/152/5/	Z9B ,PICI	,S140054	11/
SEM.:	1								
	DISCRETE STRUCTURE	PP 100 40	39	33	72 P				
	DATA STRUCTURE & PROB SOLVING				63 P				
	DATA STRUCTURE & PROB SOLVING		30	21	39 P				
	DIGITAL ELEX. & LOGIC DESIGN		35	24	59 P				
	DIGITAL ELEX. & LOGIC DESIGN		33	- 1	16 P				
	OPERATING SYSTEM & ADMIN	PP 100 40	33	18	51 P				
		TW 25 10			19 P				
	OPERATING SYSTEM & ADMIN	PR 50 20			26 P				
210245	MICROPROCESSOR ARCHITECTURE	PP 100 40	33	20	53 P				
210245	MICROPROCESSOR ARCHITECTURE	TW 25 10			16 P				
210245	MICROPROCESSOR ARCHITECTURE	OR 50 20			30 P				
210246	SOFT SKILLS	TW 25 10			21 P				
	FIRST SEM TOTAL = 465/750								
S14005	4418 RAUT SHUBHANGI SHASHIKA	NT		UJWA	LA	,715275	31D ,PICT	,S140054	118
SEM.:									
	DISCRETE STRUCTURE				83 P				
	DATA STRUCTURE & PROB SOLVING		39	30	69 P				
	DATA STRUCTURE & PROB SOLVING		34	20	34 P				
	DIGITAL ELEX. & LOGIC DESIGN		31	29	60 P				
	DIGITAL ELEX. & LOGIC DESIGN		71	1.0	20 P				
210244		PP 100 40	31	16	47 P				
	OPERATING SYSTEM & ADMIN	PR 50 20			19 P				
	OPERATING SYSTEM & ADMIN MICROPROCESSOR ARCHITECTURE	PR 30 20 PP 100 40	27	23	27 P 50 P				
	MICROPROCESSOR ARCHITECTURE MICROPROCESSOR ARCHITECTURE	TW 25 10	۷1	23	18 P				
210243	MICROPROCESSOR ARCHITECTURE	1W Z3 IU			10 P				

210245 MICROPROCESSOR ARCHITECTURE OR 50 20 35 P
210246 SOFT SKILLS TW 25 10 18 P
FIRST SEM TOTAL = 480/750

	9 S.E.(2014 PAT.)(COMPUTER) :: 25 JAN 2016	UNE	INST:	ITUTE	OF	СОМРИТ	TER '	TECHNO	OLOGY, F	PUNE.		PAGE N	o. 170	
NOTE:	FIRST LINE : SEAT NO., NAME OF	F TH	HE CAI	NDIDA	AΤE,	MOTHE	ER,	PERMAI	NENT REG	G. NO., PRE\	/IOUS SEA	T NO.,	COLLEGE, SE	AT NO.
OTHER LIN	ES: SUB.CODE, SUB.NAME, SUB.TY	PΕ,	MAX.	MARKS	, MI	N.PASS	S MA	RKS, (OE MARKS	S, TH MARKS,	OBTAINED), P/F:P	ASS/FAIL, C:PR	EV.CARRY OVER
	4419 RAVATE ABHISHEK NITIN													
314003	THIS RAVATE ABRISHER NITTIN					SUNTI	A			,71327333L	, FICT		,3140034419	
SEM.:	1													
210241	DISCRETE STRUCTURE	PP	100 4	40	29	20	49	Р						
210242	DATA STRUCTURE & PROB SOLVING	PP	100	40	27	24	51	Р						
210242	DATA STRUCTURE & PROB SOLVING	PR	50 2	20			5	F						
210243	DIGITAL ELEX. & LOGIC DESIGN	PP	100	40	33	23	56	Р						
210243	DIGITAL ELEX. & LOGIC DESIGN	TW	25	10			18							
210244	OPERATING SYSTEM & ADMIN		100 4		28	24	52							
210244			25				18							
210244	OPERATING SYSTEM & ADMIN		50 7		27	2.2	31							
210245			100		27	23	50							
210245			25 i				17 35							
	MICROPROCESSOR ARCHITECTURE SOFT SKILLS						33 18							
210240	FIRST SEM TOTAL =/750	I W	23.	10			10	r						
	4420 RISHIKA GOYAL									,71527538M				
SEM.:	1													
210241	DISCRETE STRUCTURE	PP	100	40	42	44	86	Р						
210242	DATA STRUCTURE & PROB SOLVING	PP	100 4	40	43	29	72	Р						
210242	DATA STRUCTURE & PROB SOLVING	PR	50 2	20			37	Р						
210243	DIGITAL ELEX. & LOGIC DESIGN	PP	100 4	40	32	30	62	Р						
210243	DIGITAL ELEX. & LOGIC DESIGN						19							
	OPERATING SYSTEM & ADMIN		100		39	28	67							
			25				18							
	OPERATING SYSTEM & ADMIN		50 7		20	2.5	35							
210245	MICROPROCESSOR ARCHITECTURE MICROPROCESSOR ARCHITECTURE		100 4 25 1		39	33	74 19							
			50 2				35							
	MICROPROCESSOR ARCHITECTURE SOFT SKILLS						22							
210240	FIRST SEM TOTAL = 546/750	I W	23.	10			22	г						
s14005	4421 ROHAN POULOSE									,71527539к				
											·			
SEM.:	1													
210241	DISCRETE STRUCTURE	PP	100	40	41	41	82	Р						
210242	DATA STRUCTURE & PROB SOLVING	PP	100	40	40	30	70	Р						
210242	DATA STRUCTURE & PROB SOLVING	PR	50 2	20			39	Р						
210243	DIGITAL ELEX. & LOGIC DESIGN	PP	100	40	29	28	57	Р						
210243	DIGITAL ELEX. & LOGIC DESIGN						22							
210244	OPERATING SYSTEM & ADMIN		100		39	22	61							
			25				23							
	OPERATING SYSTEM & ADMIN		50 7		-	2.2	27							
210245			100		35	26	61							
210245	MICROPROCESSOR ARCHITECTURE	TW	25	ΤO			22	Р						

210245 MICROPROCESSOR ARCHITECTURE OR 50 20 36 P
210246 SOFT SKILLS TW 25 10 22 P
FIRST SEM TOTAL = 522/750

	: 25 JAN 2016 CENTRE		INST	ITUTE OF	COMPUT	ER TECH	HNOLOGY, PU	INE.	1	PAGE NO. 171	
	ETROT LINE - CEAT NO NAME										
	FIRST LINE : SEAT NO., NAME ES: SUB.CODE, SUB.NAME, SUB			•		-		•		•	
	4422 RUPNAWAR SHIVANI NIWE										
311003	TILL KOTHAWAK SHIVAKI KIWI	(0111			JOHAN		,	7 1 5 2 7 5 1 11	,1 101	,511005	1122
SEM.:											
	DISCRETE STRUCTURE				25	56 P					
	DATA STRUCTURE & PROB SOLV				27	58 P					
210242	DATA STRUCTURE & PROB SOLV					30 P					
210243	DIGITAL ELEX. & LOGIC DESIGNATION OF THE PROPERTY OF THE PROPE				28	65 P					
210243	DIGITAL ELEX. & LOGIC DESIGNATIVE CONTRACTOR & ADMINI				1 5	23 P					
210244 210244	OPERATING SYSTEM & ADMIN OPERATING SYSTEM & ADMIN		v 25 í		15	58 P 19 P					
210244	OPERATING SYSTEM & ADMIN		v 23 . R 50 2			30 P					
210244	MICROPROCESSOR ARCHITECTURE		2 100 4		22	62 P					
210245	MICROPROCESSOR ARCHITECTURE		v 25 1		22	18 P					
210245	MICROPROCESSOR ARCHITECTURE		· 50 2			10 F					
	SOFT SKILLS					19 P					
2202.0	FIRST SEM TOTAL = $/750$	• •									
	4423 SABLE HIMANSHU KAILAS									,S14005	
									·	·	
SEM.:	1										
210241	DISCRETE STRUCTURE	PF	100 4	40 37	33	70 P					
210242	DATA STRUCTURE & PROB SOLV	ING P	2 100 4	40 42	31	73 P					
210242	DATA STRUCTURE & PROB SOLVE	ING P	R 50 2	20		39 P					
210243	DIGITAL ELEX. & LOGIC DESIG	SN PF	2 100 4	40 30	26	56 P					
210243	DIGITAL ELEX. & LOGIC DESIG	GN TV	v 25 1	10		20 P					
210244	OPERATING SYSTEM & ADMIN	PF	2 100 4	40 36	23	59 P					
210244	OPERATING SYSTEM & ADMIN	ΤV	v 25 1	10		20 P					
210244	OPERATING SYSTEM & ADMIN		R 50 2			47 P					
210245	MICROPROCESSOR ARCHITECTURE		2 100 4		38	69 P					
210245	MICROPROCESSOR ARCHITECTURE		v 25 1			20 P					
	MICROPROCESSOR ARCHITECTURE		R 50 2			33 P					
210246	SOFT SKILLS	TV	V 25 1	10		21 P					
	FIRST SEM TOTAL = 527/750										
	4424 CAJANE AMBUTA ABTNATI							71527551J		,\$14005	4424
314003	4424 SAJANE AMRUTA ADINATI	1			TEJASI	MTINT	,	/ 132/ 3313	, PICI	,514003	4424
SEM.:	1										
	DISCRETE STRUCTURE	PF	2 100 4	40 43	47	90 P					
	DATA STRUCTURE & PROB SOLVE					69 P					
						38 P					
210243	DIGITAL ELEX. & LOGIC DESIG				37	78 P					
210243	DIGITAL ELEX. & LOGIC DESIG					21 P					
210244	OPERATING SYSTEM & ADMIN				19	46 P					
210244	OPERATING SYSTEM & ADMIN		v 25 1			21 P					
210244	OPERATING SYSTEM & ADMIN	PF	R 50 2	20		31 P					
210245	MICROPROCESSOR ARCHITECTURE	E PF	100 4	40 37	29	66 P					
210245	MICROPROCESSOR ARCHITECTURE	E TV	v 25 1	10		20 P					

210245 MICROPROCESSOR ARCHITECTURE OR 50 20 11 F 210246 SOFT SKILLS TW 25 10 20 P FIRST SEM TOTAL = --/750

	:: 25 JAN 2016 CENTRE: F	PUNE IN	STITUT	ΓE OF	COMPUTE	ER T	ECHNOLOGY, PU	NE.	ı	PAGE NO). 172	
	ETDET LINE . CEAT NO NAME (
	FIRST LINE : SEAT NO., NAME (IES: SUB.CODE, SUB.NAME, SUB.T)			-		-		•		•	-	
	4425 SAJJANWAR PRATIK NANDKI											
CEM .	1											
SEM.: 210241	DISCRETE STRUCTURE	PP 10	0 40	33	28	61	р					
	DATA STRUCTURE & PROB SOLVING			29		47						
	DATA STRUCTURE & PROB SOLVING					34						
210243	DIGITAL ELEX. & LOGIC DESIGN			27	27	54						
210243	DIGITAL ELEX. & LOGIC DESIGN	TW 2	5 10			20	Р					
210244	OPERATING SYSTEM & ADMIN	PP 100	0 40	26	26	52	Р					
210244	OPERATING SYSTEM & ADMIN	TW 2	5 10			18	Р					
210244	OPERATING SYSTEM & ADMIN	PR 50	0 20			26	P					
210245	MICROPROCESSOR ARCHITECTURE	PP 100	0 40	29	28	57	P					
210245	MICROPROCESSOR ARCHITECTURE	TW 2	5 10			20	P					
210245	MICROPROCESSOR ARCHITECTURE	OR 50	0 20			22	P					
210246	SOFT SKILLS	TW 2	5 10			21	P					
	FIRST SEM TOTAL = $432/750$											
S14005	34426 SAKHADEO ARCHIT MILIND				SWATI		,	71527554C	,PICT		,S140054 ²	126
CEM .	1											
SEM.:	DISCRETE STRUCTURE	DD 10	0 40	40	42	00	D					
	DATA STRUCTURE & PROB SOLVING			38		82 70						
	DATA STRUCTURE & PROB SOLVING			30		40						
	DIGITAL ELEX. & LOGIC DESIGN			30	28	58						
210243	DIGITAL ELEX. & LOGIC DESIGN			30	_	18						
210244	OPERATING SYSTEM & ADMIN	PP 100		37		63						
210244		TW 2		31	20	22						
	OPERATING SYSTEM & ADMIN	PR 50				47						
210245		PP 100		36		68						
	MICROPROCESSOR ARCHITECTURE	TW 2				17						
		OR 50	0 20			41	Р					
210246		TW 2				17	Р					
	FIRST SEM TOTAL = 543/750											
S14005	4427 SAKSHI MALIK				NEELU		,	71527556к	,PICT		,S1400544	127
SEM.:												
	DISCRETE STRUCTURE	PP 100		47		93						
	DATA STRUCTURE & PROB SOLVING			44		84						
	DATA STRUCTURE & PROB SOLVING					41						
210243	DIGITAL ELEX. & LOGIC DESIGN			35	39	74						
210243	DIGITAL ELEX. & LOGIC DESIGN					23						
	OPERATING SYSTEM & ADMIN	PP 100		37	21	58						
210244	OPERATING SYSTEM & ADMIN	TW 2				23						
	OPERATING SYSTEM & ADMIN	PR 50		2.5	20	37						
		PP 100		36		65						
210245	MICROPROCESSOR ARCHITECTURE	TW 2	5 10			23	Р					

210245 MICROPROCESSOR ARCHITECTURE OR 50 20 44 P
210246 SOFT SKILLS TW 25 10 23 P
FIRST SEM TOTAL = 588/750

	: 25 JAN 2016 CENTRE: P	UNE	INST	TITUT	E OF	COMPU	TER	TECHNOLOG	Y, PU	NE.		PAGE N	0. 173	
	FIRST LINE : SEAT NO., NAME O													
	ES: SUB.CODE, SUB.NAME, SUB.TY													
	4428 SANGAI SAMMYAK SACHIN													
SEM.:	1													
210241	DISCRETE STRUCTURE	PP	100	40	36	42	78	Р						
210242	DATA STRUCTURE & PROB SOLVING	PP	100	40	35	32	67	Р						
210242	DATA STRUCTURE & PROB SOLVING	PR	50	20			34	Р						
210243	DIGITAL ELEX. & LOGIC DESIGN	PP	100	40	35	19	54	Р						
210243	DIGITAL ELEX. & LOGIC DESIGN	TW	25	10			18	Р						
210244	OPERATING SYSTEM & ADMIN	PP	100	40	42	24	66	Р						
210244	OPERATING SYSTEM & ADMIN	TW	25	10			20	Р						
210244	OPERATING SYSTEM & ADMIN	PR	50	20			26	Р						
210245	MICROPROCESSOR ARCHITECTURE	PP	100	40	35	29	64	Р						
210245	MICROPROCESSOR ARCHITECTURE	TW	25	10			17	Р						
210245	MICROPROCESSOR ARCHITECTURE	OR	50	20			37	Р						
210246	SOFT SKILLS	TW	25	10			20	Р						
	FIRST SEM TOTAL = $501/750$													
s14005	4429 SANGANI TEJAS RAJENDRA					SANG	ITA		,	71527563в	,PICT		,S1400544	129
SEM.:	1													
210241	DISCRETE STRUCTURE	PP	100	40	39	35	74	Р						
210242	DATA STRUCTURE & PROB SOLVING	PP	100	40	36	26	62	Р						
210242	DATA STRUCTURE & PROB SOLVING	PR	50	20			39	Р						
210243	DIGITAL ELEX. & LOGIC DESIGN	PP	100	40	41	32	73	Р						
210243	DIGITAL ELEX. & LOGIC DESIGN	TW	25	10			20	Р						
210244	OPERATING SYSTEM & ADMIN		100		26	24	50	Р						
210244	OPERATING SYSTEM & ADMIN		25				21	Р						
210244	OPERATING SYSTEM & ADMIN		50				22	Р						
	MICROPROCESSOR ARCHITECTURE		100		43	31	74							
210245	MICROPROCESSOR ARCHITECTURE	TW	25	10			20	Р						
	MICROPROCESSOR ARCHITECTURE	OR	50	20			40	Р						
210246	SOFT SKILLS	TW	25	10			20	Р						
	FIRST SEM TOTAL = $515/750$													
s14005	4430 SANJALI					NALI	NI		,	71527565J	,PICT		,S140054	130
SEM.:					2.0									
	DISCRETE STRUCTURE		100		36	40	76							
	DATA STRUCTURE & PROB SOLVING				28	29	57							
210242	DATA STRUCTURE & PROB SOLVING				24	0.0	22							
210243	DIGITAL ELEX. & LOGIC DESIGN				31	08								
210243	DIGITAL ELEX. & LOGIC DESIGN				22	10	15							
	OPERATING SYSTEM & ADMIN		100		32	18	50							
210244	OPERATING SYSTEM & ADMIN		25				18							
	OPERATING SYSTEM & ADMIN		50		2.0	2.0	23							
	MICROPROCESSOR ARCHITECTURE		100		26	26	52							
210245	MICROPROCESSOR ARCHITECTURE	١W	25	ΤO			15	Р						

210245 MICROPROCESSOR ARCHITECTURE OR 50 20 10 F
210246 SOFT SKILLS TW 25 10 16 P
FIRST SEM TOTAL = --/750

	: 25 JAN 2016 CENTRE: P	UNE	INST	TITUT	E OF	COMPUT	ΓER ⁻	TECHNOLOGY	, PUN	NE.		PAGE N	0. 174	
	FIRST LINE : SEAT NO., NAME O													
	ES: SUB.CODE, SUB.NAME, SUB.TY													
	4431 SAPKAL SWAPNIL GAJANAN													
SEM.:	1													
210241	DISCRETE STRUCTURE	PP	100	40	33	41	74	Р						
210242	DATA STRUCTURE & PROB SOLVING	PP	100	40	26	24	50	Р						
210242	DATA STRUCTURE & PROB SOLVING	PR	50	20			30	Р						
210243	DIGITAL ELEX. & LOGIC DESIGN	PP	100	40	41	32	73	Р						
210243	DIGITAL ELEX. & LOGIC DESIGN	TW	25	10			17	Р						
210244	OPERATING SYSTEM & ADMIN	PP	100	40	30	21	51	Р						
210244	OPERATING SYSTEM & ADMIN	TW	25	10			20	Р						
210244	OPERATING SYSTEM & ADMIN	PR	50	20			26	Р						
210245	MICROPROCESSOR ARCHITECTURE	PP	100	40	31	24	55	Р						
210245	MICROPROCESSOR ARCHITECTURE	TW	25	10			19	Р						
210245	MICROPROCESSOR ARCHITECTURE	OR	50	20			23	Р						
210246	SOFT SKILLS	TW	25	10			21	Р						
	FIRST SEM TOTAL = 459/750													
S14005	4432 SATHE VASANTI SATYANARA	YAN				URMII	_A		, 7	71527575F	,PICT		,S140054	432
SEM.:	1													
	DISCRETE STRUCTURE		100		39	48	87							
	DATA STRUCTURE & PROB SOLVING				35	39	74							
	DATA STRUCTURE & PROB SOLVING						21							
210243					37	23	60	-						
210243	DIGITAL ELEX. & LOGIC DESIGN						19							
210244	OPERATING SYSTEM & ADMIN		100		38	30	68							
			25				19							
-	OPERATING SYSTEM & ADMIN		50		2.7	2.2	42							
			100		37	33	70							
			25				19							
			50				43							
210246		IW	25	10			21	Р						
	FIRST SEM TOTAL = 543/750													
	4422 CATVALET BINNADE													
514003	4433 SATYAJEET BINWADE					KIIU			, /	71527577в	,PICI		,51400544	433
SEM.:	1													
	DISCRETE STRUCTURE	DD	100	40	27	36	63	D						
	DATA STRUCTURE & PROB SOLVING					25	55							
210242	DATA STRUCTURE & PROB SOLVING				30	23	22							
210242	DIGITAL ELEX. & LOGIC DESIGN				26	24	50							
210243	DIGITAL ELEX. & LOGIC DESIGN				20	24	17							
	OPERATING SYSTEM & ADMIN		100		29	20	49							
	OPERATING SYSTEM & ADMIN		25		<i>L 3</i>	20	22							
	OPERATING SYSTEM & ADMIN		50				32							
	MICROPROCESSOR ARCHITECTURE		100		26	25	51							
			25		20	_ ,	20							
210243	MICROI ROCESSON ARCHITECTURE	ı vV	23	±0			20	•						

210245 MICROPROCESSOR ARCHITECTURE OR 50 20 25 P
210246 SOFT SKILLS TW 25 10 21 P
FIRST SEM TOTAL = 427/750

DATE	L9 S.E.(2014 PAT.)(COMPUTER) E: 25 JAN 2016 CENTRE: P						•		PAGE NO. 175
NOTE:	FIRST LINE : SEAT NO., NAME O	F TH	E CAND	IDATE,	MOTI	HER, PERMANEN	NT REG. NO., PREV	IOUS SEAT	 T NO., COLLEGE, SEAT NO. , P/F:PASS/FAIL, C:PREV.CARRY OVE
									,s140054434
314003	SAUKABII SUNIL I ANSWANI				IXAIN	CHAIN	,713273702	,1101	,3140034434
SEM.:	:1								
210241	DISCRETE STRUCTURE	PP	100 40	42	39	81 P			
210242	DATA STRUCTURE & PROB SOLVING	PP	100 40	38	34	72 P			
210242	DATA STRUCTURE & PROB SOLVING					23 P			
210243	DIGITAL ELEX. & LOGIC DESIGN			41	31	72 P			
210243	DIGITAL ELEX. & LOGIC DESIGN					18 P			
210244	OPERATING SYSTEM & ADMIN		100 40		20	59 P			
			25 10			21 P			
210244			50 20			23 P			
210245			100 40	33	26	59 P			
			25 10			20 P			
			50 20			34 P			
210246	SOFT SKILLS FIRST SEM TOTAL = 502/750	TW	25 10			20 P			
	54435 SAWALA SUJEET SANJAY					ITA			,s140054435
SEM.:	:1								
210241	DISCRETE STRUCTURE	PP	100 40	39	38	77 P			
210242	DATA STRUCTURE & PROB SOLVING	PP	100 40	31	25	56 P			
210242	DATA STRUCTURE & PROB SOLVING	PR	50 20			25 P			
210243	DIGITAL ELEX. & LOGIC DESIGN	PP	100 40	40	20	60 P			
210243	DIGITAL ELEX. & LOGIC DESIGN	TW	25 10			19 P			
210244	OPERATING SYSTEM & ADMIN	PP	100 40	30	15	45 P			
210244	OPERATING SYSTEM & ADMIN	TW	25 10			22 P			
210244	OPERATING SYSTEM & ADMIN	PR	50 20			27 P			
210245	MICROPROCESSOR ARCHITECTURE	PP	100 40	34	25	59 P			
210245	MICROPROCESSOR ARCHITECTURE	TW	25 10			20 P			
210245	MICROPROCESSOR ARCHITECTURE	OR	50 20			37 P			
210246	SOFT SKILLS FIRST SEM TOTAL = 465/750	TW	25 10			18 P			
s14005					ANI		,71527580в		,s140054436
SEM.:	:1								
	DISCRETE STRUCTURE	PP	100 40	38	42	80 P			
210242				41	35	76 P			
	DATA STRUCTURE & PROB SOLVING				- -	33 P			
210243	DIGITAL ELEX. & LOGIC DESIGN			38	41	79 P			
210243			25 10	-		22 P			
210244	OPERATING SYSTEM & ADMIN		100 40	41	28	69 P			
210244	OPERATING SYSTEM & ADMIN		25 10	_	-	22 P			
210244			50 20			29 P			
210245	MICROPROCESSOR ARCHITECTURE		100 40	39	34	73 P			
	MICROPROCESSOR ARCHITECTURE		25 10			24 P			

210245 MICROPROCESSOR ARCHITECTURE OR 50 20 11 F
210246 SOFT SKILLS TW 25 10 23 P
FIRST SEM TOTAL = --/750

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANNIT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: SUB.CODE, SUB.NAME, SUB.TYPE, MAX. MARKS, MIN. PASS MARKS, DE WARKS, TH MARKS, OBTATNED, P/E-PASS/FAIL, C:PREV.CARRY OVER \$140054437 SHAGAB SHAZHI SEM.:10 \$20042 DATA STRUCTURE & PPOB SOLVING PP 100 40 30 36 66 P 210042 DATA STRUCTURE & PPOB SOLVING PP 100 40 31 26 57 P 210043 DIGITAL ELEX & LOGIC DESIGN TW 50 70 20 P 210043 DIGITAL ELEX & LOGIC DESIGN TW 25 10 17 P 210044 OPERATING SYSTEM & ADVIN TW 25 10 18 P 210044 OPERATING SYSTEM & ADVIN TW 25 10 18 P 210045 MICROPROCESSOR ARCHITECTURE TW 25 10 15 P 210045 MICROPROCESSOR ARCHITECTURE TW 25 10 16 P FIRST SEM TOTAL = 447/750 \$1440054438 SHAZHI SCHALL JAFAR \$50.11 \$200241 DISCRETE STRUCTURE & PPOB SOLVING PP 100 40 35 27 68 P 210044 OPERATING SYSTEM & DOWN TO THE STRUCTURE THE TO THE STRUCTURE & PP 100 40 35 27 68 P 210044 OPERATING SYSTEM & DOWN TO THE STRUCTURE THE TO THE STRUCTURE BY THE STRUCTUR		E : 25 JAN 2016 CE	-	NE IN	NSTITU	JTE OF	СОМРИТ	TER T	ECHNOL	_OGY, PU	INE.		PAGE	NO. 176	
SPA.:1 210421 DISCRETE STRUCTURE															
SHI-11 210241 DISCRETE STRUCTURE PP 100 40 30 36 66 P 210242 DATA STRUCTURE & PROB SOLVING PP 100 40 31 26 57 P 210243 DIGITAL ELEX. & LOGIC DESIGN PT 100 40 29 37 66 P 210244 OPERATING SYSTEM & ADMIN PT 100 40 31 15 P 210244 OPERATING SYSTEM & ADMIN PT 100 40 35 26 61 P 210244 OPERATING SYSTEM & ADMIN PT 100 40 35 26 61 P 210244 OPERATING SYSTEM & ADMIN PT 100 40 35 26 61 P 210245 MICROPROCESSOR ARCHITECTURE PT 100 40 35 26 61 P 210246 SOFT SKILLS PT 100 40 35 27 63 P 210247 DISCRETE STRUCTURE PT 100 40 36 27 63 P 210249 DISCRETE STRUCTURE PT 100 40 36 27 63 P 210240 DATA STRUCTURE & PROB SOLVING PP 100 40 36 27 63 P 210240 OPERATING SYSTEM & ADMIN PT 100 40 36 27 63 P 210241 DISCRETE STRUCTURE PT 100 40 36 27 63 P 210242 DATA STRUCTURE & PROB SOLVING PP 100 40 36 27 63 P 210243 DIGITAL ELEX. & LOGIC DESIGN PT 100 40 36 27 63 P 210244 OPERATING SYSTEM & ADMIN PT 25 10 19 P 210244 OPERATING SYSTEM & ADMIN PT 25 10 19 P 210244 OPERATING SYSTEM & ADMIN PT 25 10 19 P 210244 OPERATING SYSTEM & ADMIN PT 25 10 19 P 210244 OPERATING SYSTEM & ADMIN PT 25 10 19 P 210244 OPERATING SYSTEM & ADMIN PT 25 10 20 P 210244 OPERATING SYSTEM & ADMIN PT 25 10 18 P 210245 DISCRETE STRUCTURE PP 100 40 38 26 64 P 210245 DISCRETE STRUCTURE PP 100 40 38 26 64 P 210246 OPERATING SYSTEM & ADMIN PT 25 10 20 P 210247 DISCRETE STRUCTURE PP 100 40 38 26 64 P 210248 DISCRETE STRUCTURE PP 100 40 38 26 64 P 210249 DISCRETE STRUCTURE PP 100 40 38 26 64 P 210241 DISCRETE STRUCTURE PP 100 40 38 26 64 P 210245 DISCRETE STRUCTURE PP 100 40 38 26 64 P 210246 OPERATING SYSTEM & ADMIN PR 25 10 20 P 210247 DATA STRUCTURE & PP 100 40 38 26 64 P 210249 DISCRETE STRUCTURE PP 100 40 38 26 64 P 210240 OPERATING SYSTEM & ADMIN PR 25 10 20 P 210244 OPERATING SYSTEM & ADMIN PR 25 10 20 P 210244 OPERATING SYSTEM & ADMIN PR 25 10 20 P 210244 OPERATING SYSTEM & ADMIN PR 25 10 20 P 210244 OPERATING SYSTEM & ADMIN PR 25 10 20 P 210244 OPERATING SYSTEM & ADMIN PR 25 10 20 P 210244 OPERATING SYSTEM & ADMIN PR 25 10 20 P 210244 OPERATING SYSTEM & ADMIN PR 25 10 20 P 21	OTHER LIN	NES: SUB.CODE, SUB.NAME,	SUB.TYPE	E, MA	AX.MAF	RKS, M	IN.PASS	S MAR	KS, OE	E MARKS,	TH MARKS	S, OBTAINE	o, P/F:	PASS/FAIL,	C:PREV.CARRY OVER
210241 DISCRETE STRUCTURE															
210242 DATA STRUCTURE & PROB SOLVING PP 100 40 31 26 57 P	SEM.:	1													
200242 DATA STRICTURE & PROB SOLVING PR 50 20 20 P	210241	DISCRETE STRUCTURE	F	P 10	00 40	30	36	66	Р						
210244 DIGITAL ELEX. & LOGIC DESIGN PP 100 40 29 37 66 P 210244 OPERATING SYSTEM & ADMIN PP 100 40 33 15 48 P 210244 OPERATING SYSTEM & ADMIN PP 100 40 35 26 61 P 210244 OPERATING SYSTEM & ADMIN PP 50 20 28 P 210245 MICROPROCESSOR ARCHITECTURE PP 100 40 35 26 61 P 210245 MICROPROCESSOR ARCHITECTURE OR 50 20 35 P 210245 MICROPROCESSOR ARCHITECTURE PP 100 40 35 26 61 P 210245 MICROPROCESSOR ARCHITECTURE PP 100 40 36 36 76 P 210246 DATA STRUCTURE & PROB SOLVING PP 100 40 36 36 76 P 210242 DATA STRUCTURE & PROB SOLVING PP 100 40 36 36 62 P 210242 DATA STRUCTURE & PROB SOLVING PP 100 40 36 36 66 P 210244 DATA STRUCTURE & PROB SOLVING PP 100 40 36 36 66 P 210244 DATA STRUCTURE & PROB SOLVING PP 100 40 36 36 66 P 210244 DATA STRUCTURE & PROB SOLVING PP 100 40 36 36 66 P 210244 DATA STRUCTURE & PROB SOLVING PP 100 40 36 36 66 P 210244 DATA STRUCTURE & PROB SOLVING PP 100 40 36 36 66 P 210244 DATA STRUCTURE & PROB SOLVING PP 100 40 36 36 66 P 210244 DATA STRUCTURE & PROB SOLVING PP 100 40 36 36 66 P 210245 MICROPROCESSOR ARCHITECTURE PP 100 40 36 36 66 P 210245 MICROPROCESSOR ARCHITECTURE PP 100 40 36 36 66 P 210245 MICROPROCESSOR ARCHITECTURE PP 100 40 36 36 66 P 210245 MICROPROCESSOR ARCHITECTURE PP 100 40 36 36 74 P 210242 DATA STRUCTURE & PROB SOLVING PP 50 20 37 P 210245 MICROPROCESSOR ARCHITECTURE PP 100 40 37 27 66 P 210245 MICROPROCESSOR ARCHITECTURE PP 100 40 37 27 66 P 210244 DATA STRUCTURE & PROB SOLVING PP 50 20 37 P 210244 DATA STRUCTURE & PROB SOLVING PP 50 20 37 P 210244 DATA STRUCTURE & PROB SOLVING PP 50 20 37 P 210244 DATA STRUCTURE & PROB SOLVING PP 50 20 37 P 37 P 37	210242	DATA STRUCTURE & PROB S	SOLVING F	P 10	00 40	31	26	57	Р						
210244 OPERATING SYSTEM & ADMIN PP 100 40 33 15 48 P	210242	DATA STRUCTURE & PROB S	SOLVING F	PR !	50 20			20	Р						
210244 OPERATING SYSTEM & ADMIN PP 100 40 33 15 48 P	210243	DIGITAL ELEX. & LOGIC (DESIGN F	P 10	00 40	29	37	66	Р						
210244 OPERATING SYSTEM & ADMIN TW Z5 10 18 P	210243	DIGITAL ELEX. & LOGIC (DESIGN T	TW 2	25 10			17	Р						
210244 OPERATING SYSTEM & ADMIN	210244	OPERATING SYSTEM & ADM	IN F	PP 10	00 40	33	15	48	Р						
210245 MICROPROCESSOR ARCHITECTURE PP 100 40 35 26 61 P 210245 MICROPROCESSOR ARCHITECTURE TW 25 10 15 P 210245 MICROPROCESSOR ARCHITECTURE OR 50 20 35 P 210246 SOFT SKILLS TW 25 10 16 P FIRST SEM TOTAL = 447/750 S140054438 SHAIKH SOHAIL JAFAR SHAMIM ,716337083 ,PICT ,S140054438 SEM::1 210241 DISCRETE STRUCTURE PP 100 40 41 35 76 P 210242 DATA STRUCTURE & PROB SOLVING PP 100 40 36 27 63 P 210242 DATA STRUCTURE & PROB SOLVING PP 50 20 38 P 210243 DIGITAL ELEX. & LOGIC DESIGN TW 25 10 19 P 210244 OPERATING SYSTEM & ADMIN PR 50 20 30 P 210244 OPERATING SYSTEM & ADMIN PR 50 20 30 P 210245 MICROPROCESSOR ARCHITECTURE PP 100 40 38 26 64 P 210245 MICROPROCESSOR ARCHITECTURE PP 100 40 38 26 64 P 210246 MICROPROCESSOR ARCHITECTURE PP 100 40 38 26 64 P 210247 SINCOPROCESSOR ARCHITECTURE PP 100 40 38 26 64 P 210248 DISCRETE STRUCTURE PP 100 40 38 26 64 P 210249 MICROPROCESSOR ARCHITECTURE PP 100 40 38 26 64 P 210245 MICROPROCESSOR ARCHITECTURE PP 100 40 38 26 64 P 210246 SOFT SKILLS TW 25 10 18 P FIRST SEM TOTAL = 526/750 SEM::1 210241 DISCRETE STRUCTURE PP 100 40 36 38 74 P 210242 DATA STRUCTURE & PROS SOLVING PP 100 40 36 38 74 P 210243 DIGITAL ELEX. & LOGIC DESIGN PP 100 40 37 27 64 P 210244 DATA STRUCTURE & PROS SOLVING PP 100 40 37 27 64 P 210242 DATA STRUCTURE & PROS SOLVING PP 100 40 37 27 64 P 210244 OPERATING SYSTEM & ADMIN PP 100 40 37 27 64 P 210244 OPERATING SYSTEM & ADMIN PP 100 40 31 25 56 P 210244 OPERATING SYSTEM & ADMIN PP 100 40 31 25 56 P 210244 OPERATING SYSTEM & ADMIN PP 100 40 31 25 56 P 210244 OPERATING SYSTEM & ADMIN PP 100 40 33 28 61 P	210244	OPERATING SYSTEM & ADM	IN 7	TW 2	25 10			18	Р						
210245 MICKOPROCESSOR ARCHITECTURE TW 25 10 15 P	210244	OPERATING SYSTEM & ADM	IN F	PR !	50 20			28	Р						
210245 MICROPROCESSOR ARCHITECTURE OR 50 20 35 P 210246 SOFT SKILLS TW 25 10 16 P FIRST SEM TOTAL = 447/750 S140054438 SHAIKH SOHAIL JAFAR SHAMIM ,71633708J ,PICT ,S140054438 SEM.:1 210241 DISCRETE STRUCTURE PP 100 40 41 35 76 P 210242 DATA STRUCTURE & PROB SOLVING PP 100 40 36 27 63 P 210242 DATA STRUCTURE & PROB SOLVING PP 100 40 36 66 P 210243 DIGITAL ELEX. & LOGIC DESIGN TW 25 10 19 P 210244 OPERATING SYSTEM & ADMIN PF 50 20 30 P 210244 OPERATING SYSTEM & ADMIN PF 50 20 41 P 210245 MICROPROCESSOR ARCHITECTURE PP 100 40 38 26 64 P 210245 MICROPROCESSOR ARCHITECTURE PP 100 40 38 26 64 P 210245 MICROPROCESSOR ARCHITECTURE PP 100 40 38 26 64 P 210245 MICROPROCESSOR ARCHITECTURE PP 100 40 38 26 64 P 210245 MICROPROCESSOR ARCHITECTURE PP 100 40 38 26 64 P 210246 SOFT SKILLS TW 25 10 18 P FIRST SEM TOTAL = 526/750 SEM.:1 210241 DISCRETE STRUCTURE & PP 100 40 39 43 82 P 210242 DATA STRUCTURE & PROB SOLVING PP 100 40 37 27 64 P 210243 DIGITAL ELEX. & LOGIC DESIGN TW 25 10 15 P 210244 OPERATING SYSTEM & ADMIN PR 50 20 37 P 210243 DIGITAL ELEX. & LOGIC DESIGN TW 25 10 15 P 210244 OPERATING SYSTEM & ADMIN PR 50 20 37 P 210243 DIGITAL ELEX. & LOGIC DESIGN TW 25 10 15 P 210244 OPERATING SYSTEM & ADMIN PR 50 20 37 P 210244 OPERATING SYSTEM & ADMIN PR 50 20 37 P 210244 OPERATING SYSTEM & ADMIN PR 50 20 37 P 210244 OPERATING SYSTEM & ADMIN PR 50 20 23 P 210244 OPERATING SYSTEM & ADMIN PR 50 20 23 P 210244 OPERATING SYSTEM & ADMIN PR 50 20 23 P 210244 OPERATING SYSTEM & ADMIN PR 50 20 23 P 210244 OPERATING SYSTEM & ADMIN PR 50 20 23 P 210244 OPERATING SYSTEM & ADMIN PR 50 20 23 P 210244 OPERATING SYSTEM & ADMIN PR 50 20 23 P 210244 OPERATING SYSTEM & ADMIN PR 50 20 23 P 210244 OPERATING SYSTEM & ADMIN PR 50 20 23 P 210244 OPERATING SYSTEM & ADMIN PR 50 20 23 P 210244 OPERATING SYSTEM & ADMIN PR 50 20 23 P 210244 OPERATING SYSTEM & ADMIN PR 50 20 23 P	210245	MICROPROCESSOR ARCHITE	CTURE F	P 10	00 40	35	26	61	Р						
210246 SOFT SKILLS TW 25 10 16 P FIRST SEM TOTAL = 447/750 S140054438 SHAIKH SOHAIL JAFAR SHAMIM ,716337083 ,PICT ,S140054438 SEM.:1 210241 DISCRETE STRUCTURE PP 100 40 41 35 76 P 210242 DATA STRUCTURE & PROB SOLVING PR 50 20 38 P 210243 DIGITAL ELEX. & LOGIC DESIGN PP 100 40 43 63 30 66 P 210244 OPERATING SYSTEM & ADMIN PP 100 40 43 27 70 P 210244 OPERATING SYSTEM & ADMIN PR 50 20 30 P 210245 MICROPROCESSOR ARCHITECTURE PP 100 40 38 26 64 P 210245 SOFT SKILLS TW 25 10 20 P 210246 SOFT SKILLS TW 25 10 18 P FIRST SEM TOTAL = 526750 S140054439 SHAMS HASAN RIZVI RUBENA ,71527588H ,PICT ,S140054439 SEM.:1 210241 DISCRETE STRUCTURE PP 100 40 39 43 82 P 210242 DATA STRUCTURE & PROB SOLVING PR 50 20 30 P 210242 DATA STRUCTURE & PP 100 40 39 43 82 P 210243 DIGITAL ELEX. & LOGIC DESIGN PP 100 40 37 27 P 210244 DATA STRUCTURE & PROB SOLVING PR 50 20 37 P 210243 DIGITAL ELEX. & LOGIC DESIGN PP 100 40 37 27 64 P 210243 DIGITAL ELEX. & LOGIC DESIGN PP 100 40 37 27 64 P 210243 DIGITAL ELEX. & LOGIC DESIGN PP 100 40 37 27 64 P 210243 DIGITAL ELEX. & LOGIC DESIGN PP 100 40 37 27 64 P 210244 OPERATING SYSTEM & ADMIN PR 50 20 37 P 210244 OPERATING SYSTEM & ADMIN PR 50 20 23 P 210244 OPERATING SYSTEM & ADMIN PR 50 20 23 P 210244 OPERATING SYSTEM & ADMIN PR 50 20 23 P 210244 OPERATING SYSTEM & ADMIN PR 50 20 23 P 210244 OPERATING SYSTEM & ADMIN PR 50 20 23 P 210244 OPERATING SYSTEM & ADMIN PR 50 20 23 P 210244 OPERATING SYSTEM & ADMIN PR 50 20 23 P 210244 OPERATING SYSTEM & ADMIN PR 50 20 23 P 210244 OPERATING SYSTEM & ADMIN PR 50 20 23 P 210244 OPERATING SYSTEM & ADMIN PR 50 20 23 P 210244 OPERATING SYSTEM & ADMIN PR 50 20 23 P 210244 OPERATING SYSTEM & ADMIN PR 50 20 23 P 210244 OPERATING SYSTEM & ADMIN PR 50 20 23 P 210244 OPERATING SYSTEM & ADMIN PR 50 20 23 P 210244 OPERATING SYSTEM & ADMIN PR 50 20 23 P 210245 MICROPROCESSOR ARCHITECTURE PP 100 40 33 28 61 P	210245	MICROPROCESSOR ARCHITE	CTURE 7	TW 2	25 10			15	Р						
SIA0054438 SHAIKH SOHAIL JAFAR SHAMIM ,716337083 ,PICT ,5140054438 SEM.:1 210241 DISCRETE STRUCTURE	210245	MICROPROCESSOR ARCHITE	CTURE C	OR !	50 20			35	Р						
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210243 DIGITAL ELEX. & LOGIC DESIGN PP 100 40 36 30 66 P	210242	DATA STRUCTURE & PROB S	SOLVING F	PP 10	00 40	36	27	63	Р						
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SEM.:1 210241 DISCRETE STRUCTURE PP 100 40 39 43 82 P 210242 DATA STRUCTURE & PROB SOLVING PP 100 40 36 38 74 P 210242 DATA STRUCTURE & PROB SOLVING PR 50 20 37 P 210243 DIGITAL ELEX. & LOGIC DESIGN PP 100 40 37 27 64 P 210243 DIGITAL ELEX. & LOGIC DESIGN TW 25 10 15 P 210244 OPERATING SYSTEM & ADMIN PP 100 40 31 25 56 P 210244 OPERATING SYSTEM & ADMIN TW 25 10 18 P 210244 OPERATING SYSTEM & ADMIN PR 50 20 23 P 210245 MICROPROCESSOR ARCHITECTURE PP 100 40 33 28 61 P	210246	SOFT SKILLS	7	TW 2	25 10			18	Р						
SEM.:1 210241 DISCRETE STRUCTURE PP 100 40 39 43 82 P 210242 DATA STRUCTURE & PROB SOLVING PP 100 40 36 38 74 P 210242 DATA STRUCTURE & PROB SOLVING PR 50 20 37 P 210243 DIGITAL ELEX. & LOGIC DESIGN PP 100 40 37 27 64 P 210243 DIGITAL ELEX. & LOGIC DESIGN TW 25 10 15 P 210244 OPERATING SYSTEM & ADMIN PP 100 40 31 25 56 P 210244 OPERATING SYSTEM & ADMIN TW 25 10 18 P 210244 OPERATING SYSTEM & ADMIN PR 50 20 23 P 210245 MICROPROCESSOR ARCHITECTURE PP 100 40 33 28 61 P		FIRST SEM TOTAL = 526/	750												
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210243 DIGITAL ELEX. & LOGIC DESIGN PP 100 40 37 27 64 P 210243 DIGITAL ELEX. & LOGIC DESIGN TW 25 10 15 P 210244 OPERATING SYSTEM & ADMIN PP 100 40 31 25 56 P 210244 OPERATING SYSTEM & ADMIN TW 25 10 18 P 210244 OPERATING SYSTEM & ADMIN PR 50 20 23 P 210245 MICROPROCESSOR ARCHITECTURE PP 100 40 33 28 61 P	210242	DATA STRUCTURE & PROB S	SOLVING F	P 10	00 40	36	38	74	Р						
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210244 OPERATING SYSTEM & ADMIN PR 50 20 23 P 210245 MICROPROCESSOR ARCHITECTURE PP 100 40 33 28 61 P	210244	OPERATING SYSTEM & ADM	IN F	P 10	00 40	31	25	56	Р						
210245 MICROPROCESSOR ARCHITECTURE PP 100 40 33 28 61 P	210244	OPERATING SYSTEM & ADM	IN 7	TW 2	25 10			18	Р						
	210244	OPERATING SYSTEM & ADM	IN F	PR !	50 20			23	Р						
210245 MICROPROCESSOR ARCHITECTURE TW 25 10 16 P	210245	MICROPROCESSOR ARCHITEC	CTURE F	P 10	00 40	33	28	61	Р						
	210245	MICROPROCESSOR ARCHITEC	CTURE 7	TW 2	25 10			16	Р						

210245 MICROPROCESSOR ARCHITECTURE OR 50 20 35 P
210246 SOFT SKILLS TW 25 10 18 P
FIRST SEM TOTAL = 499/750

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	30B. CODE, 30B. NAME, 30B. 1												
	4440 SHANTANU RATHI								,71527589F				
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	DISCRETE STRUCTURE DATA STRUCTURE & PROB SOLVIN				_		45 F						
	DATA STRUCTURE & PROB SOLVIN				9		AA F						
210242	DIGITAL ELEX. & LOGIC DESIGN				٥		51 F						
210243	DIGITAL ELEX. & LOGIC DESIGN				•		18 F						
210244	OPERATING SYSTEM & ADMIN		100		8		58 F						
210244	OPERATING SYSTEM & ADMIN		1 25	_	-		20 F						
210244	OPERATING SYSTEM & ADMIN	PR	s 50	20			14 F	F					
210245	MICROPROCESSOR ARCHITECTURE	PF	100	40 3	4	22	56 F	o					
210245	MICROPROCESSOR ARCHITECTURE	Τw	1 25	10			19 F	o					
210245	MICROPROCESSOR ARCHITECTURE	OF	50	20			25 F	>					
210246	SOFT SKILLS	ΤW	ı 25	10			17 F	>					
	FIRST SEM TOTAL = $/750$												
S14005	4441 SHARMA SAURABH VINOD					VIDYA		:	,71527591н	,PICT		,S1400544	41
SEM.:													
	DISCRETE STRUCTURE						71 F						
	DATA STRUCTURE & PROB SOLVIN				3		54 F						
210242	DATA STRUCTURE & PROB SOLVIN				^		30 F						
	DIGITAL ELEX. & LOGIC DESIGN				0		49 F						
210243	DIGITAL ELEX. & LOGIC DESIGN				7		16 F						
210244 210244	OPERATING SYSTEM & ADMIN OPERATING SYSTEM & ADMIN		25 100		7		47 F						
210244	OPERATING SYSTEM & ADMIN		8 50				30 F						
210244	MICROPROCESSOR ARCHITECTURE		100		9		44 F						
210245	MICROPROCESSOR ARCHITECTURE		1 25		,		15 F						
	MICROPROCESSOR ARCHITECTURE		50				26 F						
210246			1 25				15 F						
	FIRST SEM TOTAL = 416/750												
S14005	4442 SHARONI NARANG					GEETIK	A		,71527592F	,PICT		,S1400544	42
SEM.:	1												
210241	DISCRETE STRUCTURE	PF	100	40 3	9	42	81 F						
210242	DATA STRUCTURE & PROB SOLVIN	IG PF	100	40 4	5	32	77 F	•					
210242	DATA STRUCTURE & PROB SOLVIN						35 F						
210243	DIGITAL ELEX. & LOGIC DESIGN				5		69 F						
210243	DIGITAL ELEX. & LOGIC DESIGN				•		19 F						
210244	OPERATING SYSTEM & ADMIN		100		0		65 F						
210244	OPERATING SYSTEM & ADMIN		25				22 F						
210244	OPERATING SYSTEM & ADMIN		50		0		30 F						
210245	MICROPROCESSOR ARCHITECTURE		100		9		68 F						
Z1UZ45	MICROPROCESSOR ARCHITECTURE	ΙW	1 25	TO			20 F	-					

210245 MICROPROCESSOR ARCHITECTURE OR 50 20 40 P
210246 SOFT SKILLS TW 25 10 21 P
FIRST SEM TOTAL = 547/750

	E: 25 JAN 2016 CENTRE: P	UNE	INST	ITUTE	OF	COMPUTE	R T	TECHNOLOGY, PUN	NE.		PAGE N	o. 178	
	FIRST LINE : SEAT NO., NAME O												
OTHER LIN	NES: SUB.CODE, SUB.NAME, SUB.TY	PE,	MAX.	MARKS,	, MI	N.PASS N	MAR	RKS, OE MARKS,	TH MARKS,	OBTAINED,	P/F:P	ASS/FAIL,	C:PREV.CARRY OVER
	54443 SHASHANK DASHARATH ANKA								71527593D				
SEM.:	1												
210241	DISCRETE STRUCTURE	PP	100 4	40 2	29	32	61	P					
210242	DATA STRUCTURE & PROB SOLVING	PP	100 4	40 1	17	23	40	Р					
210242	DATA STRUCTURE & PROB SOLVING	PR	50 2	20		2	27	Р					
210243	DIGITAL ELEX. & LOGIC DESIGN	PP	100 4	40 2	28	15	43	Р					
210243	DIGITAL ELEX. & LOGIC DESIGN	TW	25	10		-	12	Р					
210244	OPERATING SYSTEM & ADMIN	PP	100 4	40 3	31	22 !	53	Р					
210244	OPERATING SYSTEM & ADMIN	TW	25 3	10		2	20	Р					
210244	OPERATING SYSTEM & ADMIN	PR	50 2	20			29	Р					
210245	MICROPROCESSOR ARCHITECTURE	PP	100 4	40 2	26	20 4	46	Р					
210245	MICROPROCESSOR ARCHITECTURE	TW	25	10		-	19	Р					
210245	MICROPROCESSOR ARCHITECTURE	OR	50 2	20		-	12	F					
210246	SOFT SKILLS	TW	25 3	10		2	20	Р					
	FIRST SEM TOTAL =/750												
S14005	54444 SHASTRI SHRADHA RAJENDR	Α				SANGEE	TA	, 7	71633709G	,PICT		,S1400544	44
SEM.:	1												
210241	DISCRETE STRUCTURE	PP	100 4	40 3	38	20 !	58	Р					
210242	DATA STRUCTURE & PROB SOLVING	PP	100 4	40 3	38	28	66	P					
210242	DATA STRUCTURE & PROB SOLVING	PR	50 2	20		:	30	P					
210243	DIGITAL ELEX. & LOGIC DESIGN	PP	100 4	40 2	29	25	54	Р					
210243	DIGITAL ELEX. & LOGIC DESIGN	TW	25 2	10		2	20	Р					
210244	OPERATING SYSTEM & ADMIN	PP	100 4	40 3	35	28	63	Р					
210244	OPERATING SYSTEM & ADMIN	TW	25	10		7	21	P					
210244	OPERATING SYSTEM & ADMIN	PR	50 2	20		3	30	Р					
210245	MICROPROCESSOR ARCHITECTURE	PP	100 4	40 3	38	24	62	Р					
210245	MICROPROCESSOR ARCHITECTURE	TW	25 3	10		2	20	Р					
210245	MICROPROCESSOR ARCHITECTURE	OR	50 2	20		4	41	Р					
210246	SOFT SKILLS	TW	25 3	10		2	20	Р					
	FIRST SEM TOTAL = 485/750												
	AAAE CUELKE BUACHAT COVIND												
514005	54445 SHELKE BHAGWAT GOVIND					SUDAMA	ıΤ	, 7	71633710L	, PICI		,51400544	4)
SEM.:	1												
	DISCRETE STRUCTURE	PP	100 4	40 3	36	27	63	Р					
210242	DATA STRUCTURE & PROB SOLVING	PP	100 4	40 3	32	21 !	53	Р					
210242	DATA STRUCTURE & PROB SOLVING	PR	50 2	20		:	39	Р					
210243	DIGITAL ELEX. & LOGIC DESIGN	PP	100 4	40 3	34	23	57	Р					
210243	DIGITAL ELEX. & LOGIC DESIGN	TW	25	10		2	21	Р					
210244	OPERATING SYSTEM & ADMIN	PP	100 4	40 3	34	30	64	Р					
210244	OPERATING SYSTEM & ADMIN	TW	25 2	10			22	Р					
210244	OPERATING SYSTEM & ADMIN	PR	50 2	20			26	Р					
210245	MICROPROCESSOR ARCHITECTURE	PP	100 4	40 3	36		61						
210245	MICROPROCESSOR ARCHITECTURE	TW	25	10		2	21	Р					

210245 MICROPROCESSOR ARCHITECTURE OR 50 20 40 P
210246 SOFT SKILLS TW 25 10 18 P
FIRST SEM TOTAL = 485/750

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. DIHER LINES: SUB.CODE, SUB.NAME, SUB.TYPE, MAX.MARKS, MEN.PASS MARKS, OF MARKS, TH MARKS, OSTAINO, P/F-PASS/FAIL, C-PREV.CARRY OVER \$1.103634446 SHENDE PRASAD UMMAKNIT PRANJALI 7.1327600L PICT , \$140054446 SHEN.11 SH.11 SH.11 SH.11 SH.11 SH.11 SH.11 SH.12 SH.12 SH.11 SH.12 SH.13 SH.11 SH.12 SH.13 SH		: 25 JAN 2016 CE	•	NE]	INSTIT	UTE OF	COMPU	JTER -	TECHI	NOLOGY,	PUNE			PAGE N	0. 179	
SEA_11									:							
SEN.:1	OTHER LIN	ES: SUB.CODE, SUB.NAME,	SUB.TYPI	Ε, Ν	MAX.MA	RKS, M	IN.PAS	S MAI	RKS,	OE MAR	KS, T	H MARKS,	OBTAINED,	P/F:P	ASS/FAIL,	C:PREV.CARRY OVER
210241 DISCRETE STRUCTURE																
200242 DATA STRUCTURE & PROB SOLVING PP 100 40 37 22 59 P	SEM.:	1														
200242 DATA STRUCTURE & PROB SOLVING PR 50 20 40 P	210241	DISCRETE STRUCTURE	I	PP 1	L00 40	39	26	65	Р							
200243 DIGITAL ELEX. & LOGIC DESIGN PP 100 40 37 22 59 P	210242	DATA STRUCTURE & PROB S	SOLVING	PP 1	L00 40	28	27	55	Р							
210244 OPERATING SYSTEM & ADMIN PR 50 0 22 P	210242	DATA STRUCTURE & PROB S	SOLVING	PR	50 20)		40	Р							
210244 OPERATING SYSTEM & ADMIN PP 100 40 36 25 61 P 210244 OPERATING SYSTEM & ADMIN PR 50 20 27 P 210245 MICROPROCESSOR ARCHITECTURE PP 100 40 25 26 51 P 210245 MICROPROCESSOR ARCHITECTURE PP 100 40 25 26 51 P 210245 MICROPROCESSOR ARCHITECTURE OR 50 20 32 P 210246 MICROPROCESSOR ARCHITECTURE OR 50 20 17 P 25 10 17 P 25	210243	DIGITAL ELEX. & LOGIC (DESIGN I	PP 1	L00 40	37	22	59	Р							
210244 OPERATING SYSTEM & ADMIN TH 25 10 22 P	210243	DIGITAL ELEX. & LOGIC (DESIGN -	TW	25 10)		22	Р							
210244 OPERATING SYSTEM & ADMIN PR 50 20 27 P	210244	OPERATING SYSTEM & ADM	IN I	PP 1	L00 40	36	25	61	Р							
210245 MICROPROCESSOR ARCHITECTURE PP 100 40 25 26 51 P 210245 MICROPROCESSOR ARCHITECTURE TW 25 10 20 P 210245 MICROPROCESSOR ARCHITECTURE TW 25 10 32 P 210246 SOFT SKILLS TW 25 10 17 P FIRST SEM TOTAL = 471/750 S140054447 SHINDE NITESH SHASHIKANT MANUSHA ,71527603E ,PICT ,S140054447 SEM.:1 210241 DISCRETE STRUCTURE PP 100 40 33 34 67 P 210242 DATA STRUCTURE & PROB SOLVING PP 100 40 24 30 54 P 210242 DATA STRUCTURE & PROB SOLVING PP 100 40 38 24 56 P 210243 DIGITAL ELEX. & LOGIC DESIGN TW 25 10 18 P 210244 OPERATING SYSTEM & ADMIN PR 50 20 25 P 210245 MICROPROCESSOR ARCHITECTURE PP 100 40 32 22 54 P 210245 MICROPROCESSOR ARCHITECTURE PP 100 40 32 22 54 P 210245 MICROPROCESSOR ARCHITECTURE PP 100 40 32 22 54 P 210246 SOFT SKILLS TW 25 10 18 P 210247 FIRST SEM TOTAL = 437/750 S140054448 SHINDE SNEHAL SUNIL ALKA ,71527605M ,PICT ,S140054448 SEM.:1 210241 DISCRETE STRUCTURE & PP 100 40 32 25 58 P 210245 DISCRETE STRUCTURE PP 100 40 32 20 P 210246 OPERATING SYSTEM & ADMIN PR 50 20 29 P 210247 DISCRETE STRUCTURE PP 100 40 32 20 P 210248 DISCRETE STRUCTURE PP 100 40 32 20 P 210249 DISCRETE STRUCTURE PP 100 40 32 20 P 210240 DISCRETE STRUCTURE PP 100 40 32 20 P 210241 DISCRETE STRUCTURE PP 100 40 32 20 P 210243 DISCRETE STRUCTURE PP 100 40 33 25 58 P 210244 OPERATING SYSTEM & ADMIN PR 50 20 20 P 210244 OPERATING SYSTEM & ADMIN PP 100 40 37 31 68 P 210244 OPERATING SYSTEM & ADMIN PP 100 40 37 31 68 P 210244 OPERATING SYSTEM & ADMIN PP 100 40 39 28 67 P 210244 OPERATING SYSTEM & ADMIN PP 100 40 39 28 67 P 210244 OPERATING SYSTEM & ADMIN PP 100 40 39 28 67 P 210244 OPERATING SYSTEM & ADMIN PP 100 40 36 P 210244 OPERATING SYSTEM & ADMIN PP 100 40 36 P 210244 OPERATING SYSTEM & ADMIN PP 100 40 36 P 210244 OPERATING SYSTEM & ADMIN PP 100 40 37 11 68 P 210244 OPERATING SYSTEM & ADMIN PP 100 40 37 11 68 P 210244 OPERATING SYSTEM & ADMIN PP 100 40 39 28 67 P 210244 OPERATING SYSTEM & ADMIN PP 100 40 36 P 210244 OPERATING SYSTEM & ADMIN PP 100 40 37 11 68 P 210244 OPERATING SYSTEM & ADMIN PP 100 40 24 28 52 P	210244	OPERATING SYSTEM & ADM	IN -	TW	25 10)		22	Р							
210245 MICROPROCESSOR ARCHITECTURE TW 25 10 20 P	210244	OPERATING SYSTEM & ADM	IN I	PR	50 20)		27	Р							
210245 MICROPROCESSOR ARCHITECTURE OR 50 20 32 P To 25 10 17 P FIRST SEM TOTAL = 471/750	210245	MICROPROCESSOR ARCHITE	CTURE I	PP 1	L00 40	25	26	51	Р							
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210244 OPERATING SYSTEM & ADMIN PP 100 40 34 24 58 P 210244 OPERATING SYSTEM & ADMIN TW 25 10 19 P 210245 MICROPROCESSOR ARCHITECTURE PP 100 40 32 22 54 P 210245 MICROPROCESSOR ARCHITECTURE TW 25 10 18 P 210245 MICROPROCESSOR ARCHITECTURE TW 25 10 18 P 210246 SOFT SKILLS TW 25 10 19 P FIRST SEM TOTAL = 437/750 S140054448 SHINDE SNEHAL SUNIL ALKA ,71527605M ,PICT ,S140054448 SEM.:1 210241 DISCRETE STRUCTURE PP 100 40 42 36 78 P 210242 DATA STRUCTURE & PROB SOLVING PP 100 40 33 25 58 P 210242 DATA STRUCTURE & PROB SOLVING PR 50 20 20 P 210243 DIGITAL ELEX. & LOGIC DESIGN PP 100 40 37 31 68 P 210243 DIGITAL ELEX. & LOGIC DESIGN TW 25 10 21 P 210244 OPERATING SYSTEM & ADMIN PR 50 20 21 P 210244 OPERATING SYSTEM & ADMIN PR 50 20 21 P 210244 OPERATING SYSTEM & ADMIN PR 50 20 16 F 210245 MICROPROCESSOR ARCHITECTURE PP 100 40 24 28 52 P	210243	DIGITAL ELEX. & LOGIC (DESIGN I	PP 1	L00 40	28	28	56	Р							
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210244 OPERATING SYSTEM & ADMIN PR 50 20 25 P 210245 MICROPROCESSOR ARCHITECTURE PP 100 40 32 22 54 P 210245 MICROPROCESSOR ARCHITECTURE TW 25 10 18 P 210245 MICROPROCESSOR ARCHITECTURE OR 50 20 29 P 210246 SOFT SKILLS TW 25 10 19 P FIRST SEM TOTAL = 437/750 S140054448 SHINDE SNEHAL SUNIL ALKA ,71527605M ,PICT ,S140054448 SEM.:1 210241 DISCRETE STRUCTURE PP 100 40 42 36 78 P 210242 DATA STRUCTURE & PROB SOLVING PP 100 40 33 25 58 P 210242 DATA STRUCTURE & PROB SOLVING PR 50 20 20 P 210243 DIGITAL ELEX. & LOGIC DESIGN PP 100 40 37 31 68 P 210243 DIGITAL ELEX. & LOGIC DESIGN PP 100 40 37 31 68 P 210244 OPERATING SYSTEM & ADMIN PP 100 40 39 28 67 P 210244 OPERATING SYSTEM & ADMIN PR 50 20 16 F 210244 OPERATING SYSTEM & ADMIN PR 50 20 16 F 210245 MICROPROCESSOR ARCHITECTURE PP 100 40 24 28 52 P	210244	OPERATING SYSTEM & ADM	IN I	PP 1	L00 40	34	24	58	Р							
210245 MICROPROCESSOR ARCHITECTURE PP 100 40 32 22 54 P 210245 MICROPROCESSOR ARCHITECTURE TW 25 10 18 P 210245 MICROPROCESSOR ARCHITECTURE OR 50 20 29 P 210246 SOFT SKILLS TW 25 10 19 P FIRST SEM TOTAL = 437/750 S140054448 SHINDE SNEHAL SUNIL ALKA ,71527605M ,PICT ,S140054448 SEM.:1 210241 DISCRETE STRUCTURE PP 100 40 42 36 78 P 210242 DATA STRUCTURE & PROB SOLVING PP 100 40 33 25 58 P 210242 DATA STRUCTURE & PROB SOLVING PR 50 20 20 P 210243 DIGITAL ELEX. & LOGIC DESIGN TW 25 10 21 P 210244 OPERATING SYSTEM & ADMIN PP 100 40 39 28 67 P 210244 OPERATING SYSTEM & ADMIN PP 100 40 39 28 67 P 210244 OPERATING SYSTEM & ADMIN PR 50 20 16 F 210245 MICROPROCESSOR ARCHITECTURE PP 100 40 24 28 52 P	210244	OPERATING SYSTEM & ADM	IN -	TW	25 10)		19	Р							
210245 MICROPROCESSOR ARCHITECTURE TW 25 10 18 P 210245 MICROPROCESSOR ARCHITECTURE OR 50 20 29 P 210246 SOFT SKILLS TW 25 10 19 P FIRST SEM TOTAL = 437/750 S140054448 SHINDE SNEHAL SUNIL ALKA ,71527605M ,PICT ,S140054448 SEM.:1 210241 DISCRETE STRUCTURE PP 100 40 42 36 78 P 210242 DATA STRUCTURE & PROB SOLVING PP 100 40 33 25 58 P 210242 DATA STRUCTURE & PROB SOLVING PR 50 20 20 P 210243 DIGITAL ELEX. & LOGIC DESIGN PP 100 40 37 31 68 P 210244 OPERATING SYSTEM & ADMIN PP 100 40 39 28 67 P 210244 OPERATING SYSTEM & ADMIN TW 25 10 21 P 210244 OPERATING SYSTEM & ADMIN TW 25 10 21 P 210244 OPERATING SYSTEM & ADMIN TW 25 10 21 P 210244 OPERATING SYSTEM & ADMIN TW 25 10 21 P 210244 OPERATING SYSTEM & ADMIN TW 25 10 21 P 210244 OPERATING SYSTEM & ADMIN TW 25 10 21 P 210244 OPERATING SYSTEM & ADMIN TW 25 10 21 P 210244 OPERATING SYSTEM & ADMIN TW 25 10 21 P 210244 OPERATING SYSTEM & ADMIN TW 25 10 21 P 210245 MICROPROCESSOR ARCHITECTURE PP 100 40 24 28 52 P	210244	OPERATING SYSTEM & ADM	IN I	PR	50 20	1		25	Р							
210245 MICROPROCESSOR ARCHITECTURE OR 50 20 29 P 210246 SOFT SKILLS TW 25 10 19 P FIRST SEM TOTAL = 437/750 S140054448 SHINDE SNEHAL SUNIL ALKA ,71527605M ,PICT ,S140054448 SEM.:1 210241 DISCRETE STRUCTURE PP 100 40 42 36 78 P 210242 DATA STRUCTURE & PROB SOLVING PP 100 40 33 25 58 P 210242 DATA STRUCTURE & PROB SOLVING PR 50 20 20 P 210243 DIGITAL ELEX. & LOGIC DESIGN PP 100 40 37 31 68 P 210243 DIGITAL ELEX. & LOGIC DESIGN PP 100 40 39 28 67 P 210244 OPERATING SYSTEM & ADMIN PP 100 40 39 28 67 P 210244 OPERATING SYSTEM & ADMIN PR 50 20 16 F 210244 OPERATING SYSTEM & ADMIN PR 50 20 16 F 210245 MICROPROCESSOR ARCHITECTURE PP 100 40 24 28 52 P	210245	MICROPROCESSOR ARCHITE	CTURE I	PP 1	L00 40	32	22	54	Р							
210246 SOFT SKILLS TW 25 10 19 P FIRST SEM TOTAL = 437/750 S140054448 SHINDE SNEHAL SUNIL ALKA ,71527605M ,PICT ,S140054448 SEM.:1 210241 DISCRETE STRUCTURE PP 100 40 42 36 78 P 210242 DATA STRUCTURE & PROB SOLVING PP 100 40 33 25 58 P 210242 DATA STRUCTURE & PROB SOLVING PR 50 20 20 P 210243 DIGITAL ELEX. & LOGIC DESIGN PP 100 40 37 31 68 P 210243 DIGITAL ELEX. & LOGIC DESIGN TW 25 10 21 P 210244 OPERATING SYSTEM & ADMIN PP 100 40 39 28 67 P 210244 OPERATING SYSTEM & ADMIN PR 50 20 16 F 210244 OPERATING SYSTEM & ADMIN PR 50 20 16 F 210245 MICROPROCESSOR ARCHITECTURE PP 100 40 24 28 52 P	210245	MICROPROCESSOR ARCHITE	CTURE	TW	25 10)		18	Р							
FIRST SEM TOTAL = 437/750 S140054448 SHINDE SNEHAL SUNIL ALKA ,71527605M ,PICT ,S140054448 SEM.:1 210241 DISCRETE STRUCTURE PP 100 40 42 36 78 P 210242 DATA STRUCTURE & PROB SOLVING PP 100 40 33 25 58 P 210242 DATA STRUCTURE & PROB SOLVING PR 50 20 20 P 210243 DIGITAL ELEX. & LOGIC DESIGN PP 100 40 37 31 68 P 210243 DIGITAL ELEX. & LOGIC DESIGN TW 25 10 21 P 210244 OPERATING SYSTEM & ADMIN PP 100 40 39 28 67 P 210244 OPERATING SYSTEM & ADMIN TW 25 10 21 P 210244 OPERATING SYSTEM & ADMIN PR 50 20 16 F 210245 MICROPROCESSOR ARCHITECTURE PP 100 40 24 28 52 P	210245	MICROPROCESSOR ARCHITE	CTURE (OR	50 20)		29	Р							
SEM.:1 210241 DISCRETE STRUCTURE PP 100 40 42 36 78 P 210242 DATA STRUCTURE & PROB SOLVING PP 100 40 33 25 58 P 210242 DATA STRUCTURE & PROB SOLVING PR 50 20 20 P 210243 DIGITAL ELEX. & LOGIC DESIGN PP 100 40 37 31 68 P 210243 DIGITAL ELEX. & LOGIC DESIGN TW 25 10 21 P 210244 OPERATING SYSTEM & ADMIN PP 100 40 39 28 67 P 210244 OPERATING SYSTEM & ADMIN TW 25 10 21 P 210244 OPERATING SYSTEM & ADMIN PR 50 20 16 F 210245 MICROPROCESSOR ARCHITECTURE PP 100 40 24 28 52 P	210246	SOFT SKILLS	-	TW	25 10)		19	Р							
SEM.:1 210241 DISCRETE STRUCTURE PP 100 40 42 36 78 P 210242 DATA STRUCTURE & PROB SOLVING PP 100 40 33 25 58 P 210242 DATA STRUCTURE & PROB SOLVING PR 50 20 20 P 210243 DIGITAL ELEX. & LOGIC DESIGN PP 100 40 37 31 68 P 210243 DIGITAL ELEX. & LOGIC DESIGN TW 25 10 21 P 210244 OPERATING SYSTEM & ADMIN PP 100 40 39 28 67 P 210244 OPERATING SYSTEM & ADMIN TW 25 10 21 P 210244 OPERATING SYSTEM & ADMIN PR 50 20 16 F 210245 MICROPROCESSOR ARCHITECTURE PP 100 40 24 28 52 P		FIRST SEM TOTAL = 437/	750													
SEM.:1 210241 DISCRETE STRUCTURE PP 100 40 42 36 78 P 210242 DATA STRUCTURE & PROB SOLVING PP 100 40 33 25 58 P 210242 DATA STRUCTURE & PROB SOLVING PR 50 20 20 P 210243 DIGITAL ELEX. & LOGIC DESIGN PP 100 40 37 31 68 P 210244 OPERATING SYSTEM & ADMIN PP 100 40 39 28 67 P 210244 OPERATING SYSTEM & ADMIN TW 25 10 21 P 210244 OPERATING SYSTEM & ADMIN TW 25 10 21 P 210244 OPERATING SYSTEM & ADMIN PR 50 20 16 F 210245 MICROPROCESSOR ARCHITECTURE PP 100 40 24 28 52 P	\$14005															
210241 DISCRETE STRUCTURE PP 100 40 42 36 78 P 210242 DATA STRUCTURE & PROB SOLVING PP 100 40 33 25 58 P 210242 DATA STRUCTURE & PROB SOLVING PR 50 20 20 P 210243 DIGITAL ELEX. & LOGIC DESIGN PP 100 40 37 31 68 P 210244 DIGITAL ELEX. & LOGIC DESIGN TW 25 10 21 P 210244 OPERATING SYSTEM & ADMIN PP 100 40 39 28 67 P 210244 OPERATING SYSTEM & ADMIN TW 25 10 21 P 210244 OPERATING SYSTEM & ADMIN PR 50 20 16 F 210245 MICROPROCESSOR ARCHITECTURE PP 100 40 24 28 52 P	314003	THE SHERE SHERE SU	116				ALINA	•			,,,	327 00 JN	,, 101		,5170054	
210242 DATA STRUCTURE & PROB SOLVING PP 100 40 33 25 58 P 210242 DATA STRUCTURE & PROB SOLVING PR 50 20 20 P 210243 DIGITAL ELEX. & LOGIC DESIGN PP 100 40 37 31 68 P 210244 OPERATING SYSTEM & ADMIN PP 100 40 39 28 67 P 210244 OPERATING SYSTEM & ADMIN TW 25 10 21 P 210244 OPERATING SYSTEM & ADMIN PR 50 20 16 F 210245 MICROPROCESSOR ARCHITECTURE PP 100 40 24 28 52 P	SEM.:	1														
210242 DATA STRUCTURE & PROB SOLVING PR 50 20 20 P 210243 DIGITAL ELEX. & LOGIC DESIGN PP 100 40 37 31 68 P 210243 DIGITAL ELEX. & LOGIC DESIGN TW 25 10 21 P 210244 OPERATING SYSTEM & ADMIN PP 100 40 39 28 67 P 210244 OPERATING SYSTEM & ADMIN TW 25 10 21 P 210244 OPERATING SYSTEM & ADMIN PR 50 20 16 F 210245 MICROPROCESSOR ARCHITECTURE PP 100 40 24 28 52 P	210241	DISCRETE STRUCTURE	ı	PP 1	L00 40	42	36	78	Р							
210243 DIGITAL ELEX. & LOGIC DESIGN PP 100 40 37 31 68 P 210243 DIGITAL ELEX. & LOGIC DESIGN TW 25 10 21 P 210244 OPERATING SYSTEM & ADMIN PP 100 40 39 28 67 P 210244 OPERATING SYSTEM & ADMIN TW 25 10 21 P 210244 OPERATING SYSTEM & ADMIN PR 50 20 16 F 210245 MICROPROCESSOR ARCHITECTURE PP 100 40 24 28 52 P	210242	DATA STRUCTURE & PROB S	SOLVING	PP 1	L00 40	33	25	58	Р							
210243 DIGITAL ELEX. & LOGIC DESIGN TW 25 10 21 P 210244 OPERATING SYSTEM & ADMIN PP 100 40 39 28 67 P 210244 OPERATING SYSTEM & ADMIN TW 25 10 21 P 210244 OPERATING SYSTEM & ADMIN PR 50 20 16 F 210245 MICROPROCESSOR ARCHITECTURE PP 100 40 24 28 52 P	210242	DATA STRUCTURE & PROB S	SOLVING	PR	50 20)		20	Р							
210244 OPERATING SYSTEM & ADMIN PP 100 40 39 28 67 P 210244 OPERATING SYSTEM & ADMIN TW 25 10 21 P 210244 OPERATING SYSTEM & ADMIN PR 50 20 16 F 210245 MICROPROCESSOR ARCHITECTURE PP 100 40 24 28 52 P	210243	DIGITAL ELEX. & LOGIC	DESIGN I	PP 1	L00 40	37	31	68	Р							
210244 OPERATING SYSTEM & ADMIN TW 25 10 21 P 210244 OPERATING SYSTEM & ADMIN PR 50 20 16 F 210245 MICROPROCESSOR ARCHITECTURE PP 100 40 24 28 52 P	210243	DIGITAL ELEX. & LOGIC	DESIGN T	TW	25 10)		21	Р							
210244 OPERATING SYSTEM & ADMIN PR 50 20 16 F 210245 MICROPROCESSOR ARCHITECTURE PP 100 40 24 28 52 P	210244	OPERATING SYSTEM & ADM	IN I	PP 1	L00 40	39	28	67	Р							
210245 MICROPROCESSOR ARCHITECTURE PP 100 40 24 28 52 P	210244	OPERATING SYSTEM & ADM	IN -	TW	25 10)		21	Р							
	210244	OPERATING SYSTEM & ADM	IN I	PR	50 20)		16	F							
210245 MICROPROCESSOR ARCHITECTURE TW 25 10 20 P	210245	MICROPROCESSOR ARCHITEC	CTURE I	PP 1	L00 40	24	28	52	Р							
	210245	MICROPROCESSOR ARCHITEC	CTURE	TW	25 10)		20	Р							

210245 MICROPROCESSOR ARCHITECTURE OR 50 20 12 F
210246 SOFT SKILLS TW 25 10 21 P
FIRST SEM TOTAL = --/750

	9 S.E.(2014 PAT.)(COMPUTER) : 25 JAN 2016 CENTRE: PU	JNE	INSTIT	UTE OF	COMP	PUTER TECHNOLO	GY, PUNE.	F	PAGE NO. 180
	FIRST LINE : SEAT NO., NAME OF			•			·		
	,	-		-		·		•	P/F:PASS/FAIL, C:PREV.CARRY OVER
	4449 SHIRKE UTTKARSHA MAHENDI						,71633711յ		,s140054449
SEM.:	1								
210241	DISCRETE STRUCTURE	PP	100 40	32	26	58 P			
210242	DATA STRUCTURE & PROB SOLVING	PP	100 40	40	32	72 P			
210242	DATA STRUCTURE & PROB SOLVING	PR	50 20			29 P			
210243	DIGITAL ELEX. & LOGIC DESIGN	PP	100 40	33	30	63 P			
210243	DIGITAL ELEX. & LOGIC DESIGN	TW	25 10			20 P			
210244	OPERATING SYSTEM & ADMIN	PP	100 40	32	30	62 P			
210244			25 10			19 P			
210244	OPERATING SYSTEM & ADMIN		50 20			41 P			
210245			100 40		32	71 P			
210245			25 10			19 P			
			50 20			37 P			
210246		I W	25 10			19 P			
	FIRST SEM TOTAL = 510/750								
	4451 SHIVANKAR SHREYAS PRABHA								,S140054451
SEM.:	1								
210241	DISCRETE STRUCTURE	PP	100 40	39	28	67 P			
210242	DATA STRUCTURE & PROB SOLVING	PP	100 40	33	20	53 P			
210242	DATA STRUCTURE & PROB SOLVING	PR	50 20			25 P			
210243	DIGITAL ELEX. & LOGIC DESIGN	PP	100 40	37	19	56 P			
210243	DIGITAL ELEX. & LOGIC DESIGN					12 P			
210244	OPERATING SYSTEM & ADMIN		100 40		24	50 P			
210244			25 10			16 P			
210244	OPERATING SYSTEM & ADMIN		50 20		1 -	28 P			
210245	MICROPROCESSOR ARCHITECTURE		100 40		15	41 P			
210245 210245			25 10 50 20			13 P 11 F			
210243			25 10			18 P			
210240	FIRST SEM TOTAL =/750	I VV	23 10			10 F			
	·								
s14005	4452 SHLOK GILDA					IJANA	,71527610н		
SEM.:	1								
210241	DISCRETE STRUCTURE	PP	100 40	43	42	85 P			
210242	DATA STRUCTURE & PROB SOLVING	PP	100 40	30	30	60 P			
210242	DATA STRUCTURE & PROB SOLVING	PR	50 20			23 P			
210243	DIGITAL ELEX. & LOGIC DESIGN				21	56 P			
210243	DIGITAL ELEX. & LOGIC DESIGN					21 P			
210244	OPERATING SYSTEM & ADMIN		100 40		32	66 P			
210244			25 10			22 P			
210244	OPERATING SYSTEM & ADMIN		50 20		20	44 P			
210245			100 40		30	64 P			
210245	MICROPROCESSOR ARCHITECTURE	١W	25 10			20 P			

210245 MICROPROCESSOR ARCHITECTURE OR 50 20 42 P
210246 SOFT SKILLS TW 25 10 20 P
FIRST SEM TOTAL = 523/750

	: 25 JAN 2016 CEN	-	E IN	STITU	ΓE OF	COMPUT	ER T	ECHNOLOG	GY, PU	INE.			PAGE NO	o. 181		
	FIRST LINE : SEAT NO.,															
	ES: SUB.CODE, SUB.NAME,				-		-			•			-	•		
	4453 SHRINATH DILIP JA													,s140054		
SEM.:	1															
210241	DISCRETE STRUCTURE	Р	P 10	0 40	37	29	66	Р								
210242	DATA STRUCTURE & PROB S	OLVING P	P 10	0 40	39	31	70	Р								
210242	DATA STRUCTURE & PROB S	OLVING P	R 5	0 20			36	Р								
210243	DIGITAL ELEX. & LOGIC D	ESIGN P	P 10	0 40	40	27	67	Р								
210243	DIGITAL ELEX. & LOGIC D	ESIGN T	W 2	5 10			19	Р								
210244	OPERATING SYSTEM & ADMI	N P	P 10	0 40	36	28	64	Р								
210244	OPERATING SYSTEM & ADMI	N T	w 2	5 10			18	Р								
210244	OPERATING SYSTEM & ADMI	N P	R 5	0 20			35	Р								
210245	MICROPROCESSOR ARCHITEC	TURE P	P 10	0 40	34	22	56	Р								
210245	MICROPROCESSOR ARCHITEC	TURE T	W 2	5 10			18	Р								
210245	MICROPROCESSOR ARCHITEC	TURE 0	R 5	0 20			39	Р								
210246	SOFT SKILLS		W 2	5 10			18	Р								
	FIRST SEM TOTAL = $506/7$															
	4454 SHUBHAM YADAV		• • • •			BIMLA						,PICT		s140054		
314003	4434 SHUBHAM TADAV					DIMLA			,	71327017	<i>,</i> E	, PICI		,31400344	134	
SEM.:	1															
	DISCRETE STRUCTURE	Р	P 10	0 40	39	32	71	Р								
	DATA STRUCTURE & PROB S				32		54									
	DATA STRUCTURE & PROB S						21									
210243	DIGITAL ELEX. & LOGIC D	ESIGN P	P 10	0 40	35	21	56									
210243	DIGITAL ELEX. & LOGIC D	ESIGN T	w 2	5 10			10	Р								
210244	OPERATING SYSTEM & ADMI	N P	P 10	0 40	34	29	63	Р								
210244	OPERATING SYSTEM & ADMI	N T	w 2	5 10			18	Р								
210244	OPERATING SYSTEM & ADMI	N P	R 5	0 20			32	Р								
210245	MICROPROCESSOR ARCHITEC	TURE P	P 10	0 40	35	21	56	Р								
210245	MICROPROCESSOR ARCHITEC	TURE T	w 2	5 10			13	Р								
210245	MICROPROCESSOR ARCHITEC	TURE 0	R 5	0 20			27	Р								
210246	SOFT SKILLS	Т	w 2	5 10			10	Р								
	FIRST SEM TOTAL = $431/7$	50														
\$14005	4455 SHUKLA NAMAN SHYA	M				RAGIN	Ι		,	/152/619	ЭМ	,PICT		,S1400544	155	
SEM.:	1															
	DISCRETE STRUCTURE	В	D 10	0 40	45	43	88	D								
	DATA STRUCTURE & PROB S				41	30	71									
210242	DATA STRUCTURE & PROB S				71	30	40									
210242	DIGITAL ELEX. & LOGIC D				38	28	66									
210243	DIGITAL ELEX. & LOGIC D				33	_0	22									
210243	OPERATING SYSTEM & ADMI		P 10		38	29	67									
210244	OPERATING SYSTEM & ADMI		w 2		55		22									
210244	OPERATING SYSTEM & ADMI		R 5				33									
210245	MICROPROCESSOR ARCHITEC		P 10		38	25	63									
	MICROPROCESSOR ARCHITEC		w 2				22									

210245 MICROPROCESSOR ARCHITECTURE OR 50 20 41 P
210246 SOFT SKILLS TW 25 10 21 P
FIRST SEM TOTAL = 556/750

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	FIRST LINE : SEAT NO., NAME ES: SUB.CODE, SUB.NAME, SUB.				•	•			-		•	-	
	30B. CODE, 30B. NAME, 30B.												
	4456 SHUKLA YASH VIJAY								,71527620E				
	_												
SEM.:	1 DISCRETE STRUCTURE	DI	100	40 2	2	27 6	O D						
	DATA STRUCTURE & PROB SOLVI				_		59 P 52 P						
	DATA STRUCTURE & PROB SOLVI				1		39 P						
210242	DIGITAL ELEX. & LOGIC DESIG				6		54 P						
	DIGITAL ELEX. & LOGIC DESIG				U		.4 P						
210244	OPERATING SYSTEM & ADMIN		100		5		55 P						
210244	OPERATING SYSTEM & ADMIN		v 25		_		L6 P						
210244	OPERATING SYSTEM & ADMIN	PF	R 50	20		2	27 P						
210245	MICROPROCESSOR ARCHITECTURE	. PF	100	40 3	5	18 5	3 P						
210245	MICROPROCESSOR ARCHITECTURE	. ти	v 25	10		1	L5 P						
210245	MICROPROCESSOR ARCHITECTURE	OF	R 50	20		8	B F						
210246	SOFT SKILLS	T۷	v 25	10		1	L5 P						
	FIRST SEM TOTAL =/750												
S14005	4457 SINGH MRINAL MANOJKUM	AR				SAVITA		,	,71527622М	,PICT		,S1400544	157
SEM.:													
	DISCRETE STRUCTURE						57 P						
	DATA STRUCTURE & PROB SOLVI				5		7 P						
210242	DATA STRUCTURE & PROB SOLVI						25 P						
	DIGITAL ELEX. & LOGIC DESIG				1		52 P						
210243	DIGITAL ELEX. & LOGIC DESIG				_		L8 P						
210244 210244	OPERATING SYSTEM & ADMIN OPERATING SYSTEM & ADMIN		> 100 v 25		3		51 P 20 P						
210244	OPERATING SYSTEM & ADMIN		v 23				30 P						
210244	MICROPROCESSOR ARCHITECTURE		· 100		6		50 F 51 P						
210245	MICROPROCESSOR ARCHITECTURE		v 25		U		L8 P						
	MICROPROCESSOR ARCHITECTURE		· 50				21 P						
	SOFT SKILLS		v 25				20 P						
2202.0	FIRST SEM TOTAL = 450/750	• •	5			_							
s14005	4458 SOHONY ISHAN NITIN					NANDINI		,	,71527626D	,PICT		,S1400544	158
SEM.:	1												
210241	DISCRETE STRUCTURE	PF	100	40 3	9	42 8	31 P						
210242	DATA STRUCTURE & PROB SOLVI	NG PF	100	40 4	1	26 6	57 P						
210242	DATA STRUCTURE & PROB SOLVI						26 P						
210243	DIGITAL ELEX. & LOGIC DESIG				5		54 P						
210243	DIGITAL ELEX. & LOGIC DESIG				_		L9 P						
210244			25		5		59 P						
210244	OPERATING SYSTEM & ADMIN		v 25				21 P						
210244	OPERATING SYSTEM & ADMIN		50		0		13 P						
210245	MICROPROCESSOR ARCHITECTURE		2 100		8		54 P						
210245	MICROPROCESSOR ARCHITECTURE	TV	v 25	ΤO		2	20 P						

210245 MICROPROCESSOR ARCHITECTURE OR 50 20 21 P
210246 SOFT SKILLS TW 25 10 20 P
FIRST SEM TOTAL = 495/750

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	FIRST LINE : SEAT NO., NAME O												
	ES: SUB.CODE, SUB.NAME, SUB.TY												
	4459 SONI CHINTAN SHAILESH												
SEM.:	1												
210241	DISCRETE STRUCTURE	PP 1	L00 4	0 44	44	88	3 P						
210242	DATA STRUCTURE & PROB SOLVING	PP 1	L00 4	0 34	29	63	3 P						
210242	DATA STRUCTURE & PROB SOLVING	PR	50 2	0		44	ŀР						
210243	DIGITAL ELEX. & LOGIC DESIGN	PP 1	L00 4	0 42	28	70) P						
210243	DIGITAL ELEX. & LOGIC DESIGN	TW	25 1	0		21	L P						
210244	OPERATING SYSTEM & ADMIN	PP 1	L00 4	0 39	30	69) P						
210244	OPERATING SYSTEM & ADMIN	TW	25 1	0		20) P						
210244	OPERATING SYSTEM & ADMIN	PR	50 2	0		47	' P						
210245	MICROPROCESSOR ARCHITECTURE	PP 1	L00 4	0 36	28	64	ŀ P						
210245	MICROPROCESSOR ARCHITECTURE	TW	25 1	0		21	L P						
210245	MICROPROCESSOR ARCHITECTURE	OR	50 2	0		45	5 P						
210246	SOFT SKILLS	TW	25 1	0		22	? P						
	FIRST SEM TOTAL = $574/750$												
S14005	4460 SORGEKAR MAROTI SAHEBRA	0			NA	NDA			,71527633G	,PICT		,S1400544	160
SEM.:	1												
210241	DISCRETE STRUCTURE	PP 1	L00 4	0 45	32	77	' P						
210242	DATA STRUCTURE & PROB SOLVING	PP 1	L00 4	0 39	24	63	B P						
210242	DATA STRUCTURE & PROB SOLVING	PR	50 2	0		39) P						
210243	DIGITAL ELEX. & LOGIC DESIGN	PP 1	L00 4	0 37	35	72	2 P						
210243	DIGITAL ELEX. & LOGIC DESIGN	TW	25 1	0			B P						
210244	OPERATING SYSTEM & ADMIN		L00 4		26) P						
			25 1) P						
210244	OPERATING SYSTEM & ADMIN		50 2				6 P						
			L00 4		23		3 P						
210245	MICROPROCESSOR ARCHITECTURE	TW	25 1	0		19) P						
210245	MICROPROCESSOR ARCHITECTURE	OR	50 2	0		30) P						
210246	SOFT SKILLS	TW	25 1	0		20) P						
	FIRST SEM TOTAL = $506/750$												
	• • • • • • • • • • • • • • • • • • • •												
s14005	4461 SUDRIK PRACHI RANJIT				MAI	NGAL			,71527636М	,PICT		,S1400544	161
SEM.:													
	DISCRETE STRUCTURE		L00 4		27) P						
	DATA STRUCTURE & PROB SOLVING				31		3 P						
	DATA STRUCTURE & PROB SOLVING						3 P						
	DIGITAL ELEX. & LOGIC DESIGN				37		L P						
	DIGITAL ELEX. & LOGIC DESIGN				~ -		L P						
	OPERATING SYSTEM & ADMIN		L00 4		. 28) P						
	OPERATING SYSTEM & ADMIN		25 1				L P						
	OPERATING SYSTEM & ADMIN		50 2		~ -) P						
	MICROPROCESSOR ARCHITECTURE		L00 4		31		L P						
210245	MICROPROCESSOR ARCHITECTURE	I W	25 1	U		20) P						

210245 MICROPROCESSOR ARCHITECTURE OR 50 20 27 P
210246 SOFT SKILLS TW 25 10 20 P
FIRST SEM TOTAL = 500/750

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	: 25 JAN 2016 CE	-	IE IN	ISTITU	TE OF	COMPUT	ER -	TECHNOL	.OGY, P	UNE.			PAGE NO	D. 184		
	FIRST LINE : SEAT NO.,															•
	ES: SUB.CODE, SUB.NAME,				-		-			•			•	•		ER
	4462															
\$14005	4462 SURABHI SUHAS PO	NKSHE				SUPRI	YA			,/152/	639F	,PICT		,\$1400544	162	
SEM.:	1															
210241	DISCRETE STRUCTURE	F	PP 10	0 40	47	39	86	Р								
210242	DATA STRUCTURE & PROB	SOLVING F	PP 10	0 40	37	29	66	Р								
210242	DATA STRUCTURE & PROB	SOLVING F	PR 5	0 20			37	Р								
210243	DIGITAL ELEX. & LOGIC				40	31	71	Р								
210243	DIGITAL ELEX. & LOGIC						21									
210244	OPERATING SYSTEM & ADM			0 40	33	25	58									
210244	OPERATING SYSTEM & ADM			25 10			21									
210244	OPERATING SYSTEM & ADM			50 20			30									
210245	MICROPROCESSOR ARCHITE			00 40	46	27	73									
210245	MICROPROCESSOR ARCHITE			25 10			19									
210245	MICROPROCESSOR ARCHITE			50 20			37									
210246	SOFT SKILLS		W Z	25 10			21	Р								
	FIRST SEM TOTAL = 540/															
	4462 CURANA AROORYA C															•
514005	4463 SURANA APOORVA S	ODHIK				RAKSH	А			, / 132/	041H	,PICT		,S1400544	103	
SEM.:	1															
	DISCRETE STRUCTURE		DD 10	0 40	34	36	70	D								
	DATA STRUCTURE & PROB				24		48									
	DATA STRUCTURE & PROB				4	24	30									
	DIGITAL ELEX. & LOGIC				34	27	61									
	DIGITAL ELEX. & LOGIC				J 1	21	18									
210243	OPERATING SYSTEM & ADM			0 40	35	20	55									
	OPERATING SYSTEM & ADM			25 10	33	20	21									
210244	OPERATING SYSTEM & ADM			0 20			31									
210245	MICROPROCESSOR ARCHITE			0 40	32	23	55									
	MICROPROCESSOR ARCHITE			25 10	-		18									
	MICROPROCESSOR ARCHITE			0 20			24									
	SOFT SKILLS			25 10			18									
	FIRST SEM TOTAL = 449/							-								
s14005	4464 SURASE INDRAYANI	BABASAHE	В			ALKA				,71633	712G	,PICT		,S1400544	164	
SEM.:	1															
210241	DISCRETE STRUCTURE	F	P 10	0 40	34	30	64	Р								
210242	DATA STRUCTURE & PROB	SOLVING F	P 10	0 40	28	30	58	Р								
210242	DATA STRUCTURE & PROB	SOLVING F	PR 5	0 20			3	F								
210243	DIGITAL ELEX. & LOGIC	DESIGN F	PP 10	0 40	24	32	56	Р								
210243	DIGITAL ELEX. & LOGIC	DESIGN T	W 2	25 10			21	Р								
210244	OPERATING SYSTEM & ADM	IN F	PP 10	0 40	30	32	62	Р								
210244	OPERATING SYSTEM & ADM	IN T	W 2	25 10			21	Р								
210244	OPERATING SYSTEM & ADM	IN F	PR 5	0 20			36	Р								
	MICROPROCESSOR ARCHITE			00 40	29	32	61									
210245	MICROPROCESSOR ARCHITE	CTURE T	W 2	25 10			21	Р								

210245 MICROPROCESSOR ARCHITECTURE OR 50 20 21 P
210246 SOFT SKILLS TW 25 10 21 P
FIRST SEM TOTAL = --/750

	9 S.E.(2014 PAT.)(COMPUTER) : 25 JAN 2016 CENTRE: PU	UNE	INSTITU	TE OF	COMPUT	ER T	ECHNOLOGY, P	UNE.	PAGE 1	NO. 185
	FIRST LINE : SEAT NO., NAME OF ES: SUB.CODE, SUB.NAME, SUB.TY			-		-		•	•	•
	4465 SURVE VIJAYA VILAS								,PICT	
31.003	7.03				, , , , , , , ,	•		, . 1000 . 101	,	,52.005.105
SEM.:	1									
	DISCRETE STRUCTURE		100 40	32	27	59				
	DATA STRUCTURE & PROB SOLVING			31	28	59				
	DATA STRUCTURE & PROB SOLVING					5				
	DIGITAL ELEX. & LOGIC DESIGN			25	26	51				
210243 210244	DIGITAL ELEX. & LOGIC DESIGN			2.4	22	21				
210244	OPERATING SYSTEM & ADMIN OPERATING SYSTEM & ADMIN		100 40 25 10	34	33	67 22				
210244	OPERATING SYSTEM & ADMIN		50 20			32				
210244			100 40	25	25	50				
210245			25 10		23	21				
			50 20			8				
			25 10			22				
	FIRST SEM TOTAL =/750									
S14005	4466 TAMANNA NAYAK				ILLA			,71527648E	,PICT	,S140054466
CEM .	1									
SEM.:	DISCRETE STRUCTURE	DD	100 40	27	40	77	D			
	DATA STRUCTURE & PROB SOLVING			_	30	69				
	DATA STRUCTURE & PROB SOLVING			33	30	37				
	DIGITAL ELEX. & LOGIC DESIGN			35	22	57				
210243	DIGITAL ELEX. & LOGIC DESIGN			33		15				
210244	OPERATING SYSTEM & ADMIN	PP	100 40	29	24	53	Р			
210244	OPERATING SYSTEM & ADMIN	TW	25 10			18	Р			
210244	OPERATING SYSTEM & ADMIN	PR	50 20			38	Р			
210245	MICROPROCESSOR ARCHITECTURE	PP	100 40	32	23	55	P			
210245	MICROPROCESSOR ARCHITECTURE	TW	25 10			16	P			
210245	MICROPROCESSOR ARCHITECTURE	OR	50 20			32	P			
210246		TW	25 10			19	Р			
	FIRST SEM TOTAL = 486/750									
										C1400F4467
514005	4467 TAMHANKAR AJINKYA ABHIJ	ΙI			GEETA	١		,/132/031E	,PICT	,5140034467
SEM.:	1									
	DISCRETE STRUCTURE	PP	100 40	30	22	52	Р			
	DATA STRUCTURE & PROB SOLVING			26	15	41				
210242	DATA STRUCTURE & PROB SOLVING					4				
210243	DIGITAL ELEX. & LOGIC DESIGN			24	16	40	Р			
210243	DIGITAL ELEX. & LOGIC DESIGN					19				
210244	OPERATING SYSTEM & ADMIN	PP	100 40	29	15	44	Р			
210244	OPERATING SYSTEM & ADMIN	TW	25 10			20	Р			
210244	OPERATING SYSTEM & ADMIN	PR	50 20			32	Р			
210245	MICROPROCESSOR ARCHITECTURE	PP	100 40	24	16	40	Р			
210245	MICROPROCESSOR ARCHITECTURE	TW	25 10			19	Р			

210245 MICROPROCESSOR ARCHITECTURE OR 50 20 5 F
210246 SOFT SKILLS TW 25 10 19 P
FIRST SEM TOTAL = --/750

	: 25 JAN 2016 CENTRE: P	UNE	INST	ГІТИТІ	E OF	COMPUT	ER	TECHNOLOGY, I	PUNE.		PAGE N	0. 186	
	FIRST LINE : SEAT NO., NAME O												
	ES: SUB.CODE, SUB.NAME, SUB.TY												
	4468 TANISHK SHIVELY							IVELY					
SEM.:	1												
210241	DISCRETE STRUCTURE	PP	100	40	21	20	41	Р					
210242	DATA STRUCTURE & PROB SOLVING	PP	100	40	28	04		F					
210242	DATA STRUCTURE & PROB SOLVING	PR	50	20			27	Р					
210243	DIGITAL ELEX. & LOGIC DESIGN	PP	100	40	21	12		F					
210243	DIGITAL ELEX. & LOGIC DESIGN	TW	25	10			17	Р					
210244	OPERATING SYSTEM & ADMIN	PP	100	40	33	19	52	Р					
210244	OPERATING SYSTEM & ADMIN	TW	25	10			20	Р					
210244	OPERATING SYSTEM & ADMIN	PR	50	20			26	Р					
210245	MICROPROCESSOR ARCHITECTURE	PP	100	40	22	10		F					
210245	MICROPROCESSOR ARCHITECTURE	TW	25	10			17	Р					
210245	MICROPROCESSOR ARCHITECTURE	OR	50	20			20	Р					
210246	SOFT SKILLS	TW	25	10			21	Р					
	FIRST SEM TOTAL = $/750$												
S14005	4469 TAPANSHI AGRAWAL					RACHA	NA		,71527655	SH ,PICT		,S140054	169
SEM.:			100	4.0	2.2	2.2		_					
	DISCRETE STRUCTURE		100	_	32		64						
_	DATA STRUCTURE & PROB SOLVING			_	28	22	50						
	DATA STRUCTURE & PROB SOLVING				2.0	27	7						
	DIGITAL ELEX. & LOGIC DESIGN				36	27	63	-					
210243 210244	DIGITAL ELEX. & LOGIC DESIGN OPERATING SYSTEM & ADMIN		100		26	26	12 52						
_	OPERATING SYSTEM & ADMIN				20	20	19						
	OPERATING SYSTEM & ADMIN		50				14						
			100		34	22	56						
			25		J 1		17						
			50				7						
			25				14						
2202.0	FIRST SEM TOTAL =/750	•											
	4470 TAYADE MAMATA KAILAS									lC ,PICT			170
SEM.:	1												
210241	DISCRETE STRUCTURE	PP	100	40	41	33	74	Р					
210242	DATA STRUCTURE & PROB SOLVING	PP	100	40	36	32	68	Р					
210242	DATA STRUCTURE & PROB SOLVING	PR	50	20			37	Р					
210243	DIGITAL ELEX. & LOGIC DESIGN	PP	100	40	33	32	65	Р					
210243	DIGITAL ELEX. & LOGIC DESIGN	TW	25	10			21	Р					
210244	OPERATING SYSTEM & ADMIN	PP	100	40	37	34	71	Р					
210244	OPERATING SYSTEM & ADMIN	TW	25	10			21	P					
210244	OPERATING SYSTEM & ADMIN		50				40	Р					
	MICROPROCESSOR ARCHITECTURE		100		38	29	67	Р					
210245	MICROPROCESSOR ARCHITECTURE	TW	25	10			21	Р					

210245 MICROPROCESSOR ARCHITECTURE OR 50 20 40 P
210246 SOFT SKILLS TW 25 10 21 P
FIRST SEM TOTAL = 546/750

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	: 25 JAN 2016 CENTRE: P	UNE	INSTITU	TE OF	COMPUT	ER T	ECHNOLOGY,	PUNE.		PAGE NO	o. 187	
	FIRST LINE : SEAT NO., NAME O			-		•		•		•	-	
	ES: SUB.CODE, SUB.NAME, SUB.TY											
	4471 TEJASWINI SANJIV SHINDE											
SEM.:	1											
	DISCRETE STRUCTURE	PP	100 40	32	27	59	Р					
	DATA STRUCTURE & PROB SOLVING			24		40						
	DATA STRUCTURE & PROB SOLVING					24						
210243	DIGITAL ELEX. & LOGIC DESIGN	PP	100 40	32	21	53	Р					
210243	DIGITAL ELEX. & LOGIC DESIGN	TW	25 10			20	Р					
210244	OPERATING SYSTEM & ADMIN	PP	100 40	34	20	54	P					
210244	OPERATING SYSTEM & ADMIN	TW	25 10			21	P					
210244	OPERATING SYSTEM & ADMIN	PR	50 20			16	F					
210245	MICROPROCESSOR ARCHITECTURE	PP	100 40	26	20	46	P					
210245	MICROPROCESSOR ARCHITECTURE	TW	25 10			21	P					
210245	MICROPROCESSOR ARCHITECTURE	OR	50 20			25	P					
210246	SOFT SKILLS	TW	25 10			23	P					
	FIRST SEM TOTAL = $/750$											
S14005	4472 TEKAM YASHWANT VITTHAL				DEVIT	A		,71527660D	,PICT		,S1400544	.72
6514	4											
SEM.:		-	100 40	21	22	<i>C</i> 4	Б					
	DISCRETE STRUCTURE DATA STRUCTURE & PROB SOLVING			31 32		64 56						
210242	DATA STRUCTURE & PROB SOLVING DATA STRUCTURE & PROB SOLVING			32	24	26						
-	DIGITAL ELEX. & LOGIC DESIGN			27	23	50						
210243	DIGITAL ELEX. & LOGIC DESIGN			21	23	19						
210244	OPERATING SYSTEM & ADMIN		100 40	23	18	41						
210244	OPERATING SYSTEM & ADMIN		25 10			20						
210244	OPERATING SYSTEM & ADMIN		50 20			41						
210245			100 40	27	22	49						
210245	MICROPROCESSOR ARCHITECTURE	TW	25 10			19	Р					
210245	MICROPROCESSOR ARCHITECTURE	OR	50 20			9	F					
210246	SOFT SKILLS	TW	25 10			18	Р					
	FIRST SEM TOTAL = $/750$											
s14005	4473 TELANG ASHWINI ASHOKRAO				UMA			,71527661B	,PICT		,S1400544	73
SEM.:												
	DISCRETE STRUCTURE		100 40		24	55						
210242	DATA STRUCTURE & PROB SOLVING			31	19	50						
210242	DATA STRUCTURE & PROB SOLVING			2.0	4.5	8						
210243	DIGITAL ELEX. & LOGIC DESIGN			28	15	43						
210243	DIGITAL ELEX. & LOGIC DESIGN			27	0.0	18						
210244 210244	OPERATING SYSTEM & ADMIN OPERATING SYSTEM & ADMIN		25 10	27	08	 18						
210244	OPERATING SYSTEM & ADMIN OPERATING SYSTEM & ADMIN		50 20			16 14						
210244	MICROPROCESSOR ARCHITECTURE		100 40	28	17	45						
			25 10	20	±1	43 17						
210273	MICROI ROCESSON ARCHITECTURE	1 44	23 10			Τ,	•					

210245 MICROPROCESSOR ARCHITECTURE OR 50 20 21 P
210246 SOFT SKILLS TW 25 10 16 P
FIRST SEM TOTAL = --/750

	: : 25 JAN 2016 CENTRE	-	E INST	TITUTE	OF	COMPUTE	R TEC	HNOLOG	Y, PUNE	i.		PAGE N	ıo. 188	
	FIRST LINE : SEAT NO., NAM													
	IES: SUB.CODE, SUB.NAME, SUB				-		•			•		•	-	
	4474 TELE HRISHIKESH BHIN													
SEM.:	1													
210241	DISCRETE STRUCTURE	Pi	P 100	40	31	35	66 P							
210242	DATA STRUCTURE & PROB SOLV	/ING P	P 100	40	38	19	57 P							
210242	DATA STRUCTURE & PROB SOLV	/ING PI	R 50	20			30 P							
210243	DIGITAL ELEX. & LOGIC DESI	IGN PI	P 100	40	41	30	71 P							
210243	DIGITAL ELEX. & LOGIC DESI	EGN T\	N 25	10			17 P							
210244	OPERATING SYSTEM & ADMIN	PI	P 100	40	24	16	40 P							
210244	OPERATING SYSTEM & ADMIN	TI	N 25	10			21 P							
210244	OPERATING SYSTEM & ADMIN	PI	R 50	20			24 P							
210245	MICROPROCESSOR ARCHITECTUR	RE PI	P 100	40	37	24	61 P							
210245	MICROPROCESSOR ARCHITECTUR	RE TI	N 25	10			19 P							
210245	MICROPROCESSOR ARCHITECTUR	RE OF	R 50	20			22 P							
210246	SOFT SKILLS	T\	N 25	10			20 P							
	FIRST SEM TOTAL = $448/750$													
S14005	4475 THAKARE ANUJ RAJENDI	₹A				KANCHA	AN		,71	.5276633	,PICT		,S140054	475
SEM.:	1													
	DISCRETE STRUCTURE	D	P 100	40	29	37	66 P							
	DATA STRUCTURE & PROB SOLV			_	_		65 P							
	DATA STRUCTURE & PROB SOLV				30		22 P							
	DIGITAL ELEX. & LOGIC DESI				31	28	59 P							
	DIGITAL ELEX. & LOGIC DESI				31	_	15 P							
210244	OPERATING SYSTEM & ADMIN		P 100		26		42 P							
	OPERATING SYSTEM & ADMIN		N 25				16 P							
210244	OPERATING SYSTEM & ADMIN		R 50				25 P							
210245	MICROPROCESSOR ARCHITECTUR		P 100		36		56 P							
210245	MICROPROCESSOR ARCHITECTUR		N 25				15 P							
210245	MICROPROCESSOR ARCHITECTUR		R 50				29 P							
210246	SOFT SKILLS		N 25				15 P							
	FIRST SEM TOTAL = 425/750													
S14005	4476 THAKARE KHUSHWANT DE	EORAM				SHOBHA	١.		,71	.527664G	,PICT		,S140054	476
SEM.:														
	DISCRETE STRUCTURE						76 P							
210242					33		58 P							
210242	DATA STRUCTURE & PROB SOLV				20		22 P							
210243	DIGITAL ELEX. & LOGIC DEST				30		61 P							
210243	DIGITAL ELEX. & LOGIC DESI				2.4		19 P							
210244	OPERATING SYSTEM & ADMIN		P 100		34		55 P							
210244	OPERATING SYSTEM & ADMIN		N 25				21 P							
210244	OPERATING SYSTEM & ADMIN		R 50		20		25 P							
210245	MICROPROCESSOR ARCHITECTUR		P 100		30		63 P							
210245	MICROPROCESSOR ARCHITECTUR	∢E TV	N 25	ΤΩ			20 P							

210245 MICROPROCESSOR ARCHITECTURE OR 50 20 20 P
210246 SOFT SKILLS TW 25 10 19 P
FIRST SEM TOTAL = 459/750

	9 S.E.(2014 PAT.)(COMPUTER) : 25 JAN 2016 CENTRE: PU	UNE	INST	ITUTE	OF	COMPUTE	ER -	TECHNOLOGY	, PUN	NE.		PAGE N	0. 189		
	FIRST LINE : SEAT NO., NAME OF				-		-			-		-		•	
	ES: SUB.CODE, SUB.NAME, SUB.TY	,			•			,	,	•		, ,	•	,	
	4477 THUBE SAURABH SUBHASH									71527671K					
SEM.:	1														
210241	DISCRETE STRUCTURE	PP	100 4	40	35	43	78	Р							
210242	DATA STRUCTURE & PROB SOLVING	PP	100 4	40	38	23	61	Р							
210242	DATA STRUCTURE & PROB SOLVING	PR	50 2	20			39	Р							
210243	DIGITAL ELEX. & LOGIC DESIGN	PP	100 4	40 2	27	34	61	Р							
210243	DIGITAL ELEX. & LOGIC DESIGN	TW	25 1	10			17	Р							
210244	OPERATING SYSTEM & ADMIN	PP	100 4	40	33	25	58	Р							
210244			25 1				21								
210244	OPERATING SYSTEM & ADMIN		50 2				30								
210245			100 4		38		68								
210245			25 1				18								
			50 2				30								
210246		TW	25 1	TO			18	Р							
	FIRST SEM TOTAL = 499/750														
	4478 TOSHNIWAL ANAND BHIKULAI									71527673F					
SEM.:	1														
	DISCRETE STRUCTURE				33	15	48	Р							
210242	DATA STRUCTURE & PROB SOLVING	PP	100 4	40	28	15	43	Р							
	DATA STRUCTURE & PROB SOLVING						5								
	DIGITAL ELEX. & LOGIC DESIGN				32	15	47								
210243	DIGITAL ELEX. & LOGIC DESIGN		_	_		10	20								
210244	OPERATING SYSTEM & ADMIN		100 4		33	19	52								
210244			25 1				20								
210244	OPERATING SYSTEM & ADMIN		50 2		7	1 -	29								
210245 210245	MICROPROCESSOR ARCHITECTURE		100 ⁴ 25 1		27	15	42 20								
210245			50 2				8								
210243			25 1				20								
210240	FIRST SEM TOTAL =/750	ı vv	23 -	LO			20	r							
S14005	4479 TUSHITA SINGHEL					SANGE	EΤΑ		, 7	71527675в	,PI	СТ	,S1400)54479	
	_														
SEM.:			100	40	4.3	2.0	00	_							
	DISCRETE STRUCTURE		100 4		42 42	38	80								
	DATA STRUCTURE & PROB SOLVING				42	25	67								
210242	DATA STRUCTURE & PROB SOLVING				40	20	40								
210243	DIGITAL ELEX. & LOGIC DESIGN				40	28	68								
210243	DIGITAL ELEX. & LOGIC DESIGN				26	22	23								
210244	OPERATING SYSTEM & ADMIN		100 ⁴ 25 1		36	22	58								
210244			50 2				2346								
210244 210245	OPERATING SYSTEM & ADMIN		100 4		34	20	46 54								
			25 1		J 4	20	22								
Z1UZ43	MICROPROCESSOR ARCHITECTURE	ı W	۷ .	LO			<i>L L</i>	r							

210245 MICROPROCESSOR ARCHITECTURE OR 50 20 41 P
210246 SOFT SKILLS TW 25 10 21 P
FIRST SEM TOTAL = 543/750

DATE	L9 S.E.(2014 PAT.)(COMPUTER) E: 25 JAN 2016 CENTRE: P						•		PAGE NO. 190
NOTE:	FIRST LINE : SEAT NO., NAME O	F THI	E CAND	IDATE,	MOTH	ER, PERMANE	NT REG. NO., PRE	VIOUS SEAT	 Γ NO., COLLEGE, SEAT NO. , P/F:PASS/FAIL, C:PREV.CARRY OVE
S14005	54480 UDBHAV JAIN				KIIU		,/152/6/6L	, PICI	,S140054480
SEM.:	1								
210241	DISCRETE STRUCTURE	PP :	100 40	34	32	66 P			
210242	DATA STRUCTURE & PROB SOLVING	PP :	100 40	27	16	43 P			
210242	DATA STRUCTURE & PROB SOLVING	PR	50 20			38 P			
210243	DIGITAL ELEX. & LOGIC DESIGN				28	57 P			
210243	DIGITAL ELEX. & LOGIC DESIGN	TW	25 10			17 P			
210244	OPERATING SYSTEM & ADMIN		100 40		17	42 P			
210244	OPERATING SYSTEM & ADMIN		25 10			20 P			
210244	OPERATING SYSTEM & ADMIN	PR	50 20			45 P			
210245	MICROPROCESSOR ARCHITECTURE		100 40		15	44 P			
			25 10			18 P			
			50 20			38 P			
210246	SOFT SKILLS FIRST SEM TOTAL = 446/750	TW	25 10			18 P			
	54481 UNHALE YOGESH RAJENDRA								,s140054481
SEM.:	1								
210241	DISCRETE STRUCTURE	PP :	100 40	20	09	F			
210242	DATA STRUCTURE & PROB SOLVING	PP :	100 40	18	09	F			
210242	DATA STRUCTURE & PROB SOLVING	PR	50 20			AA F			
210243	DIGITAL ELEX. & LOGIC DESIGN	PP :	100 40	27	80	F			
210243	DIGITAL ELEX. & LOGIC DESIGN	TW	25 10			10 P			
210244	OPERATING SYSTEM & ADMIN	PP :	100 40	26	16	42 P			
210244	OPERATING SYSTEM & ADMIN	TW	25 10			10 P			
210244	OPERATING SYSTEM & ADMIN	PR	50 20			AA F			
210245	MICROPROCESSOR ARCHITECTURE	PP :	100 40	22	11	F			
210245	MICROPROCESSOR ARCHITECTURE	TW	25 10			10 P			
210245	MICROPROCESSOR ARCHITECTURE	OR	50 20			AA F			
210246	SOFT SKILLS FIRST SEM TOTAL =/750	TW	25 10			10 P			
	54482 UTEKAR SAGAR VASANT					NDA			, \$140054482
SEM.:	:1								
	DISCRETE STRUCTURE	PP ·	100 40	35	15	50 P			
210242					AA	F			
	DATA STRUCTURE & PROB SOLVING				- •	25 P			
210243	DIGITAL ELEX. & LOGIC DESIGN				16	52 P			
210243			25 10		-	19 P			
210244	OPERATING SYSTEM & ADMIN		100 40		20	52 P			
210244	OPERATING SYSTEM & ADMIN		25 10		-	21 P			
210244			50 20			47 P			
210245	MICROPROCESSOR ARCHITECTURE		100 40		28	59 P			
	MICROPROCESSOR ARCHITECTURE		25 10		-	20 P			

210245 MICROPROCESSOR ARCHITECTURE OR 50 20 38 P
210246 SOFT SKILLS TW 25 10 20 P
FIRST SEM TOTAL = --/750

	: 25 JAN 2016 CENTRE: P	UNE	INST	ГІТИТ	E OF	COMPU	TER -	TECHNOLOG	Y, PU	NE.		PAGE N	0. 191	
	FIRST LINE : SEAT NO., NAME O													
	ES: SUB.CODE, SUB.NAME, SUB.TY													
	4483 VAIDYA PRATYUSH AJAY									71527682E				
SEM.:	1													
210241	DISCRETE STRUCTURE	PP	100	40	47	32	79	Р						
210242	DATA STRUCTURE & PROB SOLVING	PP	100	40	37	19	56	Р						
210242	DATA STRUCTURE & PROB SOLVING	PR	50	20			28	Р						
210243	DIGITAL ELEX. & LOGIC DESIGN	PP	100	40	39	28	67	Р						
210243	DIGITAL ELEX. & LOGIC DESIGN	TW	25	10			23	Р						
210244	OPERATING SYSTEM & ADMIN	PP	100	40	38	21	59	Р						
210244	OPERATING SYSTEM & ADMIN	TW	25	10			21	Р						
210244	OPERATING SYSTEM & ADMIN	PR	50	20			40	Р						
210245	MICROPROCESSOR ARCHITECTURE	PP	100	40	40	24	64	Р						
210245	MICROPROCESSOR ARCHITECTURE	TW	25	10			23	Р						
210245	MICROPROCESSOR ARCHITECTURE	OR	50	20			40	Р						
210246	SOFT SKILLS	TW	25	10			22	Р						
	FIRST SEM TOTAL = $522/750$													
s14005	4484 VARUN SRIKANTH					SHOBI	HA		,	71527686н	,PICT		,S1400544	184
SEM.:	1													
210241	DISCRETE STRUCTURE	PP	100	40	28	20	48	Р						
210242	DATA STRUCTURE & PROB SOLVING	PP	100	40	19	80		F						
210242	DATA STRUCTURE & PROB SOLVING	PR	50	20			5	F						
210243	DIGITAL ELEX. & LOGIC DESIGN	PP	100	40	27	17	44	Р						
210243	DIGITAL ELEX. & LOGIC DESIGN	TW	25	10			11	Р						
210244	OPERATING SYSTEM & ADMIN		100		32	15	47	Р						
210244	OPERATING SYSTEM & ADMIN		25				14	Р						
210244	OPERATING SYSTEM & ADMIN		50				31	Р						
			100		33	15	48	Р						
210245	MICROPROCESSOR ARCHITECTURE	TW	25	10			13	Р						
210245	MICROPROCESSOR ARCHITECTURE	OR	50	20			17	F						
210246	SOFT SKILLS	TW	25	10			12	Р						
	FIRST SEM TOTAL = $/750$													
s14005	4485 VETAL PALLAVI SOPAN					SUNI	TΑ		,	71633716к	,PICT		,S140054	185
SEM.:			400											
	DISCRETE STRUCTURE		100			31	75							
	DATA STRUCTURE & PROB SOLVING				41	25	66							
210242	DATA STRUCTURE & PROB SOLVING						32							
210243	DIGITAL ELEX. & LOGIC DESIGN				35	40	75							
210243	DIGITAL ELEX. & LOGIC DESIGN				20	27	20							
	OPERATING SYSTEM & ADMIN		100		39	21	66							
210244	OPERATING SYSTEM & ADMIN		25				22							
	OPERATING SYSTEM & ADMIN		50		4.4	2.5	40							
	MICROPROCESSOR ARCHITECTURE		100		44	25	69							
210245	MICROPROCESSOR ARCHITECTURE	ΙW	25	ΤO			20	Р						

210245 MICROPROCESSOR ARCHITECTURE OR 50 20 40 P
210246 SOFT SKILLS TW 25 10 20 P
FIRST SEM TOTAL = 545/750

	:: 25 JAN 2016 CENTRE: PI	UNE IN	STITUT	E OF	COMPU	TER TE	ECHNOLOGY	, PUNE.			PAGE NO	. 192	
	FIRST LINE : SEAT NO., NAME OF SUB.CODE, SUB.NAME, SUB.TY			-		-			-		-	•	
	4486 VINAYAK KALUNGE									pICT			
								,		•		•	
SEM.:													
	DISCRETE STRUCTURE	PP 10		43	38	81 F							
210242	DATA STRUCTURE & PROB SOLVING			25	16	41 F							
210242	DATA STRUCTURE & PROB SOLVING					34 F							
210243	DIGITAL ELEX. & LOGIC DESIGN			38	24	62 F							
210243	DIGITAL ELEX. & LOGIC DESIGN			4.0	2.2	19 F							
210244	OPERATING SYSTEM & ADMIN	PP 10		40	23	63 F							
210244	OPERATING SYSTEM & ADMIN	TW 2				20 F							
210244	OPERATING SYSTEM & ADMIN	PR 5		4.4	27	44 F							
210245	MICROPROCESSOR ARCHITECTURE	PP 10		44	27	71 F							
210245	MICROPROCESSOR ARCHITECTURE	TW 2				19 F							
		OR 5				41 F							
210246	SOFT SKILLS FIRST SEM TOTAL = 514/750	TW 2	2 10			19 F							
	FIRST SEM TOTAL - 314/730												
	4487 VIVEK BHASKAR SARODE									,PICT			
314003	THO VIVER BIASKAK SAKODE				JANG.	117		, 7 132	7 0036	,1101		,31400344	.07
SEM.:	1												
	DISCRETE STRUCTURE	PP 10	0 40	39	42	81 F)						
	DATA STRUCTURE & PROB SOLVING			39	23	62 F							
	DATA STRUCTURE & PROB SOLVING					37 F							
	DIGITAL ELEX. & LOGIC DESIGN			33	36	69 F							
210243	DIGITAL ELEX. & LOGIC DESIGN	TW 2	5 10			21 F							
	OPERATING SYSTEM & ADMIN	PP 10		41	17	58 F							
210244	OPERATING SYSTEM & ADMIN	TW 2	5 10			22 F)						
210244	OPERATING SYSTEM & ADMIN	PR 5	0 20			39 F	•						
210245	MICROPROCESSOR ARCHITECTURE	PP 10	0 40	41	24	65 F	•						
210245	MICROPROCESSOR ARCHITECTURE	TW 2	5 10			21 F							
210245	MICROPROCESSOR ARCHITECTURE	OR 5	0 20			38 F	•						
210246	SOFT SKILLS	TW 2	5 10			21 F							
	FIRST SEM TOTAL = 534/750												
S14005	4488 VYAWAHARE SAIPRASAD VIS	TANAWH	Ή		PRAT:	IBHA		,7152	7690F	,PICT		,S1400544	88
SEM.:	1												
210241	DISCRETE STRUCTURE	PP 10	0 40	30	40	70 F							
	DATA STRUCTURE & PROB SOLVING			37	18	55 F							
	DATA STRUCTURE & PROB SOLVING					38 F							
	DIGITAL ELEX. & LOGIC DESIGN			35	32	67 F							
	DIGITAL ELEX. & LOGIC DESIGN				2 -	19 F							
210244		PP 10		36	25	61 F							
		TW 2				22 F							
	OPERATING SYSTEM & ADMIN	PR 5		2.5	2.0	37 F							
		PP 10		35	30	65 F							
210245	MICROPROCESSOR ARCHITECTURE	TW 2	2 TO			19 F	,						

210245 MICROPROCESSOR ARCHITECTURE OR 50 20 24 P
210246 SOFT SKILLS TW 25 10 19 P
FIRST SEM TOTAL = 496/750

	.9	JNE INS	STITUT	E OF	СОМРИТ	TER TI	ECHNOLOG'	Y. PUNE.	ı	PAGE NO. 193
NOTE:	FIRST LINE : SEAT NO., NAME OF	F THE (CANDID	ATE,	MOTHE	ER, PI	ERMANENT	REG. NO., PREVI	OUS SEAT	NO., COLLEGE, SEAT NO.
OTHER LIN	IES: SUB.CODE, SUB.NAME, SUB.TY	PE, MAX	K.MARK	S, MI	N.PASS	S MARI	KS, OE M	ARKS, TH MARKS, C	BTAINED,	P/F:PASS/FAIL, C:PREV.CARRY OVER
S14005	4489 WADE SHREYAS GAJANAN				ANJAL	_I		,71527691D	,PICT	,s140054489
SEM.:		DD 100	. 40	27	4.6	02 1	5			
	DISCRETE STRUCTURE					83				
210242 210242	DATA STRUCTURE & PROB SOLVING DATA STRUCTURE & PROB SOLVING			38	32	70 i 32 i				
210242	DIGITAL ELEX. & LOGIC DESIGN			31	3.8	69 1				
210243	DIGITAL ELEX. & LOGIC DESIGN			31	30	16 1				
210244	OPERATING SYSTEM & ADMIN	PP 100		31	30	61				
210244	OPERATING SYSTEM & ADMIN	TW 25				19 1				
210244	OPERATING SYSTEM & ADMIN	PR 50	20			35 I				
210245	MICROPROCESSOR ARCHITECTURE	PP 100	0 40	33	30	63 1	P			
210245	MICROPROCESSOR ARCHITECTURE	TW 25	5 10			18	P			
210245	MICROPROCESSOR ARCHITECTURE	OR 50	20			37 I	P			
210246	SOFT SKILLS	TW 25	5 10			20 I	P			
	FIRST SEM TOTAL = 523/750									
S14005	4490 WADEWALE GANESH ASHOKLA	L			GEETA	4		,71527692в	,PICT	,s140054490
SEM.:										
	DISCRETE STRUCTURE	PP 100				74 1				
	DATA STRUCTURE & PROB SOLVING			31	34	65 1				
	DATA STRUCTURE & PROB SOLVING			2.5	41	25				
	DIGITAL ELEX. & LOGIC DESIGN			35	41	76 1				
210243	DIGITAL ELEX. & LOGIC DESIGN OPERATING SYSTEM & ADMIN	PP 100		34	1 0	20 i 52 i				
		TW 25		J -1	10	20 1				
	OPERATING SYSTEM & ADMIN	PR 50				40 1				
210245	MICROPROCESSOR ARCHITECTURE	PP 100		38	25	63 1				
		TW 25				16				
		OR 50				32 1				
	SOFT SKILLS					17				
	FIRST SEM TOTAL = 500/750									
S14005	4491 WADILE DHANASHREE SAMBHA	AJI			RANJI	ITA		,71633717н	,PICT	,5140054491
SEM.:	1									
210241	DISCRETE STRUCTURE	PP 100	0 40	26	17	43 ।	P			
	DATA STRUCTURE & PROB SOLVING			19	21	40 I				
	DATA STRUCTURE & PROB SOLVING					4 1				
210243	DIGITAL ELEX. & LOGIC DESIGN			23	17	40 1				
210243	DIGITAL ELEX. & LOGIC DESIGN			2.0	1-	18				
210244	OPERATING SYSTEM & ADMIN	PP 100		28	1/	45				
		TW 25				18				
	OPERATING SYSTEM & ADMIN	PR 50		22	20	26				
210245		PP 100		32	20	52 I				
210243	MICROPROCESSOR ARCHITECTURE	TW 25) TO			18	-			

210245 MICROPROCESSOR ARCHITECTURE OR 50 20 9 F
210246 SOFT SKILLS TW 25 10 20 P
FIRST SEM TOTAL = --/750

	.9	JNE I	NSTITU	TE OF	COMPUT	ΓER T	ECHNOL	OGY. PUNE.		ı	PAGE NO). 194	
								,				-	
NOTE:	FIRST LINE : SEAT NO., NAME OF	F THE	CANDI	DATE,	MOTHE	ER, F	PERMANE	NT REG. NO.	, PREVI	OUS SEAT	NO.,	COLLEGE, SEA	Γ NO.
OTHER LIN	IES: SUB.CODE, SUB.NAME, SUB.TY	PE, M	1AX.MAR	KS, MI	N.PASS	S MAR	KS, OE	MARKS, TH	MARKS, C	BTAINED,	P/F:PA	SS/FAIL, C:PRE	CARRY OVER
S14005	4492 WAGH SAMEER RAVINDRANATI	-1			SHARN	MILA		,71527	7695G	,PICT		,S140054492	
SEM.:		DD 1	00 40	42	26	70	Б						
210241	DISCRETE STRUCTURE DATA STRUCTURE & PROB SOLVING		.00 40	42 36	36 30	78 66							
-				30	30	35							
210242	DIGITAL ELEX. & LOGIC DESIGN			39	32	71							
210243	DIGITAL ELEX. & LOGIC DESIGN			33	32	19							
210244	OPERATING SYSTEM & ADMIN		.00 40	40	27	67							
210244			25 10			20	Р						
210244	OPERATING SYSTEM & ADMIN	PR	50 20			34	Р						
210245	MICROPROCESSOR ARCHITECTURE	PP 1	00 40	41	27	68	Р						
210245	MICROPROCESSOR ARCHITECTURE	TW	25 10			18	Р						
210245	MICROPROCESSOR ARCHITECTURE	OR	50 20			24	Р						
210246	SOFT SKILLS	TW	25 10			20	Р						
	FIRST SEM TOTAL = 520/750												
S14005	4493 WAGHMARE SHARVARI SAMIR				SWAT]	Ι		,71527	7697C	,PICT		,S140054493	
SEM.:		nn 1	00 40	21	2.0	60	_						
	DISCRETE STRUCTURE		.00 40			69 54							
	DATA STRUCTURE & PROB SOLVING			29	25	54							
	DATA STRUCTURE & PROB SOLVING DIGITAL ELEX. & LOGIC DESIGN			26	3./	21 60							
210243	_			20	34	19	-						
	OPERATING SYSTEM & ADMIN		.00 40	32	20	52							
			25 10	32	2.0	21							
	OPERATING SYSTEM & ADMIN		50 20			26							
	MICROPROCESSOR ARCHITECTURE		.00 40	29	23	52							
210245	MICROPROCESSOR ARCHITECTURE	TW	25 10			19	Р						
210245	MICROPROCESSOR ARCHITECTURE	OR	50 20			33	Р						
210246	SOFT SKILLS	TW	25 10			20	Р						
	FIRST SEM TOTAL = 446/750												
S14005	4494 WAGHULDE JAGRUTI RAJENDI	RA			ANJAL	_I		,71633	3718F	,PICT		,S140054494	
SEM.:													
	DISCRETE STRUCTURE		-00 40	37		74							
	DATA STRUCTURE & PROB SOLVING			41	42	83							
	DATA STRUCTURE & PROB SOLVING			2=	2.4	39							
210243	DIGITAL ELEX. & LOGIC DESIGN			37	34	71							
210243	DIGITAL ELEX. & LOGIC DESIGN			20	2.4	20							
210244	OPERATING SYSTEM & ADMIN		.00 40	38	24	62							
			25 10			21							
	OPERATING SYSTEM & ADMIN		50 20	32	36	39 68							
210245			25 10	32	20	20							
210243	MICROPROCESSOR ARCHITECTURE	ı VV	23 IU			20	r						

210245 MICROPROCESSOR ARCHITECTURE OR 50 20 44 P
210246 SOFT SKILLS TW 25 10 21 P
FIRST SEM TOTAL = 562/750

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DATE	19 S.E.(2014 PAT.)(COMPUTER) E : 25 JAN 2016 CENTRE: P					·		PAGE NO. 195
NOTE:	FIRST LINE : SEAT NO., NAME O	F THE CAN	NDIDATE	, MOT	HER, PERMANE	NT REG. NO., PREV	IOUS SEAT	T NO., COLLEGE, SEAT NO., P/F:PASS/FAIL, C:PREV.CARRY OVE
514003	54495 WAKCHAURE ANIKET AVINAS	п		VAN	IDANA	,71327700G	, PICI	,5140054495
SEM.:	:1							
210241	DISCRETE STRUCTURE	PP 100 4	40 46	47	93 P			
210242	DATA STRUCTURE & PROB SOLVING	PP 100 4	40 35	30	65 P			
	DATA STRUCTURE & PROB SOLVING				38 P			
210243				33	70 P			
210243	DIGITAL ELEX. & LOGIC DESIGN				20 P			
210244		PP 100 4		22	57 P			
		TW 25 1			20 P			
	OPERATING SYSTEM & ADMIN	PR 50 2		27	30 P			
210245		PP 100 4		27	54 P			
	MICROPROCESSOR ARCHITECTURE MICROPROCESSOR ARCHITECTURE	TW 25 1			21 P 38 P			
	SOFT SKILLS				36 P 21 P			
	FIRST SEM TOTAL = 527/750							
	54496 WALKE SANCHIT SHIVAJIRA							,s140054496
SEM.:	:1							
210241	DISCRETE STRUCTURE	PP 100 4	40 27	34	61 P			
210242	DATA STRUCTURE & PROB SOLVING	PP 100 4	40 19	28	47 P			
210242	DATA STRUCTURE & PROB SOLVING	PR 50 2	20		36 P			
210243	DIGITAL ELEX. & LOGIC DESIGN	PP 100 4	40 31	28	59 P			
210243	DIGITAL ELEX. & LOGIC DESIGN	TW 25 2	10		20 P			
210244		PP 100 4		18	49 P			
210244		TW 25 1			21 P			
210244		PR 50 2			24 P			
210245		PP 100 4		25	52 P			
210245		TW 25 1			20 P			
210245		OR 50 2			21 P			
210246	SOFT SKILLS FIRST SEM TOTAL = 430/750	TW 25 1	10		20 P			
	54497 WANARE ANIKET DNYANDEO				ЕКНА			,s140054497
SEM.:	:1							
	DISCRETE STRUCTURE	PP 100 4	40 24	34	58 P			
210242					69 P			
	DATA STRUCTURE & PROB SOLVING			-	39 P			
210243	DIGITAL ELEX. & LOGIC DESIGN	PP 100 4	40 37	26	63 P			
210243	DIGITAL ELEX. & LOGIC DESIGN	TW 25 1			15 P			
210244	OPERATING SYSTEM & ADMIN	PP 100 4	40 31	15	46 P			
210244	OPERATING SYSTEM & ADMIN	TW 25 2	10		21 P			
210244	OPERATING SYSTEM & ADMIN	PR 50 2	20		12 F			
210245	MICROPROCESSOR ARCHITECTURE	PP 100 4	40 31	08	F			
210245	MICROPROCESSOR ARCHITECTURE	TW 25 2	10		15 P			

210245 MICROPROCESSOR ARCHITECTURE OR 50 20 11 F 210246 SOFT SKILLS TW 25 10 10 P FIRST SEM TOTAL = --/750

	9 S.E.(2014 PAT.)(COMPUTER) : 25 JAN 2016 CENTRE: PI	JNE	INSTIT	UTE OF	F CO	MPUTER T	ECHNOLOGY, PU	JNE.	PAGE N	0. 196
	FIRST LINE : SEAT NO., NAME OF ES: SUB.CODE, SUB.NAME, SUB.TY				•	•		•	•	·
	es. Sub.Code, Sub.NAME, Sub.ITI	,		•				•	, ,	
	4498 WANKHADE ANOMA MOHAN								,PICT	
SEM.:	1									
210241	DISCRETE STRUCTURE	PP	100 40	35	2	2 57	Р			
210242	DATA STRUCTURE & PROB SOLVING	PP	100 40	22	1	8 40	Р			
210242	DATA STRUCTURE & PROB SOLVING	PR	50 20)		4	F			
210243	DIGITAL ELEX. & LOGIC DESIGN	PP	100 40	24	1	6 40	Р			
210243	DIGITAL ELEX. & LOGIC DESIGN	TW	25 10			17				
210244	OPERATING SYSTEM & ADMIN		100 40		1					
210244			25 10			20				
210244	OPERATING SYSTEM & ADMIN		50 20		_	12				
210245			100 40		2					
210245			25 10			16				
210245			50 20			20				
210240	SOFT SKILLS FIRST SEM TOTAL =/750	I W	25 10			17	P			
	FIRST SEM TOTAL =/130									
	4400								,PICT	
SEM.:										
	DISCRETE STRUCTURE		100 40							
	DATA STRUCTURE & PROB SOLVING				2					
					_	3				
	DIGITAL ELEX. & LOGIC DESIGN				2					
210243	DIGITAL ELEX. & LOGIC DESIGN				_	16				
210244	OPERATING SYSTEM & ADMIN		100 40		2					
210244 210244	OPERATING SYSTEM & ADMIN OPERATING SYSTEM & ADMIN		25 10 50 20			20 24				
210244	MICROPROCESSOR ARCHITECTURE		100 40		2					
			25 10		2	16				
210245			50 20			15				
	SOFT SKILLS					13				
210210	FIRST SEM TOTAL =/750		23 10			13				
S14005	4500 YADAV NIKHIL PANDHARINA	ΤН			S	ANGEETA	,	71527707D	,PICT	,S140054500
	-									
SEM.:			100 40	2.4	2	4 60	_			
	DISCRETE STRUCTURE					4 68				
	DATA STRUCTURE & PROB SOLVING				3	0 68				
210242	DATA STRUCTURE & PROB SOLVING				2	28				
210243 210243	DIGITAL ELEX. & LOGIC DESIGN DIGITAL ELEX. & LOGIC DESIGN				3	5 71 20				
210243	OPERATING SYSTEM & ADMIN		100 40		2					
210244			25 10		2	0 37 22				
210244	OPERATING SYSTEM & ADMIN		50 20			40				
210244			100 40		2					
			25 10			19				
			_3 _0			19	•			

210245 MICROPROCESSOR ARCHITECTURE OR 50 20 37 P
210246 SOFT SKILLS TW 25 10 21 P
FIRST SEM TOTAL = 513/750

DATE	L9 S.E.(2014 PAT.)(COMPUTER) E: 25 JAN 2016 CENTRE: P							•			PAGE NO. 197	
NOTE:	FIRST LINE : SEAT NO., NAME ON SES: SUB.CODE, SUB.NAME, SUB.TY	F TH	HE CAN	NDIDAT	ΓЕ,	мотні	ER, PERMA	NENT REG.	NO., PRE	VIOUS SEAT	NO., COLLEGE,	SEAT NO.
S14005	54501 YASH RATHI					LALI	ΓΑ	,	/132//U8B	,PICI	,s140054	201
SEM.:	1											
210241	DISCRETE STRUCTURE	PP	100 4	10 3	34	46	80 P					
210242	DATA STRUCTURE & PROB SOLVING	PP	100 4	10 3	37	38	75 P					
210242	DATA STRUCTURE & PROB SOLVING	PR	50 2	20			22 P					
210243					27	31	58 P					
210243	DIGITAL ELEX. & LOGIC DESIGN						20 P					
210244	OPERATING SYSTEM & ADMIN				37	20	57 P					
210244			25 1				20 P					
210244			50 2				30 P					
210245			100 4		10	22	62 P					
210245			25 1				17 P					
210245			50 2				25 P					
210246	SOFT SKILLS FIRST SEM TOTAL = 484/750	ıw	25 1	LU			18 P					
	54502 YASH THAKUR LEKHWANI										,s140054	
311003	77302 TASIN THANKSIN ELIKIMANI					DIII/(VI	.,	,	, 132, , 032	,, 10,	,3110031	302
SEM.:	:1											
210241	DISCRETE STRUCTURE	PP	100 4	10 3	37	26	63 P					
210242	DATA STRUCTURE & PROB SOLVING	PP	100 4	10 3	37	22	59 P					
210242	DATA STRUCTURE & PROB SOLVING	PR	50 2	20			22 P					
210243	DIGITAL ELEX. & LOGIC DESIGN	PP	100 4	10 3	34	21	55 P					
210243	DIGITAL ELEX. & LOGIC DESIGN	TW	25 1	LO			18 P					
210244			100 4		36	15	51 P					
210244			25 1				20 P					
210244	OPERATING SYSTEM & ADMIN		50 2		_		36 P					
210245	MICROPROCESSOR ARCHITECTURE		100 4		30	21	51 P					
210245	MICROPROCESSOR ARCHITECTURE		25 1				20 P					
210245			50 2				11 F					
210246	SOFT SKILLS FIRST SEM TOTAL =/750	IW	25 1	LO			21 P					
S14005	54503 YENDHE POOJA ANIL					AKSH	ADA	,	71527710D	,PICT	,S140054	503
SEM.:	1											
210241	DISCRETE STRUCTURE	PP	100 4	10 3	37	40	77 P					
210242	DATA STRUCTURE & PROB SOLVING	PP	100 4	10 3	30	24	54 P					
210242	DATA STRUCTURE & PROB SOLVING	PR	50 2	20			28 P					
210243	DIGITAL ELEX. & LOGIC DESIGN	PP	100 4	10 2	29	32	61 P					
210243	DIGITAL ELEX. & LOGIC DESIGN	TW	25 1	LO			18 P					
210244	OPERATING SYSTEM & ADMIN	PP	100 4	10 3	34	21	55 P					
210244	OPERATING SYSTEM & ADMIN	TW	25 1	LO			20 P					
210244	OPERATING SYSTEM & ADMIN	PR	50 2	20			30 P					
210245	MICROPROCESSOR ARCHITECTURE	PP	100 4	10 2	28	25	53 P					
210245	MICROPROCESSOR ARCHITECTURE	TW	25 1	LO			19 P					

210245 MICROPROCESSOR ARCHITECTURE OR 50 20 36 P
210246 SOFT SKILLS TW 25 10 21 P
FIRST SEM TOTAL = 472/750

DATE	E: 25 JAN 2016 CENTRE: P						-			PAGE NO		
NOTE:	FIRST LINE : SEAT NO., NAME O	F TH	IE CANDI	DATE,	мотн	IER, PERM	MANENT REG.	NO., PRE	VIOUS SEAT	NO.,	COLLEGE,	SEAT NO.
	IES: SUB.CODE, SUB.NAME, SUB.TY	,		•		·	•	ŕ	•	•		
S14005	4504 YEOLA HARSHAL ANIL				NILI	MA	,	71527712L	,PICT		,S1400545	04
SEM.:	1											
210241	DISCRETE STRUCTURE	PP	100 40	40	39	79 P						
210242	DATA STRUCTURE & PROB SOLVING	PP	100 40	27	26	53 P						
210242	DATA STRUCTURE & PROB SOLVING	PR	50 20			26 P						
210243	DIGITAL ELEX. & LOGIC DESIGN	PP	100 40	21	29	50 P						
210243	DIGITAL ELEX. & LOGIC DESIGN	TW	25 10			19 P						
210244	OPERATING SYSTEM & ADMIN	PP	100 40	40	22	62 P						
210244	OPERATING SYSTEM & ADMIN	TW	25 10			21 P						
210244	OPERATING SYSTEM & ADMIN	PR	50 20			22 P						
210245	MICROPROCESSOR ARCHITECTURE	PP	100 40	34	26	60 P						
210245	MICROPROCESSOR ARCHITECTURE	TW	25 10			19 P						
210245	MICROPROCESSOR ARCHITECTURE	OR	50 20			27 P						
210246	SOFT SKILLS	TW	25 10			21 P						
	FIRST SEM TOTAL = 459/750											
	34505 ZAFAR HUSAIN ARSHAD					IN			,PICT		,S1400545	
SEM.:			100 40	2.0	42	70 5						
			100 40			78 P						
210242				37	38	75 P						
210242				40	20	39 P						
	DIGITAL ELEX. & LOGIC DESIGN			40	38	78 P						
	DIGITAL ELEX. & LOGIC DESIGN			2.0	21	18 P						
			100 40	36	21	57 P						
	OPERATING SYSTEM & ADMIN					21 P						
210244			50 20	20	21	44 P						
210245			100 40	39	31	70 P						
			25 10			18 P						
	MICROPROCESSOR ARCHITECTURE					35 P						
210246	SOFT SKILLS FIRST SEM TOTAL = 555/750	IW	25 10			22 P						
s14005	34506 ZOPE YOGESH SUNIL				RATN	IA	,	71527715E	,PICT		,S1400545	06
SEM.:	1											
	DISCRETE STRUCTURE	PP	100 40	35	35	70 P						
210242						48 P						
210242	DATA STRUCTURE & PROB SOLVING	PR	50 20			38 P						
210243	DIGITAL ELEX. & LOGIC DESIGN	PP	100 40	31	21	52 P						
210243	DIGITAL ELEX. & LOGIC DESIGN	TW	25 10			18 P						
210244				32	18	50 P						
210244			25 10			20 P						
210244			50 20			23 P						
210245			100 40	38	19	57 P						
			25 10	- -		18 P						

210245	MICROPROCESSOR ARCHITECTURE	OR	50 20	28 P			
210246	SOFT SKILLS	TW	25 10	19 P			
	FIRST SEM TOTAL = $441/750$						
	SAVITE	RIBAI	PHULE PUNE U	NIVERSITY ,S.E.(201	.4 COURSE)EX	CAMINATION,OCT 2015	
BRANCH: 2	9 S.E.(2014 PAT.)(INFORMATION	M TEC	CHNOLOGY)				
DATE	: 25 JAN 2016 CENTRE:	PUNE	INSTITUTE OF	COMPUTER TECHNOLOG	Y, PUNE.	PAGE NO. 198	
NOTE:	FIRST LINE : SEAT NO., NAME	OF TH	HE CANDIDATE,	MOTHER, PERMANENT	REG. NO.,	PREVIOUS SEAT NO., COLLEGE,	SEAT NO.
OTHER LIN	ES: SUB.CODE, SUB.NAME, SUB.7	YPE,	MAX.MARKS, M	IIN.PASS MARKS, OE M	ARKS, TH MA	RKS, OBTAINED, P/F:PASS/FAIL, C:	PREV.CARRY OVER

	E: 25 JAN 2016 CENTRE: PUNE						•			PAGE NO.	
NOTE:	FIRST LINE : SEAT NO., NAME OF T NES: SUB.CODE, SUB.NAME, SUB.TYPE,	HE C	ANDIDA	ATE,	MOTHE	R, P	ERMANENT REG.	NO., PRE	VIOUS SEAT	NO., CO	LLEGE, SEAT NO.
	58501 AAKRUTI SHITUT						, , , , , , , , , , , , , , , , , , , ,				
SEM.:	.1										
214441		100	40	40	20	60	D				
214442				29	15	44					
214443	DIGITAL ELEX & LOGIC DESIGN PR			40	19	59					
214444	FUNDAMENTAL OF DATASTRUCTURE PR			37	27	64					
214445	PROB SOLVING & OBJ ORIENT PROGPE		_		23	54					
214446	DIGITAL ELECTRONICS LABORATORYPE			J L	23	5					
214446	DIGITAL ELECTRONICS LABORATORYOR					10					
214447	PROGRAMMING LABORATORY TW					38					
214447											
	COMMINICATION & LANGUAGE LAB. TW					37					
	FIRST SEM TOTAL =/750										
s14005	58502 AGRAWAL AVADH PRADEEP								, PICT		
311003	AGIVANAL AVADII TINADELI				INTI		,	7 132 7 0 3 0 1 1	,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,	, 5	110030302
SEM.:											
214441				44	31	75					
214442	COMPUTER ORGANIZATION PF			28	35	63					
214443				40	26	66					
214444	FUNDAMENTAL OF DATASTRUCTURE PF			36	37	73					
214445				34	25	59					
214446	DIGITAL ELECTRONICS LABORATORYPR					25					
214446	DIGITAL ELECTRONICS LABORATORYOR					30					
214447	PROGRAMMING LABORATORY TW					47					
	PROGRAMMING LABORATORY PR					43					
214448	COMMINICATION & LANGUAGE LAB. TW FIRST SEM TOTAL = 524/750	50	20			43	P				
	58503 AGRAWAL MANTHAN ANIL								pICT		
311003	AGIO MALE PARTITION AND AND AND AND AND AND AND AND AND AN				7.3117.		,	7 1327 0325	,, 101	, 3	110030303
SEM.:		100	40	1 1	27	71	В				
214441						71					
	COMPUTER ORGANIZATION PE			31	- -	58 72					
214443	DIGITAL ELEX & LOGIC DESIGN PE			42	30	72 72					
214444	FUNDAMENTAL OF DATASTRUCTURE PE			34	38	72 53					
214445	PROB SOLVING & OBJ ORIENT PROGPE			32	20	52					
214446	DIGITAL ELECTRONICS LABORATORYON					27					
214446	DIGITAL ELECTRONICS LABORATORYON					30					
214447	PROGRAMMING LABORATORY TW					35					
214447						43					
∠14448	COMMINICATION & LANGUAGE LAB. TW FIRST SEM TOTAL = 494/750	50	20			34	۲				

	E: 25 JAN 2016 CENTRE: PUN									PAGE NO. 200	
NOTE:	FIRST LINE : SEAT NO., NAME OF SES: SUB.CODE, SUB.NAME, SUB.TYPE	THE (CANDI	DATE,	МОТН	ER, PERMAN	NENT REG.	NO., PRE	/IOUS SEAT	NO., COLLEGE,	SEAT NO.
	8504 AGRAWAL UJJWAL VIJAY									,s140058	
SEM.:	1										
214441	DISCRETE STRUCTURES P	P 100	40	35	31	66 P					
214442	COMPUTER ORGANIZATION P	P 100	40	20	23	43 P					
214443	DIGITAL ELEX & LOGIC DESIGN P	P 100	40	34	25	59 P					
214444	FUNDAMENTAL OF DATASTRUCTURE P	P 100	40	24	20	44 P					
214445	PROB SOLVING & OBJ ORIENT PROGP	P 100	40	29	18	47 P					
214446	DIGITAL ELECTRONICS LABORATORYP	R 50	20			28 P					
214446	DIGITAL ELECTRONICS LABORATORYO	R 50	20			20 P					
214447	PROGRAMMING LABORATORY T	w 50	20			34 P					
214447	PROGRAMMING LABORATORY P	R 50	20			44 P					
214448	COMMINICATION & LANGUAGE LAB. T FIRST SEM TOTAL = 425/750	w 50	20			40 P					
	8505 AGRAWAL YASH RAJESH					 OSH				,5140058	
CEM .	1										
SEM.:	DISCRETE STRUCTURES P	n 10/	10	21	20	[1 D					
	COMPUTER ORGANIZATION P				20 18	51 P 40 P					
214443	DIGITAL ELEX & LOGIC DESIGN P			38	19	40 P 57 P					
214444	FUNDAMENTAL OF DATASTRUCTURE P			28	23	51 P					
	PROB SOLVING & OBJ ORIENT PROGP				23 17	47 P					
214446	DIGITAL ELECTRONICS LABORATORYP			30	Τ,	29 P					
214446	DIGITAL ELECTRONICS LABORATORYO					30 P					
214447	PROGRAMMING LABORATORY T					36 P					
214447	PROGRAMMING LABORATORY P					42 P					
	COMMINICATION & LANGUAGE LAB. T					38 P					
	FIRST SEM TOTAL = 421/750										
	8506 AGWANE NIKHIL BALAJI				USHA				,PICT		
SEM.:	1										
214441	DISCRETE STRUCTURES P	P 100	40	24	20	44 P					
214442	COMPUTER ORGANIZATION P	P 100	40	26	15	41 P					
214443	DIGITAL ELEX & LOGIC DESIGN P	P 100	40	25	22	47 P					
214444	FUNDAMENTAL OF DATASTRUCTURE P	P 100	40	18	04	F					
214445	PROB SOLVING & OBJ ORIENT PROGP	P 100	40	26	07	F					
214446	DIGITAL ELECTRONICS LABORATORYP	R 50	20			AA F					
214446	DIGITAL ELECTRONICS LABORATORYO	R 50	20			AA F					
214447	PROGRAMMING LABORATORY T	w 50	20			20 P					
214447	PROGRAMMING LABORATORY P	R 50	20			AA F					
214448	COMMINICATION & LANGUAGE LAB. T FIRST SEM TOTAL = $/750$	w 50	20			20 P					

	E: 25 JAN 2016 CENTRE: PUN									PAGE NO. 201	
HER LIN	FIRST LINE : SEAT NO., NAME OF THES: SUB.CODE, SUB.NAME, SUB.TYPE	, MAX	.MARK	S, M	IN.PAS	S MARI	KS, OE MARKS	, TH MARKS,	OBTAINED,	P/F:PASS/FAIL,	C:PREV.CARRY OVE
	8507 AMLE SAI MUKUND									,\$140058	
SEM.:	1										
214441		P 100	40	43	32	75	P				
214442	COMPUTER ORGANIZATION P			36	37	73					
214443	DIGITAL ELEX & LOGIC DESIGN P	P 100	40	36	32	68	P				
214444	FUNDAMENTAL OF DATASTRUCTURE P	P 100	40	34	29	63	P				
214445	PROB SOLVING & OBJ ORIENT PROGP	P 100	40	42	26	68	P				
214446	DIGITAL ELECTRONICS LABORATORYP	R 50	20			27	P				
214446	DIGITAL ELECTRONICS LABORATORYO	R 50	20			20	P				
214447	PROGRAMMING LABORATORY T	N 50	20			45	P				
214447	PROGRAMMING LABORATORY P	R 50	20			45	P				
	COMMINICATION & LANGUAGE LAB. TO FIRST SEM TOTAL = 524/750					40					
	8508 ANJALI JAIN									,s140058	
SEM.:	1										
214441	DISCRETE STRUCTURES P	P 100	40	32	33	65	P				
214442	COMPUTER ORGANIZATION P	P 100	40	27	21	48	P				
214443	DIGITAL ELEX & LOGIC DESIGN P	P 100	40	35	15	50	P				
214444	FUNDAMENTAL OF DATASTRUCTURE P	P 100	40	30	27	57	P				
214445	PROB SOLVING & OBJ ORIENT PROGP	P 100	40	24	21	45	P				
214446	DIGITAL ELECTRONICS LABORATORYP	R 50	20			22	P				
214446	DIGITAL ELECTRONICS LABORATORYO	R 50	20			20	P				
214447	PROGRAMMING LABORATORY T	w 50	20			37	P				
214447	PROGRAMMING LABORATORY P	R 50	20			45	P				
214448	COMMINICATION & LANGUAGE LAB. TO FIRST SEM TOTAL = 429/750	w 50	20			40	P				
 \$14005	8509 ARORA MITESH SANJAYKUMAR				СННА			,71527120c	,PICT	,s140058	
SEM.:	1										
214441		p 100	40	36	31	67	Þ				
	COMPUTER ORGANIZATION P			34	32	66					
214443				34	28	62					
214444	FUNDAMENTAL OF DATASTRUCTURE P			25	35	60					
214445	PROB SOLVING & OBJ ORIENT PROGP			35	23	58					
214446	DIGITAL ELECTRONICS LABORATORYP			,,		8					
214446	DIGITAL ELECTRONICS LABORATORYO					10					
214447	PROGRAMMING LABORATORY					44					
214447	PROGRAMMING LABORATORY P					40					
	COMMINICATION & LANGUAGE LAB. TO FIRST SEM TOTAL =/750					41					

	9 S.E.(2014 PAT.)(INFORMATIOM E: 25 JAN 2016 CENTRE: PU		-	COMPU	TER TECHNOL	OGY, PUNE.	Р	AGE NO. 202
NOTE:	FIRST LINE : SEAT NO., NAME OF	THE CAN	NDIDATE,	мотні	ER, PERMANE	ENT REG. NO., PREV	OUS SEAT	NO., COLLEGE, SEAT NO. P/F:PASS/FAIL, C:PREV.CARRY OVE
	8510 ARUN SHUKLA					,71527122К		,s140058510
SEM.:	1							
214441	DISCRETE STRUCTURES	PP 100 4	10 43	34	77 P			
214442	COMPUTER ORGANIZATION	PP 100 4	10 27	33	60 P			
214443	DIGITAL ELEX & LOGIC DESIGN	PP 100 4	10 41	39	80 P			
214444	FUNDAMENTAL OF DATASTRUCTURE	PP 100 4	10 39	29	68 P			
214445	PROB SOLVING & OBJ ORIENT PROG	PP 100 4	10 29	32	61 P			
214446	DIGITAL ELECTRONICS LABORATORY	'PR 50 2	20		10 F			
214446	DIGITAL ELECTRONICS LABORATORY	OR 50 2	20		5 F			
214447	PROGRAMMING LABORATORY	TW 50 2	20		42 P			
214447	PROGRAMMING LABORATORY	PR 50 2	20		40 P			
214448	COMMINICATION & LANGUAGE LAB. FIRST SEM TOTAL = $/750$	TW 50 2	20		45 P			
	8511 BADGUJAR GAYATRI SANTOSH				HAN			,S140058511
SEM.:	1							
214441		DD 100 /	10 25	15	40 P			
214442				23	40 P			
214443	DIGITAL ELEX & LOGIC DESIGN			15	48 P			
214444	FUNDAMENTAL OF DATASTRUCTURE			20	40 P			
				19	50 P			
214446	DIGITAL ELECTRONICS LABORATORY			13	10 F			
214446	DIGITAL ELECTRONICS LABORATORY		-		6 F			
214447		TW 50 2			42 P			
214447	PROGRAMMING LABORATORY		-		42 P 35 P			
	COMMINICATION & LANGUAGE LAB.				33 P 41 P			
	FIRST SEM TOTAL =/750							
	8512 BADGUJAR NILAM VIJAY					,71527133E		
SEM.:	1							
214441	DISCRETE STRUCTURES	PP 100 4	10 34	32	66 P			
214442	COMPUTER ORGANIZATION	PP 100 4	10 34	34	68 P			
214443	DIGITAL ELEX & LOGIC DESIGN	PP 100 4	10 39	38	77 P			
214444	FUNDAMENTAL OF DATASTRUCTURE	PP 100 4	10 29	37	66 P			
214445	PROB SOLVING & OBJ ORIENT PROG	iPP 100 4	10 39	26	65 P			
214446	DIGITAL ELECTRONICS LABORATORY	'PR 50 2	20		37 P			
214446	DIGITAL ELECTRONICS LABORATORY	OR 50 2	20		38 P			
214447	PROGRAMMING LABORATORY	TW 50 2	20		47 P			
214447	PROGRAMMING LABORATORY	PR 50 2	20		42 P			
214448	COMMINICATION & LANGUAGE LAB. FIRST SEM TOTAL = 554/750	TW 50 2	20		48 P			

	29 S.E.(2014 PAT.)(INFORMATIOM TE: 25 JAN 2016 CENTRE: PUN		-	COMP	UTER TECHNOL	OGY, PUNE.	P.A	AGE NO. 203
NOTE:	FIRST LINE : SEAT NO., NAME OF	THE CA	NDIDATE,	МОТ	HER, PERMANE	NT REG. NO., PREV	OUS SEAT N	NO., COLLEGE, SEAT NO.
	88513 BAKARE ABHISHEK JAYAPRAKA					,71527139D		
SEM.:	1							
214441	DISCRETE STRUCTURES	PP 100	40 35	22	57 P			
214442	COMPUTER ORGANIZATION	PP 100	40 31	37	68 P			
214443	DIGITAL ELEX & LOGIC DESIGN	PP 100	40 41	27	68 P			
214444	FUNDAMENTAL OF DATASTRUCTURE I	PP 100	40 32	29	61 P			
214445	PROB SOLVING & OBJ ORIENT PROGR	PP 100	40 33	24	57 P			
214446	DIGITAL ELECTRONICS LABORATORY	PR 50	20		34 P			
214446	DIGITAL ELECTRONICS LABORATORY	OR 50	20		36 P			
214447	PROGRAMMING LABORATORY	гw 50	20		43 P			
214447	PROGRAMMING LABORATORY	PR 50	20		44 P			
214448	COMMINICATION & LANGUAGE LAB. T FIRST SEM TOTAL = 507/750	ΓW 50	20		39 P			
	8514 BALLULLAYA SOURABH SHREED							
SEM.:	1							
214441		DP 100	40 45	29	74 P			
	COMPUTER ORGANIZATION				67 P			
214443	DIGITAL ELEX & LOGIC DESIGN F				73 P			
214444	FUNDAMENTAL OF DATASTRUCTURE F			28	73 P			
	PROB SOLVING & OBJ ORIENT PROG			29	65 P			
214446	DIGITAL ELECTRONICS LABORATORY			23	35 P			
214446	DIGITAL ELECTRONICS LABORATORY				29 P			
214447	PROGRAMMING LABORATORY				47 P			
214447			-		47 P 45 P			
	PROGRAMMING LABORATORY COMMINICATION & LANGUAGE LAB. 7				43 P 48 P			
214440	FIRST SEM TOTAL = 550/750							
s14005	58515 BAYASKAR TEJAL GUNWANT					,71527147E		
SEM.:	1							
214441	DISCRETE STRUCTURES	PP 100	40 32	22	54 P			
214442	COMPUTER ORGANIZATION	PP 100	40 25	35	60 P			
214443	DIGITAL ELEX & LOGIC DESIGN	PP 100	40 33	24	57 P			
214444	FUNDAMENTAL OF DATASTRUCTURE I	PP 100	40 30	24	54 P			
214445	PROB SOLVING & OBJ ORIENT PROGR			27	66 P			
214446	DIGITAL ELECTRONICS LABORATORY				20 P			
214446	DIGITAL ELECTRONICS LABORATORY				20 P			
214447	PROGRAMMING LABORATORY				38 P			
214447	PROGRAMMING LABORATORY				37 P			
	COMMINICATION & LANGUAGE LAB. T				38 P			

	9 S.E.(2014 PAT.)(INFORMATIOM TE : 25 JAN 2016 CENTRE: PUNE		-	F COMP	UTER TECHNOL	LOGY, PUNE.	PAC	GE NO. 204
NOTE:	FIRST LINE : SEAT NO., NAME OF T	HE CAN	NDIDATE	, мот	HER, PERMANE	ENT REG. NO., PREV	IOUS SEAT NO	O., COLLEGE, SEAT NO.
OTHER LIN	IES: SUB.CODE, SUB.NAME, SUB.TYPE,	MAX.N	MARKS,	MIN.PA	SS MARKS, OF	E MARKS, TH MARKS,	OBTAINED, P	F:PASS/FAIL, C:PREV.CARRY OVER
	8516 BHANGALE CHAITANYA SANJAY							
SEM.:	1							
	DISCRETE STRUCTURES PP	100 /	10 42	35	77 P			
	COMPUTER ORGANIZATION PP				69 P			
214443	DIGITAL ELEX & LOGIC DESIGN PP			_	82 P			
214444	FUNDAMENTAL OF DATASTRUCTURE PP				61 P			
214445	PROB SOLVING & OBJ ORIENT PROGPP				65 P			
214446	DIGITAL ELECTRONICS LABORATORYPR			21	20 P			
214446	DIGITAL ELECTRONICS LABORATORYOR				20 P			
214447	PROGRAMMING LABORATORY TW				43 P			
214447	PROGRAMMING LABORATORY PR				38 P			
	COMMINICATION & LANGUAGE LAB. TW				43 P			
	FIRST SEM TOTAL = 520/750							
	8517 BHANGALE UJJWAL RAVINDRA				ТІВНА			,S140058517
SEM.:	1							
	DISCRETE STRUCTURES PP	100	10 42	35	77 P			
	COMPUTER ORGANIZATION PP				77 P 56 P			
214443	DIGITAL ELEX & LOGIC DESIGN PP				74 P			
214444	FUNDAMENTAL OF DATASTRUCTURE PP				74 P 68 P			
	PROB SOLVING & OBJ ORIENT PROGPP			17	56 P			
214446	DIGITAL ELECTRONICS LABORATORYPR			17	30 P			
214446	DIGITAL ELECTRONICS LABORATORYPR				30 P			
214447	PROGRAMMING LABORATORY TW				36 P			
214447	PROGRAMMING LABORATORY PR				30 P			
	COMMINICATION & LANGUAGE LAB. TW		-		37 P 42 P			
214446	FIRST SEM TOTAL = 506/750	30 2	20		42 P			
S14005	8518 BHOR SURAJ VISHWAS			ANI	TA	,71527159〕	,PICT	,S140058518
SEM.:	1							
214441		100 4	40 42	28	70 P			
		100 4			76 P			
214443	DIGITAL ELEX & LOGIC DESIGN PP				75 P			
214444	FUNDAMENTAL OF DATASTRUCTURE PP			29	62 P			
214445	PROB SOLVING & OBJ ORIENT PROGPP			30	68 P			
214446	DIGITAL ELECTRONICS LABORATORYPR			30	36 P			
214446	DIGITAL ELECTRONICS LABORATORYPR				40 P			
214447	PROGRAMMING LABORATORY TW				40 P 42 P			
214447	PROGRAMMING LABORATORY PR				42 P 43 P			
214448	COMMINICATION & LANGUAGE LAB. TW				45 P			
217440	FIRST SEM TOTAL = 547/750	JU 2	_0		TJ F			

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HER LIN	FIRST LINE : SEAT NO., NAME OF T IES: SUB.CODE, SUB.NAME, SUB.TYPE,	MAX.	MARKS,	MIN	N.PASS	MARKS	, OE MARKS	, TH MARKS,	OBTAINED,	P/F:PASS/FAIL,	C:PREV.CARRY OV
	8519 BHUTE ABHIJEET SURESH									,5140058	
SEM.:	1										
214441		100	40 4	-5	22	67 P					
214442	COMPUTER ORGANIZATION PF			25	19	44 P					
214443	DIGITAL ELEX & LOGIC DESIGN PF	100	40 4	1	22	63 P					
214444	FUNDAMENTAL OF DATASTRUCTURE PP	100	40 3	8	28	66 P					
214445	PROB SOLVING & OBJ ORIENT PROGPE	100	40 4	1	21	62 P					
214446	DIGITAL ELECTRONICS LABORATORYPR					30 P					
214446	DIGITAL ELECTRONICS LABORATORYOR					25 P					
214447	PROGRAMMING LABORATORY TW	50	20			44 P					
214447	PROGRAMMING LABORATORY PR					38 P					
	COMMINICATION & LANGUAGE LAB. TW FIRST SEM TOTAL = 478/750	50	20			39 P					
	8520 CHAUDHARI NAMRATA ANIL									,s140058	
SEM.:	1										
214441	DISCRETE STRUCTURES PF	100	40 4	2	22	64 P					
214442	COMPUTER ORGANIZATION PF	100	40 3	6	32	68 P					
214443	DIGITAL ELEX & LOGIC DESIGN PF	100	40 3	8	22	60 P					
214444	FUNDAMENTAL OF DATASTRUCTURE PP	100	40 2	27	28	55 P					
214445	PROB SOLVING & OBJ ORIENT PROGPE	100	40 4	1	21	62 P					
214446	DIGITAL ELECTRONICS LABORATORYPR	50	20			35 P					
214446	DIGITAL ELECTRONICS LABORATORYOR	50	20			30 P					
214447	PROGRAMMING LABORATORY TW	50	20			38 P					
214447	PROGRAMMING LABORATORY PR	50	20			44 P					
214448	COMMINICATION & LANGUAGE LAB. TW FIRST SEM TOTAL = 504/750	50	20			48 P					
 \$14005	8521 CHOUGULE MADHURA MAHAVIR				MANIS			 ,71527187D	pict	,s140058	 3521
c = \cdots	1										
SEM.: 214441		100	40 4	0	24	64 P					
	COMPUTER ORGANIZATION PF			-	25	40 P					
214442	DIGITAL ELEX & LOGIC DESIGN PF			_	30	40 P					
214444	FUNDAMENTAL OF DATASTRUCTURE PF			· ·	21	45 P					
214444	PROB SOLVING & OBJ ORIENT PROGPE				15	55 P					
214445	DIGITAL ELECTRONICS LABORATORYPR			·U	T)	33 P					
						20 P					
214446	DIGITAL ELECTRONICS LABORATORYOR										
214447	PROGRAMMING LABORATORY TW					36 P					
214447	PROGRAMMING LABORATORY PR					35 P					
Z14448	COMMINICATION & LANGUAGE LAB. TW FIRST SEM TOTAL = 419/750	50	20			35 P					

	9 S.E.(2014 PAT.)(INFORMATIOM TE :: 25 JAN 2016 CENTRE: PUNE		-	E OF	COMPU	TER TECHN	OLOGY, Pl	JNE.		PAGE NO. 206	
NOTE:	FIRST LINE : SEAT NO., NAME OF T SES: SUB.CODE, SUB.NAME, SUB.TYPE,	HE CA	ANDID	ATE,	мотн	ER, PERMA	NENT REG	. NO., PRE	VIOUS SEAT	NO., COLLEGE,	SEAT NO.
	8522 CHOUGULE RONIT RAJKUMAR									,\$140058	
SEM.:	1										
214441		100	40	32	22	54 P					
	COMPUTER ORGANIZATION PP			22	18	40 P					
214443	DIGITAL ELEX & LOGIC DESIGN PP			33	24	57 P					
214444	FUNDAMENTAL OF DATASTRUCTURE PP			29	25	54 P					
214445	PROB SOLVING & OBJ ORIENT PROGPP			32	21	53 P					
214446	DIGITAL ELECTRONICS LABORATORYPR			32	21	20 P					
214446	DIGITAL ELECTRONICS LABORATORYOR					26 P					
214447	PROGRAMMING LABORATORY TW					38 P					
214447	PROGRAMMING LABORATORY PR					36 P					
	COMMINICATION & LANGUAGE LAB. TW					42 P					
211110	FIRST SEM TOTAL = $420/750$	30	20			12 1					
	8523 DARADE KRUSHNA KESHAV					 TA				,s140058	
							•		,	ŕ	
SEM.:	1										
214441	DISCRETE STRUCTURES PP	100	40	44	30	74 P					
214442	COMPUTER ORGANIZATION PP	100	40	35	33	68 P					
214443	DIGITAL ELEX & LOGIC DESIGN PP	100	40	41	33	74 P					
214444	FUNDAMENTAL OF DATASTRUCTURE PP	100	40	36	32	68 P					
214445	PROB SOLVING & OBJ ORIENT PROGPP	100	40	32	17	49 P					
214446	DIGITAL ELECTRONICS LABORATORYPR	50	20			30 P					
214446	DIGITAL ELECTRONICS LABORATORYOR	50	20			30 P					
214447		50				39 P					
214447						40 P					
214448	COMMINICATION & LANGUAGE LAB. TW FIRST SEM TOTAL = 517/750	50	20			45 P					
s14005	8524 DEVAN SHREYASH RAJENDRA				SNEH			 ,71527208L	pICT	,\$140058	
SEM.:	1										
3EM 214441		100	40	42	22	64 P					
	COMPUTER ORGANIZATION PP			30	33	63 P					
214443	DIGITAL ELEX & LOGIC DESIGN PP			40	32	72 P					
214444	FUNDAMENTAL OF DATASTRUCTURE PP			28	23	72 F 51 P					
214445	PROB SOLVING & OBJ ORIENT PROGPP			32	22	54 P					
214446	DIGITAL ELECTRONICS LABORATORYPR			J.L		35 P					
214446	DIGITAL ELECTRONICS LABORATORYOR					35 P					
214447		50				43 P					
214447	PROGRAMMING LABORATORY PR					40 P					
	COMMINICATION & LANGUAGE LAB. TW					40 P					
2177 7 0	FIRST SEM TOTAL = 498/750	30	20			14 1					

	29 S.E.(2014 PAT.)(INFORMATIOM T E: 25 JAN 2016 CENTRE: PUN		-	OF	COMPUTER	TECHNOLOGY	, PUNE.	PAG	GE NO. 207
NOTE:	FIRST LINE : SEAT NO., NAME OF NES: SUB.CODE, SUB.NAME, SUB.TYPE	THE C	ANDIDA	ΤE,	MOTHER,	PERMANENT I	REG. NO., PREVI	OUS SEAT NO	O., COLLEGE, SEAT NO.
	58525 DEVENDRA LALCHAND OSWAL								,s140058525
SEM.:	:1								
214441	DISCRETE STRUCTURES P	P 100	40	40	21 6	1 P			
214442	COMPUTER ORGANIZATION P	P 100	40 2	21	28 4	9 P			
214443	DIGITAL ELEX & LOGIC DESIGN P	P 100	40	43	27 7	0 P			
214444	FUNDAMENTAL OF DATASTRUCTURE P	P 100	40	21	28 4	9 P			
214445	PROB SOLVING & OBJ ORIENT PROGP	P 100	40	29	25 5	4 P			
214446	DIGITAL ELECTRONICS LABORATORYP	R 50	20		2	0 P			
214446	DIGITAL ELECTRONICS LABORATORYO	R 50	20		2	4 P			
214447	PROGRAMMING LABORATORY T	w 50	20		3	6 P			
214447	PROGRAMMING LABORATORY P	R 50	20		4	1 P			
214448	COMMINICATION & LANGUAGE LAB. T FIRST SEM TOTAL = 440/750	w 50	20		3	6 P			
	58526 DHOLE ASHWIN RAJKUMAR					I			,s140058526
CEM •	.1								
SEM.:		n 100	40	1 7	00 -	F			
214441						- F			
214442				_		- F			
214443	DIGITAL ELEX & LOGIC DESIGN P					- F			
214444	FUNDAMENTAL OF DATASTRUCTURE P PROB SOLVING & OBJ ORIENT PROGP			22 18		2 P			
				10		7 P			
214446	DIGITAL ELECTRONICS LABORATORYP		-			F			
214446	DIGITAL ELECTRONICS LABORATORYO				•	F			
214447			-			0 P			
214447						A F			
214448	COMMINICATION & LANGUAGE LAB. T FIRST SEM TOTAL =/750					3 P			
s14005	58527 DHONDAGE JAGRUTI ASHOK								,s140058527
SEM.:	:1								
	DISCRETE STRUCTURES P	P 100	40	38	23 6	1 P			
214442				24		9 P			
214443	DIGITAL ELEX & LOGIC DESIGN P			34		8 P			
214444						3 P			
214445	PROB SOLVING & OBJ ORIENT PROGP			37		2 P			
214446	DIGITAL ELECTRONICS LABORATORYP					2 P			
214446	DIGITAL ELECTRONICS LABORATORYO					5 P			
214447	PROGRAMMING LABORATORY T					0 P			
214447						3 P			
	COMMINICATION & LANGUAGE LAB. T FIRST SEM TOTAL = 481/750					8 P			
	·								

	9 S.E.(2014 PAT.)(INFORMATIOM TE E: 25 JAN 2016 CENTRE: PUNE		-		COMPUT	TER T	ECHNOLOGY, PU	INE.	Р	PAGE NO. 208	
NOTE:	FIRST LINE : SEAT NO., NAME OF TIES: SUB.CODE, SUB.NAME, SUB.TYPE,	HE CA	ANDID	ATE,	МОТНЕ	ER, P	PERMANENT REG.	NO., PREV	IOUS SEAT	NO., COLLEGE,	SEAT NO.
S14005	8528 DHONDGE RUCHA CHANDRAKANT				VAIS	HALI	,	71527214E	,PICT	,S1400585	28
CEM •	1										
SEM.: 214441		100	40	42	16	88	D				
	COMPUTER ORGANIZATION PP			40	33	73					
214443				48	25	73					
214444	FUNDAMENTAL OF DATASTRUCTURE PP			45	39	84					
214445	PROB SOLVING & OBJ ORIENT PROGPP		-		34	72					
214446	DIGITAL ELECTRONICS LABORATORYPR			30	31	26					
214446	DIGITAL ELECTRONICS LABORATORYOR					30					
214447	PROGRAMMING LABORATORY TW										
214447	PROGRAMMING LABORATORY PR					45					
	COMMINICATION & LANGUAGE LAB. TW					48					
	FIRST SEM TOTAL = 586/750										
	8529 DIWAN AISHWARYA MADAN									,\$1400585	
SEM.:		100	40	4.0	2.2	4.0	_				
	DISCRETE STRUCTURES PP					40					
	COMPUTER ORGANIZATION PP				28	52					
214443	DIGITAL ELEX & LOGIC DESIGN PP			22	30	52					
214444	FUNDAMENTAL OF DATASTRUCTURE PP			30	22	52					
	PROB SOLVING & OBJ ORIENT PROGPP DIGITAL ELECTRONICS LABORATORYPR			26	29	55 25					
214446 214446						20					
214447	DIGITAL ELECTRONICS LABORATORYOR PROGRAMMING LABORATORY TW					27					
214447	PROGRAMMING LABORATORY TW PROGRAMMING LABORATORY PR					12					
	COMMINICATION & LANGUAGE LAB. TW					31					
214440	FIRST SEM TOTAL = $/750$	30	20			21	r				
s14005	8530 DOKE PIYUSH BALSHIRAM				ASHA		,	71527220к	,PICT	,S1400585	30
SEM.:	1										
214441		100	40	38	31	69	Р				
214442		100		30	25	55					
214443		100		41	35	76					
214444	FUNDAMENTAL OF DATASTRUCTURE PP			30	30	60					
214445	PROB SOLVING & OBJ ORIENT PROGPP			29	20	49					
214446	DIGITAL ELECTRONICS LABORATORYPR				-	30					
214446	DIGITAL ELECTRONICS LABORATORYOR					30					
214447		50				38					
214447		50				42					
214448						40					
	FIRST SEM TOTAL = 489/750										

	29 S.E.(2014 PAT.)(INFORMATIOM T E: 25 JAN 2016 CENTRE: PUN		-		COMPU	JTER T	ECHNOLOGY,	PUNE.	P.	AGE NO. 209
NOTE:	FIRST LINE: SEAT NO., NAME OF	THE C	CANDI	DATE,	мотн	IER, P	PERMANENT R	EG. NO., PREV	OUS SEAT N	NO., COLLEGE, SEAT NO.
	58531 GADEKAR RAHUL KONDIBHAU									,S140058531
SEM.:	:1									
214441	DISCRETE STRUCTURES F	P 100	40	19	06		F			
214442	COMPUTER ORGANIZATION F	P 100	40	27	22	49	Р			
214443	DIGITAL ELEX & LOGIC DESIGN F	P 100	40	20	20	40	Р			
214444	FUNDAMENTAL OF DATASTRUCTURE F	P 100	40	19	21	40	P			
214445	PROB SOLVING & OBJ ORIENT PROGR	P 100	40	18	00		F			
214446	DIGITAL ELECTRONICS LABORATORYF	PR 50	20			9	F			
214446	DIGITAL ELECTRONICS LABORATORYO)R 50	20			7	F			
214447	PROGRAMMING LABORATORY T	w 50	20			36	Р			
214447	PROGRAMMING LABORATORY F	PR 50	20			28	Р			
214448	COMMINICATION & LANGUAGE LAB. T FIRST SEM TOTAL =/750	W 50	20			35	Р			
	 58532 GAWALI JAGNNATH RAMNATH									,s140058532
311003	JOSSE GAMALE SAGMANTA TOWNSTANT				5/11(1			,, 132, 233	, , , , , , , , , , , , , , , , , , , ,	,5110030332
SEM.:	:1									
214441	DISCRETE STRUCTURES F	P 100	40	41	30	71	P			
214442	COMPUTER ORGANIZATION F	P 100	40	30	22	52	P			
214443	DIGITAL ELEX & LOGIC DESIGN F	P 100	40	40	16	56	P			
214444	FUNDAMENTAL OF DATASTRUCTURE F	P 100	40	38	25	63	P			
214445	PROB SOLVING & OBJ ORIENT PROGF	P 100	40	34	25	59	P			
214446	DIGITAL ELECTRONICS LABORATORYF	PR 50	20			27	P			
214446	DIGITAL ELECTRONICS LABORATORYO)R 50	20			25	P			
214447	PROGRAMMING LABORATORY T	w 50	20			45	Р			
214447	PROGRAMMING LABORATORY F	PR 50	20			42	Р			
214448	COMMINICATION & LANGUAGE LAB. T FIRST SEM TOTAL = 479/750	™ 50	20			39	Р			
	58533 GAWALI MANASI SURESH									,S140058533
SEM.:	:1									
214441	DISCRETE STRUCTURES F	P 100	40	36	21	57	P			
214442	COMPUTER ORGANIZATION F	P 100	40	30	29	59	P			
214443	DIGITAL ELEX & LOGIC DESIGN F	P 100	40	44	17	61	P			
214444	FUNDAMENTAL OF DATASTRUCTURE F	P 100	40	38	34	72	P			
214445	PROB SOLVING & OBJ ORIENT PROGR	P 100	40	24	35	59	P			
214446	DIGITAL ELECTRONICS LABORATORYF	PR 50	20			30	P			
214446	DIGITAL ELECTRONICS LABORATORYO)R 50	20			30	P			
214447	PROGRAMMING LABORATORY T	w 50	20			47	Р			
214447	PROGRAMMING LABORATORY F	PR 50	20			41	Р			
214448	COMMINICATION & LANGUAGE LAB. T FIRST SEM TOTAL = 499/750	™ 50	20			43	Р			

	9 S.E.(2014 PAT.)(INFORMATIOM T E: 25 JAN 2016 CENTRE: PUN		-	COMPUT	ER TECHNOL	OGY, PUNE.	P/	AGE NO. 210
NOTE:	FIRST LINE : SEAT NO., NAME OF SES: SUB.CODE, SUB.NAME, SUB.TYPE	THE CAN	DIDATE,	MOTHE	R, PERMANE	ENT REG. NO., PREV	OUS SEAT N	NO., COLLEGE, SEAT NO.
	88534 GHEWARI ROMA SANJAY					,71527240D		
SEM.:	1							
214441	DISCRETE STRUCTURES P	P 100 4	0 45	40	85 P			
214442	COMPUTER ORGANIZATION P	P 100 4	0 40	34	74 P			
214443	DIGITAL ELEX & LOGIC DESIGN P	P 100 4	0 45	38	83 P			
214444	FUNDAMENTAL OF DATASTRUCTURE P	P 100 4	0 41	28	69 P			
214445	PROB SOLVING & OBJ ORIENT PROGP	P 100 4	0 34	26	60 P			
214446	DIGITAL ELECTRONICS LABORATORYP	R 50 2	0		35 P			
214446	DIGITAL ELECTRONICS LABORATORYO	R 50 2	0		42 P			
214447	PROGRAMMING LABORATORY T	N 50 2	0		47 P			
214447	PROGRAMMING LABORATORY P	R 50 2	0		46 P			
214448	COMMINICATION & LANGUAGE LAB. T FIRST SEM TOTAL = 586/750	w 50 20	0		45 P			
	8535 GHORPADE NIDHI SHRIRAM				 KA			,s140058535
SEM.:								
214441	DISCRETE STRUCTURES P	P 100 4	0 21	11	F			
214442	COMPUTER ORGANIZATION P	P 100 4	0 17	35	52 P			
214443	DIGITAL ELEX & LOGIC DESIGN P	P 100 4	0 28	15	43 P			
214444	FUNDAMENTAL OF DATASTRUCTURE P	P 100 4	0 17	26	43 P			
214445	PROB SOLVING & OBJ ORIENT PROGP	P 100 4	0 30	20	50 P			
214446	DIGITAL ELECTRONICS LABORATORYP	R 50 2	0		30 P			
214446	DIGITAL ELECTRONICS LABORATORYO	R 50 2	0		24 P			
214447	PROGRAMMING LABORATORY T	w 50 2	0		38 P			
214447	PROGRAMMING LABORATORY P	R 50 2	0		33 P			
214448	COMMINICATION & LANGUAGE LAB. T FIRST SEM TOTAL = $/750$	w 50 20	0		38 P			
s14005	8536 JAIDEEP SINGH NARINDER AH					,71527267F		,s140058536
SEM.:	1							
	DISCRETE STRUCTURES P	P 100 4	0 33	31	64 P			
214442				31	65 P			
214443	DIGITAL ELEX & LOGIC DESIGN P	P 100 4	0 46	32	78 P			
214444	FUNDAMENTAL OF DATASTRUCTURE P			26	62 P			
214445	PROB SOLVING & OBJ ORIENT PROGP			29	65 P			
214446	DIGITAL ELECTRONICS LABORATORYP				40 P			
214446	DIGITAL ELECTRONICS LABORATORYO				38 P			
214447	PROGRAMMING LABORATORY T				47 P			
214447	PROGRAMMING LABORATORY P				44 P			
	COMMINICATION & LANGUAGE LAB. T FIRST SEM TOTAL = 551/750				48 P			

	9 S.E.(2014 PAT.)(INFORMATIOM TE :: 25 JAN 2016 CENTRE: PUNE		-	OF	COMPU	TER T	ECHNOLOGY, P	UNE.	PAGE	NO. 211	
NOTE:	FIRST LINE : SEAT NO., NAME OF T	HE CA	NDIDA	ΛΤE,	мотн	ER, P	PERMANENT REG	. NO., PREV	IOUS SEAT NO.,	COLLEGE,	SEAT NO.
OTHER LIN	IES: SUB.CODE, SUB.NAME, SUB.TYPE,	MAX.	MARKS	, MI	N.PAS	S MAR	KS, OE MARKS	, TH MARKS,	OBTAINED, P/F:	PASS/FAIL, C	::PREV.CARRY OVER
	8537 JUNAGADE ATHARVA SAMEER										
SEM.:	1										
	DISCRETE STRUCTURES PP	100	40	35	33	68	D				
	COMPUTER ORGANIZATION PP				26	47					
214443	DIGITAL ELEX & LOGIC DESIGN PP				23	61					
214444	FUNDAMENTAL OF DATASTRUCTURE PP			31	25	56					
214445	PROB SOLVING & OBJ ORIENT PROGPP		_	_	19	44					
214446	DIGITAL ELECTRONICS LABORATORYPR					27					
214446	DIGITAL ELECTRONICS LABORATORYOR					20					
214447	PROGRAMMING LABORATORY TW					38					
214447	PROGRAMMING LABORATORY PR										
	COMMINICATION & LANGUAGE LAB. TW					43					
	FIRST SEM TOTAL = 440/750										
	8538 KADAM JEEVAN NATHURAM								,PICT		
SEM.:	1										
214441	DISCRETE STRUCTURES PP	100	40	22	04		F				
214442	COMPUTER ORGANIZATION PP	100	40	16	24	40	Р				
214443	DIGITAL ELEX & LOGIC DESIGN PP	100	40	19	21	40	Р				
214444	FUNDAMENTAL OF DATASTRUCTURE PP	100	40	16	24	40	Р				
214445	PROB SOLVING & OBJ ORIENT PROGPP	100	40	19	12		F				
214446	DIGITAL ELECTRONICS LABORATORYPR	50	20			26	Р				
214446	DIGITAL ELECTRONICS LABORATORYOR	50	20			20	Р				
214447	PROGRAMMING LABORATORY TW	50	20			37	Р				
214447	PROGRAMMING LABORATORY PR	50	20			5	F				
214448	COMMINICATION & LANGUAGE LAB. TW	50	20			41	Р				
	FIRST SEM TOTAL =/750										
	OF20 MALDA CAUTI DATECH										
S14003	8539 KALRA SAHIL RAJESH				SAPN	А		,/1033/23J	,PICT	,514005655	9
SEM.:	1										
214441		100	40	33	29	62	Р				
	COMPUTER ORGANIZATION PP			33	33	66					
214443				39	29	68					
214444	FUNDAMENTAL OF DATASTRUCTURE PP			36	29	65					
214445	PROB SOLVING & OBJ ORIENT PROGPP			31	29	60					
214446	DIGITAL ELECTRONICS LABORATORYPR			J T	23	30					
214446	DIGITAL ELECTRONICS LABORATORYOR					30					
214447	PROGRAMMING LABORATORY TW					42					
214447	PROGRAMMING LABORATORY PR					45					
214448	COMMINICATION & LANGUAGE LAB. TW					43					
217440	FIRST SEM TOTAL = 511/750	50	20			73	•				

NOTE: F	IRST LINE : SEAT NO., NAME OF T							
	S. Sobicobe, Sobilitaine, Sobilitie,			-	THER, PERMANE	NT REG. NO., PREVI	OUS SEAT N	
S140058	540 KAMBLE HARSHAD RAJU					,71527285D		
SEM.:1								
214441	DISCRETE STRUCTURES PF	100	40 37	15	52 P			
214442	COMPUTER ORGANIZATION PF	100	40 23	20	43 P			
214443	DIGITAL ELEX & LOGIC DESIGN PF	100	40 34	15	49 P			
214444	FUNDAMENTAL OF DATASTRUCTURE PF	100	40 31	17	48 P			
214445	PROB SOLVING & OBJ ORIENT PROGPF	100	40 29	05	F			
214446	DIGITAL ELECTRONICS LABORATORYPE	50	20		AA F			
214446	DIGITAL ELECTRONICS LABORATORYOF	50	20		AA F			
214447	PROGRAMMING LABORATORY TW	50	20		22 P			
214447	PROGRAMMING LABORATORY PR	50	20		AA F			
	COMMINICATION & LANGUAGE LAB. TW FIRST SEM TOTAL = $/750$	50	20		20 P			
	541 KAMBLE PALASH MAHENDRA				 NSI			,s140058541
SEM.:1								
214441	DISCRETE STRUCTURES PF	100	40 25	24	49 P			
214442	COMPUTER ORGANIZATION PF	100	40 27	17	44 P			
214443	DIGITAL ELEX & LOGIC DESIGN PF	100	40 29	25	54 P			
214444	FUNDAMENTAL OF DATASTRUCTURE PF	100	40 24	21	45 P			
214445	PROB SOLVING & OBJ ORIENT PROGPE	100	40 26	15	41 P			
214446	DIGITAL ELECTRONICS LABORATORYPR	50	20		28 P			
214446	DIGITAL ELECTRONICS LABORATORYOR	50	20		22 P			
214447	PROGRAMMING LABORATORY TW	50	20		34 P			
214447	PROGRAMMING LABORATORY PR	50	20		27 P			
	COMMINICATION & LANGUAGE LAB. TW FIRST SEM TOTAL = 377/750				33 P			
s140058	542 KAMBLE SUNIL BALAJI					,71633726G		,s140058542
SEM.:1								
214441	DISCRETE STRUCTURES PF	100	40 30	21	51 P			
214442	COMPUTER ORGANIZATION PF	100	40 27	27	54 P			
214443	DIGITAL ELEX & LOGIC DESIGN PF	100	40 34	18	52 P			
214444	FUNDAMENTAL OF DATASTRUCTURE PF	100	40 28	21	49 P			
	PROB SOLVING & OBJ ORIENT PROGPE			23				
	DIGITAL ELECTRONICS LABORATORYPR				29 P			
	DIGITAL ELECTRONICS LABORATORYOR				28 P			
	PROGRAMMING LABORATORY TW				32 P			
	PROGRAMMING LABORATORY PR				31 P			
214448	COMMINICATION & LANGUAGE LAB. TW				34 P			

	FIRST LINE : SEAT NO., NAME OF T										
	IES: SUB.CODE, SUB.NAME, SUB.TYPE,			,		•		•		•	
										, s140058	
	4							,	ŕ	ŕ	
SEM.: 214441		100	40	21	20	54	D				
	COMPUTER ORGANIZATION PF			22	18	40					
214443	DIGITAL ELEX & LOGIC DESIGN PR				15	47					
214444	FUNDAMENTAL OF DATASTRUCTURE PR			24	23	47					
214445	PROB SOLVING & OBJ ORIENT PROGPE			30	16	46					
214446	DIGITAL ELECTRONICS LABORATORYPE			30	10	22					
214446	DIGITAL ELECTRONICS LABORATORYOF					25					
214447	PROGRAMMING LABORATORY TV					30					
214447	PROGRAMMING LABORATORY PR										
	COMMINICATION & LANGUAGE LAB. TV					36					
214440	FIRST SEM TOTAL =/750	30	20			30	•				
	58544 KAPADEKAR SAI MAHENDRA									, \$140058	
								•	ŕ	,	
SEM.:											
214441				35	17	52	Р				
214442	COMPUTER ORGANIZATION PR			21	20	41					
214443	DIGITAL ELEX & LOGIC DESIGN PR	100	40	32	15	47					
214444	FUNDAMENTAL OF DATASTRUCTURE PR			19	21	40					
	PROB SOLVING & OBJ ORIENT PROGPE	100	40	29	15	44					
214446	DIGITAL ELECTRONICS LABORATORYPE	50	20			23					
214446	DIGITAL ELECTRONICS LABORATORYOF	50	20			35					
214447						37	Р				
214447	PROGRAMMING LABORATORY PR	50	20			40					
214448	COMMINICATION & LANGUAGE LAB. TV FIRST SEM TOTAL = 397/750	50	20			38	P				
	8545 KARAN SINGH RATHORE				UMA					,s140058	
SEM.:	1										
214441		100	40	42	49	91	P				
214442				36	44	80					
214443	DIGITAL ELEX & LOGIC DESIGN PR			39		63					
214444	FUNDAMENTAL OF DATASTRUCTURE PR			40	33	73					
214445	PROB SOLVING & OBJ ORIENT PROGPE			33	30	63					
214446	DIGITAL ELECTRONICS LABORATORYPE			,,	30	38					
214446	DIGITAL ELECTRONICS LABORATORYPH					35					
214447	PROGRAMMING LABORATORY TV					48					
214447	PROGRAMMING LABORATORY PR					47					
	COMMINICATION & LANGUAGE LAB. TV					44					
~ 1 1 1 1 0	FIRST SEM TOTAL = $582/750$	50	20			74	ı				

	9 S.E.(2014 PAT.)(INFORMATIOM T : 25 JAN 2016 CENTRE: PUN		-	E OF	COMPU	TER TECHNOL	OGY, PUNE.	Р	PAGE NO. 214
NOTE:	FIRST LINE : SEAT NO., NAME OF ES: SUB.CODE, SUB.NAME, SUB.TYPE	THE C	CANDID	ATE,	МОТН	ER, PERMANE	NT REG. NO., PREVI	OUS SEAT	NO., COLLEGE, SEAT NO.
	8546 KASAR SURAJKUMAR LAXMANRA								
SEM.:	1								
214441	DISCRETE STRUCTURES P	P 100	40	34	22	56 P			
214442	COMPUTER ORGANIZATION F	P 100	40	23	27	50 P			
214443	DIGITAL ELEX & LOGIC DESIGN F	P 100	40	40	18	58 P			
214444	FUNDAMENTAL OF DATASTRUCTURE F	P 100	40	30	22	52 P			
214445	PROB SOLVING & OBJ ORIENT PROGR	P 100	40	32	15	47 P			
214446	DIGITAL ELECTRONICS LABORATORYF	R 50	20			39 P			
214446	DIGITAL ELECTRONICS LABORATORYO	R 50	20			35 P			
214447	PROGRAMMING LABORATORY T	w 50	20			46 P			
214447	PROGRAMMING LABORATORY F	R 50	20			39 P			
214448	COMMINICATION & LANGUAGE LAB. T FIRST SEM TOTAL = 467/750	w 50	20			45 P			
	8547 KATE SHIVPRASAD SHYAMSUND								
SEM.:	1								
		p 100	10	40	23	63 P			
	COMPUTER ORGANIZATION F			25	28	53 P			
214443	DIGITAL ELEX & LOGIC DESIGN F			41	27	68 P			
214444	FUNDAMENTAL OF DATASTRUCTURE F			31	25	56 P			
	PROB SOLVING & OBJ ORIENT PROGR				15	47 P			
214446	DIGITAL ELECTRONICS LABORATORYP			32	13	47 P 35 P			
214446	DIGITAL ELECTRONICS LABORATORYO					30 P			
214447	PROGRAMMING LABORATORY T					40 P			
214447	PROGRAMMING LABORATORY P					40 P 35 P			
	COMMINICATION & LANGUAGE LAB. T					35 P			
	FIRST SEM TOTAL = 462/750								
S14005	8548 KAVATKAR AMIT MEGHASHYAM					A			,s140058548
SEM.:	1								
214441	DISCRETE STRUCTURES F	Р 100	40	33	22	55 P			
214442	COMPUTER ORGANIZATION F	Р 100	40	23	32	55 P			
214443	DIGITAL ELEX & LOGIC DESIGN F	P 100	40	38	28	66 P			
214444	FUNDAMENTAL OF DATASTRUCTURE F	Р 100	40	30	33	63 P			
214445	PROB SOLVING & OBJ ORIENT PROGR	P 100	40	34	19	53 P			
214446	DIGITAL ELECTRONICS LABORATORYP	R 50	20			31 P			
214446	DIGITAL ELECTRONICS LABORATORYO	R 50	20			31 P			
214447	PROGRAMMING LABORATORY T	w 50	20			42 P			
214447	PROGRAMMING LABORATORY P	R 50	20			32 P			
214448	COMMINICATION & LANGUAGE LAB. T FIRST SEM TOTAL = 472/750	w 50	20			44 P			

DATE	9 S.E.(2014 PAT.)(INFORMATION : 25 JAN 2016 CENTRE: P	UNE	INSTIT	UTE OF					AGE NO. 215	
NOTE: HER LIN	FIRST LINE : SEAT NO., NAME OES: SUB.CODE, SUB.NAME, SUB.TY)F TH 'PE,	IE CAND MAX.MA	IDATE, RKS, MI	MOTHE N.PASS	R, PERMANEN MARKS, OE	IT REG. NO., PRE MARKS, TH MARKS,	VIOUS SEAT N OBTAINED, F	O., COLLEGE, P/F:PASS/FAIL, C	SEAT NO. C:PREV.CARRY OVE
	8549 KAWATHEKAR SAMRUDDHI PA									
SEM.:	1									
214441	DISCRETE STRUCTURES	PP	100 40	27	22	49 P				
214442	COMPUTER ORGANIZATION	PP	100 40	24	25	49 P				
214443	DIGITAL ELEX & LOGIC DESIGN	PP	100 40	37	26	63 P				
214444	FUNDAMENTAL OF DATASTRUCTURE	PP	100 40	18	22	40 P				
214445	PROB SOLVING & OBJ ORIENT PRO	GPP	100 40	28	15	43 P				
214446	DIGITAL ELECTRONICS LABORATOR	YPR	50 20)		32 P				
214446	DIGITAL ELECTRONICS LABORATOR	YOR	50 20)		34 P				
214447	PROGRAMMING LABORATORY	TW	50 20)		42 P				
214447	PROGRAMMING LABORATORY	PR	50 20)		37 P				
214448	COMMINICATION & LANGUAGE LAB. FIRST SEM TOTAL = 430/750	TW	50 20	1		41 P				
S14005	8550 KHINVASARA MRUNAL MANIS	SH			VAISH	ALI	,71527317F	,PICT	,S14005855	50
SEM.:	1									
214441	DISCRETE STRUCTURES	PP	100 40	40	40	80 P				
214442	COMPUTER ORGANIZATION	PP	100 40	39	32	71 P				
214443	DIGITAL ELEX & LOGIC DESIGN	PP	100 40	43	35	78 P				
214444	FUNDAMENTAL OF DATASTRUCTURE	PP	100 40	38	33	71 P				
214445	PROB SOLVING & OBJ ORIENT PRO	GPP	100 40	34	33	67 P				
214446	DIGITAL ELECTRONICS LABORATOR	YPR	50 20)		42 P				
214446	DIGITAL ELECTRONICS LABORATOR	YOR	50 20)		35 P				
214447	PROGRAMMING LABORATORY	TW	50 20)		47 P				
214447	PROGRAMMING LABORATORY	PR	50 20)		44 P				
214448	COMMINICATION & LANGUAGE LAB. FIRST SEM TOTAL = 583/750	TW	50 20	1		48 P				
	8551 KODAG SWAPNAJA RANGRAO					 A				
	•									
SEM.: 214441	1 DISCRETE STRUCTURES	חם	100 40	36	24	60 P				
214441	COMPUTER ORGANIZATION		100 40		27	48 P				
214442	DIGITAL ELEX & LOGIC DESIGN		100 40		27	40 P 59 P				
214444	FUNDAMENTAL OF DATASTRUCTURE				30	59 P				
214444	PROB SOLVING & OBJ ORIENT PRO				30 37	62 P				
214445	DIGITAL ELECTRONICS LABORATOR				31	32 P				
214446	DIGITAL ELECTRONICS LABORATOR					32 P 28 P				
214446			50 20			28 P 46 P				
214447	PROGRAMMING LABORATORY PROGRAMMING LABORATORY					40 P 37 P				
214447	COMMINICATION & LANGUAGE LAB.					37 P 45 P				
<u>4</u> 17740	FIRST SEM TOTAL = 476/750	I VV	JU 20	,		1 3 F				

	29 S.E.(2014 PAT.)(INFORMATIOM TE E: 25 JAN 2016 CENTRE: PUNE		-	OF	COMPU	TER TECHNOL	OGY, PUNE.	P.	AGE NO. 216
NOTE:	FIRST LINE : SEAT NO., NAME OF T	ГНЕ С	ANDIDA	TE,	MOTH	IER, PERMANE	NT REG. NO., PREVI	OUS SEAT	NO., COLLEGE, SEAT NO.
	58552 KODE JANHAVI PRAVIN						,71527320F		
SEM.:	1								
214441	DISCRETE STRUCTURES PF	100	40	41	30	71 P			
214442	COMPUTER ORGANIZATION PF	100	40	30	07	F			
214443	DIGITAL ELEX & LOGIC DESIGN PF	100	40	38	24	62 P			
214444	FUNDAMENTAL OF DATASTRUCTURE PF	100	40	33	26	59 P			
214445	PROB SOLVING & OBJ ORIENT PROGPF	100	40	29	18	47 P			
214446	DIGITAL ELECTRONICS LABORATORYPE	R 50	20			38 P			
214446	DIGITAL ELECTRONICS LABORATORYOF	R 50	20			34 P			
214447	PROGRAMMING LABORATORY TW	v 50	20			39 P			
214447	PROGRAMMING LABORATORY PR	R 50	20			35 P			
214448	COMMINICATION & LANGUAGE LAB. TW FIRST SEM TOTAL =/750	v 50	20			41 P			
	58553 KOTHIKAR JAYKUMAR SHYAMSUN								,s140058553
SEM.:	.1								
214441		100	40	21	17	48 P			
214442					16	40 P			
214443	DIGITAL ELEX & LOGIC DESIGN PF				15	40 P 62 P			
214444	FUNDAMENTAL OF DATASTRUCTURE PF				20	02 P 44 P			
				2 4 27	16	44 P 43 P			
214446	DIGITAL ELECTRONICS LABORATORYPE			<i>L 1</i>	10	5 F			
214446	DIGITAL ELECTRONICS LABORATORYOF					7 F			
214447						7 г 37 Р			
214447	PROGRAMMING LABORATORY PR		_			37 P 32 P			
	COMMINICATION & LANGUAGE LAB. TW					30 P			
	FIRST SEM TOTAL =/750								
	58554 KUCHERIA KSHITIJ MADHUR					TA			,S140058554
SEM.:	1								
214441	DISCRETE STRUCTURES PF	100	40	33	25	58 P			
214442	COMPUTER ORGANIZATION PF	100	40	23	19	42 P			
214443	DIGITAL ELEX & LOGIC DESIGN PF	100	40	37	20	57 P			
214444	FUNDAMENTAL OF DATASTRUCTURE PF	100	40	32	24	56 P			
214445	PROB SOLVING & OBJ ORIENT PROGPF	100	40	30	06	F			
214446	DIGITAL ELECTRONICS LABORATORYPE	R 50	20			20 P			
214446	DIGITAL ELECTRONICS LABORATORYOF	R 50	20			35 P			
214447	PROGRAMMING LABORATORY TW	v 50	20			39 P			
214447	PROGRAMMING LABORATORY PF	R 50	20			40 P			
214448	COMMINICATION & LANGUAGE LAB. TW FIRST SEM TOTAL =/750	v 50	20			35 P			

	9 S.E.(2014 PAT.)(INFORMATIOM TE :: 25 JAN 2016 CENTRE: PUNE		-	OF	COMPL	JTER TECHNOL	OGY, PUNE.	PAG	GE NO. 217
NOTE:	FIRST LINE : SEAT NO., NAME OF T	HE CA	NDIDA	TE,	MOTH	HER, PERMANE	NT REG. NO., PREV	IOUS SEAT N	O., COLLEGE, SEAT NO.
OTHER LIN	IES: SUB.CODE, SUB.NAME, SUB.TYPE,	MAX.	MARKS	, MI	N. PAS	SS MARKS, OE	MARKS, TH MARKS, (OBTAINED, P	F:PASS/FAIL, C:PREV.CARRY OVER
	8555 KULKARNI NAYAN PRADEEP								
SEM.:	1								
	DISCRETE STRUCTURES PF	100	40	33	26	59 P			
	COMPUTER ORGANIZATION PF			30	39	69 P			
214443	DIGITAL ELEX & LOGIC DESIGN PF			38	24	62 P			
214444	FUNDAMENTAL OF DATASTRUCTURE PF			36	22	58 P			
214445	PROB SOLVING & OBJ ORIENT PROGPE	100	40	30	20	50 P			
214446	DIGITAL ELECTRONICS LABORATORYPR					40 P			
214446	DIGITAL ELECTRONICS LABORATORYOR					40 P			
214447	PROGRAMMING LABORATORY TW					41 P			
214447	PROGRAMMING LABORATORY PR					45 P			
	COMMINICATION & LANGUAGE LAB. TW					42 P			
	FIRST SEM TOTAL = 506/750								
	8556 KULKARNI OMKAR SUNIL								,S140058556
SEM.:	1								
214441	DISCRETE STRUCTURES PF	100	40	38	29	67 P			
214442	COMPUTER ORGANIZATION PF	100	40	29	27	56 P			
214443	DIGITAL ELEX & LOGIC DESIGN PF	100	40	47	15	62 P			
214444	FUNDAMENTAL OF DATASTRUCTURE PF	100	40	37	28	65 P			
214445	PROB SOLVING & OBJ ORIENT PROGPF	100	40	32	15	47 P			
214446	DIGITAL ELECTRONICS LABORATORYPR	50	20			38 P			
214446	DIGITAL ELECTRONICS LABORATORYOR	50	20			40 P			
214447	PROGRAMMING LABORATORY TW	50	20			43 P			
214447	PROGRAMMING LABORATORY PR	50	20			46 P			
214448	COMMINICATION & LANGUAGE LAB. TW FIRST SEM TOTAL = 507/750	50	20			43 P			
	8557 KUMARI SUSHMITA				SUN		,71527342G		
SEM.:	1								
214441		100	40	43	34	77 P			
		100		33	29	62 P			
214443		100		46	33	79 P			
214444	FUNDAMENTAL OF DATASTRUCTURE PF			38	27	65 P			
214445	PROB SOLVING & OBJ ORIENT PROGPE			38	38	76 P			
214446	DIGITAL ELECTRONICS LABORATORYPR			- -		38 P			
214446	DIGITAL ELECTRONICS LABORATORYOR					40 P			
214447	PROGRAMMING LABORATORY TW					47 P			
214447	PROGRAMMING LABORATORY PR					42 P			
214448	COMMINICATION & LANGUAGE LAB. TW					45 P			
	FIRST SEM TOTAL = 571/750								

NOTE: F	IRST LINE : SEAT NO., NAME OF TS: SUB.CODE, SUB.NAME, SUB.TYPE,									
				-	мотні	ER, P	ERMANENT R	EG. NO., PREV	OUS SEAT NO	., COLLEGE, SEAT NO.
	558 KUNTE DHARATI VASANT									,S140058558
SEM.:1										
214441	DISCRETE STRUCTURES PP	100	40	18	05		F			
214442	COMPUTER ORGANIZATION PP	100	40	13	24		F			
214443	DIGITAL ELEX & LOGIC DESIGN PP	100	40	26	08		F			
214444	FUNDAMENTAL OF DATASTRUCTURE PP	100	40	21	21	42	Р			
214445	PROB SOLVING & OBJ ORIENT PROGPP	100	40	30	15	45	Р			
214446	DIGITAL ELECTRONICS LABORATORYPR	50	20			5	F			
214446	DIGITAL ELECTRONICS LABORATORYOR	50	20			7	F			
214447	PROGRAMMING LABORATORY TW	50	20			36	Р			
214447	PROGRAMMING LABORATORY PR	50	20			10	F			
	COMMINICATION & LANGUAGE LAB. TW FIRST SEM TOTAL =/750	50	20			40	Р			
	559 KUYATE MONIKA RAJENDRA									,s140058559
SEM.:1										
214441	DISCRETE STRUCTURES PP	100	40	34	15	49	Р			
214442	COMPUTER ORGANIZATION PF	100	40	28	32	60	Р			
214443	DIGITAL ELEX & LOGIC DESIGN PP	100	40	38	29	67	Р			
214444	FUNDAMENTAL OF DATASTRUCTURE PP	100	40	37	26	63	Р			
214445	PROB SOLVING & OBJ ORIENT PROGPE	100	40	26	18	44	Р			
214446	DIGITAL ELECTRONICS LABORATORYPR	50	20			10	F			
214446	DIGITAL ELECTRONICS LABORATORYOR	50	20			30	Р			
214447	PROGRAMMING LABORATORY TW	50	20			34	Р			
214447	PROGRAMMING LABORATORY PR	50	20			39	Р			
	COMMINICATION & LANGUAGE LAB. TW FIRST SEM TOTAL = $/750$	50	20			40	Р			
	560 LADDHA ARTI MANOHAR									,s140058560
SEM.:1										
	DISCRETE STRUCTURES PP	100	40	31	26	57	Р			
	COMPUTER ORGANIZATION PP			35	31	66				
214443	DIGITAL ELEX & LOGIC DESIGN PP	100	40	39	38	77				
	FUNDAMENTAL OF DATASTRUCTURE PP			33	30	63	Р			
	PROB SOLVING & OBJ ORIENT PROGPE			32	20	52				
	DIGITAL ELECTRONICS LABORATORYPR					40				
	DIGITAL ELECTRONICS LABORATORYOR					40				
	PROGRAMMING LABORATORY TW					37				
	PROGRAMMING LABORATORY PR					37				
214448	COMMINICATION & LANGUAGE LAB. TW FIRST SEM TOTAL = 511/750					42				

	9 S.E.(2014 PAT.)(INFORMATIOM TO : 25 JAN 2016 CENTRE: PUN		-		COMPU	TER TECHNOLOG	Y, PUNE.	P	PAGE NO. 219
NOTE:	FIRST LINE : SEAT NO., NAME OF SES: SUB.CODE, SUB.NAME, SUB.TYPE	THE (CANDII	DATE,	мотн	ER, PERMANENT	REG. NO., PRE	VIOUS SEAT	NO., COLLEGE, SEAT NO.
	8561 LOHAKARE SONALI KHANDU								,s140058561
SEM.:	1								
214441	DISCRETE STRUCTURES P	P 10	0 40	28	17	45 P			
	COMPUTER ORGANIZATION P			25	19	44 P			
214443	DIGITAL ELEX & LOGIC DESIGN P	P 10	0 40	30	30	60 P			
214444	FUNDAMENTAL OF DATASTRUCTURE P			18	27	45 P			
214445	PROB SOLVING & OBJ ORIENT PROGP			21	27	48 P			
214446	DIGITAL ELECTRONICS LABORATORYP					20 P			
214446	DIGITAL ELECTRONICS LABORATORYO					35 P			
214447	PROGRAMMING LABORATORY TO					44 P			
214447	PROGRAMMING LABORATORY PI					37 P			
	COMMINICATION & LANGUAGE LAB. TO FIRST SEM TOTAL = 423/750					45 P			
	8562 M SHUSHANTH								,s140058562
SEM.:	1								
214441	DISCRETE STRUCTURES P	P 10	0 40	39	31	70 P			
214442	COMPUTER ORGANIZATION P	P 10	0 40	39	28	67 P			
214443	DIGITAL ELEX & LOGIC DESIGN P	P 10	0 40	44	29	73 P			
214444	FUNDAMENTAL OF DATASTRUCTURE P	P 10	0 40	40	34	74 P			
214445	PROB SOLVING & OBJ ORIENT PROGP	P 10	0 40	32	27	59 P			
214446	DIGITAL ELECTRONICS LABORATORYPH	R 50	20			38 P			
214446	DIGITAL ELECTRONICS LABORATORYO	R 50	20			34 P			
214447	PROGRAMMING LABORATORY TO	N 5	20			43 P			
214447	PROGRAMMING LABORATORY P	R 50	20			45 P			
214448	COMMINICATION & LANGUAGE LAB. TO FIRST SEM TOTAL = 542/750					39 P			
s14005	8563 MAHAJAN MANDAR LAXMIKANT								,s140058563
SEM.:	1								
214441	DISCRETE STRUCTURES P	P 10	0 40	44	30	74 P			
214442	COMPUTER ORGANIZATION P	P 10	0 40	33	22	55 P			
214443	DIGITAL ELEX & LOGIC DESIGN P	P 10	0 40	43	36	79 P			
214444	FUNDAMENTAL OF DATASTRUCTURE P	P 10	0 40	44	30	74 P			
214445	PROB SOLVING & OBJ ORIENT PROGP	P 10	0 40	30	31	61 P			
214446	DIGITAL ELECTRONICS LABORATORYP	R 50	20			41 P			
214446	DIGITAL ELECTRONICS LABORATORYO	R 50	20			42 P			
214447	PROGRAMMING LABORATORY TO					45 P			
214447	PROGRAMMING LABORATORY PI					47 P			
	COMMINICATION & LANGUAGE LAB. TO FIRST SEM TOTAL = 562/750					44 P			

	E: 25 JAN 2016 CENTRE: PUI FIRST LINE: SEAT NO., NAME OF								
	IES: SUB.CODE, SUB.NAME, SUB.TYP			-		•	·		
	8564 MAJALI SHRAVANI AVINASH								
SEM.:	1								
214441	DISCRETE STRUCTURES	PP 10	0 40	38	24	62 P			
214442	COMPUTER ORGANIZATION	PP 10	0 40	19	25	44 P			
214443	DIGITAL ELEX & LOGIC DESIGN	PP 10	0 40	37	33	70 P			
214444	FUNDAMENTAL OF DATASTRUCTURE	PP 10	0 40	14	31	F			
214445	PROB SOLVING & OBJ ORIENT PROG	PP 10	0 40	27	25	52 P			
214446	DIGITAL ELECTRONICS LABORATORY	PR 5	0 20			32 P			
214446	DIGITAL ELECTRONICS LABORATORY	OR 5	0 20			30 P			
214447	PROGRAMMING LABORATORY	™ 5	0 20			32 P			
214447	PROGRAMMING LABORATORY	PR 5	0 20			35 P			
214448	COMMINICATION & LANGUAGE LAB. FIRST SEM TOTAL =/750	™ 5	0 20			37 P			
	20565								
\$14005	58565 MALANI KAILASH SATYANARA	/AN			GEET	Α	,/152/36/B	, PICT	,s140058565
SEM.:	1								
214441	DISCRETE STRUCTURES	PP 10	0 40	38	33	71 P			
214442	COMPUTER ORGANIZATION	PP 10	0 40	31	28	59 P			
214443	DIGITAL ELEX & LOGIC DESIGN	PP 10	0 40	43	35	78 P			
214444	FUNDAMENTAL OF DATASTRUCTURE	PP 10	0 40	34	31	65 P			
214445	PROB SOLVING & OBJ ORIENT PROG	PP 10	0 40	27	24	51 P			
214446	DIGITAL ELECTRONICS LABORATORY	PR 5	0 20			38 P			
214446	DIGITAL ELECTRONICS LABORATORY	OR 5	0 20			8 F			
214447	PROGRAMMING LABORATORY	™ 5	0 20			36 P			
214447	PROGRAMMING LABORATORY	PR 5	0 20			37 P			
214448	COMMINICATION & LANGUAGE LAB. FIRST SEM TOTAL =/750	™ 5	0 20			41 P			
	8566 MALVE SHRADDHA SANJAY					IVANI			
SEM.:	1								
214441		PP 10	0 40	32	28	60 P			
214442			0 40	26	28	54 P			
214443				36	20	56 P			
214444	FUNDAMENTAL OF DATASTRUCTURE			24	30	54 P			
214445	PROB SOLVING & OBJ ORIENT PROG			27	28	55 P			
214446	DIGITAL ELECTRONICS LABORATORY			= =	-	35 P			
214446	DIGITAL ELECTRONICS LABORATORY					36 P			
214447	PROGRAMMING LABORATORY					30 P			
214447	PROGRAMMING LABORATORY					36 P			
	COMMINICATION & LANGUAGE LAB. FIRST SEM TOTAL = 455/750					39 P			

	E: 25 JAN 2016 CENTRE: PU								
HER LIN	FIRST LINE : SEAT NO., NAME OF IES: SUB.CODE, SUB.NAME, SUB.TYP	E, MA	X.MAI	RKS, M	IN.PAS	S MARKS, OE	MARKS, TH MARKS,	OBTAINED,	P/F:PASS/FAIL, C:PREV.CARRY OV
	8567 MANDHANI NIKITA LAXMINIW								
SEM.:	1								
214441	DISCRETE STRUCTURES	PP 10	0 40	39	44	83 P			
214442	COMPUTER ORGANIZATION	PP 10	0 40	34	23	57 P			
214443	DIGITAL ELEX & LOGIC DESIGN	PP 10	0 40	47	35	82 P			
214444	FUNDAMENTAL OF DATASTRUCTURE	PP 10	0 40	33	34	67 P			
214445	PROB SOLVING & OBJ ORIENT PROG	PP 10	0 40	26	27	53 P			
214446	DIGITAL ELECTRONICS LABORATORY	PR 5	0 20			36 P			
214446	DIGITAL ELECTRONICS LABORATORY	OR 5	0 20			37 P			
214447	PROGRAMMING LABORATORY	ΓW 5	0 20			46 P			
214447	PROGRAMMING LABORATORY	PR 5	0 20			43 P			
214448	COMMINICATION & LANGUAGE LAB. FIRST SEM TOTAL = 552/750	ΓW 5	0 20			48 P			
						U			,S140058568
311003	14 11 12 511				1-17 (01)		,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,	,, 101	,3110030300
SEM.:	1								
214441	DISCRETE STRUCTURES	PP 10	0 40	40	34	74 P			
214442	COMPUTER ORGANIZATION	PP 10	0 40	28	33	61 P			
214443	DIGITAL ELEX & LOGIC DESIGN	PP 10	0 40	38	33	71 P			
214444	FUNDAMENTAL OF DATASTRUCTURE	PP 10	0 40	34	24	58 P			
214445	PROB SOLVING & OBJ ORIENT PROG	PP 10	0 40	27	24	51 P			
214446	DIGITAL ELECTRONICS LABORATORY	PR 5	0 20			38 P			
214446	DIGITAL ELECTRONICS LABORATORY	OR 5	0 20			25 P			
214447	PROGRAMMING LABORATORY	TW 5	0 20			33 P			
214447	PROGRAMMING LABORATORY	PR 5	0 20			38 P			
214448	COMMINICATION & LANGUAGE LAB. FIRST SEM TOTAL = 483/750	ΓW 5	0 20			34 P			
	8569 MANTRI SHUBHAM SATYANARA					EETA			
SEM.:	1								
214441		PP 10	0 40	38	15	53 P			
214442		PP 10			07	F			
214443					22	59 P			
214444	FUNDAMENTAL OF DATASTRUCTURE			31	29	60 P			
214445	PROB SOLVING & OBJ ORIENT PROG			26	17	43 P			
214446	DIGITAL ELECTRONICS LABORATORY					38 P			
214446	DIGITAL ELECTRONICS LABORATORY					34 P			
214447	PROGRAMMING LABORATORY					43 P			
214447	PROGRAMMING LABORATORY					42 P			
	COMMINICATION & LANGUAGE LAB. FIRST SEM TOTAL =/750					42 P			

	29 S.E.(2014 PAT.)(INFORMATIOM T E: 25 JAN 2016 CENTRE: PUN			-	COMPL	JTER T	ECHNOLOGY, P	UNE.		PAGE NO.	222	
NOTE: OTHER LIN	FIRST LINE : SEAT NO., NAME OF NES: SUB.CODE, SUB.NAME, SUB.TYPE	THE , M	CANDI	DATE, RKS, M	MOTH IN.PAS	HER, P SS MAR	ERMANENT REG KS, OE MARKS	. NO., PRE , TH MARKS,	OBTAINED,	NO., C	OLLEGE, S/FAIL,	SEAT NO. C:PREV.CARRY OVER
	58570 MEHROTRA MAANAV VISHAL							,71527395н				
SEM.:	:1											
	DISCRETE STRUCTURES F	P 1	.00 40	50	49	99	Р					
	COMPUTER ORGANIZATION F			41	38	79						
214443	DIGITAL ELEX & LOGIC DESIGN P	P 1	.00 40	45	46	91	Р					
214444	FUNDAMENTAL OF DATASTRUCTURE P	P 1	.00 40	42	29	71	Р					
214445	PROB SOLVING & OBJ ORIENT PROGR	P 1	.00 40	37	28	65	Р					
214446	DIGITAL ELECTRONICS LABORATORYP	PR	50 20			41	Р					
214446	DIGITAL ELECTRONICS LABORATORYO)R	50 20			40	Р					
214447	PROGRAMMING LABORATORY T	W	50 20			47	Р					
214447	PROGRAMMING LABORATORY P	PR	50 20			45	Р					
214448	COMMINICATION & LANGUAGE LAB. T FIRST SEM TOTAL = 626/750					48						
s14005	58571 METE HANSRAJ SANJAY							,71527399L				
SEM.:	:1											
	DISCRETE STRUCTURES F	P 1	.00 40	37	28	65	Р					
214442				39	22	61						
214443	DIGITAL ELEX & LOGIC DESIGN F			44	40	84						
214444	FUNDAMENTAL OF DATASTRUCTURE F			35	31	66						
214445	PROB SOLVING & OBJ ORIENT PROGR	P 1	.00 40	31	28	59	Р					
214446	DIGITAL ELECTRONICS LABORATORYP	PR	50 20			32	Р					
214446	DIGITAL ELECTRONICS LABORATORYO)R	50 20			38	Р					
214447	PROGRAMMING LABORATORY T	W	50 20			47	Р					
214447	PROGRAMMING LABORATORY P	PR	50 20			43	Р					
214448	COMMINICATION & LANGUAGE LAB. T FIRST SEM TOTAL = 541/750	W	50 20			46	Р					
s14005	 58572 MISAL PRIYA BABASAHEB					 AMALA		, 71527403в	pICT	,	s1400585	572
SEM.:	•1											
214441		р 1	.00 40	34	23	57	P					
214442			.00 40	25	17	42						
214443	DIGITAL ELEX & LOGIC DESIGN F			33	17	50						
214444	FUNDAMENTAL OF DATASTRUCTURE F			19	28	47						
214445	PROB SOLVING & OBJ ORIENT PROGR			31	15	46						
214446	DIGITAL ELECTRONICS LABORATORYF					28						
214446	DIGITAL ELECTRONICS LABORATORYO					27						
214447			50 20			31						
214447			50 20			8						
214448	COMMINICATION & LANGUAGE LAB. T FIRST SEM TOTAL =/750					33						

RST LINE : SEAT NO., NAME O : SUB.CODE, SUB.NAME, SUB.TY 73 MODY ANKIT MANOJ ISCRETE STRUCTURES OMPUTER ORGANIZATION IGITAL ELEX & LOGIC DESIGN UNDAMENTAL OF DATASTRUCTURE ROB SOLVING & OBJ ORIENT PRO	F TIPE,	HE CAND	IDATE, RKS, M	MOTHE IN.PASS	R, PERMANENT I MARKS, OE MAI	REG. NO., PREVI	OUS SEAT N	NO., COLLEGE, P/F:PASS/FAIL,	SEAT NO.
73 MODY ANKIT MANOJ ISCRETE STRUCTURES OMPUTER ORGANIZATION IGITAL ELEX & LOGIC DESIGN UNDAMENTAL OF DATASTRUCTURE	PP PP								
OMPUTER ORGANIZATION IGITAL ELEX & LOGIC DESIGN UNDAMENTAL OF DATASTRUCTURE	PP	100 40				,71327409M	,PICT	,S1400585	
OMPUTER ORGANIZATION IGITAL ELEX & LOGIC DESIGN UNDAMENTAL OF DATASTRUCTURE	PP	100 40							
IGITAL ELEX & LOGIC DESIGN UNDAMENTAL OF DATASTRUCTURE			40	30	70 P				
UNDAMENTAL OF DATASTRUCTURE	PP	100 40	31	15	46 P				
		100 40	42	29	71 P				
ROR SOLVING & ORI ORIENT PRO	PP	100 40	30	33	63 P				
NOB BOLVENO & OBS CHILLING THO	GPP	100 40	35	27	62 P				
IGITAL ELECTRONICS LABORATOR	YPR	50 20			30 P				
IGITAL ELECTRONICS LABORATOR	YOR	50 20			38 P				
ROGRAMMING LABORATORY	TW	50 20			47 P				
ROGRAMMING LABORATORY	PR	50 20			46 P				
OMMINICATION & LANGUAGE LAB. IRST SEM TOTAL = 518/750	TW	50 20			45 P				
74 MUHALKAK SATYAJEET SAKJI	EKA	5		VIDYA		,71527410E	, PICI	,51400585	0/4
ISCRETE STRUCTURES	PP	100 40	41	30	71 P				
OMPUTER ORGANIZATION	PP	100 40	22	24	46 P				
IGITAL ELEX & LOGIC DESIGN	PP	100 40	31	32	63 P				
UNDAMENTAL OF DATASTRUCTURE	PP	100 40	24	31	55 P				
ROB SOLVING & OBJ ORIENT PRO	GPP	100 40	23	31	54 P				
IGITAL ELECTRONICS LABORATOR	YPR	50 20			35 P				
IGITAL ELECTRONICS LABORATOR	YOR	50 20			36 P				
ROGRAMMING LABORATORY	TW	50 20			38 P				
ROGRAMMING LABORATORY	PR	50 20			37 P				
OMMINICATION & LANGUAGE LAB. IRST SEM TOTAL = 469/750	TW	50 20			34 P				
ISCRETE STRUCTURES	PP	100 40	28	23	51 P				
				36	65 P				
					66 P				
				28					
				31	68 P				
					47 P				
OMMINICATION & LANGUAGE LAB.					42 P				
	IGITAL ELECTRONICS LABORATOR' IGITAL ELECTRONICS LABORATOR' ROGRAMMING LABORATORY ROGRAMMING LABORATORY DOMMINICATION & LANGUAGE LAB. IRST SEM TOTAL = 518/750	IGITAL ELECTRONICS LABORATORYPR IGITAL ELECTRONICS LABORATORYOR ROGRAMMING LABORATORY TW ROGRAMMING LABORATORY PR DMMINICATION & LANGUAGE LAB. TW IRST SEM TOTAL = 518/750	IGITAL ELECTRONICS LABORATORYPR 50 20 IGITAL ELECTRONICS LABORATORYOR 50 20 ROGRAMMING LABORATORY TW 50 20 ROGRAMMING LABORATORY PR 50 20 DMMINICATION & LANGUAGE LAB. TW 50 20 IRST SEM TOTAL = 518/750 ISCRETE STRUCTURES PP 100 40 INDIAMENTAL OF DATASTRUCTURE PP 100 40 IGITAL ELECTRONICS LABORATORYPR 50 20 IGITAL ELECTRONICS LABORATORYPR 50 20 IRST SEM TOTAL = 469/750 IRST SEM TOTAL = 469/750 ISCRETE STRUCTURES PP 100 40 INDIAMENTAL OF DATASTRUCTURE PP 100 40 INDIAMENTAL OF DATASTRUCTURE PP 100 40 INDIAMENTAL ELECTRONICS LABORATORYPR 50 20 INDIAMENTAL ELECTRONICS LABORATORY PR 50 20 INDIAMENTAL ELECTRONICS LABORATORY PR 50 20 INDIAMENTAL ABORATORY PR 50 20 INDIAMENTAL ABORATORY PR 50 20 INDIAMENTAL ABORATORY PR 50 20 INDIAMENTAL OF DATASTRUCTURE PP 100 40 INDIAMENTAL ELECTRONICS LABORATORYPR 50 20 INDI	IGITAL ELECTRONICS LABORATORYPR 50 20 IGITAL ELECTRONICS LABORATORYOR 50 20 ROGRAMMING LABORATORY TW 50 20 ROGRAMMING LABORATORY PR 50 20 DOMMINICATION & LANGUAGE LAB. TW 50 20 IRST SEM TOTAL = 518/750 TOTAL MOHALKAR SATYAJEET SARJERAO ISCRETE STRUCTURES PP 100 40 21 IGITAL ELEX & LOGIC DESIGN PP 100 40 24 ROB SOLVING & OBJ ORIENT PROGPP 100 40 23 IGITAL ELECTRONICS LABORATORYOR 50 20 ROGRAMMING LABORATORY TW 50 20 ROGRAMMING LABORATORY PR 50 20 IRST SEM TOTAL = 469/750 TOTAL ELEX & LOGIC DESIGN PP 100 40 29 IGITAL ELEX & LOGIC DESIGN PP 100 40 33 INDICATION & LANGUAGE LAB. TW 50 20 IRST SEM TOTAL = 469/750 TOTAL ELEX & LOGIC DESIGN PP 100 40 33 INDICATION & LANGUAGE LAB. TW 50 20 IRST SEM TOTAL = 469/750 TOTAL ELEX & LOGIC DESIGN PP 100 40 33 INDICATION & DATASTRUCTURE PP 100 40 33 INDICATION & OBJ ORIENT PROGPP 100 40 37 IGITAL ELEX & LOGIC DESIGN PP 100 40 37 IGITAL ELEX & LOGIC DESIGN PP 100 40 37 IGITAL ELEX & LOGIC DESIGN PP 100 40 37 IGITAL ELEX & LOGIC DESIGN PP 100 40 37 IGITAL ELEX & LOGIC DESIGN PP 100 40 37 IGITAL ELEX & LOGIC DESIGN PP 100 40 37 IGITAL ELEX & LOGIC DESIGN PP 100 40 37 IGITAL ELEX & LOGIC DESIGN PP 100 40 37 IGITAL ELEX & DATASTRUCTURE PP 100 40 37 IGITAL ELECTRONICS LABORATORYPR 50 20 IGROGRAMMING LABORATORY TW 50 20 IGITAL ELECTRONICS LABORATORYPR 50 20 IGROGRAMMING LABORATORY TW 50 20 IGROGRAMMING LABORATORY PR 50 20 IGNOMINICATION & LANGUAGE LAB. TW 50 20 IDMINICATION & LANGUAGE LAB. TW 50 20 IDMINICATION & LANGUAGE LAB. TW 50 20	IGITAL ELECTRONICS LABORATORYPR 50 20 IGITAL ELECTRONICS LABORATORYOR 50 20 ROGRAMMING LABORATORY TW 50 20 ROGRAMMING LABORATORY PR 50 20 IMMINICATION & LANGUAGE LAB. TW 50 20 IRST SEM TOTAL = 518/750 74 MOHALKAR SATYAJEET SARJERAO VIDYA ISCRETE STRUCTURES PP 100 40 41 30 IMPUTER ORGANIZATION PP 100 40 22 24 IGITAL ELEX & LOGIC DESIGN PP 100 40 23 31 IGITAL ELECTRONICS LABORATORYPR 50 20 IGITAL ELECTRONICS LABORATORYPR 50 20 ROGRAMMING LABORATORY TW 50 20 IRST SEM TOTAL = 469/750 75 MOMIN SOHEL ABDUAL SATTAR RAZIY ISCRETE STRUCTURES PP 100 40 28 23 IMPUTER ORGANIZATION PP 100 40 29 36 IGITAL ELECTRONICS LABORATORY PR 50 20 IRST SEM TOTAL = 469/750 75 MOMIN SOHEL ABDUAL SATTAR RAZIY ISCRETE STRUCTURES PP 100 40 33 33 INDAMENTAL OF DATASTRUCTURE PP 100 40 33 38 INDAMENTAL OF DATASTRUCTURE PP 100 40 33 38 INDAMENTAL OF DATASTRUCTURE PP 100 40 33 38 INDAMENTAL OF DATASTRUCTURE PP 100 40 37 31 IGITAL ELECTRONICS LABORATORYPR 50 20 IRGRAMMING LABORATORY TW 50 20 IRGRAMMING LABORATORY TW 50 20 IRGRAMMING LABORATORY PR 50 20 INDIMINICATION & LANGUAGE LAB. TW 50 20 INDIMINICATION & LANGUAGE LAB. TW 50 20 INDIMINICATION & LANGUAGE LAB. TW 50 20	IGITAL ELECTRONICS LABORATORYPR 50 20 38 P ROGRAMMING LABORATORY TW 50 20 47 P ROGRAMMING LABORATORY PR 50 20 46 P DOMMINICATION & LANGUAGE LAB. TW 50 20 45 P ISSCRETE STRUCTURES PP 100 40 41 30 71 P DOMMINICATION & CONTROL PR 100 40 41 30 71 P DOMMINICATION & LANGUAGE LAB. TW 50 20 46 P ISSCRETE STRUCTURES PP 100 40 41 30 71 P DOMMINICATION PP 100 40 41 31 32 63 P UNDAMENTAL OF DATASTRUCTURE PP 100 40 24 31 55 P ROGRAMMING & OBJ ORIENT PROGPP 100 40 23 31 54 P IGITAL ELECTRONICS LABORATORYPR 50 20 35 P IGITAL ELECTRONICS LABORATORYPR 50 20 36 P ROGRAMMING LABORATORY TW 50 20 38 P ROGRAMMING LABORATORY PR 50 20 37 P DOMMINICATION & LANGUAGE LAB. TW 50 20 34 P ISSCRETE STRUCTURES PP 100 40 28 23 51 P DOMMINICATION & LANGUAGE LAB. TW 50 20 36 65 P IGITAL ELECTRONICS LABORATORY PP 100 40 33 33 66 P ROGRAMMING LABORATORY PP 100 40 33 33 66 P ROGRAMMING LABORATORY PP 100 40 33 33 66 P ROGRAMMING LABORATORY PP 100 40 33 33 66 P ROGRAMMING LABORATORY PP 100 40 33 32 61 P ROB SOLVING & OBJ ORIENT PROGPP 100 40 37 31 68 P ROB SOLVING & OBJ ORIENT PROGPP 100 40 37 31 68 P ROB SOLVING & OBJ ORIENT PROGPP 100 40 37 31 68 P ROB SOLVING & OBJ ORIENT PROGPP 100 40 37 31 68 P ROB SOLVING & OBJ ORIENT PROGPP 100 40 37 31 68 P ROB SOLVING & OBJ ORIENT PROGPP 100 40 37 31 68 P ROB SOLVING & OBJ ORIENT PROGPP 100 40 37 31 68 P ROB SOLVING & OBJ ORIENT PROGPP 100 40 37 31 68 P ROGRAMMING LABORATORY TW 50 20 30 P ROGRAMMING LABORATORY TW 50 20 47 P ROGRAMMING LABORATORY TW 50 20 47 P ROGRAMMING LABORATORY TW 50 20 47 P ROGRAMMING LABORATORY TW 50 20 41 P ROMMINICATION & LANGUAGE LAB. TW 50 20 41 P	IGITAL ELECTRONICS LABORATORYPR 50 20 38 P IGITAL ELECTRONICS LABORATORYOR 50 20 38 P ROGRAMMING LABORATORY TW 50 20 47 P ROGRAMMING LABORATORY PR 50 20 46 P DOMMINICATION & LANGUAGE LAB. TW 50 20 45 P IRST SEM TOTAL = 518/750 74 MOHALKAR SATYAJEET SARJERAO VIDYA ,71527410E ISCRETE STRUCTURES PP 100 40 41 30 71 P DMPUTER ORGANIZATION PP 100 40 22 24 46 P IGITAL ELEX & LOGIC DESIGN PP 100 40 31 32 63 P IUNDAMENTAL OF DATASTRUCTURE PP 100 40 24 31 55 P ROB SOLVING & OBJ ORIENT PROGPP 100 40 23 31 54 P IGITAL ELECTRONICS LABORATORYPR 50 20 35 P IGITAL ELECTRONICS LABORATORYPR 50 20 36 P ROGRAMMING LABORATORY TW 50 20 37 P DMMINICATION & LANGUAGE LAB. TW 50 20 34 P IRST SEM TOTAL = 469/750 TSCRETE STRUCTURES PP 100 40 28 23 51 P DMPUTER ORGANIZATION PP 100 40 29 36 65 P INDMPUTER ORGANIZATION PP 100 40 29 36 6 P ROGRAMMING LABORATORY PR 50 20 34 P IRST SEM TOTAL = 469/750 TS MOMIN SOHEL ABDUAL SATTAR RAZIYA BEGUM ,71633731C ISCRETE STRUCTURES PP 100 40 28 23 51 P INDMPUTER ORGANIZATION PP 100 40 29 36 6 P IUNDAMENTAL OF DATASTRUCTURE PP 100 40 33 33 66 P IUNDAMENTAL OF DATASTRUCTURE PP 100 40 37 31 68 P IUNDAMENTAL OF DATASTRUCTURE PP 100 40 37 31 68 P IUNDAMENTAL OF DATASTRUCTURE PP 100 40 37 31 68 P IUNDAMENTAL OF DATASTRUCTURE PP 100 40 37 31 68 P IUNDAMENTAL OF DATASTRUCTURE PP 100 40 37 31 68 P IUNDAMENTAL OF DATASTRUCTURE PP 100 40 37 31 68 P IUNDAMENTAL OF DATASTRUCTURE PP 100 40 37 31 68 P IUNDAMENTAL OF DATASTRUCTURE PP 100 40 37 31 68 P IUNDAMENTAL OF DATASTRUCTURE PP 100 40 37 31 68 P IUNDAMENTAL OF DATASTRUCTURE PP 100 40 37 31 68 P IUNDAMENTAL OF DATASTRUCTURE PP 100 40 37 31 68 P IUNDAMENTAL OF DATASTRUCTURE PP 100 40 37 31 68 P IUNDAMENTAL OF DATASTRUCTURE PP 100 40 37 31 68 P IUNDAMENTAL OF DATASTRUCTURE PP 100 40 37 31 68 P IUNDAMENTAL OF DATASTRUCTURE PP 100 40 37 31 68 P IUNDAMENTAL OF DATASTRUCTURE PP 100 40 37 31 68 P IUNDAMENTAL OF DATASTRUCTURE PP 100 40 37 31 68 P IUNDAMENTAL OF DATASTRUCTURE PP 100 40 37 31 68 P IUNDAMENTAL OF DATASTRUCTURE PP 100 40 37 31 68 P IUNDAMENTAL OF DATASTRUC	IGITAL ELECTRONICS LABORATORYPR 50 20 30 P IGITAL ELECTRONICS LABORATORYPR 50 20 38 P ROGRAMMING LABORATORY PR 50 20 47 P ROGRAMMING LABORATORY PR 50 20 46 P DOMMINICATION & LANGUAGE LAB. TW 50 20 45 P IRST SEM TOTAL = 518/750 TAMBER OF THE PROOF OF T	IGITAL ELECTRONICS LABORATORYPR 50 20 30 P IGITAL ELECTRONICS LABORATORYPR 50 20 38 P ROGRAMMING LABORATORY TW 50 20 47 P ROGRAMMING LABORATORY PR 50 20 46 P MMINICATION & LANGUAGE LAB. TW 50 20 45 P ISSCRETE STRUCTURES PP 100 40 41 30 71 P MORPUTER ORGANIZATION PP 100 40 22 24 46 P IGITAL ELECTRONICS LABORATORYPR 90 20 31 54 P IGITAL ELECTRONICS LABORATORYPR 90 20 33 5 P IGITAL ELECTRONICS LABORATORYPR 50 20 38 P ROGRAMMING LABORATORY TW 50 20 38 P ROGRAMMING LABORATORYPR 50 20 37 P MOMINICATION & LANGUAGE LAB. TW 50 20 34 P ISSCRETE STRUCTURES PP 100 40 42 31 55 P ROGRAMMING LABORATORYPR 50 20 38 P ROGRAMMING LABORATORYPR 50 20 37 P MOMINICATION & LANGUAGE LAB. TW 50 20 34 P ISSCRETE STRUCTURES PP 100 40 28 23 51 P MOMINICATION & LANGUAGE LAB. TW 50 20 34 P ISSCRETE STRUCTURES PP 100 40 33 32 66 P ROGRAMMING LABORATORY PR 50 20 37 P MOMINICATION & LANGUAGE LAB. TW 50 20 36 65 P ISSCRETE STRUCTURES PP 100 40 33 32 86 61 P ROB SOLVING & OBJ ORIENT PROGPP 100 40 33 32 86 61 P ROB SOLVING & OBJ ORIENT PROGPP 100 40 33 32 86 61 P ROB SOLVING & OBJ ORIENT PROGPP 100 40 37 31 68 P ISSCRETE STRUCTURES PP 100 40 37 31 68 P ISSCRETE STRUCTURES PP 100 40 37 31 68 P ISSCRETE STRUCTURES PP 100 40 37 31 68 P ISSCRETE STRUCTURE PROGPP 100 40 37 31 68 P ISSCRETE STRUCTURE PROGPP 100 40 37 31 68 P ISSCRETE STRUCTURE PROGPP 100 40 37 31 68 P ISSCRETE STRUCTURE PROGPP 100 40 37 31 68 P ISSCRETE STRUCTURE PROGPP 100 40 37 31 68 P ISSCRETE STRUCTURE PROGPP 100 40 37 31 68 P ISSCRETE STRUCTURE PROGPP 100 40 37 31 68 P ISSCRETE STRUCTURE PROGPP 100 40 37 31 68 P ISSCRETE STRUCTURE PROGPP 100 40 37 31 68 P ISSCRETE STRUCTURE PROGPP 100 40 37 31 68 P ISSCRETE STRUCTURE PROGPP 100 40 37 31 68 P ISSCRETE STRUCTURE PROGPP 100 40 37 31 68 P ISSCRETE STRUCTURE PROGPP 100 40 37 31 68 P ISSCRETE STRUCTURE PROGPP 100 40 37 31 68 P ISSCRETE STRUCTURE PROGPP 100 40 37 31 68 P ISSCRETE STRUCTURE PROGPP 100 40 37 31 68 P ISSCRETE STRUCTURE PROGPP 100 40 37 31 68 P ISSCRETE STRUCTURE PROGPP 100 40 37

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	ES: SUB.CODE, SUB.NAME, SUB.TYP			-		•	•		
	8576 MUCHAL PAVANDEEPAJEET SI								
SEM.:	1								
214441	DISCRETE STRUCTURES	PP 100	40	35	34	69 P			
214442	COMPUTER ORGANIZATION	PP 100	40	22	19	41 P			
214443	DIGITAL ELEX & LOGIC DESIGN	PP 100	40	36	27	63 P			
214444	FUNDAMENTAL OF DATASTRUCTURE	PP 100	40	32	29	61 P			
214445	PROB SOLVING & OBJ ORIENT PROG	PP 100	40	25	21	46 P			
214446	DIGITAL ELECTRONICS LABORATORY	PR 50	20			20 P			
214446	DIGITAL ELECTRONICS LABORATORY	OR 50	20			20 P			
214447	PROGRAMMING LABORATORY	rw 50	20			35 P			
214447	PROGRAMMING LABORATORY	PR 50	20			10 F			
214448	COMMINICATION & LANGUAGE LAB. FIRST SEM TOTAL = $/750$	ΓW 50	20			39 P			
	0577 MULEY DRAZAKTA DRADED								
S14005	8577 MULEY PRAJAKTA PRADEEP				PRAD	NYA	,/1633/32M	,PICI	,S140058577
SEM.:	1								
214441	DISCRETE STRUCTURES	PP 100	40	29	16	45 P			
214442	COMPUTER ORGANIZATION	PP 100	40	22	18	40 P			
214443	DIGITAL ELEX & LOGIC DESIGN	PP 100	40	43	21	64 P			
214444	FUNDAMENTAL OF DATASTRUCTURE	PP 100	40	31	23	54 P			
214445	PROB SOLVING & OBJ ORIENT PROG	PP 100	40	29	19	48 P			
214446	DIGITAL ELECTRONICS LABORATORY	PR 50	20			25 P			
214446	DIGITAL ELECTRONICS LABORATORY	OR 50	20			22 P			
214447	PROGRAMMING LABORATORY					38 P			
214447	PROGRAMMING LABORATORY	PR 50	20			38 P			
214448	COMMINICATION & LANGUAGE LAB. FIRST SEM TOTAL = 415/750	ΓW 5C	20			41 P			
	8578 MUNDADA AAKANKSHA GIRISH					 L			
SEM.:	1								
214441		PP 100) 40	24	21	45 P			
214442		PP 100		27	19	46 P			
214443				35	33	68 P			
214444	FUNDAMENTAL OF DATASTRUCTURE			32	30	62 P			
214445	PROB SOLVING & OBJ ORIENT PROG			34	19	53 P			
214446	DIGITAL ELECTRONICS LABORATORY				-	41 P			
214446	DIGITAL ELECTRONICS LABORATORY					41 P			
214447	PROGRAMMING LABORATORY					43 P			
214447	PROGRAMMING LABORATORY					36 P			
	COMMINICATION & LANGUAGE LAB. FIRST SEM TOTAL = 483/750					48 P			

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NOTE:	FIRST LINE : SEAT NO., NAME OF THE SUB.CODE, SUB.NAME, SUB.TYPE,	HE C	ANDID	ATE,	MOTHE	ER, P	PERMANENT REG.	NO., PRE	VIOUS SEAT	NO., COLLEG	E, SEAT NO.
	8579 MUNDALIK SURAJ SHAMKUMAR										
GEW -	1										
SEM.:		100	40	2.7	24	60	_				
214441						68					
214442				26 36	26	52					
214443 214444	DIGITAL ELEX & LOGIC DESIGN PRE- FUNDAMENTAL OF DATASTRUCTURE PRE-			32	28 29	64 61					
214445	PROB SOLVING & OBJ ORIENT PROGPE				31	60					
214446	DIGITAL ELECTRONICS LABORATORYPE			29	31	34					
214446	DIGITAL ELECTRONICS LABORATORYPH					28					
214447	PROGRAMMING LABORATORY TV					45					
214447											
	COMMINICATION & LANGUAGE LAB. TV					42					
214440	FIRST SEM TOTAL = 501/750										
s14005	8580 MUSALE SHARAYU ANIL									,s1400	
CEM .	1										
SEM.:		100	40	4.4	26	70	D				
214441 214442	COMPUTER ORGANIZATION PR			44 35		70 67					
214443				34	32 33	67					
214444	FUNDAMENTAL OF DATASTRUCTURE PR			3 4 37	33 37	74					
	PROB SOLVING & OBJ ORIENT PROGPE			23	32	55					
214446	DIGITAL ELECTRONICS LABORATORYPE			23	32	42					
214446	DIGITAL ELECTRONICS LABORATORYOF					40					
214447	PROGRAMMING LABORATORY TV					47					
	PROGRAMMING LABORATORY PR					42					
	COMMINICATION & LANGUAGE LAB. TV					44					
214440	FIRST SEM TOTAL = 548/750	30	20			77	•				
	8581 MUTHA SAKSHI MANOJ									,s1400	
SEM.:	1										
	DISCRETE STRUCTURES PR	100	40	40	41	81	Р				
	COMPUTER ORGANIZATION PR			32	19	51					
214443	DIGITAL ELEX & LOGIC DESIGN PR			31	30	61					
214444				34	29	63					
214445	PROB SOLVING & OBJ ORIENT PROGPE			26		54					
214446	DIGITAL ELECTRONICS LABORATORYPH	50	20			38					
214446	DIGITAL ELECTRONICS LABORATORYOF					25					
214447	PROGRAMMING LABORATORY TV	50	20			47	Р				
214447	PROGRAMMING LABORATORY PR	50	20			27	Р				
214448	COMMINICATION & LANGUAGE LAB. TO FIRST SEM TOTAL = 495/750	50	20			48	Р				

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									s14005	
					10 (110 (,71101022	,,,,,,	,51.003	0302
SEM.:		100	40 3		4 7	45 -				
214441				28		45 P				
	COMPUTER ORGANIZATION PP				14	F				
214443	DIGITAL ELEX & LOGIC DESIGN PP				19	47 P				
214444	FUNDAMENTAL OF DATASTRUCTURE PP				07	F				
214445	PROB SOLVING & OBJ ORIENT PROGPP			23	17	40 P				
214446	DIGITAL ELECTRONICS LABORATORYPR					27 P				
214446	DIGITAL ELECTRONICS LABORATORYOR					30 P				
214447	PROGRAMMING LABORATORY TW					38 P				
214447	PROGRAMMING LABORATORY PR					8 F				
214448	COMMINICATION & LANGUAGE LAB. TW FIRST SEM TOTAL =/750	50	20			38 P				
	8583 NALAWADE PIYUSH VIJAY					EVANI			, \$14005	
SEM.:	1									
214441	DISCRETE STRUCTURES PP	100	40 4	17	26	73 P				
214442	COMPUTER ORGANIZATION PP	100	40 2	23	34	57 P				
214443	DIGITAL ELEX & LOGIC DESIGN PP	100	40 3	88	36	74 P				
214444	FUNDAMENTAL OF DATASTRUCTURE PP	100	40 2	27	31	58 P				
214445	PROB SOLVING & OBJ ORIENT PROGPP	100	40 3	32	25	57 P				
214446	DIGITAL ELECTRONICS LABORATORYPR	50	20			22 P				
214446	DIGITAL ELECTRONICS LABORATORYOR	50	20			26 P				
214447	PROGRAMMING LABORATORY TW	50	20			39 P				
214447	PROGRAMMING LABORATORY PR	50	20			39 P				
214448	COMMINICATION & LANGUAGE LAB. TW FIRST SEM TOTAL = 484/750	50	20			39 P				
	8584 NATTHANI SHIVAMKUMAR DEEPA								,s14005	
SEM.:	1									
214441		100	40 3	88	33	71 P				
214442				_	32	71 P 69 P				
214443	DIGITAL ELEX & LOGIC DESIGN PP			-	29	75 P				
214444					2 <i>9</i> 27	73 P 68 P				
214445	PROB SOLVING & OBJ ORIENT PROGPP				19	54 P				
214446	DIGITAL ELECTRONICS LABORATORYPR			, ,	± J	42 P				
214446	DIGITAL ELECTRONICS LABORATORYPR DIGITAL ELECTRONICS LABORATORYOR					42 P 44 P				
214447	PROGRAMMING LABORATORY TW					47 P				
214447	PROGRAMMING LABORATORY PR					42 P				
Z14448	COMMINICATION & LANGUAGE LAB. TW FIRST SEM TOTAL = 553/750	50	20			41 P				

	9 S.E.(2014 PAT.)(INFORMATIOM E: 25 JAN 2016 CENTRE: P		-	COMPU	TER TECHNOL	OGY, PUNE.	Р	PAGE NO. 227
NOTE:	FIRST LINE : SEAT NO., NAME O	F THE CA	NDIDATE,	МОТН	ER, PERMANE	ENT REG. NO., PREV	OUS SEAT	NO., COLLEGE, SEAT NO. P/F:PASS/FAIL, C:PREV.CARRY OVE
	8585 NIKHIL MEHTA					,71527441E		,s140058585
SEM.:	1							
214441	DISCRETE STRUCTURES	PP 100	40 42	25	67 P			
214442	COMPUTER ORGANIZATION	PP 100	40 28	23	51 P			
214443	DIGITAL ELEX & LOGIC DESIGN	PP 100	40 44	33	77 P			
214444	FUNDAMENTAL OF DATASTRUCTURE	PP 100	40 29	31	60 P			
214445	PROB SOLVING & OBJ ORIENT PRO	GPP 100	40 31	19	50 P			
214446	DIGITAL ELECTRONICS LABORATOR	YPR 50	20		38 P			
214446	DIGITAL ELECTRONICS LABORATOR	YOR 50	20		39 P			
214447	PROGRAMMING LABORATORY	TW 50	20		40 P			
214447	PROGRAMMING LABORATORY	PR 50	20		39 P			
214448	COMMINICATION & LANGUAGE LAB. FIRST SEM TOTAL = 496/750	TW 50	20		35 P			
	8586 OHOL ABHIJEET SANJAY					,71527447D		,S140058586
SEM.:	1							
214441		PP 100	40 40	32	72 P			
214442				24	51 P			
214443	DIGITAL ELEX & LOGIC DESIGN			26	59 P			
214444	FUNDAMENTAL OF DATASTRUCTURE			30	71 P			
				19	56 P			
214446	DIGITAL ELECTRONICS LABORATOR				30 P			
214446	DIGITAL ELECTRONICS LABORATOR				31 P			
214447		TW 50			38 P			
214447	PROGRAMMING LABORATORY		-		37 P			
	COMMINICATION & LANGUAGE LAB. FIRST SEM TOTAL = 485/750				40 P			
s14005	8587 PADMASALI PRANAV DILIP				Α			,s140058587
SEM.:	1							
214441	DISCRETE STRUCTURES	PP 100	40 35	33	68 P			
214442	COMPUTER ORGANIZATION	PP 100	40 26	29	55 P			
214443	DIGITAL ELEX & LOGIC DESIGN	PP 100	40 40	31	71 P			
214444	FUNDAMENTAL OF DATASTRUCTURE	PP 100	40 37	24	61 P			
214445	PROB SOLVING & OBJ ORIENT PRO	GPP 100	40 29	22	51 P			
214446	DIGITAL ELECTRONICS LABORATOR	YPR 50	20		27 P			
214446	DIGITAL ELECTRONICS LABORATOR	YOR 50	20		33 P			
214447	PROGRAMMING LABORATORY	TW 50	20		45 P			
214447	PROGRAMMING LABORATORY	PR 50	20		37 P			
214448	COMMINICATION & LANGUAGE LAB. FIRST SEM TOTAL = 490/750	TW 50	20		42 P			

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NOTE:	FIRST LINE : SEAT NO., NAME OF T NES: SUB.CODE, SUB.NAME, SUB.TYPE,	HE CA	ANDIDA	TE,	MOTHE	R, P	ERMANENT REG.	NO., PRE	VIOUS SEAT	NO., COLLEG	GE, SEAT NO.
	 58588 PALLAVI BANSAL			• • •						,s1400	
CEM .	.1										
SEM.: 214441		100	40	41	38	79	D				
214442				39	30	69					
214443	DIGITAL ELEX & LOGIC DESIGN PP			44	29	73					
214444	FUNDAMENTAL OF DATASTRUCTURE PP			32	25	57					
214445	PROB SOLVING & OBJ ORIENT PROGPP				19	57					
214446	DIGITAL ELECTRONICS LABORATORYPR			30	13	31					
214446	DIGITAL ELECTRONICS LABORATORYOR					29					
214447	PROGRAMMING LABORATORY TW					39					
214447											
214448	COMMINICATION & LANGUAGE LAB. TW					44					
	FIRST SEM TOTAL = 523/750										
c14005	 58589 PATANGRAO NAMRATA DIPAK									,51400	
314003	00305 PATANGRAU NAMRATA DIPAK				SHAKU	A	,	/1033/34n	,PICI	, 51400	736369
SEM.:	:1										
214441	DISCRETE STRUCTURES PP	100	40	28	21	49	Р				
214442	COMPUTER ORGANIZATION PP	100	40	30	31	61	Р				
214443	DIGITAL ELEX & LOGIC DESIGN PP	100	40	38	22	60	Р				
214444	FUNDAMENTAL OF DATASTRUCTURE PP	100	40	27	24	51	Р				
214445	PROB SOLVING & OBJ ORIENT PROGPP	100	40	33	20	53					
214446	DIGITAL ELECTRONICS LABORATORYPR		_			25					
214446	DIGITAL ELECTRONICS LABORATORYOR					32					
214447	PROGRAMMING LABORATORY TW					39					
	PROGRAMMING LABORATORY PR					35					
214448	COMMINICATION & LANGUAGE LAB. TW FIRST SEM TOTAL = 446/750	50	20			41	Р				
	 58590 patekari awais jafar iqbal									,s1400	
SEM.: 214441		100	40	41	33	74	P				
	COMPUTER ORGANIZATION PP			35	30	65					
214443	DIGITAL ELEX & LOGIC DESIGN PP			46	32	78					
214444				30	29	59					
214445	PROB SOLVING & OBJ ORIENT PROGPP			36		59					
214446					_5	35					
214446	DIGITAL ELECTRONICS LABORATORYOR					38					
214447	PROGRAMMING LABORATORY TW					36					
214447						41					
						36					
	COMMINICATION & LANGUAGE LAB. TW	50	20			50					

	: 25 JAN 2016 CENTRE: PUNE								PAGE NO. 229	
HER LIN	FIRST LINE : SEAT NO., NAME OF T ES: SUB.CODE, SUB.NAME, SUB.TYPE,	MAX.	MARKS,	MIN	.PASS M	ARKS, OE MARKS	S, TH MARKS,	OBTAINED,	P/F:PASS/FAIL,	C:PREV.CARRY OVI
	8591 PATIL PRAJAKTA NIMBA								,s140058	
SEM.:	1									
214441	DISCRETE STRUCTURES PF	100	40 3	7	20 5	7 P				
214442	COMPUTER ORGANIZATION PF	100	40 2	6	31 5	7 P				
214443	DIGITAL ELEX & LOGIC DESIGN PF	100	40 2	9	36 6	5 P				
214444	FUNDAMENTAL OF DATASTRUCTURE PF	100	40 2	0	33 5	3 P				
214445	PROB SOLVING & OBJ ORIENT PROGPE	100	40 2	8	30 5	8 P				
214446	DIGITAL ELECTRONICS LABORATORYPR	50	20		3	6 P				
214446	DIGITAL ELECTRONICS LABORATORYOR	50	20		3	8 P				
214447	PROGRAMMING LABORATORY TW	50	20		4	1 P				
214447	PROGRAMMING LABORATORY PR	50	20		3	5 P				
	COMMINICATION & LANGUAGE LAB. TW FIRST SEM TOTAL = 482/750					2 P				
	8592 PATIL PRASAD SUDHIR								,s140058	
SEM.:	1									
214441	DISCRETE STRUCTURES PF	100	40 4	0	33 7	3 P				
214442	COMPUTER ORGANIZATION PF	100	40 2	8	26 5	4 P				
214443	DIGITAL ELEX & LOGIC DESIGN PP	100	40 4	3	37 8	0 P				
214444	FUNDAMENTAL OF DATASTRUCTURE PP	100	40 3	4	39 7	3 P				
214445	PROB SOLVING & OBJ ORIENT PROGPE	100	40 3	0	38 6	8 P				
214446	DIGITAL ELECTRONICS LABORATORYPR	50	20		4	1 P				
214446	DIGITAL ELECTRONICS LABORATORYOR	50	20		3	9 P				
214447	PROGRAMMING LABORATORY TW	50	20		4	2 P				
214447	PROGRAMMING LABORATORY PR	50	20		4	3 P				
214448	COMMINICATION & LANGUAGE LAB. TW FIRST SEM TOTAL = 553/750	50	20		4	0 P				
	0503						716227265			
S14005	8593 PATIL PUNAM UMESH			:	SUREKHA		,71633736D	,PICT	,S140058	593
SEM.:		100	40 3		n	7 D				
214441	DISCRETE STRUCTURES PF					7 P				
	COMPUTER ORGANIZATION PF					7 P				
214443	DIGITAL ELEX & LOGIC DESIGN PR					4 P				
214444	FUNDAMENTAL OF DATASTRUCTURE PE					2 P				
214445	PROB SOLVING & OBJ ORIENT PROGPE			7		2 P				
214446	DIGITAL ELECTRONICS LABORATORYPR					9 P				
214446	DIGITAL ELECTRONICS LABORATORYOR					5 P				
214447	PROGRAMMING LABORATORY TW					6 P				
214447	PROGRAMMING LABORATORY PR					0 P				
214448	COMMINICATION & LANGUAGE LAB. TW FIRST SEM TOTAL = 446/750	50	20		4	4 P				

	E: 25 JAN 2016 CENTRE: PUNE									PAGE NO. 230	
HER LIN	FIRST LINE : SEAT NO., NAME OF T IES: SUB.CODE, SUB.NAME, SUB.TYPE,	MAX	.MARKS	, M	N.PAS	S MAR	KS, OE MARKS	, TH MARKS,	OBTAINED,	P/F:PASS/FAIL,	C:PREV.CARRY OVI
	8594 PATIL YATISH VISHWAS									, s140058	
SEM.:	1										
214441	DISCRETE STRUCTURES PP	100	40	38	37	75	Р				
214442	COMPUTER ORGANIZATION PP	100	40	39	34	73	Р				
214443	DIGITAL ELEX & LOGIC DESIGN PP	100	40	40	36	76	Р				
214444	FUNDAMENTAL OF DATASTRUCTURE PP	100	40	35	41	76	Р				
214445	PROB SOLVING & OBJ ORIENT PROGPP	100	40	33	19	52	Р				
214446	DIGITAL ELECTRONICS LABORATORYPR	50	20			42	Р				
214446	DIGITAL ELECTRONICS LABORATORYOR	50	20			43	Р				
214447	PROGRAMMING LABORATORY TW	50	20			47	Р				
214447	PROGRAMMING LABORATORY PR	50	20			40	Р				
	COMMINICATION & LANGUAGE LAB. TW FIRST SEM TOTAL = 572/750					48					
	8595 PATOLE BHAGYASHREE VIJAY									,s140058	
SEM.:	1										
214441	DISCRETE STRUCTURES PP	100	40	37	24	61	Р				
214442	COMPUTER ORGANIZATION PP	100	40	28	33	61	Р				
214443	DIGITAL ELEX & LOGIC DESIGN PP	100	40	38	29	67	Р				
214444	FUNDAMENTAL OF DATASTRUCTURE PP	100	40	29	24	53	Р				
214445	PROB SOLVING & OBJ ORIENT PROGPP	100	40	38	15	53	Р				
214446	DIGITAL ELECTRONICS LABORATORYPR	50	20			30	Р				
214446	DIGITAL ELECTRONICS LABORATORYOR	50	20			33	Р				
214447	PROGRAMMING LABORATORY TW	50	20			38	Р				
214447	PROGRAMMING LABORATORY PR	50	20			38	Р				
214448	COMMINICATION & LANGUAGE LAB. TW FIRST SEM TOTAL = 472/750	50	20			38	Р				
s14005	8596 PAWAR YASHVARDHAN MANOHAR				MANI			,71527485G	,PICT	,s140058	596
SEM.:	1										
214441		100	40	29	19	48	Р				
	COMPUTER ORGANIZATION PP			29	20	49					
214443	DIGITAL ELEX & LOGIC DESIGN PP			41	18	59					
214444	FUNDAMENTAL OF DATASTRUCTURE PP	100	40	39	35	74	Р				
214445	PROB SOLVING & OBJ ORIENT PROGPP	100	40	31	23	54	Р				
214446	DIGITAL ELECTRONICS LABORATORYPR	50	20			34	Р				
214446	DIGITAL ELECTRONICS LABORATORYOR	50	20			32	Р				
214447	PROGRAMMING LABORATORY TW	50	20			39	Р				
214447	PROGRAMMING LABORATORY PR	50	20			42	Р				
214448	COMMINICATION & LANGUAGE LAB. TW FIRST SEM TOTAL = 470/750	50	20			39	P				

	ETROT LINE & COAT NO NAME OF T							
	FIRST LINE : SEAT NO., NAME OF THE SUB.CODE, SUB.NAME, SUB.TYPE,			•		,	,	
	8597 PETKAR NISHANT KIRAN							,s140058597
SEM.:	1							
214441		100	40	30	19	58 P		
	COMPUTER ORGANIZATION PF			32	18	50 P		
214443	DIGITAL ELEX & LOGIC DESIGN PR			39	19	58 P		
214444	FUNDAMENTAL OF DATASTRUCTURE PR			33	27	60 P		
214445	PROB SOLVING & OBJ ORIENT PROGPE			33	24	57 P		
214446	DIGITAL ELECTRONICS LABORATORYPE			,,	24	37 P		
214446	DIGITAL ELECTRONICS LABORATORYPH					30 P		
214447	PROGRAMMING LABORATORY TV					40 P		
214447	PROGRAMMING LABORATORY PR					40 P 37 P		
	COMMINICATION & LANGUAGE LAB. TV					37 P		
214440	FIRST SEM TOTAL = 467/750	30	20			37 P		
	8598 PINGALE KANCHAN ASHOK					ULLATA		,s140058598
							·	
SEM.:								
214441				32	15	47 P		
214442	COMPUTER ORGANIZATION PR	100	40	34	24	58 P		
214443	DIGITAL ELEX & LOGIC DESIGN PR	100	40	38	23	61 P		
214444	FUNDAMENTAL OF DATASTRUCTURE PR	100	40	32	32	64 P		
214445	PROB SOLVING & OBJ ORIENT PROGPE	100	40	33	20	53 P		
214446	DIGITAL ELECTRONICS LABORATORYPE	50	20			38 P		
214446	DIGITAL ELECTRONICS LABORATORYOF	50	20			33 P		
214447	PROGRAMMING LABORATORY TV	50	20			41 P		
214447	PROGRAMMING LABORATORY PR	50	20			31 P		
214448	COMMINICATION & LANGUAGE LAB. TO FIRST SEM TOTAL = 469/750	50	20			43 P		
								, s140058599
CEM .	1							
SEM.: 214441		100	40	35	37	72 P		
214441				33 24	34	72 P 58 P		
214442	DIGITAL ELEX & LOGIC DESIGN PR			24 47		36 P 81 P		
214444				31	5 4 41	72 P		
214444	PROB SOLVING & OBJ ORIENT PROGPE			32	22	72 P 54 P		
214445				3	44	54 P 43 P		
	DIGITAL ELECTRONICS LABORATORYPE					43 P 42 P		
214446	DIGITAL ELECTRONICS LABORATORYOF							
214447	PROGRAMMING LABORATORY TV					47 P		
214447	PROGRAMMING LABORATORY PR					45 P		
Z14448	COMMINICATION & LANGUAGE LAB. TV FIRST SEM TOTAL = 561/750	50	2 0			47 P		

	E: 25 JAN 2016 CENTRE: PUNE						•			PAGE NO. 232	
NOTE:	FIRST LINE : SEAT NO., NAME OF T NES: SUB.CODE, SUB.NAME, SUB.TYPE,	HE C	ANDIDA	TE,	MOTHE	R, P	PERMANENT REG.	NO., PRE	VIOUS SEAT	NO., COLLEGE	, SEAT NO.
	58600 PRATEEK MALHOTRA									,\$14005	
SEM.:	.1										
214441		100	40	48	41	89	D				
214442				34	40	74					
214443	DIGITAL ELEX & LOGIC DESIGN PF			38	28	66					
214444	FUNDAMENTAL OF DATASTRUCTURE PF			38	25	63					
214445	PROB SOLVING & OBJ ORIENT PROGPE		_		26	62					
214446	DIGITAL ELECTRONICS LABORATORYPR					35					
214446	DIGITAL ELECTRONICS LABORATORYOR					44					
214447	PROGRAMMING LABORATORY TW					45					
214447											
	COMMINICATION & LANGUAGE LAB. TW					39					
	FIRST SEM TOTAL = 561/750										
s14005	58601 PRIYANKA FULZELE									, s14005	
SEM.:		100	40	20	22	71	_				
214441				39		71					
214442				27	15 21	42					
214443				38	21	59					
214444	FUNDAMENTAL OF DATASTRUCTURE PE			29 33	31 17	60 50					
214445 214446	PROB SOLVING & OBJ ORIENT PROGPF DIGITAL ELECTRONICS LABORATORYPR		_	33	17	12					
214446	DIGITAL ELECTRONICS LABORATORYPR		_			24					
214447	PROGRAMMING LABORATORY TW					29					
	PROGRAMMING LABORATORY PR					34					
	COMMINICATION & LANGUAGE LAB. TW					43					
214440	FIRST SEM TOTAL =/750	30	20			43	r				
	58602 PULIN CHAUDHARY			• • •						,S14005	
SEM.:	:1										
	DISCRETE STRUCTURES PF	100	40	32	15	47	Р				
	COMPUTER ORGANIZATION PF					43					
214443	DIGITAL ELEX & LOGIC DESIGN PF			31	22	53					
214444				24	25	49					
214445					27	43					
214446				-	· -	35					
214446	DIGITAL ELECTRONICS LABORATORYOR					30					
214447	PROGRAMMING LABORATORY TW					38					
	PROGRAMMING LABORATORY PR					32					
	COMMINICATION & LANGUAGE LAB. TW					35					
	FIRST SEM TOTAL = 405/750										

	29 S.E.(2014 PAT.)(INFORMATIOM T E: 25 JAN 2016 CENTRE: PUN		-	OF	COMPU	TER TECHNOLOGY	, PUNE.	PA	GE NO. 233
NOTE:	FIRST LINE : SEAT NO., NAME OF NES: SUB.CODE, SUB.NAME, SUB.TYPE	THE C	ANDIDA	TE,	мотні	ER, PERMANENT	REG. NO., PREV	OUS SEAT N	O., COLLEGE, SEAT NO.
	58603 PURVA CHAKKARWAR								,s140058603
SEM.:	:1								
214441	DISCRETE STRUCTURES P	Р 100	40	40	42	82 P			
214442	COMPUTER ORGANIZATION P	Р 100	40	32	26	58 P			
214443	DIGITAL ELEX & LOGIC DESIGN P	Р 100	40	38	35	73 P			
214444	FUNDAMENTAL OF DATASTRUCTURE P	Р 100	40	39	39	78 P			
214445	PROB SOLVING & OBJ ORIENT PROGP	Р 100	40	38	37	75 P			
214446	DIGITAL ELECTRONICS LABORATORYP	R 50	20			30 P			
214446	DIGITAL ELECTRONICS LABORATORYO	R 50	20			29 P			
214447	PROGRAMMING LABORATORY T	w 50	20			44 P			
214447	PROGRAMMING LABORATORY P	R 50	20			43 P			
214448	COMMINICATION & LANGUAGE LAB. T FIRST SEM TOTAL = 560/750	w 50	20			48 P			
	58604 RAHUL SUNIL CHAUDHARI								,s140058604
SEM.:									
214441					33	78 P			
214442					32	68 P			
214443	DIGITAL ELEX & LOGIC DESIGN P	P 100	40	35	30	65 P			
214444	FUNDAMENTAL OF DATASTRUCTURE P			40	31	71 P			
				29	32	61 P			
214446	DIGITAL ELECTRONICS LABORATORYP					36 P			
214446	DIGITAL ELECTRONICS LABORATORYO					39 P			
214447						46 P			
214447	PROGRAMMING LABORATORY P					44 P			
214448	COMMINICATION & LANGUAGE LAB. T FIRST SEM TOTAL = 551/750					43 P			
s14005	58605 RAJAT RAUSHAN				BIVA				,s140058605
SEM.:	:1								
214441	DISCRETE STRUCTURES P	P 100	40	41	37	78 P			
214442				19	28	47 P			
214443	DIGITAL ELEX & LOGIC DESIGN P	P 100	40	35	23	58 P			
214444	FUNDAMENTAL OF DATASTRUCTURE P	P 100	40	38	34	72 P			
214445	PROB SOLVING & OBJ ORIENT PROGP			31	16	47 P			
214446	DIGITAL ELECTRONICS LABORATORYP					15 F			
214446	DIGITAL ELECTRONICS LABORATORYO					24 P			
214447	PROGRAMMING LABORATORY T					37 P			
214447	PROGRAMMING LABORATORY P					31 P			
	COMMINICATION & LANGUAGE LAB. T FIRST SEM TOTAL =/750					34 P			

	9 S.E.(2014 PAT.)(INFORMATIOM TE :: 25 JAN 2016 CENTRE: PUNE		-	OF	COMPU	TER TECH	NOLOGY, PU	NE.	F	PAGE NO. 234	
NOTE:	FIRST LINE : SEAT NO., NAME OF T	HE CA	NDIDA	TE,	мотн	ER, PERM	IANENT REG.	NO., PRE	/IOUS SEAT	NO., COLLEGE,	SEAT NO.
OTHER LIN	IES: SUB.CODE, SUB.NAME, SUB.TYPE,	MAX.	MARKS	, MI	N. PAS	S MARKS,	OE MARKS,	TH MARKS,	OBTAINED,	P/F:PASS/FAIL, C:	PREV.CARRY OVER
	8606 RAMRAKHYA RAM KANHAIYA										
SEM.:	1										
	DISCRETE STRUCTURES PP	100	40	41	38	79 P					
	COMPUTER ORGANIZATION PP			38	34	72 P					
214443	DIGITAL ELEX & LOGIC DESIGN PP			42	28	70 P					
214444	FUNDAMENTAL OF DATASTRUCTURE PP	100	40	41	39	80 P					
214445	PROB SOLVING & OBJ ORIENT PROGPP	100	40	38	34	72 P					
214446	DIGITAL ELECTRONICS LABORATORYPR	50	20			42 P					
214446	DIGITAL ELECTRONICS LABORATORYOR	50	20			43 P					
214447	PROGRAMMING LABORATORY TW	50	20			47 P					
214447	PROGRAMMING LABORATORY PR	50	20			47 P					
214448	COMMINICATION & LANGUAGE LAB. TW FIRST SEM TOTAL = 596/750					44 P					
	8607 RANE ANURAG RAJENDRAKUMAR									,S140058607	
SEM.:	1										
	DISCRETE STRUCTURES PP	100	40	46	39	85 P					
	COMPUTER ORGANIZATION PP			36	30	66 P					
214443	DIGITAL ELEX & LOGIC DESIGN PP			43	24	67 P					
214444	FUNDAMENTAL OF DATASTRUCTURE PP			35	36	71 P					
	PROB SOLVING & OBJ ORIENT PROGPP			37	26	63 P					
214446	DIGITAL ELECTRONICS LABORATORYPR					39 P					
214446	DIGITAL ELECTRONICS LABORATORYOR		20			40 P					
214447	PROGRAMMING LABORATORY TW					47 P					
214447	PROGRAMMING LABORATORY PR	50	20			46 P					
214448	COMMINICATION & LANGUAGE LAB. TW	50	20			44 P					
	FIRST SEM TOTAL = 568/750										
	8608 RASHNEET KAUR RAJPAL				PRIT			71527520J			
52.005						-	,		,. ==:	, 22 . 33 3 3 3 3 3	
SEM.:	1										
214441	DISCRETE STRUCTURES PP	100	40	42	22	64 P					
214442	COMPUTER ORGANIZATION PP	100	40	36	24	60 P					
214443	DIGITAL ELEX & LOGIC DESIGN PP	100	40	44	33	77 P					
214444	FUNDAMENTAL OF DATASTRUCTURE PP	100	40	35	38	73 P					
214445	PROB SOLVING & OBJ ORIENT PROGPP	100	40	34	30	64 P					
214446	DIGITAL ELECTRONICS LABORATORYPR	50	20			8 F					
214446	DIGITAL ELECTRONICS LABORATORYOR	50	20			10 F					
214447	PROGRAMMING LABORATORY TW	50	20			40 P					
214447	PROGRAMMING LABORATORY PR	50	20			39 P					
214448	COMMINICATION & LANGUAGE LAB. TW	50	20			36 P					
	FIRST SEM TOTAL =/750										

	29 S.E.(2014 PAT.)(INFORMATIOM T E: 25 JAN 2016 CENTRE: PUN		-	COMPL	JTER TECHNOL	OGY, PUNE.	PΑ	GE NO. 235
NOTE:	FIRST LINE : SEAT NO., NAME OF NES: SUB.CODE, SUB.NAME, SUB.TYPE	THE CAN	DIDATE,	MOTH	IER, PERMANE	NT REG. NO., PREVI	OUS SEAT N	O., COLLEGE, SEAT NO.
	58609 RATNA PRABHA BHAIRAGOND					,71527527F		
SEM.:	:1							
214441	DISCRETE STRUCTURES P	P 100 4	0 44	44	88 P			
214442	COMPUTER ORGANIZATION P	P 100 4	0 34	21	55 P			
214443	DIGITAL ELEX & LOGIC DESIGN P	P 100 4	0 37	25	62 P			
214444	FUNDAMENTAL OF DATASTRUCTURE P	P 100 4	0 40	27	67 P			
214445	PROB SOLVING & OBJ ORIENT PROGP	P 100 4	0 40	17	57 P			
214446	DIGITAL ELECTRONICS LABORATORYP	R 50 2	0		37 P			
214446	DIGITAL ELECTRONICS LABORATORYO	R 50 2	0		29 P			
214447	PROGRAMMING LABORATORY T	w 50 2	0		47 P			
214447	PROGRAMMING LABORATORY P	R 50 2	0		41 P			
214448	COMMINICATION & LANGUAGE LAB. T FIRST SEM TOTAL = 526/750	w 50 2	0		43 P			
	50610 DAWALE DATMADEED CAUTAM							
S14005	58610 RAWALE RATNADEEP GAUTAM			KAJA	ANI	,/152/535G	,PICI	,S140058610
SEM.:	:1							
214441	DISCRETE STRUCTURES P	P 100 4	0 45	38	83 P			
214442	COMPUTER ORGANIZATION P	P 100 4	0 12	19	F			
214443	DIGITAL ELEX & LOGIC DESIGN P	P 100 4	0 33	24	57 P			
214444	FUNDAMENTAL OF DATASTRUCTURE P	P 100 4	0 29	21	50 P			
214445	PROB SOLVING & OBJ ORIENT PROGP	P 100 4	0 30	18	48 P			
214446	DIGITAL ELECTRONICS LABORATORYP	R 50 2	0		38 P			
214446	DIGITAL ELECTRONICS LABORATORYO	R 50 2	0		35 P			
214447	PROGRAMMING LABORATORY T	w 50 2	0		43 P			
214447		R 50 2	0		36 P			
	COMMINICATION & LANGUAGE LAB. T FIRST SEM TOTAL =/750				46 P			
s14005	58611 RAYOMAND KAIPUSHIN RAIMAL					,71527536E		
SEM.:	:1							
	DISCRETE STRUCTURES P	P 100 4	0 34	32	66 P			
214442				22	47 P			
214443	DIGITAL ELEX & LOGIC DESIGN P			28	67 P			
214444				31	62 P			
214445	PROB SOLVING & OBJ ORIENT PROGP			21	42 P			
214446	DIGITAL ELECTRONICS LABORATORYP				40 P			
214446	DIGITAL ELECTRONICS LABORATORYO				32 P			
214447	PROGRAMMING LABORATORY T				41 P			
214447					38 P			
	COMMINICATION & LANGUAGE LAB. T				43 P			
	FIRST SEM TOTAL = 478/750							

	: 25 JAN 2016 CENTRE: PUNI								
HER LIN	FIRST LINE : SEAT NO., NAME OF THES: SUB.CODE, SUB.NAME, SUB.TYPE	, MAX	. MARI	KS, MI	IN.PAS	S MARKS, OE	MARKS, TH MARKS,	OBTAINED,	P/F:PASS/FAIL, C:PREV.CARRY OV
	8612 RUDRAWAR SHRUTI SHRINIVAS								
SEM.:	1								
214441	DISCRETE STRUCTURES PI	100	40	28	25	53 P			
214442	COMPUTER ORGANIZATION PI	100	40	20	20	40 P			
214443	DIGITAL ELEX & LOGIC DESIGN P	100	40	33	25	58 P			
214444	FUNDAMENTAL OF DATASTRUCTURE PI	100	40	24	19	43 P			
214445	PROB SOLVING & OBJ ORIENT PROGP	100	40	24	16	40 P			
214446	DIGITAL ELECTRONICS LABORATORYPH	R 50	20			38 P			
214446	DIGITAL ELECTRONICS LABORATORYO	R 50	20			35 P			
214447	PROGRAMMING LABORATORY T	v 50	20			40 P			
214447	PROGRAMMING LABORATORY PI	R 50	20			30 P			
214448	COMMINICATION & LANGUAGE LAB. TO FIRST SEM TOTAL = $417/750$	v 50	20			40 P			
S14005	8613 SAGAR AMOL KOLHATKAR				ANUJ	А	,/152/548J	,PICT	,s140058613
SEM.:	1								
214441	DISCRETE STRUCTURES PI	100	40	40	39	79 P			
214442	COMPUTER ORGANIZATION P	100	40	17	23	40 P			
214443	DIGITAL ELEX & LOGIC DESIGN P	100	40	36	27	63 P			
214444	FUNDAMENTAL OF DATASTRUCTURE PI	100	40	25	27	52 P			
214445	PROB SOLVING & OBJ ORIENT PROGP	100	40	32	19	51 P			
214446	DIGITAL ELECTRONICS LABORATORYPH	R 50	20			37 P			
214446	DIGITAL ELECTRONICS LABORATORYO	R 50	20			35 P			
214447	PROGRAMMING LABORATORY T	v 50	20			39 P			
214447	PROGRAMMING LABORATORY PI	R 50	20			38 P			
214448	COMMINICATION & LANGUAGE LAB. TO FIRST SEM TOTAL = 469/750	v 50	20			35 P			
	8614 SAGAR PRIYADARSHNI SURESH					HA			
SEM.:	1								
36M 214441	DISCRETE STRUCTURES PI	2 100	40	35	28	63 P			
214442				24	18	42 P			
214443				37	31	68 P			
214444	FUNDAMENTAL OF DATASTRUCTURE PI			30	21	51 P			
214445	PROB SOLVING & OBJ ORIENT PROGPI			23	21	44 P			
214446	DIGITAL ELECTRONICS LABORATORYPI					36 P			
214446	DIGITAL ELECTRONICS LABORATORYO					15 F			
214447	PROGRAMMING LABORATORY TV					38 P			
214447	PROGRAMMING LABORATORY PI					7 F			
	COMMINICATION & LANGUAGE LAB. TO FIRST SEM TOTAL =/750					41 P			

	ETROT LINE & CEAT NO NAME OF T								
	FIRST LINE : SEAT NO., NAME OF THE SUB.CODE, SUB.NAME, SUB.TYPE,			•		•	ŕ		,
	8615 SAHOO MILIND NARENDRA						,71527550L		
SEM.:	1								
214441		100	40	41	28	69 P			
214442				35	32	67 P			
214443	DIGITAL ELEX & LOGIC DESIGN PR			42	26	68 P			
214444	FUNDAMENTAL OF DATASTRUCTURE PR			39	30	69 P			
214445	PROB SOLVING & OBJ ORIENT PROGPE			37	30	67 P			
214446	DIGITAL ELECTRONICS LABORATORYPE			31	30	39 P			
214446	DIGITAL ELECTRONICS LABORATORYOF					38 P			
214447	PROGRAMMING LABORATORY TV					42 P			
214447	PROGRAMMING LABORATORY PR					42 P			
	COMMINICATION & LANGUAGE LAB. TV					44 P			
214440	FIRST SEM TOTAL = 549/750	, 30	20			40 F			
									,s140058616
0000	5, 1, 5, 2, 1, 1, 1, 1, 1, 1, 1, 1, 1, 1, 1, 1, 1,				<i>57</i> 1 <i>5</i> .	, ,	,,	,. = 0.	,02.00000
SEM.:	1								
214441	DISCRETE STRUCTURES PF	100	40	43	24	67 P			
214442	COMPUTER ORGANIZATION PR	100	40	21	25	46 P			
214443	DIGITAL ELEX & LOGIC DESIGN PR	100	40	38	22	60 P			
214444	FUNDAMENTAL OF DATASTRUCTURE PR	100	40	38	25	63 P			
214445	PROB SOLVING & OBJ ORIENT PROGPE	100	40	40	18	58 P			
214446	DIGITAL ELECTRONICS LABORATORYPE	50	20			35 P			
214446	DIGITAL ELECTRONICS LABORATORYOF	50	20			29 P			
214447	PROGRAMMING LABORATORY TV	50	20			40 P			
214447	PROGRAMMING LABORATORY PR	50	20			38 P			
214448	COMMINICATION & LANGUAGE LAB. TV FIRST SEM TOTAL = 472/750	<i>i</i> 50	20			36 P			
						 HAN			, s140058617
CEM .	1								
SEM.: 214441		100	40	36	40	76 P			
214442				25	29	70 P 54 P			
214443	DIGITAL ELEX & LOGIC DESIGN PR			32	27	59 P			
214444	FUNDAMENTAL OF DATASTRUCTURE PR			39	33	72 P			
214445	PROB SOLVING & OBJ ORIENT PROGPE			28	27	72 P 55 P			
214446	DIGITAL ELECTRONICS LABORATORYPE			20	<i>L1</i>	33 P 32 P			
214446	DIGITAL ELECTRONICS LABORATORYPH					32 P 27 P			
214447	PROGRAMMING LABORATORY TV					27 P 43 P			
214447	PROGRAMMING LABORATORY PROGRAMMING LABORATORY PR					43 P 44 P			
Z14448	COMMINICATION & LANGUAGE LAB. TV FIRST SEM TOTAL = 503/750	ı 3U	20			41 P			

DATE	: 25 JAN 2016 CENTRE: PUN		OLOGY) STITU		COMPUT	TER TECHNOL	OGY, PUNE.	Р	AGE NO. 238
NOTE:	FIRST LINE : SEAT NO., NAME OF ES: SUB.CODE, SUB.NAME, SUB.TYPE	THE (CANDII	DATE,	MOTHE	ER, PERMANE	ENT REG. NO., PREV	IOUS SEAT	NO., COLLEGE, SEAT NO.
	8618 SANKET PRAKASH KULKARNI						,71527567E		
SEM.:	1								
214441	DISCRETE STRUCTURES F	P 100	40	35	38	73 P			
214442	COMPUTER ORGANIZATION F	P 100	40	29	25	54 P			
214443	DIGITAL ELEX & LOGIC DESIGN F	P 100	0 40	35	24	59 P			
214444	FUNDAMENTAL OF DATASTRUCTURE F	P 100	40	29	25	54 P			
214445	PROB SOLVING & OBJ ORIENT PROGR	P 100	0 40	32	35	67 P			
214446	DIGITAL ELECTRONICS LABORATORY	R 50	20			43 P			
214446	DIGITAL ELECTRONICS LABORATORYO	R 50	20			42 P			
214447	PROGRAMMING LABORATORY	w 50	20			32 P			
214447	PROGRAMMING LABORATORY F	R 50	20			44 P			
214448	COMMINICATION & LANGUAGE LAB. T FIRST SEM TOTAL = 512/750	W 50	20			44 P			
	8619 SANKPAL AKSHAYA MAHADEV					NIMA			,S140058619
CEM .	1								
SEM.:		n 100	. 40	40	20	60 5			
	DISCRETE STRUCTURES F			40	20	60 P			
	COMPUTER ORGANIZATION F			22 34	18	40 P			
214443	DIGITAL ELEX & LOGIC DESIGN F				15 21	49 P			
214444	FUNDAMENTAL OF DATASTRUCTURE F			27	21 15	48 P			
	PROB SOLVING & OBJ ORIENT PROGR			26	тэ	41 P			
214446	DIGITAL ELECTRONICS LABORATORY					10 F			
214446	DIGITAL ELECTRONICS LABORATORY					8 F			
214447	PROGRAMMING LABORATORY T					26 P			
214447	PROGRAMMING LABORATORY F					42 P			
214446	COMMINICATION & LANGUAGE LAB. T FIRST SEM TOTAL =/750					36 P			
s14005	8620 SARNOT SALONI ALIAS PUJA						,71527574н		,s140058620
SEM.:	1								
	DISCRETE STRUCTURES F	P 100	0 40	36	31	67 P			
214442	COMPUTER ORGANIZATION F			32	37	69 P			
	DIGITAL ELEX & LOGIC DESIGN F			46		76 P			
214444	FUNDAMENTAL OF DATASTRUCTURE F			41	36	77 P			
214445	PROB SOLVING & OBJ ORIENT PROGR			37	27	64 P			
214446	DIGITAL ELECTRONICS LABORATORY					45 P			
214446	DIGITAL ELECTRONICS LABORATORYO					46 P			
214447	PROGRAMMING LABORATORY					40 P			
214447	PROGRAMMING LABORATORY F					43 P			
	COMMINICATION & LANGUAGE LAB. T FIRST SEM TOTAL = 563/750					36 P			

	9 S.E.(2014 PAT.)(INFORMATIOM TE : 25 JAN 2016 CENTRE: PUNE		-		COMPU	TER -	TECHNOLOGY,	PUNE.	PAG	GE NO. 239	
NOTE:	FIRST LINE : SEAT NO., NAME OF T	HE C	ANDID	ATE,	МОТН	ER, I	PERMANENT R	EG. NO., PREV	IOUS SEAT NO	., COLLEGE,	SEAT NO.
OTHER LIN	IES: SUB.CODE, SUB.NAME, SUB.TYPE,	MAX	. MARK	S, MI	N.PAS	S MAI	RKS, OE MAR	KS, TH MARKS, (OBTAINED, P/	F:PASS/FAIL,	C:PREV.CARRY OVER
	8621 SATVIK RAJENDRA SHUKLA										
SEM.:	1										
	DISCRETE STRUCTURES PP	100	40	29	15	44	Р				
214442	COMPUTER ORGANIZATION PP	100	40	29	15	44					
214443	DIGITAL ELEX & LOGIC DESIGN PP			37	18	55	Р				
214444	FUNDAMENTAL OF DATASTRUCTURE PP	100	40	19	21	40	Р				
214445	PROB SOLVING & OBJ ORIENT PROGPP	100	40	24	18	42	Р				
214446	DIGITAL ELECTRONICS LABORATORYPR	50	20			22	Р				
214446	DIGITAL ELECTRONICS LABORATORYOR	50	20			7	F				
214447	PROGRAMMING LABORATORY TW	50	20			25	Р				
214447	PROGRAMMING LABORATORY PR	50	20			37	Р				
	COMMINICATION & LANGUAGE LAB. TW FIRST SEM TOTAL =/750					29					
	8622 SHAH ESHA JAYESH									,s1400586	
SEM.:	1										
214441	DISCRETE STRUCTURES PP	100	40	40	31	71	Р				
214442	COMPUTER ORGANIZATION PP	100	40	25	15	40	Р				
214443	DIGITAL ELEX & LOGIC DESIGN PP	100	40	31	16	47	Р				
214444	FUNDAMENTAL OF DATASTRUCTURE PP	100	40	27	22	49	Р				
214445	PROB SOLVING & OBJ ORIENT PROGPP	100	40	28	27	55	Р				
214446	DIGITAL ELECTRONICS LABORATORYPR	50	20			8	F				
214446	DIGITAL ELECTRONICS LABORATORYOR	50	20			28	Р				
214447	PROGRAMMING LABORATORY TW	50	20			24	Р				
214447	PROGRAMMING LABORATORY PR	50	20			4	F				
214448	COMMINICATION & LANGUAGE LAB. TW FIRST SEM TOTAL = $/750$	50	20			38	P				
S14005	8623 SHAH SHAKTI BHUPENDRA				LEEN	Α		,71633739〕	,PICT	,S1400586	23
SEM.:											
214441				42	42	84					
214442	COMPUTER ORGANIZATION PP			28	30	58					
214443	DIGITAL ELEX & LOGIC DESIGN PP			34	34	68					
214444	FUNDAMENTAL OF DATASTRUCTURE PP			37	36	73					
214445	PROB SOLVING & OBJ ORIENT PROGPP			36	39	75					
214446	DIGITAL ELECTRONICS LABORATORYPR					35					
214446	DIGITAL ELECTRONICS LABORATORYOR					38					
214447	PROGRAMMING LABORATORY TW					46					
214447	PROGRAMMING LABORATORY PR					45					
214448	COMMINICATION & LANGUAGE LAB. TW FIRST SEM TOTAL = 565/750	50	20			43	Р				

	9 S.E.(2014 PAT.)(INFORMATIOM E: 25 JAN 2016 CENTRE: PU			-	COMPU	TER TECHNOL	OGY, PUNE.	P	PAGE NO. 240
NOTE:	FIRST LINE : SEAT NO., NAME OF	THE	CAND	IDATE,	МОТН	ER, PERMANE	NT REG. NO., PREV	OUS SEAT	NO., COLLEGE, SEAT NO. P/F:PASS/FAIL, C:PREV.CARRY OVE
	8624 SHAIKH SHAHISTA SHOUKATA								,s140058624
SEM.:	1								
214441	DISCRETE STRUCTURES	PP 1	00 40	42	30	72 P			
214442	COMPUTER ORGANIZATION	PP 1	00 40	33	24	57 P			
214443	DIGITAL ELEX & LOGIC DESIGN	PP 1	00 40	41	30	71 P			
214444	FUNDAMENTAL OF DATASTRUCTURE	PP 1	00 40	38	25	63 P			
214445	PROB SOLVING & OBJ ORIENT PROG	PP 1	00 40	40	38	78 P			
214446	DIGITAL ELECTRONICS LABORATORY	'PR	50 20)		39 P			
214446	DIGITAL ELECTRONICS LABORATORY	OR	50 20)		41 P			
214447	PROGRAMMING LABORATORY	TW	50 20)		42 P			
214447	PROGRAMMING LABORATORY	PR	50 20)		40 P			
214448	COMMINICATION & LANGUAGE LAB. FIRST SEM TOTAL = 547/750	TW	50 20)		44 P			
	8625 SHAMBHARKAR SACHI DILIP								,s140058625
							·	·	•
SEM.:	1								
214441	DISCRETE STRUCTURES	PP 1	00 40	43	18	61 P			
214442	COMPUTER ORGANIZATION	PP 1	00 40	28	15	43 P			
214443	DIGITAL ELEX & LOGIC DESIGN	PP 1	00 40	36	19	55 P			
214444	FUNDAMENTAL OF DATASTRUCTURE	PP 1	00 40	31	22	53 P			
214445	PROB SOLVING & OBJ ORIENT PROG	PP 1	00 40	33	21	54 P			
214446	DIGITAL ELECTRONICS LABORATORY	'PR	50 20)		35 P			
214446	DIGITAL ELECTRONICS LABORATORY	OR	50 20)		21 P			
214447	PROGRAMMING LABORATORY	TW	50 20)		45 P			
214447	PROGRAMMING LABORATORY	PR	50 20)		32 P			
214448	COMMINICATION & LANGUAGE LAB. FIRST SEM TOTAL = 442/750	TW	50 20)		43 P			
	8626 SHELAR ASHWINI GANPAT								,S140058626
SEM.:	1								
	DISCRETE STRUCTURES	PP 1	00 40	34	18	52 P			
214442					05	F			
214443	DIGITAL ELEX & LOGIC DESIGN	PP 1	00 40	34	15	49 P			
214444	FUNDAMENTAL OF DATASTRUCTURE				21	48 P			
214445	PROB SOLVING & OBJ ORIENT PROG				18	43 P			
214446	DIGITAL ELECTRONICS LABORATORY					33 P			
214446	DIGITAL ELECTRONICS LABORATORY					14 F			
214447	PROGRAMMING LABORATORY					31 P			
214447	PROGRAMMING LABORATORY					32 P			
	COMMINICATION & LANGUAGE LAB. FIRST SEM TOTAL =/750					40 P			

	9 S.E.(2014 PAT.)(INFORMATIOM E: 25 JAN 2016 CENTRE: PU			-	COMPU	TER TECHNOL	OGY, PUNE.	Р	PAGE NO. 241
NOTE:	FIRST LINE : SEAT NO., NAME OF	THE	CAND	IDATE,	МОТН	ER, PERMANE	NT REG. NO., PREV	OUS SEAT	NO., COLLEGE, SEAT NO. P/F:PASS/FAIL, C:PREV.CARRY OVE
	8627 SHELAR HARSHADA AJAY						,71527597G		, \$140058627
SEM.:	1								
214441	DISCRETE STRUCTURES	PP 10	00 40	39	21	60 P			
214442	COMPUTER ORGANIZATION	PP 10	00 40	40	20	60 P			
214443	DIGITAL ELEX & LOGIC DESIGN	PP 10	00 40	35	26	61 P			
214444	FUNDAMENTAL OF DATASTRUCTURE	PP 10	00 40	31	29	60 P			
214445	PROB SOLVING & OBJ ORIENT PROG	PP 10	00 40	27	25	52 P			
214446	DIGITAL ELECTRONICS LABORATORY	PR !	50 20			39 P			
214446	DIGITAL ELECTRONICS LABORATORY	OR !	50 20			37 P			
214447	PROGRAMMING LABORATORY	TW 5	50 20			39 P			
214447	PROGRAMMING LABORATORY	PR !	50 20			44 P			
214448	COMMINICATION & LANGUAGE LAB. FIRST SEM TOTAL = 489/750	TW !	50 20			37 P			
	8628 SHINDE AKSHAY RAMCHANDRA					HADA			,s140058628
CEM .	1								
SEM.:		DD 1/	00 40	42	20	01 5			
214441					39	81 P			
214442					37 26	65 P			
214443	DIGITAL ELEX & LOGIC DESIGN				26	71 P			
214444	FUNDAMENTAL OF DATASTRUCTURE PROB SOLVING & OBJ ORIENT PROG				39 24	79 P			
				34	34	68 P			
214446	DIGITAL ELECTRONICS LABORATORY					42 P			
214446	DIGITAL ELECTRONICS LABORATORY					45 P			
214447	PROGRAMMING LABORATORY					46 P			
214447	PROGRAMMING LABORATORY					42 P			
214446	COMMINICATION & LANGUAGE LAB. FIRST SEM TOTAL = 583/750	IW ;	50 20			44 P			
s14005	8629 SHINDE SARVADA SURENDRA						,71633740в		,s140058629
SEM.:	1								
214441	DISCRETE STRUCTURES	PP 10	00 40	37	37	74 P			
214442					23	47 P			
214443	DIGITAL ELEX & LOGIC DESIGN	PP 10	00 40	41	35	76 P			
214444	FUNDAMENTAL OF DATASTRUCTURE	PP 10	00 40	30	34	64 P			
214445	PROB SOLVING & OBJ ORIENT PROG				41	73 P			
214446	DIGITAL ELECTRONICS LABORATORY					28 P			
214446	DIGITAL ELECTRONICS LABORATORY					30 P			
214447	PROGRAMMING LABORATORY					41 P			
214447	PROGRAMMING LABORATORY					35 P			
	COMMINICATION & LANGUAGE LAB. FIRST SEM TOTAL = 514/750					46 P			

	9 S.E.(2014 PAT.)(INFORMATIOM T E: 25 JAN 2016 CENTRE: PUN			-	COMPL	TER T	ECHNOLOGY, P	UNE.		PAGE NO. 242	
NOTE:	FIRST LINE : SEAT NO., NAME OF NES: SUB.CODE, SUB.NAME, SUB.TYPE	THE	CAND	IDATE,	MOTH	IER, P	ERMANENT REG	. NO., PRE	VIOUS SEAT	NO., COLLEGE,	SEAT NO.
	8630 SHIRORE CHINMAY PURUSHOTT										
SEM.:	1										
214441	DISCRETE STRUCTURES F	P 10	00 40	44	33	77	Р				
214442	COMPUTER ORGANIZATION F	P 10	00 40	33	19	52	Р				
214443	DIGITAL ELEX & LOGIC DESIGN F	P 10	00 40	39	21	60	Р				
214444	FUNDAMENTAL OF DATASTRUCTURE F	P 10	00 40	36	20	56	Р				
214445	PROB SOLVING & OBJ ORIENT PROGR	P 10	00 40	35	37	72	Р				
214446	DIGITAL ELECTRONICS LABORATORYF	R !	50 20			42	Р				
214446	DIGITAL ELECTRONICS LABORATORYO	R !	50 20			41	Р				
214447	PROGRAMMING LABORATORY T	W 5	50 20			45	Р				
214447	PROGRAMMING LABORATORY F	R !	50 20			45	Р				
214448	COMMINICATION & LANGUAGE LAB. T FIRST SEM TOTAL = 529/750	W !	50 20			39	Р				
	8631 SHREYA GALANI									, s140058	
CEM .	1										
SEM.:	DISCRETE STRUCTURES F	ND 16	00 40	42	17	60	D				
	COMPUTER ORGANIZATION F				17 15	60 50					
214443	DIGITAL ELEX & LOGIC DESIGN F				24	50 65					
					29	66					
214444	FUNDAMENTAL OF DATASTRUCTURE F PROB SOLVING & OBJ ORIENT PROGF				26	56					
				30	20						
214446	DIGITAL ELECTRONICS LABORATORYF DIGITAL ELECTRONICS LABORATORYC					30					
214446						37					
214447	PROGRAMMING LABORATORY T					35					
214447	PROGRAMMING LABORATORY F COMMINICATION & LANGUAGE LAB. T					43					
214440	FIRST SEM TOTAL = 477/750	w .	50 20			35	r				
s14005	8632 SHREYA MALVIYA									,s140058	
SEM.:	1										
214441	DISCRETE STRUCTURES F	P 10	00 40	44	30	74	Р				
214442	COMPUTER ORGANIZATION F	P 10	00 40	30	17	47	Р				
214443	DIGITAL ELEX & LOGIC DESIGN F	P 10	00 40	32	15	47	Р				
214444	FUNDAMENTAL OF DATASTRUCTURE F	P 10	00 40	23	18	41	Р				
214445	PROB SOLVING & OBJ ORIENT PROGR	P 10	00 40	24	20	44	Р				
214446	DIGITAL ELECTRONICS LABORATORYF	PR !	50 20			7	F				
214446	DIGITAL ELECTRONICS LABORATORYO	R !	50 20			9	F				
214447	PROGRAMMING LABORATORY T	W :	50 20			36	Р				
214447	PROGRAMMING LABORATORY F	R !	50 20			30	Р				
214448	COMMINICATION & LANGUAGE LAB. T FIRST SEM TOTAL = $/750$	W :	50 20			30	Р				

	E: 25 JAN 2016 CENTRE: PU								
HER LIN	FIRST LINE : SEAT NO., NAME OF IES: SUB.CODE, SUB.NAME, SUB.TYF	PE, MAX	X.MAR	KS, MI	IN.PAS	SS MARKS, OE	MARKS, TH MARKS,	OBTAINED,	P/F:PASS/FAIL, C:PREV.CARRY OV
	8633 SIRMAGADUM AISHWARYA RA								
SEM.:	1								
214441	DISCRETE STRUCTURES	PP 100	0 40	45	24	69 P			
214442	COMPUTER ORGANIZATION	PP 100	0 40	34	24	58 P			
214443	DIGITAL ELEX & LOGIC DESIGN	PP 100	0 40	40	26	66 P			
214444	FUNDAMENTAL OF DATASTRUCTURE	PP 100	0 40	35	33	68 P			
214445	PROB SOLVING & OBJ ORIENT PROC	PP 10	0 40	38	25	63 P			
214446	DIGITAL ELECTRONICS LABORATORY	′PR 50	0 20			35 P			
214446	DIGITAL ELECTRONICS LABORATORY	'OR 50	0 20			32 P			
214447	PROGRAMMING LABORATORY	TW 50	0 20			44 P			
214447	PROGRAMMING LABORATORY	PR 50	0 20			41 P			
214448	COMMINICATION & LANGUAGE LAB. FIRST SEM TOTAL = 519/750	TW 50	0 20			43 P			
	20024								
S14005	8634 SOMWANSHI RUSHIL AJAY				VALS	HALI	,71527629J	,PICI	,S140058634
SEM.:	1								
214441	DISCRETE STRUCTURES	PP 100	0 40	44	31	75 P			
214442	COMPUTER ORGANIZATION	PP 10	0 40	33	17	50 P			
214443	DIGITAL ELEX & LOGIC DESIGN	PP 100	0 40	40	24	64 P			
214444	FUNDAMENTAL OF DATASTRUCTURE	PP 100	0 40	39	35	74 P			
214445	PROB SOLVING & OBJ ORIENT PROC	SPP 100	0 40	41	34	75 P			
214446	DIGITAL ELECTRONICS LABORATORY	′PR 50	0 20			32 P			
214446	DIGITAL ELECTRONICS LABORATORY	'OR 50	0 20			32 P			
214447	PROGRAMMING LABORATORY	TW 50	20			47 P			
214447	PROGRAMMING LABORATORY	PR 50	20			42 P			
214448	COMMINICATION & LANGUAGE LAB. FIRST SEM TOTAL = 537/750	TW 50	0 20			46 P			
	8635 SONAWANE PRAJAKTA PRAVIN					NAKSHI			
SEM.:	1								
214441		PP 100	0 40	38	23	61 P			
214442		PP 100		18	11	F			
214443				37	18	55 P			
214444	FUNDAMENTAL OF DATASTRUCTURE			31	35	66 P			
214445	PROB SOLVING & OBJ ORIENT PROC			30	33	63 P			
214446	DIGITAL ELECTRONICS LABORATORY			-		21 P			
214446	DIGITAL ELECTRONICS LABORATORY					28 P			
214447	PROGRAMMING LABORATORY					37 P			
214447	PROGRAMMING LABORATORY					8 F			
	COMMINICATION & LANGUAGE LAB. FIRST SEM TOTAL =/750					35 P			

	: 25 JAN 2016 CENTRE: P											10. 244	
NOTE:	FIRST LINE : SEAT NO., NAME O	F TH	HE CA	ANDID	ATE,	MOTHE	R,	PERMANENT REG	i. NO., PF	REVIOUS SEAT	NO.,	COLLEGE,	SEAT NO.
	IES: SUB.CODE, SUB.NAME, SUB.TY												
	8636 SUHAVAN GUPTA									,PICT			
CEM .	1												
SEM.:		DD	100	40	47	25	0.7	В					
	DISCRETE STRUCTURES COMPUTER ORGANIZATION				47	35 31	82 71						
214442 214443	DIGITAL ELEX & LOGIC DESIGN				45	35	80						
214444	FUNDAMENTAL OF DATASTRUCTURE				43	3 <i>3</i>	80						
214445	PROB SOLVING & OBJ ORIENT PRO				45	38	83						
214446	DIGITAL ELECTRONICS LABORATOR				43	30	38						
214446	DIGITAL ELECTRONICS LABORATOR						31						
214447	PROGRAMMING LABORATORY						42						
214447	PROGRAMMING LABORATORY												
	COMMINICATION & LANGUAGE LAB.						34						
214440	FIRST SEM TOTAL = 583/750												
s14005	8637 SUMEET KUMAR							DEVI					
SEM.:	1												
	DISCRETE STRUCTURES	DD	100	40	42	20	62	D					
214442	COMPUTER ORGANIZATION				29	15	44						
214443	DIGITAL ELEX & LOGIC DESIGN				35	22	57						
214444	FUNDAMENTAL OF DATASTRUCTURE				30	34	64						
214445	PROB SOLVING & OBJ ORIENT PRO				37	36	73						
214446	DIGITAL ELECTRONICS LABORATOR				51	30	24						
214446	DIGITAL ELECTRONICS LABORATOR						27						
214447	PROGRAMMING LABORATORY						40						
214447	PROGRAMMING LABORATORY						30						
	COMMINICATION & LANGUAGE LAB.						42						
214440	FIRST SEM TOTAL = 463/750	1 00	30	20			72	•					
	8638 TAPADIYA AMRUTA SHAMSUN									,PICT			
SEM.:	1												
	DISCRETE STRUCTURES	PP	100	40	44	28	72	Р					
	COMPUTER ORGANIZATION				28	16	44						
214443	DIGITAL ELEX & LOGIC DESIGN				38	25	63						
214444	FUNDAMENTAL OF DATASTRUCTURE				40	29	69						
214445	PROB SOLVING & OBJ ORIENT PRO				35	36	71						
214446	DIGITAL ELECTRONICS LABORATOR				-	-	35						
214446	DIGITAL ELECTRONICS LABORATOR						35						
214447	PROGRAMMING LABORATORY						45						
214447	PROGRAMMING LABORATORY						43						
	COMMINICATION & LANGUAGE LAB.						41						

NOTE: FI OTHER LINES	RST LINE: SEAT NO., NAME OF TENTE SUB.CODE, SUB.NAME, SUB.TYPE	THE CA	ANDID .MARK	ATE,				
S1400586 SEM.:1				- ,	IN.PAS	S MARKS, OE	·	
							,71527656F	
214441 D								
	ISCRETE STRUCTURES PI	100	40	46	36	82 P		
214442 C	OMPUTER ORGANIZATION P	100	40	40	28	68 P		
214443 D	IGITAL ELEX & LOGIC DESIGN P	100	40	48	29	77 P		
214444 F	UNDAMENTAL OF DATASTRUCTURE PI	100	40	37	33	70 P		
214445 P	ROB SOLVING & OBJ ORIENT PROGP	100	40	47	36	83 P		
214446 D	IGITAL ELECTRONICS LABORATORYPI	R 50	20			42 P		
214446 D	IGITAL ELECTRONICS LABORATORYO	₹ 50	20			41 P		
214447 P	ROGRAMMING LABORATORY T	w 50	20			47 P		
214447 P	ROGRAMMING LABORATORY PI	R 50	20			47 P		
	OMMINICATION & LANGUAGE LAB. T IRST SEM TOTAL = 603/750	w 50	20			46 P		
	40 TEJAS SANDESH KULKARNI							,s140058640
сгм .1								
SEM.:1	ISCRETE STRUCTURES P	- 100	40	20	27	66 5		
	OMPUTER ORGANIZATION PI			25	27 27	66 P 52 P		
	IGITAL ELEX & LOGIC DESIGN P			36	27	63 P		
	UNDAMENTAL OF DATASTRUCTURE PI			42	35	77 P		
	ROB SOLVING & OBJ ORIENT PROGP			40	27	77 P 67 P		
	IGITAL ELECTRONICS LABORATORYPI			40	21	07 P 26 P		
_	IGITAL ELECTRONICS LABORATORYPHICATION OF THE SECOND SECON		_			20 F 13 F		
	ROGRAMMING LABORATORY T							
			-			37 P		
	ROGRAMMING LABORATORY PI OMMINICATION & LANGUAGE LAB. TV					40 P 36 P		
	IRST SEM TOTAL =/750							
s1400586	41 TEJOMURTULA ANIRUDDH SHANI						,71527659L	
SEM.:1								
	ISCRETE STRUCTURES PI	P 100	40	49	36	85 P		
	OMPUTER ORGANIZATION PI			32	17	49 P		
	IGITAL ELEX & LOGIC DESIGN P			44	23	67 P		
	UNDAMENTAL OF DATASTRUCTURE PI			40	26	66 P		
	ROB SOLVING & OBJ ORIENT PROGP			30	35	65 P		
	IGITAL ELECTRONICS LABORATORYPI					34 P		
	IGITAL ELECTRONICS LABORATORYO					32 P		
	ROGRAMMING LABORATORY T					37 P		
	ROGRAMMING LABORATORY PI					47 P		
214448 C	OMMINICATION & LANGUAGE LAB. TO IRST SEM TOTAL = 515/750					33 P		

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NOTE:	FIRST LINE : SEAT NO., NAME OF TES: SUB.CODE, SUB.NAME, SUB.TYPE,	HE CA	ANDID	ATE,	MOTHE	R, P	ERMANENT REG	. NO., PRE	VIOUS SEAT	NO., COLLEGE,	SEAT NO.
	8642 TOTALA CHAITALI LAXIMIKANT										
CEM .	1										
SEM.:		100	40	2.4	22	cc	D				
214441				34		66 46					
214442	COMPUTER ORGANIZATION PP DIGITAL ELEX & LOGIC DESIGN PP			26 40	20 19	46 59					
214444	FUNDAMENTAL OF DATASTRUCTURE PP			32	34	66					
214444				32	45	77					
	PROB SOLVING & OBJ ORIENT PROGPP			32	43						
214446	DIGITAL ELECTRONICS LABORATORYPR					34					
214446	DIGITAL ELECTRONICS LABORATORYOR					28					
214447	PROGRAMMING LABORATORY TW					40					
214447	PROGRAMMING LABORATORY PR					40					
	COMMINICATION & LANGUAGE LAB. TW FIRST SEM TOTAL = 499/750					43					
	8643 TRUPTI BHAMARE									,s140058	
CEM .	1										
SEM.:		100	40	27	20	C٢	Б				
214441				37		65					
214442				34	22	56					
214443	DIGITAL ELEX & LOGIC DESIGN PP			41	18	59					
214444	FUNDAMENTAL OF DATASTRUCTURE PP			32	24	56					
214445				32	32	64					
214446	DIGITAL ELECTRONICS LABORATORYPR					41					
214446	DIGITAL ELECTRONICS LABORATORYOR					38					
214447		50				44					
214447	PROGRAMMING LABORATORY PR					33					
214448	COMMINICATION & LANGUAGE LAB. TW FIRST SEM TOTAL = 499/750	50	20			43	Р				
s14005	8644 UGHADE TEJAL ASHOK				VINIT			,71527678G	,PICT	,s140058	644
SEM.:	1										
214441	DISCRETE STRUCTURES PP	100	40	41	22	63	Р				
214442	COMPUTER ORGANIZATION PP	100	40	27	08		F				
214443	DIGITAL ELEX & LOGIC DESIGN PP	100	40	27	27	54	Р				
214444	FUNDAMENTAL OF DATASTRUCTURE PP	100	40	36	27	63	Р				
214445	PROB SOLVING & OBJ ORIENT PROGPP	100	40	31	27	58	Р				
214446	DIGITAL ELECTRONICS LABORATORYPR	50	20			42	Р				
214446	DIGITAL ELECTRONICS LABORATORYOR	50	20			37	Р				
214447	PROGRAMMING LABORATORY TW					37					
214447	PROGRAMMING LABORATORY PR					33					
	COMMINICATION & LANGUAGE LAB. TW FIRST SEM TOTAL =/750					39					

DATE	9 S.E.(2014 PAT.)(INFORMATIOM E: 25 JAN 2016 CENTRE: P	UNE :	INSTI	TUTE OI					AGE NO. 247
NOTE:	FIRST LINE : SEAT NO., NAME O	F TH	E CAN	IDIDATE	, мот	HER, PERMANE	ENT REG. NO., PF	REVIOUS SEAT	NO., COLLEGE, SEAT NO.
	8645 VAIBHAV LOHANI								,s140058645
SEM.:	1								
214441	DISCRETE STRUCTURES	PP :	100 4	0 45	15	60 P			
214442	COMPUTER ORGANIZATION	PP	100 4	0 41	20	61 P			
214443	DIGITAL ELEX & LOGIC DESIGN	PP :	100 4	10 49	33	82 P			
214444	FUNDAMENTAL OF DATASTRUCTURE	PP :	100 4	40	23	63 P			
214445	PROB SOLVING & OBJ ORIENT PRO	GPP :	100 4	10 42	37	79 P			
214446	DIGITAL ELECTRONICS LABORATOR	YPR	50 2	20		22 P			
214446	DIGITAL ELECTRONICS LABORATOR	YOR	50 2	20		35 P			
214447	PROGRAMMING LABORATORY	TW	50 2	20		41 P			
214447	PROGRAMMING LABORATORY	PR	50 2	20		35 P			
214448	COMMINICATION & LANGUAGE LAB. FIRST SEM TOTAL = 509/750	TW	50 2	20		31 P			
	8646 VEER POOJA LAXMAN					ISHA			,S140058646
SEM.:	1								
214441		PP	100 4	10 34	23	57 P			
214442						46 P			
214443	DIGITAL ELEX & LOGIC DESIGN					61 P			
214444	FUNDAMENTAL OF DATASTRUCTURE					73 P			
	PROB SOLVING & OBJ ORIENT PRO					80 P			
214446	DIGITAL ELECTRONICS LABORATOR					35 P			
214446	DIGITAL ELECTRONICS LABORATOR					32 P			
214447	PROGRAMMING LABORATORY					47 P			
214447	PROGRAMMING LABORATORY			-		40 P			
	COMMINICATION & LANGUAGE LAB. FIRST SEM TOTAL = 518/750					47 P			
s14005	8647 WADJE SHASHANK VENKATRA								,S140058647
SEM.:	1								
214441		PP :	100 4	10 43	25	68 P			
214442						43 P			
214443	DIGITAL ELEX & LOGIC DESIGN					62 P			
214444	FUNDAMENTAL OF DATASTRUCTURE					55 P			
214445	PROB SOLVING & OBJ ORIENT PRO				31	69 P			
214446	DIGITAL ELECTRONICS LABORATOR					32 P			
214446	DIGITAL ELECTRONICS LABORATOR					35 P			
214447	PROGRAMMING LABORATORY					43 P			
214447	PROGRAMMING LABORATORY					42 P			
	COMMINICATION & LANGUAGE LAB. FIRST SEM TOTAL = 486/750					37 P			

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	FIRST LINE : SEAT NO., NAME OF T									
OTHER LIN	IES: SUB.CODE, SUB.NAME, SUB.TYPE,	MAX.MA	ARKS, M	IN.PAS	SS MARKS, OE	MARKS, TH MARKS, O	OBTAINED, P	/F:PASS/FAIL, C:PREV.CARRY OVER		
S14005	8648 WAJIRE PANKAJ PANDURANG			GOK/	ARNA	,71527699к	,PICT	,s140058648		
SEM.:										
	DISCRETE STRUCTURES PP			_	45 P					
	COMPUTER ORGANIZATION PP			05	F					
214443	DIGITAL ELEX & LOGIC DESIGN PP			18	55 P					
214444	FUNDAMENTAL OF DATASTRUCTURE PP		_	34	62 P					
214445	PROB SOLVING & OBJ ORIENT PROGPP			30	56 P					
214446	DIGITAL ELECTRONICS LABORATORYPR				24 P					
214446	DIGITAL ELECTRONICS LABORATORYOR				27 P					
214447	PROGRAMMING LABORATORY TW				35 P					
214447	PROGRAMMING LABORATORY PR				29 P					
214448	COMMINICATION & LANGUAGE LAB. TW FIRST SEM TOTAL = $/750$	50 20)		39 P					
	8649 WATTAMWAR SUYASH SANJEEV							,s140058649		
SEM.:										
	DISCRETE STRUCTURES PP			21						
	COMPUTER ORGANIZATION PP			20	40 P					
214443	DIGITAL ELEX & LOGIC DESIGN PP			21	52 P					
214444	FUNDAMENTAL OF DATASTRUCTURE PP			33	62 P					
	PROB SOLVING & OBJ ORIENT PROGPP) 35	27	62 P					
214446	DIGITAL ELECTRONICS LABORATORYPR				12 F					
214446	DIGITAL ELECTRONICS LABORATORYOR				23 P					
214447	PROGRAMMING LABORATORY TW				33 P					
214447	PROGRAMMING LABORATORY PR				35 P					
214448	COMMINICATION & LANGUAGE LAB. TW FIRST SEM TOTAL = $/750$	50 20)		26 P					
S14005	8650 WAYDANDE SNEHAL RAJARAM			KAV:	ITA	,71633743G	,PICT	,S140058650		
SEM.:	1									
214441		100 40) 24	16	40 P					
	COMPUTER ORGANIZATION PP			17	41 P					
214443				22	49 P					
214444	FUNDAMENTAL OF DATASTRUCTURE PP			20	48 P					
214445	PROB SOLVING & OBJ ORIENT PROGPP			29	57 P					
214446	DIGITAL ELECTRONICS LABORATORYPR			-	22 P					
214446	DIGITAL ELECTRONICS LABORATORYOR				21 P					
214447	PROGRAMMING LABORATORY TW				30 P					
214447	PROGRAMMING LABORATORY PR				30 P					
214448	COMMINICATION & LANGUAGE LAB. TW				34 P					
	FIRST SEM TOTAL = 372/750	30 20								

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FIRST SEM TOTAL = --/750

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	N 2016 CENTRE: PI										PAGE NO. 249	
NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: SUB.CODE, SUB.NAME, SUB.TYPE, MAX.MARKS, MIN.PASS MARKS, OE MARKS, TH MARKS, OBTAINED, P/F:PASS/FAIL, C:PREV.CARRY OVER												
	AGTAP RAHUL JAIRAJ								,71400882		,s140058	
SEM.:1												
214441 DISCRETE	STRUCTURES	PP	100	40 2	28	03		F				
214442 COMPUTER	R ORGANIZATION	PP	100	40 1	17	06		F				
214443 DIGITAL	ELEX & LOGIC DESIGN	PP	100	40 3	30	15	45	Р				
214444 FUNDAMEN	NTAL OF DATASTRUCTURE	PP	100	40 2	25	16	41	Р				
214445 PROB SOI	VING & OBJ ORIENT PRO	GPP	100	40 1	18	11		F				
214446 DIGITAL	ELECTRONICS LABORATOR	YPR	50	20			AA	F				
214446 DIGITAL	ELECTRONICS LABORATOR	YOR	50	20			AA	F				
214447 PROGRAMM	MING LABORATORY	TW	50	20			20	Р				
214447 PROGRAMM	MING LABORATORY	PR	50	20			AA	F				
214448 COMMINIO	CATION & LANGUAGE LAB.	TW	50	20			21	Р				