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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.

      OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S8053001   ABDULE MONALI MUKUND   SANDHYA   , 71045354L   , S8053001 , PICT   , S8053001

01. SIGNAL AND SYSTEMS	PP	100	40	40	P C		11. ENGINEERING MATHEMATICS III	PP	100	40	75	P
02. SIGNAL AND SYSTEMS	OR	50	20	32	P C		12. ENGINEERING MATHEMATICS III	TW	25	10	24	P
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	40	P C		13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	40	P
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	40	P C		14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	30	P
05. NETWORK ANALYSIS	PP	100	40	40	P C		15. ELECTROMAGNETIC	PP	100	40	55	P
06. DIGITAL LOGIC DESIGN	PP	100	40	49	P C		16. ELECTROMAGNETIC	TW	25	10	16	P
07. DIGITAL LOGIC DESIGN	PR	50	20	35	P C		17. DATA STRUCTURES	PP	100	40	56	P
08. POWER DEVICES AND MACHINES	PP	100	40	42	P C		18. DATA STRUCTURES	PR	50	20	34	P
09. NETWORK AND POWER LAB.	TW	50	20	42	P C		19. COMMUNICATION THEORY	PP	100	40	42	P
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	41	P C		20. COMMUNICATION THEORY	OR	50	20	32	P
							21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	39	P

GRAND TOTAL = 844/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

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S8053002   ABHINIT KUMAR   ASHA DEVI   , 71050778L   , S8053002 , PICT   , S8053002

01. SIGNAL AND SYSTEMS	PP	100	40	70	P C		11. ENGINEERING MATHEMATICS III	PP	100	40	75	P
02. SIGNAL AND SYSTEMS	OR	50	20	30	P C		12. ENGINEERING MATHEMATICS III	TW	25	10	23	P
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	50	P C		13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	63	P
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	20	P C		14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	29	P
05. NETWORK ANALYSIS	PP	100	40	50	P C		15. ELECTROMAGNETIC	PP	100	40	60	P
06. DIGITAL LOGIC DESIGN	PP	100	40	50	P C		16. ELECTROMAGNETIC	TW	25	10	24	P

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07. DIGITAL LOGIC DESIGN	PR	50	20	21	P		17. DATA STRUCTURES	PP	100	40	43	P
08. POWER DEVICES AND MACHINES	PP	100	40	45	P	C	18. DATA STRUCTURES	PR	50	20	38	P
09. NETWORK AND POWER LAB.	TW	50	20	39	P	C	19. COMMUNICATION THEORY	PP	100	40	68	P
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	36	P	C	20. COMMUNICATION THEORY	OR	50	20	40	P
							21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	36	P

GRAND TOTAL = 910/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

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S8053003   ABHISHEK ANAND   INDRANI   , 71045356G   , S8053003 ,   PICT   , S8053003

01. SIGNAL AND SYSTEMS	PP	100	40	46	P	C	11. ENGINEERING MATHEMATICS III	PP	100	40	44	P
02. SIGNAL AND SYSTEMS	OR	50	20	34	P	C	12. ENGINEERING MATHEMATICS III	TW	25	10	16	P
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	40	P	C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	47	P
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	20	P		14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	35	P
05. NETWORK ANALYSIS	PP	100	40	40	P	C	15. ELECTROMAGNETIC	PP	100	40	48	P
06. DIGITAL LOGIC DESIGN	PP	100	40	40	P	C	16. ELECTROMAGNETIC	TW	25	10	17	P
07. DIGITAL LOGIC DESIGN	PR	50	20	30	P	C	17. DATA STRUCTURES	PP	100	40	49	P
08. POWER DEVICES AND MACHINES	PP	100	40	46	P		18. DATA STRUCTURES	PR	50	20	35	P
09. NETWORK AND POWER LAB.	TW	50	20	33	P	C	19. COMMUNICATION THEORY	PP	100	40	44	P
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	35	P	C	20. COMMUNICATION THEORY	OR	50	20	38	P
							21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	28	P

GRAND TOTAL = 765/1500, RESULT: SECOND CLASS

ORDN. 1 MARKS :

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 NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.

       OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S8053004   AMIT KUMAR   RITA   , 71045367B   , S8053004 ,   PICT   , S8053004

01. SIGNAL AND SYSTEMS	PP	100	40	83	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	91	P
02. SIGNAL AND SYSTEMS	OR	50	20	30	P	12. ENGINEERING MATHEMATICS III	TW	25	10	18	P
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	63	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	70	P
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	21	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	23	P
05. NETWORK ANALYSIS	PP	100	40	57	P C	15. ELECTROMAGNETIC	PP	100	40	57	P
06. DIGITAL LOGIC DESIGN	PP	100	40	45	P C	16. ELECTROMAGNETIC	TW	25	10	20	P
07. DIGITAL LOGIC DESIGN	PR	50	20	39	P C	17. DATA STRUCTURES	PP	100	40	65	P
08. POWER DEVICES AND MACHINES	PP	100	40	49	P C	18. DATA STRUCTURES	PR	50	20	32	P
09. NETWORK AND POWER LAB.	TW	50	20	35	P C	19. COMMUNICATION THEORY	PP	100	40	77	P
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	35	P C	20. COMMUNICATION THEORY	OR	50	20	38	P
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	32	P

GRAND TOTAL = 980/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

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S8053005   ANAGH PAL   MANDIRA   , 71045368L   , S8053005 ,   PICT   , S8053005

01. SIGNAL AND SYSTEMS	PP	100	40	88	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	75	P
02. SIGNAL AND SYSTEMS	OR	50	20	43	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	19	P
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	67	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	54	P
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	41	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	35	P
05. NETWORK ANALYSIS	PP	100	40	70	P C	15. ELECTROMAGNETIC	PP	100	40	72	P
06. DIGITAL LOGIC DESIGN	PP	100	40	51	P C	16. ELECTROMAGNETIC	TW	25	10	23	P
07. DIGITAL LOGIC DESIGN	PR	50	20	44	P C	17. DATA STRUCTURES	PP	100	40	49	P
08. POWER DEVICES AND MACHINES	PP	100	40	53	P C	18. DATA STRUCTURES	PR	50	20	48	P
09. NETWORK AND POWER LAB.	TW	50	20	37	P C	19. COMMUNICATION THEORY	PP	100	40	53	P
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	41	P C	20. COMMUNICATION THEORY	OR	50	20	25	P
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	41	P

GRAND TOTAL = 1029/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

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S8053006   ANANNYA MAZUMDER   NEELA   , 71045369J   , S8053006 ,   PICT   , S8053006

01. SIGNAL AND SYSTEMS	PP	100	40	74	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	90	P
02. SIGNAL AND SYSTEMS	OR	50	20	33	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	20	P
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	67	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	62	P
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	34	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	40	P
05. NETWORK ANALYSIS	PP	100	40	57	P C	15. ELECTROMAGNETIC	PP	100	40	79	P
06. DIGITAL LOGIC DESIGN	PP	100	40	62	P C	16. ELECTROMAGNETIC	TW	25	10	22	P
07. DIGITAL LOGIC DESIGN	PR	50	20	30	P C	17. DATA STRUCTURES	PP	100	40	67	P
08. POWER DEVICES AND MACHINES	PP	100	40	65	P C	18. DATA STRUCTURES	PR	50	20	46	P
09. NETWORK AND POWER LAB.	TW	50	20	40	P C	19. COMMUNICATION THEORY	PP	100	40	71	P
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	38	P C	20. COMMUNICATION THEORY	OR	50	20	35	P
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	38	P

GRAND TOTAL = 1070/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

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                       UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(ELECTRONICS & TELECOM.)
    DATE : 18 AUG. 2011   CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.   PAGE NO. 03 (327)
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  NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.

        OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S8053007   ATTAR BENAZIR MEHBOOB   HASINA   , 71129924C   , S8053007 ,   PICT   , S8053007

| 01. SIGNAL AND SYSTEMS | PP | 100 | 40 | 70 | P C | 11. ENGINEERING MATHEMATICS III | PP | 100 | 40 | 73 | P |
| 02. SIGNAL AND SYSTEMS | OR | 50 | 20 | 30 | P C | 12. ENGINEERING MATHEMATICS III | TW | 25 | 10 | 22 | P |

03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	52	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	68	P
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	35	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	32	P
05. NETWORK ANALYSIS	PP	100	40	61	P C	15. ELECTROMAGNETIC	PP	100	40	49	P
06. DIGITAL LOGIC DESIGN	PP	100	40	42	P C	16. ELECTROMAGNETIC	TW	25	10	20	P
07. DIGITAL LOGIC DESIGN	PR	50	20	38	P C	17. DATA STRUCTURES	PP	100	40	46	P
08. POWER DEVICES AND MACHINES	PP	100	40	58	P C	18. DATA STRUCTURES	PR	50	20	32	P
09. NETWORK AND POWER LAB.	TW	50	20	41	P C	19. COMMUNICATION THEORY	PP	100	40	72	P
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	38	P C	20. COMMUNICATION THEORY	OR	50	20	42	P
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	42	P

GRAND TOTAL = 963/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

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S8053008   AWASARE VRUSHALI VALMIK   URMILA   , 71045379F   , S8053008 ,   PICT   , S8053008

01. SIGNAL AND SYSTEMS	PP	100	40	58	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	47	P
02. SIGNAL AND SYSTEMS	OR	50	20	28	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	19	P
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	51	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	40	P
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	40	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	32	P
05. NETWORK ANALYSIS	PP	100	40	56	P C	15. ELECTROMAGNETIC	PP	100	40	69	P
06. DIGITAL LOGIC DESIGN	PP	100	40	53	P C	16. ELECTROMAGNETIC	TW	25	10	23	P
07. DIGITAL LOGIC DESIGN	PR	50	20	38	P C	17. DATA STRUCTURES	PP	100	40	40	P
08. POWER DEVICES AND MACHINES	PP	100	40	50	P	18. DATA STRUCTURES	PR	50	20	38	P
09. NETWORK AND POWER LAB.	TW	50	20	37	P C	19. COMMUNICATION THEORY	PP	100	40	27	F
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	42	P C	20. COMMUNICATION THEORY	OR	50	20	29	P
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	41	P

GRAND TOTAL = 858/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

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S8053009   BAGMAR RITESH SURESH   ANITA   , 71045381H   , S8053009 ,   PICT   , S8053009

01. SIGNAL AND SYSTEMS	PP	100	40	81	P C		11. ENGINEERING MATHEMATICS III	PP	100	40	65	P	
02. SIGNAL AND SYSTEMS	OR	50	20	28	P C		12. ENGINEERING MATHEMATICS III	TW	25	10	23	P	
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	52	P C		13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	63	P	
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	38	P C		14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	32	P	
05. NETWORK ANALYSIS	PP	100	40	44	P C		15. ELECTROMAGNETIC	PP	100	40	61	P	
06. DIGITAL LOGIC DESIGN	PP	100	40	45	P C		16. ELECTROMAGNETIC	TW	25	10	22	P	
07. DIGITAL LOGIC DESIGN	PR	50	20	28	P C		17. DATA STRUCTURES	PP	100	40	62	P	
08. POWER DEVICES AND MACHINES	PP	100	40	55	P C		18. DATA STRUCTURES	PR	50	20	34	P	
09. NETWORK AND POWER LAB.	TW	50	20	38	P C		19. COMMUNICATION THEORY	PP	100	40	68	P	
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	43	P C		20. COMMUNICATION THEORY	OR	50	20	35	P	
							21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	37	P	

GRAND TOTAL = 954/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

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                 UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(ELECTRONICS & TELECOM.)

     DATE : 18 AUG. 2011   CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.   PAGE NO. 04 (328)
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  NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.

        OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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  S8053010   BAHETI BHAKTI VILAS   PADMA   , 71045382F   , S8053010 ,   PICT   , S8053010

01. SIGNAL AND SYSTEMS	PP	100	40	80	P C		11. ENGINEERING MATHEMATICS III	PP	100	40	89	P	
02. SIGNAL AND SYSTEMS	OR	50	20	35	P C		12. ENGINEERING MATHEMATICS III	TW	25	10	23	P	
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	69	P C		13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	57	P	
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	44	P C		14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	45	P	
05. NETWORK ANALYSIS	PP	100	40	58	P C		15. ELECTROMAGNETIC	PP	100	40	68	P	
06. DIGITAL LOGIC DESIGN	PP	100	40	63	P C		16. ELECTROMAGNETIC	TW	25	10	20	P	
07. DIGITAL LOGIC DESIGN	PR	50	20	44	P C		17. DATA STRUCTURES	PP	100	40	54	P	

08. POWER DEVICES AND MACHINES	PP	100	40	55	P C		18. DATA STRUCTURES	PR	50	20	47	P
09. NETWORK AND POWER LAB.	TW	50	20	42	P C		19. COMMUNICATION THEORY	PP	100	40	69	P
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	44	P C		20. COMMUNICATION THEORY	OR	50	20	37	P
							21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	41	P

GRAND TOTAL = 1084/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

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S8053011   BANSAL SHRUTI SANJAY   MEENA   , 71129925M   , S8053011 ,   PICT   , S8053011

01. SIGNAL AND SYSTEMS	PP	100	40	63	P C		11. ENGINEERING MATHEMATICS III	PP	100	40	59	P
02. SIGNAL AND SYSTEMS	OR	50	20	32	P C		12. ENGINEERING MATHEMATICS III	TW	25	10	21	P
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	62	P C		13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	54	P
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	33	P C		14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	35	P
05. NETWORK ANALYSIS	PP	100	40	54	P C		15. ELECTROMAGNETIC	PP	100	40	43	P
06. DIGITAL LOGIC DESIGN	PP	100	40	61	P C		16. ELECTROMAGNETIC	TW	25	10	22	P
07. DIGITAL LOGIC DESIGN	PR	50	20	32	P C		17. DATA STRUCTURES	PP	100	40	58	P
08. POWER DEVICES AND MACHINES	PP	100	40	48	P C		18. DATA STRUCTURES	PR	50	20	38	P
09. NETWORK AND POWER LAB.	TW	50	20	37	P C		19. COMMUNICATION THEORY	PP	100	40	64	P
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	41	P C		20. COMMUNICATION THEORY	OR	50	20	42	P
							21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	40	P

GRAND TOTAL = 939/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

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S8053012   BHAGAT ABHILASHA RAMDAS   MADHURI   , 71045386J   , S8053012 ,   PICT   , S8053012

01. SIGNAL AND SYSTEMS	PP	100	40	40	P C		11. ENGINEERING MATHEMATICS III	PP	100	40	49	P
02. SIGNAL AND SYSTEMS	OR	50	20	35	P C		12. ENGINEERING MATHEMATICS III	TW	25	10	22	P
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	43	P C		13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	40	P
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	32	P C		14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	28	P

05. NETWORK ANALYSIS	PP	100	40	40	P C		15. ELECTROMAGNETIC	PP	100	40	40	P
06. DIGITAL LOGIC DESIGN	PP	100	40	50	P C		16. ELECTROMAGNETIC	TW	25	10	19	P
07. DIGITAL LOGIC DESIGN	PR	50	20	21	P C		17. DATA STRUCTURES	PP	100	40	59	P
08. POWER DEVICES AND MACHINES	PP	100	40	47	P C		18. DATA STRUCTURES	PR	50	20	34	P
09. NETWORK AND POWER LAB.	TW	50	20	33	P C		19. COMMUNICATION THEORY	PP	100	40	54	P
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	35	P C		20. COMMUNICATION THEORY	OR	50	20	28	P
							21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	31	P

GRAND TOTAL = 780/1500, RESULT: SECOND CLASS

ORDN. 1 MARKS :

. . . . UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(ELECTRONICS & TELECOM.) . . . .

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  NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.

        OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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  S8053013   BHANDALKAR VISHAL MAHADEO   LAXMI   , 70801362H   , S8053013 , PICT   , S8053013

01. SIGNAL AND SYSTEMS	PP	100	40	13	F
02. SIGNAL AND SYSTEMS	OR	50	20	AA	F
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	13	F
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	AA	F
05. NETWORK ANALYSIS	PP	100	40	AA	F
06. DIGITAL LOGIC DESIGN	PP	100	40	16	F
07. DIGITAL LOGIC DESIGN	PR	50	20	AA	F
08. POWER DEVICES AND MACHINES	PP	100	40	31	F
09. NETWORK AND POWER LAB.	TW	50	20	24	P C
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	20	P C

  FIRST TERM TOTAL = 117/750.

ORDN. 1 MARKS :

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S8053014   BHAT TUSHAR GOVIND   VEENA   , 71045388E   , S8053014 ,   PICT   , S8053014

01. SIGNAL AND SYSTEMS	PP	100	40	84	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	77	P
02. SIGNAL AND SYSTEMS	OR	50	20	38	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	23	P
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	56	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	71	P
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	40	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	28	P
05. NETWORK ANALYSIS	PP	100	40	62	P C	15. ELECTROMAGNETIC	PP	100	40	78	P
06. DIGITAL LOGIC DESIGN	PP	100	40	61	P C	16. ELECTROMAGNETIC	TW	25	10	20	P
07. DIGITAL LOGIC DESIGN	PR	50	20	32	P C	17. DATA STRUCTURES	PP	100	40	69	P
08. POWER DEVICES AND MACHINES	PP	100	40	52	P C	18. DATA STRUCTURES	PR	50	20	42	P
09. NETWORK AND POWER LAB.	TW	50	20	42	P C	19. COMMUNICATION THEORY	PP	100	40	81	P
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	44	P C	20. COMMUNICATION THEORY	OR	50	20	35	P
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	39	P

GRAND TOTAL = 1074/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

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S8053015   BHOITE AJINKYA CHANDRAKANT   MADHAVI   , 71045392C   , S8053015 ,   PICT   , S8053015

01. SIGNAL AND SYSTEMS	PP	100	40	84	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	75	P
02. SIGNAL AND SYSTEMS	OR	50	20	38	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	23	P
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	71	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	50	P
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	27	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	33	P
05. NETWORK ANALYSIS	PP	100	40	54	P C	15. ELECTROMAGNETIC	PP	100	40	78	P
06. DIGITAL LOGIC DESIGN	PP	100	40	66	P C	16. ELECTROMAGNETIC	TW	25	10	20	P
07. DIGITAL LOGIC DESIGN	PR	50	20	32	P C	17. DATA STRUCTURES	PP	100	40	70	P
08. POWER DEVICES AND MACHINES	PP	100	40	54	P C	18. DATA STRUCTURES	PR	50	20	38	P
09. NETWORK AND POWER LAB.	TW	50	20	44	P C	19. COMMUNICATION THEORY	PP	100	40	58	P

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10. ELECTRONIC INSTRUMENTS AND TOOLS TW    50   20   38  P C      20. COMMUNICATION THEORY           OR    50   20   32  P
                                                                  21. CIRCUIT SIMULATION AND TOOLS  TW    50   20   41  P
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GRAND TOTAL = 1026/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

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                  UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(ELECTRONICS & TELECOM.)

    DATE : 18 AUG. 2011   CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.   PAGE NO. 06 (330)
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   NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.

         OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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   S8053016   BHOSALE PRASHANT DNYANADEO   RANJANA   , 71045393M   , S8053016 , PICT   , S8053016

   01. SIGNAL AND SYSTEMS   PP   100   40   71 P C   11. ENGINEERING MATHEMATICS III   PP   100   40   85 P
   02. SIGNAL AND SYSTEMS   OR   50   20   35 P C   12. ENGINEERING MATHEMATICS III   TW   25   10   22 P
   03. SOLID STATES DEVICES AND CIRCUITSPP 100   40   57 P C   13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40   47 P
   04. SOLID STATES DEVICES AND CIRCUITSPR   50   20   33 P C   14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20   26 P
   05. NETWORK ANALYSIS   PP   100   40   59 P C   15. ELECTROMAGNETIC   PP   100   40   67 P
   06. DIGITAL LOGIC DESIGN   PP   100   40   52 P C   16. ELECTROMAGNETIC   TW   25   10   19 P
   07. DIGITAL LOGIC DESIGN   PR   50   20   20 P C   17. DATA STRUCTURES   PP   100   40   45 P
   08. POWER DEVICES AND MACHINES   PP   100   40   42 P C   18. DATA STRUCTURES   PR   50   20   21 P
   09. NETWORK AND POWER LAB.   TW   50   20   34 P C   19. COMMUNICATION THEORY   PP   100   40   72 P
   10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50   20   41 P C   20. COMMUNICATION THEORY   OR   50   20   27 P
                                                                  21. CIRCUIT SIMULATION AND TOOLS TW   50   20   36 P
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GRAND TOTAL =  911/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

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   S8053017   BICHU TANMAY NITIN                   DHANASHREE        , 71045396F    , S8053017 ,  PICT     , S8053017

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|---|---|---|---|---|---|---|---|---|---|---|---|
| 01. SIGNAL AND SYSTEMS | PP | 100 | 40 | 83 | P C | 11. ENGINEERING MATHEMATICS III | PP | 100 | 40 | 92 | P |
| 02. SIGNAL AND SYSTEMS | OR | 50 | 20 | 48 | P C | 12. ENGINEERING MATHEMATICS III | TW | 25 | 10 | 24 | P |
| 03. SOLID STATES DEVICES AND CIRCUITS | PP | 100 | 40 | 73 | P C | 13. INTEGRATED CIRCUITS APPLICATIONS | PP | 100 | 40 | 79 | P |
| 04. SOLID STATES DEVICES AND CIRCUITS | PR | 50 | 20 | 45 | P C | 14. INTEGRATED CIRCUITS APPLICATIONS | PR | 50 | 20 | 44 | P |
| 05. NETWORK ANALYSIS | PP | 100 | 40 | 72 | P C | 15. ELECTROMAGNETIC | PP | 100 | 40 | 68 | P |
| 06. DIGITAL LOGIC DESIGN | PP | 100 | 40 | 72 | P C | 16. ELECTROMAGNETIC | TW | 25 | 10 | 22 | P |
| 07. DIGITAL LOGIC DESIGN | PR | 50 | 20 | 44 | P C | 17. DATA STRUCTURES | PP | 100 | 40 | 69 | P |
| 08. POWER DEVICES AND MACHINES | PP | 100 | 40 | 67 | P C | 18. DATA STRUCTURES | PR | 50 | 20 | 42 | P |
| 09. NETWORK AND POWER LAB. | TW | 50 | 20 | 45 | P C | 19. COMMUNICATION THEORY | PP | 100 | 40 | 78 | P |
| 10. ELECTRONIC INSTRUMENTS AND TOOLS | TW | 50 | 20 | 42 | P C | 20. COMMUNICATION THEORY | OR | 50 | 20 | 42 | P |
| | | | | | | 21. CIRCUIT SIMULATION AND TOOLS | TW | 50 | 20 | 43 | P |

GRAND TOTAL = 1194/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

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S8053018    CHAVAN TRUPTI GANESH              LATA              , 71045406G     , S8053018 ,   PICT      , S8053018

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|---|---|---|---|---|---|---|---|---|---|---|---|
| 01. SIGNAL AND SYSTEMS | PP | 100 | 40 | 85 | P C | 11. ENGINEERING MATHEMATICS III | PP | 100 | 40 | 79 | P |
| 02. SIGNAL AND SYSTEMS | OR | 50 | 20 | 42 | P C | 12. ENGINEERING MATHEMATICS III | TW | 25 | 10 | 23 | P |
| 03. SOLID STATES DEVICES AND CIRCUITS | PP | 100 | 40 | 71 | P C | 13. INTEGRATED CIRCUITS APPLICATIONS | PP | 100 | 40 | 63 | P |
| 04. SOLID STATES DEVICES AND CIRCUITS | PR | 50 | 20 | 35 | P C | 14. INTEGRATED CIRCUITS APPLICATIONS | PR | 50 | 20 | 44 | P |
| 05. NETWORK ANALYSIS | PP | 100 | 40 | 53 | P C | 15. ELECTROMAGNETIC | PP | 100 | 40 | 52 | P |
| 06. DIGITAL LOGIC DESIGN | PP | 100 | 40 | 71 | P C | 16. ELECTROMAGNETIC | TW | 25 | 10 | 24 | P |
| 07. DIGITAL LOGIC DESIGN | PR | 50 | 20 | 30 | P C | 17. DATA STRUCTURES | PP | 100 | 40 | 68 | P |
| 08. POWER DEVICES AND MACHINES | PP | 100 | 40 | 59 | P C | 18. DATA STRUCTURES | PR | 50 | 20 | 45 | P |
| 09. NETWORK AND POWER LAB. | TW | 50 | 20 | 43 | P C | 19. COMMUNICATION THEORY | PP | 100 | 40 | 68 | P |
| 10. ELECTRONIC INSTRUMENTS AND TOOLS | TW | 50 | 20 | 43 | P C | 20. COMMUNICATION THEORY | OR | 50 | 20 | 35 | P |
| | | | | | | 21. CIRCUIT SIMULATION AND TOOLS | TW | 50 | 20 | 40 | P |

GRAND TOTAL = 1073/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

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. . . . . . . . . . . . . . . . . .     UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(ELECTRONICS & TELECOM.) . . . . . . . . . . . . . . . . . . . . . . . . . .

    DATE : 18 AUG. 2011              CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.              PAGE NO.  07  (  331)

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  NOTE: FIRST LINE : SEAT NO.,  NAME OF THE CANDIDATE,  MOTHER, PERMANENT REG. NO.,  PREVIOUS SEAT NO.,  COLLEGE,   SEAT NO.

          OTHER LINES: HEAD OF PASSING,  MAX. MARKS,  MIN. PASS MARKS,  MARKS OBTAINED,  P/F:PASS/FAIL,  C:PREVIOUS CARRY OVER

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S8053019   DABHADE ROHIDAS RAGHUNATH   BEBEE   , 71045409M   , S8053019 , PICT   , S8053019

01. SIGNAL AND SYSTEMS	PP	100	40	63	P C		11. ENGINEERING MATHEMATICS III	PP	100	40	63	P
02. SIGNAL AND SYSTEMS	OR	50	20	24	P		12. ENGINEERING MATHEMATICS III	TW	25	10	24	P
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	49	P C		13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	51	P
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	25	P C		14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	28	P
05. NETWORK ANALYSIS	PP	100	40	56	P C		15. ELECTROMAGNETIC	PP	100	40	68	P
06. DIGITAL LOGIC DESIGN	PP	100	40	59	P C		16. ELECTROMAGNETIC	TW	25	10	17	P
07. DIGITAL LOGIC DESIGN	PR	50	20	33	P C		17. DATA STRUCTURES	PP	100	40	53	P
08. POWER DEVICES AND MACHINES	PP	100	40	60	P C		18. DATA STRUCTURES	PR	50	20	46	P
09. NETWORK AND POWER LAB.	TW	50	20	37	P C		19. COMMUNICATION THEORY	PP	100	40	44	P
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	39	P C		20. COMMUNICATION THEORY	OR	50	20	30	P
							21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	38	P

GRAND TOTAL = 907/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

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S8053020   DESAI SHWETA ARUN   ANJANI   , 71129926K   , S8053020 , PICT   , S8053020

01. SIGNAL AND SYSTEMS	PP	100	40	42	P C		11. ENGINEERING MATHEMATICS III	PP	100	40	45	P
02. SIGNAL AND SYSTEMS	OR	50	20	30	P C		12. ENGINEERING MATHEMATICS III	TW	25	10	20	P
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	47	P C		13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	59	P
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	35	P C		14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	35	P
05. NETWORK ANALYSIS	PP	100	40	40	P C		15. ELECTROMAGNETIC	PP	100	40	53	P

06. DIGITAL LOGIC DESIGN	PP	100	40	40	P C	16. ELECTROMAGNETIC	TW	25	10	20	P
07. DIGITAL LOGIC DESIGN	PR	50	20	25	P C	17. DATA STRUCTURES	PP	100	40	61	P
08. POWER DEVICES AND MACHINES	PP	100	40	50	P C	18. DATA STRUCTURES	PR	50	20	34	P
09. NETWORK AND POWER LAB.	TW	50	20	43	P C	19. COMMUNICATION THEORY	PP	100	40	50	P
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	39	P C	20. COMMUNICATION THEORY	OR	50	20	38	P
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	38	P

GRAND TOTAL = 844/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

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S8053021   DHANNE ANJALI BASAVRAJ   MADHURI   , 71129927H   , S8053021 ,   PICT   , S8053021

01. SIGNAL AND SYSTEMS	PP	100	40	57	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	55	P
02. SIGNAL AND SYSTEMS	OR	50	20	27	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	22	P
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	53	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	70	P
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	33	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	30	P
05. NETWORK ANALYSIS	PP	100	40	45	P C	15. ELECTROMAGNETIC	PP	100	40	50	P
06. DIGITAL LOGIC DESIGN	PP	100	40	45	P C	16. ELECTROMAGNETIC	TW	25	10	20	P
07. DIGITAL LOGIC DESIGN	PR	50	20	35	P	17. DATA STRUCTURES	PP	100	40	51	P
08. POWER DEVICES AND MACHINES	PP	100	40	59	P C	18. DATA STRUCTURES	PR	50	20	37	P
09. NETWORK AND POWER LAB.	TW	50	20	38	P C	19. COMMUNICATION THEORY	PP	100	40	62	P
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	43	P C	20. COMMUNICATION THEORY	OR	50	20	37	P
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	36	P

GRAND TOTAL = 905/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

. . . .
                         UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(ELECTRONICS & TELECOM.)

   DATE : 18 AUG. 2011   CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.   PAGE NO. 08 (332)

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  NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.
                                                           Page 13

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S8053022   DHIKALE JITENDRA BABAN   USHA   , 71045414H   , S8053022 , PICT   , S8053022

01. SIGNAL AND SYSTEMS	PP	100	40	93	P	C	11. ENGINEERING MATHEMATICS III	PP	100	40	96	P	
02. SIGNAL AND SYSTEMS	OR	50	20	40	P	C	12. ENGINEERING MATHEMATICS III	TW	25	10	25	P	
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	63	P	C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	65	P	
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	42	P	C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	36	P	
05. NETWORK ANALYSIS	PP	100	40	75	P	C	15. ELECTROMAGNETIC	PP	100	40	75	P	
06. DIGITAL LOGIC DESIGN	PP	100	40	59	P	C	16. ELECTROMAGNETIC	TW	25	10	23	P	
07. DIGITAL LOGIC DESIGN	PR	50	20	38	P	C	17. DATA STRUCTURES	PP	100	40	60	P	
08. POWER DEVICES AND MACHINES	PP	100	40	65	P	C	18. DATA STRUCTURES	PR	50	20	48	P	
09. NETWORK AND POWER LAB.	TW	50	20	42	P	C	19. COMMUNICATION THEORY	PP	100	40	60	P	
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	40	P	C	20. COMMUNICATION THEORY	OR	50	20	29	P	
							21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	34	P	

GRAND TOTAL = 1108/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

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S8053023   DIGHE GANESH DATTATRAY   SHOBHA   , 71045417B   , S8053023 , PICT   , S8053023

01. SIGNAL AND SYSTEMS	PP	100	40	67	P	C	11. ENGINEERING MATHEMATICS III	PP	100	40	85	P	
02. SIGNAL AND SYSTEMS	OR	50	20	28	P	C	12. ENGINEERING MATHEMATICS III	TW	25	10	17	P	
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	57	P	C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	58	P	
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	36	P	C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	29	P	
05. NETWORK ANALYSIS	PP	100	40	53	P	C	15. ELECTROMAGNETIC	PP	100	40	65	P	
06. DIGITAL LOGIC DESIGN	PP	100	40	43	P	C	16. ELECTROMAGNETIC	TW	25	10	18	P	
07. DIGITAL LOGIC DESIGN	PR	50	20	39	P	C	17. DATA STRUCTURES	PP	100	40	64	P	
08. POWER DEVICES AND MACHINES	PP	100	40	51	P	C	18. DATA STRUCTURES	PR	50	20	48	P	
09. NETWORK AND POWER LAB.	TW	50	20	40	P	C	19. COMMUNICATION THEORY	PP	100	40	65	P	
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	39	P	C	20. COMMUNICATION THEORY	OR	50	20	39	P	

21. CIRCUIT SIMULATION AND TOOLS   TW   50   20   35 P

GRAND TOTAL = 976/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

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S8053024   FIRAKE PARESH RAVINDRA   LAXMI   , 71045425C   , S8053024 ,   PICT   , S8053024

01. SIGNAL AND SYSTEMS   PP   100   40   66 P C   11. ENGINEERING MATHEMATICS III   PP   100   40   92 P
02. SIGNAL AND SYSTEMS   OR   50   20   38 P C   12. ENGINEERING MATHEMATICS III   TW   25   10   23 P
03. SOLID STATES DEVICES AND CIRCUITSPP 100   40   71 P C   13. INTEGRATED CIRCUITS APPLICATIONS PP   100   40   59 P
04. SOLID STATES DEVICES AND CIRCUITSPR   50   20   45 P C   14. INTEGRATED CIRCUITS APPLICATIONS PR   50   20   38 P
05. NETWORK ANALYSIS   PP   100   40   70 P C   15. ELECTROMAGNETIC   PP   100   40   58 P
06. DIGITAL LOGIC DESIGN   PP   100   40   61 P C   16. ELECTROMAGNETIC   TW   25   10   23 P
07. DIGITAL LOGIC DESIGN   PR   50   20   46 P C   17. DATA STRUCTURES   PP   100   40   61 P
08. POWER DEVICES AND MACHINES   PP   100   40   62 P C   18. DATA STRUCTURES   PR   50   20   48 P
09. NETWORK AND POWER LAB.   TW   50   20   42 P C   19. COMMUNICATION THEORY   PP   100   40   66 P
10. ELECTRONIC INSTRUMENTS AND TOOLS TW   50   20   42 P C   20. COMMUNICATION THEORY   OR   50   20   43 P
                                                               21. CIRCUIT SIMULATION AND TOOLS   TW   50   20   42 P

GRAND TOTAL = 1096/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

. . . . UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(ELECTRONICS & TELECOM.) . . . .
    DATE : 18 AUG. 2011   CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.   PAGE NO. 09 (333)
. . . .
 NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.
       OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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S8053025   GADHE GANESH VITTHALRAO   KEVALBAI   , 71045427K   , S8053025 ,   PICT   , S8053025

01. SIGNAL AND SYSTEMS   PP   100   40   42 P C   11. ENGINEERING MATHEMATICS III   PP   100   40   55 P

02. SIGNAL AND SYSTEMS	OR	50	20	30	P		12. ENGINEERING MATHEMATICS III	TW	25	10	23	P
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	41	P C		13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	47	P
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	20	P C		14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	29	P
05. NETWORK ANALYSIS	PP	100	40	40	P C		15. ELECTROMAGNETIC	PP	100	40	45	P
06. DIGITAL LOGIC DESIGN	PP	100	40	45	P C		16. ELECTROMAGNETIC	TW	25	10	18	P
07. DIGITAL LOGIC DESIGN	PR	50	20	35	P		17. DATA STRUCTURES	PP	100	40	44	P
08. POWER DEVICES AND MACHINES	PP	100	40	40	P C		18. DATA STRUCTURES	PR	50	20	37	P
09. NETWORK AND POWER LAB.	TW	50	20	36	P C		19. COMMUNICATION THEORY	PP	100	40	40	P
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	40	P C		20. COMMUNICATION THEORY	OR	50	20	32	P
							21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	35	P

GRAND TOTAL = 774/1500, RESULT: SECOND CLASS

ORDN. 1 MARKS :

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S8053026   GAVNEKAR SHREYAS SANJIV   NILIMA   , 71045435L   , S8053026 ,   PICT   , S8053026

01. SIGNAL AND SYSTEMS	PP	100	40	59	P C		11. ENGINEERING MATHEMATICS III	PP	100	40	72	P
02. SIGNAL AND SYSTEMS	OR	50	20	29	P C		12. ENGINEERING MATHEMATICS III	TW	25	10	23	P
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	48	P C		13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	60	P
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	29	P C		14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	37	P
05. NETWORK ANALYSIS	PP	100	40	40	P C		15. ELECTROMAGNETIC	PP	100	40	40	P
06. DIGITAL LOGIC DESIGN	PP	100	40	43	P C		16. ELECTROMAGNETIC	TW	25	10	20	P
07. DIGITAL LOGIC DESIGN	PR	50	20	30	P C		17. DATA STRUCTURES	PP	100	40	47	P
08. POWER DEVICES AND MACHINES	PP	100	40	46	P C		18. DATA STRUCTURES	PR	50	20	38	P
09. NETWORK AND POWER LAB.	TW	50	20	38	P C		19. COMMUNICATION THEORY	PP	100	40	28	F
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	39	P C		20. COMMUNICATION THEORY	OR	50	20	26	P
							21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	41	P

GRAND TOTAL = 833/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

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S8053027   GHOGARE SAYALI RAJESH   SUJATA   , 71129928F   , S8053027 ,   PICT   , S8053027

01. SIGNAL AND SYSTEMS	PP	100	40	62	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	40	P
02. SIGNAL AND SYSTEMS	OR	50	20	26	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	22	P
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	40	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	56	P
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	32	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	33	P
05. NETWORK ANALYSIS	PP	100	40	46	P C	15. ELECTROMAGNETIC	PP	100	40	41	P
06. DIGITAL LOGIC DESIGN	PP	100	40	41	P C	16. ELECTROMAGNETIC	TW	25	10	23	P
07. DIGITAL LOGIC DESIGN	PR	50	20	38	P C	17. DATA STRUCTURES	PP	100	40	55	P
08. POWER DEVICES AND MACHINES	PP	100	40	44	P C	18. DATA STRUCTURES	PR	50	20	35	P
09. NETWORK AND POWER LAB.	TW	50	20	41	P C	19. COMMUNICATION THEORY	PP	100	40	49	P
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	38	P C	20. COMMUNICATION THEORY	OR	50	20	29	P
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	38	P

GRAND TOTAL = 829/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

. . . .
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    DATE : 18 AUG. 2011   CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.   PAGE NO. 10 (334)

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  NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.

        OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S8053028   GHONE PRASAD SHARAD   HEMLATA   , 71045437G   , S8053028 ,   PICT   , S8053028

01. SIGNAL AND SYSTEMS	PP	100	40	62	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	59	P
02. SIGNAL AND SYSTEMS	OR	50	20	29	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	22	P
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	49	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	57	P
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	26	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	30	P
05. NETWORK ANALYSIS	PP	100	40	51	P C	15. ELECTROMAGNETIC	PP	100	40	59	P
06. DIGITAL LOGIC DESIGN	PP	100	40	41	P C	16. ELECTROMAGNETIC	TW	25	10	17	P

07. DIGITAL LOGIC DESIGN	PR	50	20	27	P C	17. DATA STRUCTURES	PP	100	40	60	P
08. POWER DEVICES AND MACHINES	PP	100	40	50	P C	18. DATA STRUCTURES	PR	50	20	48	P
09. NETWORK AND POWER LAB.	TW	50	20	34	P C	19. COMMUNICATION THEORY	PP	100	40	56	P
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	34	P C	20. COMMUNICATION THEORY	OR	50	20	22	P
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	33	P

GRAND TOTAL = 866/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

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S8053029   GUJAR AKASH SANJAY   ROHINI   , 71045438E   , S8053029 ,   PICT   , S8053029

01. SIGNAL AND SYSTEMS	PP	100	40	79	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	72	P
02. SIGNAL AND SYSTEMS	OR	50	20	31	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	20	P
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	57	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	62	P
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	22	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	35	P
05. NETWORK ANALYSIS	PP	100	40	52	P C	15. ELECTROMAGNETIC	PP	100	40	41	P
06. DIGITAL LOGIC DESIGN	PP	100	40	63	P C	16. ELECTROMAGNETIC	TW	25	10	18	P
07. DIGITAL LOGIC DESIGN	PR	50	20	28	P C	17. DATA STRUCTURES	PP	100	40	54	P
08. POWER DEVICES AND MACHINES	PP	100	40	46	P C	18. DATA STRUCTURES	PR	50	20	46	P
09. NETWORK AND POWER LAB.	TW	50	20	37	P C	19. COMMUNICATION THEORY	PP	100	40	66	P
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	39	P C	20. COMMUNICATION THEORY	OR	50	20	29	P
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	38	P

GRAND TOTAL = 935/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

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S8053030   GULAVANI ADITYA VISHRAM   MEGHA   , 71045439C   , S8053030 ,   PICT   , S8053030

| 01. SIGNAL AND SYSTEMS | PP | 100 | 40 | 81 | P C | 11. ENGINEERING MATHEMATICS III | PP | 100 | 40 | 72 | P |
| 02. SIGNAL AND SYSTEMS | OR | 50 | 20 | 34 | P C | 12. ENGINEERING MATHEMATICS III | TW | 25 | 10 | 15 | P |

03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	50	P C		13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	54	P
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	30	P C		14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	42	P
05. NETWORK ANALYSIS	PP	100	40	55	P C		15. ELECTROMAGNETIC	PP	100	40	71	P
06. DIGITAL LOGIC DESIGN	PP	100	40	70	P C		16. ELECTROMAGNETIC	TW	25	10	19	P
07. DIGITAL LOGIC DESIGN	PR	50	20	34	P C		17. DATA STRUCTURES	PP	100	40	58	P
08. POWER DEVICES AND MACHINES	PP	100	40	48	P C		18. DATA STRUCTURES	PR	50	20	46	P
09. NETWORK AND POWER LAB.	TW	50	20	39	P C		19. COMMUNICATION THEORY	PP	100	40	61	P
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	40	P C		20. COMMUNICATION THEORY	OR	50	20	35	P
							21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	37	P

GRAND TOTAL = 991/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

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UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(ELECTRONICS & TELECOM.)

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S8053031   GULSHEEN KAUR AHUJA   BHUP INDER   , 71045440G   , S8053031 , PICT   , S8053031

01. SIGNAL AND SYSTEMS	PP	100	40	78	P C		11. ENGINEERING MATHEMATICS III	PP	100	40	96	P
02. SIGNAL AND SYSTEMS	OR	50	20	48	P C		12. ENGINEERING MATHEMATICS III	TW	25	10	23	P
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	72	P C		13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	59	P
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	45	P C		14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	45	P
05. NETWORK ANALYSIS	PP	100	40	61	P C		15. ELECTROMAGNETIC	PP	100	40	57	P
06. DIGITAL LOGIC DESIGN	PP	100	40	61	P C		16. ELECTROMAGNETIC	TW	25	10	24	P
07. DIGITAL LOGIC DESIGN	PR	50	20	43	P C		17. DATA STRUCTURES	PP	100	40	71	P
08. POWER DEVICES AND MACHINES	PP	100	40	45	P C		18. DATA STRUCTURES	PR	50	20	48	P
09. NETWORK AND POWER LAB.	TW	50	20	45	P C		19. COMMUNICATION THEORY	PP	100	40	63	P
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	44	P C		20. COMMUNICATION THEORY	OR	50	20	42	P
							21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	40	P

GRAND TOTAL = 1110/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

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S8053032   GUNDI NACHIKET DINESH   VINEETA   , 71045441E   , S8053032 , PICT   , S8053032

01. SIGNAL AND SYSTEMS	PP	100	40	73	P C		11. ENGINEERING MATHEMATICS III	PP	100	40	73	P
02. SIGNAL AND SYSTEMS	OR	50	20	33	P C		12. ENGINEERING MATHEMATICS III	TW	25	10	25	P
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	53	P C		13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	47	P
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	35	P C		14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	25	P
05. NETWORK ANALYSIS	PP	100	40	59	P C		15. ELECTROMAGNETIC	PP	100	40	57	P
06. DIGITAL LOGIC DESIGN	PP	100	40	55	P C		16. ELECTROMAGNETIC	TW	25	10	19	P
07. DIGITAL LOGIC DESIGN	PR	50	20	31	P C		17. DATA STRUCTURES	PP	100	40	42	P
08. POWER DEVICES AND MACHINES	PP	100	40	49	P C		18. DATA STRUCTURES	PR	50	20	37	P
09. NETWORK AND POWER LAB.	TW	50	20	41	P C		19. COMMUNICATION THEORY	PP	100	40	40	P
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	42	P C		20. COMMUNICATION THEORY	OR	50	20	32	P
							21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	38	P

GRAND TOTAL = 906/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

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S8053033   HIMANSHU KAMAT   ROOPASHRI   , 71045446F   , S8053033 , PICT   , S8053033

01. SIGNAL AND SYSTEMS	PP	100	40	67	P C		11. ENGINEERING MATHEMATICS III	PP	100	40	63	P
02. SIGNAL AND SYSTEMS	OR	50	20	25	P C		12. ENGINEERING MATHEMATICS III	TW	25	10	20	P
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	54	P C		13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	44	P
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	40	P C		14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	36	P
05. NETWORK ANALYSIS	PP	100	40	40	P C		15. ELECTROMAGNETIC	PP	100	40	56	P
06. DIGITAL LOGIC DESIGN	PP	100	40	46	P C		16. ELECTROMAGNETIC	TW	25	10	20	P
07. DIGITAL LOGIC DESIGN	PR	50	20	33	P C		17. DATA STRUCTURES	PP	100	40	65	P

cols05
08. POWER DEVICES AND MACHINES   PP   100   40   45 P C   18. DATA STRUCTURES   PR   50   20   43 P
09. NETWORK AND POWER LAB.   TW   50   20   37 P C   19. COMMUNICATION THEORY   PP   100   40   46 P
10. ELECTRONIC INSTRUMENTS AND TOOLS TW   50   20   41 P C   20. COMMUNICATION THEORY   OR   50   20   32 P
                                                                21. CIRCUIT SIMULATION AND TOOLS TW   50   20   33 P

GRAND TOTAL = 886/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

. . . . UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(ELECTRONICS & TELECOM.) . . . .

    DATE : 18 AUG. 2011   CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.   PAGE NO. 12 (336)

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  NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.

        OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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  S8053034   HISARIYA RISHI RAMMOHAN   SANDHYA   , 71129929D   , S8053034 , PICT   , S8053034

01. SIGNAL AND SYSTEMS   PP   100   40   56 P C   11. ENGINEERING MATHEMATICS III   PP   100   40   58 P
02. SIGNAL AND SYSTEMS   OR   50   20   20 P C   12. ENGINEERING MATHEMATICS III   TW   25   10   19 P
03. SOLID STATES DEVICES AND CIRCUITSPP   100   40   42 P C   13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40   59 P
04. SOLID STATES DEVICES AND CIRCUITSPR   50   20   30 P C   14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20   35 P
05. NETWORK ANALYSIS   PP   100   40   40 P C   15. ELECTROMAGNETIC   PP   100   40   46 P
06. DIGITAL LOGIC DESIGN   PP   100   40   49 P C   16. ELECTROMAGNETIC   TW   25   10   20 P
07. DIGITAL LOGIC DESIGN   PR   50   20   35 P   17. DATA STRUCTURES   PP   100   40   55 P
08. POWER DEVICES AND MACHINES   PP   100   40   55 P C   18. DATA STRUCTURES   PR   50   20   48 P
09. NETWORK AND POWER LAB.   TW   50   20   38 P C   19. COMMUNICATION THEORY   PP   100   40   46 P
10. ELECTRONIC INSTRUMENTS AND TOOLS TW   50   20   42 P C   20. COMMUNICATION THEORY   OR   50   20   38 P
                                                                21. CIRCUIT SIMULATION AND TOOLS TW   50   20   36 P

GRAND TOTAL = 867/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

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S8053035   INGLE CHETAN VASUDEO   SHALINI   , 71129930H   , S8053035 ,   PICT   , S8053035

01. SIGNAL AND SYSTEMS	PP	100	40	40	P C	
02. SIGNAL AND SYSTEMS	OR	50	20	20	P	
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	56	P C	
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	22	P C	
05. NETWORK ANALYSIS	PP	100	40	40	P C	
06. DIGITAL LOGIC DESIGN	PP	100	40	44	P C	
07. DIGITAL LOGIC DESIGN	PR	50	20	29	P C	
08. POWER DEVICES AND MACHINES	PP	100	40	53	P C	
09. NETWORK AND POWER LAB.	TW	50	20	42	P C	
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	39	P C	
11. ENGINEERING MATHEMATICS III	PP	100	40	70	P	
12. ENGINEERING MATHEMATICS III	TW	25	10	22	P	
13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	66	P	
14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	30	P	
15. ELECTROMAGNETIC	PP	100	40	66	P	
16. ELECTROMAGNETIC	TW	25	10	19	P	
17. DATA STRUCTURES	PP	100	40	61	P	
18. DATA STRUCTURES	PR	50	20	48	P	
19. COMMUNICATION THEORY	PP	100	40	76	P	
20. COMMUNICATION THEORY	OR	50	20	38	P	
21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	35	P	

GRAND TOTAL = 916/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

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S8053036   ISHTE PRAKASH BHAGVAN   KAMAL   , 70925442D   , S8053036 ,   PICT   , S8053036

01. SIGNAL AND SYSTEMS	PP	100	40	40	P	
02. SIGNAL AND SYSTEMS	OR	50	20	24	P C	
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	40	P C	
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	20	P	
05. NETWORK ANALYSIS	PP	100	40	27	F	
06. DIGITAL LOGIC DESIGN	PP	100	40	20	F	
07. DIGITAL LOGIC DESIGN	PR	50	20	11	F	
08. POWER DEVICES AND MACHINES	PP	100	40	AA	F	
09. NETWORK AND POWER LAB.	TW	50	20	32	P C	
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	35	P C	
11. ENGINEERING MATHEMATICS III	PP	100	40	22	F	
12. ENGINEERING MATHEMATICS III	TW	25	10	18	P	
13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	32	F	
14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	25	P	
15. ELECTROMAGNETIC	PP	100	40	57	P	
16. ELECTROMAGNETIC	TW	25	10	10	P	
17. DATA STRUCTURES	PP	100	40	29	F	
18. DATA STRUCTURES	PR	50	20	43	P	
19. COMMUNICATION THEORY	PP	100	40	40	P	
20. COMMUNICATION THEORY	OR	50	20	22	P	
21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	26	P	

GRAND TOTAL = 573/1500, RESULT: FAILS

ORDN. 1 MARKS :

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                        UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(ELECTRONICS & TELECOM.)
     DATE : 18 AUG. 2011   CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.   PAGE NO. 13 (337)
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   NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.

         OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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   S8053037   JADHAV ARTI GULAB   VIMAL   , 71129931F   , S8053037 , PICT   , S8053037

   01. SIGNAL AND SYSTEMS   PP   100   40   57 P C   11. ENGINEERING MATHEMATICS III   PP   100   40   71 P
   02. SIGNAL AND SYSTEMS   OR   50   20   34 P C   12. ENGINEERING MATHEMATICS III   TW   25   10   20 P
   03. SOLID STATES DEVICES AND CIRCUITSPP   100   40   65 P C   13. INTEGRATED CIRCUITS APPLICATIONS PP   100   40   71 P
   04. SOLID STATES DEVICES AND CIRCUITSPR   50   20   28 P C   14. INTEGRATED CIRCUITS APPLICATIONS PR   50   20   34 P
   05. NETWORK ANALYSIS   PP   100   40   58 P C   15. ELECTROMAGNETIC   PP   100   40   75 P
   06. DIGITAL LOGIC DESIGN   PP   100   40   75 P C   16. ELECTROMAGNETIC   TW   25   10   20 P
   07. DIGITAL LOGIC DESIGN   PR   50   20   33 P C   17. DATA STRUCTURES   PP   100   40   70 P
   08. POWER DEVICES AND MACHINES   PP   100   40   60 P C   18. DATA STRUCTURES   PR   50   20   47 P
   09. NETWORK AND POWER LAB.   TW   50   20   39 P C   19. COMMUNICATION THEORY   PP   100   40   59 P
   10. ELECTRONIC INSTRUMENTS AND TOOLS TW   50   20   42 P C   20. COMMUNICATION THEORY   OR   50   20   21 P
                                                                    21. CIRCUIT SIMULATION AND TOOLS   TW   50   20   39 P

GRAND TOTAL = 1018/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

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   S8053038   JAIN AMEY MANOJ   NILIMA   , 71045455E   , S8053038 , PICT   , S8053038

   01. SIGNAL AND SYSTEMS   PP   100   40   40 P C   11. ENGINEERING MATHEMATICS III   PP   100   40   70 P
   02. SIGNAL AND SYSTEMS   OR   50   20   28 P C   12. ENGINEERING MATHEMATICS III   TW   25   10   19 P
   03. SOLID STATES DEVICES AND CIRCUITSPP   100   40   40 P C   13. INTEGRATED CIRCUITS APPLICATIONS PP   100   40   51 P

04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	25	P C		14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	30	P
05. NETWORK ANALYSIS	PP	100	40	40	P C		15. ELECTROMAGNETIC	PP	100	40	54	P
06. DIGITAL LOGIC DESIGN	PP	100	40	48	P C		16. ELECTROMAGNETIC	TW	25	10	17	P
07. DIGITAL LOGIC DESIGN	PR	50	20	30	P C		17. DATA STRUCTURES	PP	100	40	57	P
08. POWER DEVICES AND MACHINES	PP	100	40	44	P C		18. DATA STRUCTURES	PR	50	20	46	P
09. NETWORK AND POWER LAB.	TW	50	20	35	P C		19. COMMUNICATION THEORY	PP	100	40	40	P
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	35	P C		20. COMMUNICATION THEORY	OR	50	20	30	P
							21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	34	P

GRAND TOTAL = 813/1500, RESULT: SECOND CLASS

ORDN. 1 MARKS :

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S8053039   JAJOO POONAM ASHOK   JAYWANTI   , 71045459H   , S8053039 ,   PICT   , S8053039

01. SIGNAL AND SYSTEMS	PP	100	40	74	P C		11. ENGINEERING MATHEMATICS III	PP	100	40	63	P
02. SIGNAL AND SYSTEMS	OR	50	20	33	P C		12. ENGINEERING MATHEMATICS III	TW	25	10	23	P
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	42	P C		13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	56	P
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	39	P C		14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	42	P
05. NETWORK ANALYSIS	PP	100	40	54	P C		15. ELECTROMAGNETIC	PP	100	40	49	P
06. DIGITAL LOGIC DESIGN	PP	100	40	73	P C		16. ELECTROMAGNETIC	TW	25	10	24	P
07. DIGITAL LOGIC DESIGN	PR	50	20	35	P C		17. DATA STRUCTURES	PP	100	40	65	P
08. POWER DEVICES AND MACHINES	PP	100	40	50	P C		18. DATA STRUCTURES	PR	50	20	45	P
09. NETWORK AND POWER LAB.	TW	50	20	40	P C		19. COMMUNICATION THEORY	PP	100	40	62	P
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	44	P C		20. COMMUNICATION THEORY	OR	50	20	34	P
							21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	40	P

GRAND TOTAL = 987+05/1500, RESULT: FIRST CLASS[O.163]

ORDN. 1 MARKS :

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S8053040   JOSHI PURVA AVINASH   AARTI   , 71045463F   , S8053040 ,   PICT   , S8053040

01. SIGNAL AND SYSTEMS	PP	100	40	81	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	82	P
02. SIGNAL AND SYSTEMS	OR	50	20	37	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	24	P
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	61	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	59	P
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	42	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	40	P
05. NETWORK ANALYSIS	PP	100	40	58	P C	15. ELECTROMAGNETIC	PP	100	40	79	P
06. DIGITAL LOGIC DESIGN	PP	100	40	78	P C	16. ELECTROMAGNETIC	TW	25	10	23	P
07. DIGITAL LOGIC DESIGN	PR	50	20	31	P C	17. DATA STRUCTURES	PP	100	40	72	P
08. POWER DEVICES AND MACHINES	PP	100	40	51	P C	18. DATA STRUCTURES	PR	50	20	42	P
09. NETWORK AND POWER LAB.	TW	50	20	42	P C	19. COMMUNICATION THEORY	PP	100	40	75	P
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	44	P C	20. COMMUNICATION THEORY	OR	50	20	41	P
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	44	P

GRAND TOTAL = 1106/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

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S8053041   KAINGADE SARTHAK RANJEET   RINISHA   , 71045464D   , S8053041 ,   PICT   , S8053041

01. SIGNAL AND SYSTEMS	PP	100	40	83	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	83	P
02. SIGNAL AND SYSTEMS	OR	50	20	32	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	24	P
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	80	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	75	P
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	41	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	40	P
05. NETWORK ANALYSIS	PP	100	40	70	P C	15. ELECTROMAGNETIC	PP	100	40	73	P
06. DIGITAL LOGIC DESIGN	PP	100	40	90	P C	16. ELECTROMAGNETIC	TW	25	10	23	P
07. DIGITAL LOGIC DESIGN	PR	50	20	46	P C	17. DATA STRUCTURES	PP	100	40	71	P
08. POWER DEVICES AND MACHINES	PP	100	40	63	P C	18. DATA STRUCTURES	PR	50	20	48	P

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09. NETWORK AND POWER LAB.          TW    50   20   43  P C    19. COMMUNICATION THEORY            PP   100   40   72  P
10. ELECTRONIC INSTRUMENTS AND TOOLS TW  50   20   42  P C    20. COMMUNICATION THEORY            OR    50   20   47  P
                                                              21. CIRCUIT SIMULATION AND TOOLS   TW    50   20   40  P

GRAND TOTAL = 1186/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :


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  S8053042    KAKADE ABHIJIT SURESH              VANDANA          , 71045465B    , S8053042 ,  PICT     , S8053042

01. SIGNAL AND SYSTEMS              PP   100   40   66  P C    11. ENGINEERING MATHEMATICS III     PP   100   40   71  P
02. SIGNAL AND SYSTEMS              OR    50   20   36  P C    12. ENGINEERING MATHEMATICS III     TW    25   10   19  P
03. SOLID STATES DEVICES AND CIRCUITSPP  100   40   56  P C    13. INTEGRATED CIRCUITS APPLICATIONS PP  100   40   41  P
04. SOLID STATES DEVICES AND CIRCUITSPR   50   20   30  P C    14. INTEGRATED CIRCUITS APPLICATIONS PR   50   20   37  P
05. NETWORK ANALYSIS               PP   100   40   70  P C    15. ELECTROMAGNETIC                 PP   100   40   81  P
06. DIGITAL LOGIC DESIGN           PP   100   40   73  P C    16. ELECTROMAGNETIC                 TW    25   10   21  P
07. DIGITAL LOGIC DESIGN           PR    50   20   28  P C    17. DATA STRUCTURES                PP   100   40   65  P
08. POWER DEVICES AND MACHINES     PP   100   40   45  P C    18. DATA STRUCTURES                PR    50   20   44  P
09. NETWORK AND POWER LAB.          TW    50   20   39  P C    19. COMMUNICATION THEORY            PP   100   40   30* P
10. ELECTRONIC INSTRUMENTS AND TOOLS TW  50   20   41  P C    20. COMMUNICATION THEORY            OR    50   20   32  P
                                                              21. CIRCUIT SIMULATION AND TOOLS   TW    50   20   42  P

GRAND TOTAL =  967/1500, RESULT: FIRST CLASS   * [O.4]

ORDN. 1 MARKS :


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                    UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(ELECTRONICS & TELECOM.)
   DATE : 18 AUG. 2011            CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.          PAGE NO.  15  (  339)
. . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . .
  NOTE: FIRST LINE : SEAT NO.,  NAME OF THE CANDIDATE,  MOTHER, PERMANENT REG. NO.,  PREVIOUS SEAT NO.,  COLLEGE,   SEAT NO.
        OTHER LINES: HEAD OF PASSING,  MAX. MARKS,  MIN. PASS MARKS,  MARKS OBTAINED,  P/F:PASS/FAIL,  C:PREVIOUS CARRY OVER
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  S8053043    KAMPU DHANANJAY ANANTRAO           SHUBHANGI        , 71045467J    , S8053043 ,  PICT     , S8053043
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01. SIGNAL AND SYSTEMS	PP	100	40	71	P C		11. ENGINEERING MATHEMATICS III	PP	100	40	68	P
02. SIGNAL AND SYSTEMS	OR	50	20	33	P C		12. ENGINEERING MATHEMATICS III	TW	25	10	18	P
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	69	P C		13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	52	P
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	32	P C		14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	28	P
05. NETWORK ANALYSIS	PP	100	40	44	P C		15. ELECTROMAGNETIC	PP	100	40	80	P
06. DIGITAL LOGIC DESIGN	PP	100	40	67	P C		16. ELECTROMAGNETIC	TW	25	10	20	P
07. DIGITAL LOGIC DESIGN	PR	50	20	42	P C		17. DATA STRUCTURES	PP	100	40	60	P
08. POWER DEVICES AND MACHINES	PP	100	40	48	P C		18. DATA STRUCTURES	PR	50	20	46	P
09. NETWORK AND POWER LAB.	TW	50	20	41	P C		19. COMMUNICATION THEORY	PP	100	40	51	P
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	43	P C		20. COMMUNICATION THEORY	OR	50	20	32	P
							21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	36	P

GRAND TOTAL = 981/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

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S8053044   KARE NACHIKET CHANDRASHEKHAR   NANDINI   , 71045473C   , S8053044 ,   PICT   , S8053044

01. SIGNAL AND SYSTEMS	PP	100	40	86	P C		11. ENGINEERING MATHEMATICS III	PP	100	40	83	P
02. SIGNAL AND SYSTEMS	OR	50	20	39	P C		12. ENGINEERING MATHEMATICS III	TW	25	10	25	P
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	76	P C		13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	59	P
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	29	P C		14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	33	P
05. NETWORK ANALYSIS	PP	100	40	58	P C		15. ELECTROMAGNETIC	PP	100	40	61	P
06. DIGITAL LOGIC DESIGN	PP	100	40	67	P C		16. ELECTROMAGNETIC	TW	25	10	22	P
07. DIGITAL LOGIC DESIGN	PR	50	20	39	P C		17. DATA STRUCTURES	PP	100	40	69	P
08. POWER DEVICES AND MACHINES	PP	100	40	58	P C		18. DATA STRUCTURES	PR	50	20	48	P
09. NETWORK AND POWER LAB.	TW	50	20	42	P C		19. COMMUNICATION THEORY	PP	100	40	71	P
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	41	P C		20. COMMUNICATION THEORY	OR	50	20	43	P
							21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	42	P

GRAND TOTAL = 1091/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

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S8053045   KULKARNI AKSHADA VIJAY   VIDYA   , 71045490C   , S8053045 , PICT   , S8053045

01. SIGNAL AND SYSTEMS	PP	100	40	88	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	84	P
02. SIGNAL AND SYSTEMS	OR	50	20	38	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	23	P
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	64	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	53	P
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	35	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	42	P
05. NETWORK ANALYSIS	PP	100	40	45	P C	15. ELECTROMAGNETIC	PP	100	40	55	P
06. DIGITAL LOGIC DESIGN	PP	100	40	75	P C	16. ELECTROMAGNETIC	TW	25	10	24	P
07. DIGITAL LOGIC DESIGN	PR	50	20	32	P C	17. DATA STRUCTURES	PP	100	40	61	P
08. POWER DEVICES AND MACHINES	PP	100	40	42	P C	18. DATA STRUCTURES	PR	50	20	45	P
09. NETWORK AND POWER LAB.	TW	50	20	38	P C	19. COMMUNICATION THEORY	PP	100	40	56	P
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	42	P C	20. COMMUNICATION THEORY	OR	50	20	35	P
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	36	P

GRAND TOTAL = 1013/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

. . . . UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(ELECTRONICS & TELECOM.) . . . .

   DATE : 18 AUG. 2011   CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.   PAGE NO. 16 (340)
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  NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.

        OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S8053046   KULKARNI GAURAV PRADEEP   NISHA   , 71059376H   , S8053046 , PICT   , S8053046

01. SIGNAL AND SYSTEMS	PP	100	40	60	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	66	P
02. SIGNAL AND SYSTEMS	OR	50	20	31	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	24	P
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	77	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	62	P
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	35	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	37	P

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05. NETWORK ANALYSIS                 PP  100  40  40  P C    15. ELECTROMAGNETIC                   PP  100  40  72  P
06. DIGITAL LOGIC DESIGN             PP  100  40  56  P C    16. ELECTROMAGNETIC                   TW   25  10  22  P
07. DIGITAL LOGIC DESIGN             PR   50  20  33  P C    17. DATA STRUCTURES                   PP  100  40  65  P
08. POWER DEVICES AND MACHINES       PP  100  40  61  P C    18. DATA STRUCTURES                   PR   50  20  47  P
09. NETWORK AND POWER LAB.           TW   50  20  41  P C    19. COMMUNICATION THEORY              PP  100  40  62  P
10. ELECTRONIC INSTRUMENTS AND TOOLS TW   50  20  39  P C    20. COMMUNICATION THEORY              OR   50  20  35  P
                                                             21. CIRCUIT SIMULATION AND TOOLS     TW   50  20  42  P
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GRAND TOTAL = 1007/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

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S8053047   KULKARNI HARSHAD RAVINDRA   SADHANA   , 71045495D   , S8053047 ,   PICT   , S8053047

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01. SIGNAL AND SYSTEMS               PP  100  40  74  P C    11. ENGINEERING MATHEMATICS III       PP  100  40  64  P
02. SIGNAL AND SYSTEMS               OR   50  20  40  P C    12. ENGINEERING MATHEMATICS III       TW   25  10  22  P
03. SOLID STATES DEVICES AND CIRCUITSPP  100  40  58  P C    13. INTEGRATED CIRCUITS APPLICATIONS PP  100  40  54  P
04. SOLID STATES DEVICES AND CIRCUITSPR   50  20  40  P C    14. INTEGRATED CIRCUITS APPLICATIONS PR   50  20  33  P
05. NETWORK ANALYSIS                 PP  100  40  55  P C    15. ELECTROMAGNETIC                   PP  100  40  54  P
06. DIGITAL LOGIC DESIGN             PP  100  40  60  P C    16. ELECTROMAGNETIC                   TW   25  10  17  P
07. DIGITAL LOGIC DESIGN             PR   50  20  34  P C    17. DATA STRUCTURES                   PP  100  40  74  P
08. POWER DEVICES AND MACHINES       PP  100  40  44  P C    18. DATA STRUCTURES                   PR   50  20  46  P
09. NETWORK AND POWER LAB.           TW   50  20  40  P C    19. COMMUNICATION THEORY              PP  100  40  53  P
10. ELECTRONIC INSTRUMENTS AND TOOLS TW   50  20  41  P C    20. COMMUNICATION THEORY              OR   50  20  38  P
                                                             21. CIRCUIT SIMULATION AND TOOLS     TW   50  20  39  P
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GRAND TOTAL = 980/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

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S8053048   KULKARNI SAURABH CHANDRAKANT   SWARALI   , 71045497L   , S8053048 ,   PICT   , S8053048

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01. SIGNAL AND SYSTEMS               PP  100  40  73  P C    11. ENGINEERING MATHEMATICS III       PP  100  40  84  P
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02. SIGNAL AND SYSTEMS	OR	50	20	37	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	23	P
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	68	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	55	P
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	22	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	35	P
05. NETWORK ANALYSIS	PP	100	40	56	P C	15. ELECTROMAGNETIC	PP	100	40	82	P
06. DIGITAL LOGIC DESIGN	PP	100	40	63	P C	16. ELECTROMAGNETIC	TW	25	10	19	P
07. DIGITAL LOGIC DESIGN	PR	50	20	35	P C	17. DATA STRUCTURES	PP	100	40	69	P
08. POWER DEVICES AND MACHINES	PP	100	40	45	P C	18. DATA STRUCTURES	PR	50	20	38	P
09. NETWORK AND POWER LAB.	TW	50	20	43	P C	19. COMMUNICATION THEORY	PP	100	40	69	P
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	43	P C	20. COMMUNICATION THEORY	OR	50	20	31	P
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	41	P

GRAND TOTAL = 1031/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

. . . . UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(ELECTRONICS & TELECOM.) . . . .

DATE : 18 AUG. 2011   CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.   PAGE NO. 17 (341)

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S8053049   KULKARNI YASH VIJAY   VINITA   , 71045499G   , S8053049 , PICT   , S8053049

01. SIGNAL AND SYSTEMS	PP	100	40	57	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	40	P
02. SIGNAL AND SYSTEMS	OR	50	20	43	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	18	P
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	40	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	27	F
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	29	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	26	P
05. NETWORK ANALYSIS	PP	100	40	40	P C	15. ELECTROMAGNETIC	PP	100	40	61	P
06. DIGITAL LOGIC DESIGN	PP	100	40	45	P C	16. ELECTROMAGNETIC	TW	25	10	20	P
07. DIGITAL LOGIC DESIGN	PR	50	20	32	P C	17. DATA STRUCTURES	PP	100	40	54	P
08. POWER DEVICES AND MACHINES	PP	100	40	40	P C	18. DATA STRUCTURES	PR	50	20	33	P
09. NETWORK AND POWER LAB.	TW	50	20	30	P C	19. COMMUNICATION THEORY	PP	100	40	48	P

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 10. ELECTRONIC INSTRUMENTS AND TOOLS TW    50   20   29  P C      20. COMMUNICATION THEORY              OR    50   20   35  P
                                                                   21. CIRCUIT SIMULATION AND TOOLS     TW    50   20   36  P
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GRAND TOTAL = 783/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

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 S8053050   KUMBHAKARN MANSI ARUN              SULBHA            , 71045500D     , S8053050 ,  PICT      , S8053050

 01. SIGNAL AND SYSTEMS                PP   100   40   89  P C      11. ENGINEERING MATHEMATICS III      PP   100   40   82  P
 02. SIGNAL AND SYSTEMS                OR    50   20   40  P C      12. ENGINEERING MATHEMATICS III      TW    25   10   23  P
 03. SOLID STATES DEVICES AND CIRCUITSPP   100   40   66  P C      13. INTEGRATED CIRCUITS APPLICATIONS PP   100   40   45  P
 04. SOLID STATES DEVICES AND CIRCUITSPR    50   20   35  P C      14. INTEGRATED CIRCUITS APPLICATIONS PR    50   20   45  P
 05. NETWORK ANALYSIS                  PP   100   40   54  P C      15. ELECTROMAGNETIC                  PP   100   40   51  P
 06. DIGITAL LOGIC DESIGN              PP   100   40   68  P C      16. ELECTROMAGNETIC                  TW    25   10   20  P
 07. DIGITAL LOGIC DESIGN              PR    50   20   34  P C      17. DATA STRUCTURES                  PP   100   40   75  P
 08. POWER DEVICES AND MACHINES        PP   100   40   52  P C      18. DATA STRUCTURES                  PR    50   20   42  P
 09. NETWORK AND POWER LAB.            TW    50   20   44  P C      19. COMMUNICATION THEORY             PP   100   40   68  P
 10. ELECTRONIC INSTRUMENTS AND TOOLS TW    50   20   44  P C      20. COMMUNICATION THEORY             OR    50   20   40  P
                                                                   21. CIRCUIT SIMULATION AND TOOLS    TW    50   20   39  P
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GRAND TOTAL = 1056/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

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 S8053051   LIMBHORE NITIN SHIVAJI            SANGITA           , 71129932D     , S8053051 ,  PICT      , S8053051

 01. SIGNAL AND SYSTEMS                PP   100   40   58  P C      11. ENGINEERING MATHEMATICS III      PP   100   40   84  P
 02. SIGNAL AND SYSTEMS                OR    50   20   20  P C      12. ENGINEERING MATHEMATICS III      TW    25   10   23  P
 03. SOLID STATES DEVICES AND CIRCUITSPP   100   40   40  P C      13. INTEGRATED CIRCUITS APPLICATIONS PP   100   40   53  P
 04. SOLID STATES DEVICES AND CIRCUITSPR    50   20   25  P C      14. INTEGRATED CIRCUITS APPLICATIONS PR    50   20   33  P
 05. NETWORK ANALYSIS                  PP   100   40   40  P C      15. ELECTROMAGNETIC                  PP   100   40   51  P
 06. DIGITAL LOGIC DESIGN              PP   100   40   58  P C      16. ELECTROMAGNETIC                  TW    25   10   17  P
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07. DIGITAL LOGIC DESIGN	PR	50	20	35	P C		17. DATA STRUCTURES	PP	100	40	71	P
08. POWER DEVICES AND MACHINES	PP	100	40	51	P C		18. DATA STRUCTURES	PR	50	20	45	P
09. NETWORK AND POWER LAB.	TW	50	20	40	P C		19. COMMUNICATION THEORY	PP	100	40	66	P
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	35	P C		20. COMMUNICATION THEORY	OR	50	20	29	P
							21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	41	P

GRAND TOTAL = 915/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

. . . .
                  UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(ELECTRONICS & TELECOM.)
     DATE : 18 AUG. 2011   CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.   PAGE NO. 18 (342)
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  NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.
        OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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  S8053052   LONDHE AARTI SURESH   NANDA   , 71045505E   , S8053052 , PICT   , S8053052

01. SIGNAL AND SYSTEMS	PP	100	40	81	P C		11. ENGINEERING MATHEMATICS III	PP	100	40	76	P
02. SIGNAL AND SYSTEMS	OR	50	20	26	P C		12. ENGINEERING MATHEMATICS III	TW	25	10	22	P
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	65	P C		13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	47	P
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	32	P C		14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	34	P
05. NETWORK ANALYSIS	PP	100	40	42	P C		15. ELECTROMAGNETIC	PP	100	40	55	P
06. DIGITAL LOGIC DESIGN	PP	100	40	64	P C		16. ELECTROMAGNETIC	TW	25	10	20	P
07. DIGITAL LOGIC DESIGN	PR	50	20	33	P C		17. DATA STRUCTURES	PP	100	40	64	P
08. POWER DEVICES AND MACHINES	PP	100	40	47	P C		18. DATA STRUCTURES	PR	50	20	38	P
09. NETWORK AND POWER LAB.	TW	50	20	37	P C		19. COMMUNICATION THEORY	PP	100	40	60	P
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	42	P C		20. COMMUNICATION THEORY	OR	50	20	32	P
							21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	38	P

GRAND TOTAL = 955+05/1500, RESULT: FIRST CLASS[O.163]

ORDN. 1 MARKS :

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S8053053   LUCKY KUMAR   SARITA DEVI   , 71051136B   , S8053053 ,   PICT   , S8053053

01. SIGNAL AND SYSTEMS	PP	100	40	91	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	88	P
02. SIGNAL AND SYSTEMS	OR	50	20	38	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	18	P
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	78	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	55	P
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	38	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	38	P
05. NETWORK ANALYSIS	PP	100	40	57	P C	15. ELECTROMAGNETIC	PP	100	40	62	P
06. DIGITAL LOGIC DESIGN	PP	100	40	79	P C	16. ELECTROMAGNETIC	TW	25	10	20	P
07. DIGITAL LOGIC DESIGN	PR	50	20	45	P C	17. DATA STRUCTURES	PP	100	40	60	P
08. POWER DEVICES AND MACHINES	PP	100	40	58	P C	18. DATA STRUCTURES	PR	50	20	43	P
09. NETWORK AND POWER LAB.	TW	50	20	41	P C	19. COMMUNICATION THEORY	PP	100	40	51	P
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	40	P C	20. COMMUNICATION THEORY	OR	50	20	37	P
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	35	P

GRAND TOTAL = 1072/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

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S8053054   MAGDUM FAREEN JABBAR   YASMIN   , 71045506C   , S8053054 ,   PICT   , S8053054

01. SIGNAL AND SYSTEMS	PP	100	40	60	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	44	P
02. SIGNAL AND SYSTEMS	OR	50	20	20	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	21	P
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	65	P	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	40	P
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	26	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	39	P
05. NETWORK ANALYSIS	PP	100	40	40	P	15. ELECTROMAGNETIC	PP	100	40	55	P
06. DIGITAL LOGIC DESIGN	PP	100	40	57	P C	16. ELECTROMAGNETIC	TW	25	10	19	P
07. DIGITAL LOGIC DESIGN	PR	50	20	21	P C	17. DATA STRUCTURES	PP	100	40	45	P
08. POWER DEVICES AND MACHINES	PP	100	40	40	P C	18. DATA STRUCTURES	PR	50	20	44	P
09. NETWORK AND POWER LAB.	TW	50	20	39	P C	19. COMMUNICATION THEORY	PP	100	40	52	P
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	41	P C	20. COMMUNICATION THEORY	OR	50	20	32	P
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	42	P

GRAND TOTAL = 842/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

. . . . UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(ELECTRONICS & TELECOM.) . . . .

    DATE : 18 AUG. 2011   CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.   PAGE NO. 19 (343)

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  NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.

        OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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  S8053055   MAHAJAN TUSHAR GOPAL   JANABAI   , 71045508K   , S8053055 , PICT   , S8053055

  01. SIGNAL AND SYSTEMS   PP   100   40   67 P C   11. ENGINEERING MATHEMATICS III   PP   100   40   53 P
  02. SIGNAL AND SYSTEMS   OR   50   20   33 P C   12. ENGINEERING MATHEMATICS III   TW   25   10   22 P
  03. SOLID STATES DEVICES AND CIRCUITSPP   100   40   59 P C   13. INTEGRATED CIRCUITS APPLICATIONS PP   100   40   58 P
  04. SOLID STATES DEVICES AND CIRCUITSPR   50   20   38 P C   14. INTEGRATED CIRCUITS APPLICATIONS PR   50   20   30 P
  05. NETWORK ANALYSIS   PP   100   40   49 P C   15. ELECTROMAGNETIC   PP   100   40   69 P
  06. DIGITAL LOGIC DESIGN   PP   100   40   43 P C   16. ELECTROMAGNETIC   TW   25   10   13 P
  07. DIGITAL LOGIC DESIGN   PR   50   20   32 P C   17. DATA STRUCTURES   PP   100   40   50 P
  08. POWER DEVICES AND MACHINES   PP   100   40   50 P C   18. DATA STRUCTURES   PR   50   20   34 P
  09. NETWORK AND POWER LAB.   TW   50   20   38 P C   19. COMMUNICATION THEORY   PP   100   40   54 P
  10. ELECTRONIC INSTRUMENTS AND TOOLS TW   50   20   35 P C   20. COMMUNICATION THEORY   OR   50   20   32 P
                                                                 21. CIRCUIT SIMULATION AND TOOLS   TW   50   20   34 P

GRAND TOTAL = 893+07/1500, RESULT: FIRST CLASS [O.2]

ORDN. 1 MARKS :

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  S8053056   MAHENDRA CHOUDHARY   HEERA   , 71045511K   , S8053056 , PICT   , S8053056

  01. SIGNAL AND SYSTEMS   PP   100   40   64 P C   11. ENGINEERING MATHEMATICS III   PP   100   40   71 P
  02. SIGNAL AND SYSTEMS   OR   50   20   43 P C   12. ENGINEERING MATHEMATICS III   TW   25   10   17 P

03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	50	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	54	P
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	26	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	32	P
05. NETWORK ANALYSIS	PP	100	40	47	P C	15. ELECTROMAGNETIC	PP	100	40	70	P
06. DIGITAL LOGIC DESIGN	PP	100	40	63	P C	16. ELECTROMAGNETIC	TW	25	10	14	P
07. DIGITAL LOGIC DESIGN	PR	50	20	32	P C	17. DATA STRUCTURES	PP	100	40	57	P
08. POWER DEVICES AND MACHINES	PP	100	40	43	P C	18. DATA STRUCTURES	PR	50	20	32	P
09. NETWORK AND POWER LAB.	TW	50	20	37	P C	19. COMMUNICATION THEORY	PP	100	40	45	P
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	39	P C	20. COMMUNICATION THEORY	OR	50	20	29	P
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	38	P

GRAND TOTAL = 903/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

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S8053057   MAHESHWARI SNEHAL RAVINDRA   AARTI   , 71045512H   , S8053057 ,   PICT   , S8053057

01. SIGNAL AND SYSTEMS	PP	100	40	58	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	57	P
02. SIGNAL AND SYSTEMS	OR	50	20	20	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	21	P
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	48	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	41	P
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	25	P	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	30	P
05. NETWORK ANALYSIS	PP	100	40	42	P C	15. ELECTROMAGNETIC	PP	100	40	40	P
06. DIGITAL LOGIC DESIGN	PP	100	40	48	P C	16. ELECTROMAGNETIC	TW	25	10	20	P
07. DIGITAL LOGIC DESIGN	PR	50	20	30	P C	17. DATA STRUCTURES	PP	100	40	48	P
08. POWER DEVICES AND MACHINES	PP	100	40	40	P C	18. DATA STRUCTURES	PR	50	20	38	P
09. NETWORK AND POWER LAB.	TW	50	20	40	P C	19. COMMUNICATION THEORY	PP	100	40	46	P
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	43	P C	20. COMMUNICATION THEORY	OR	50	20	30	P
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	36	P

GRAND TOTAL = 801/1500, RESULT: SECOND CLASS

ORDN. 1 MARKS :

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.

      OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S8053058   MALDODE RAHUL SUDHAKAR   SANGEETA   , 71045514D   , S8053058 , PICT   , S8053058

01. SIGNAL AND SYSTEMS	PP	100	40	51	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	40	P
02. SIGNAL AND SYSTEMS	OR	50	20	26	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	21	P
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	56	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	61	P
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	22	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	30	P
05. NETWORK ANALYSIS	PP	100	40	40	P C	15. ELECTROMAGNETIC	PP	100	40	66	P
06. DIGITAL LOGIC DESIGN	PP	100	40	55	P C	16. ELECTROMAGNETIC	TW	25	10	19	P
07. DIGITAL LOGIC DESIGN	PR	50	20	25	P C	17. DATA STRUCTURES	PP	100	40	56	P
08. POWER DEVICES AND MACHINES	PP	100	40	52	P C	18. DATA STRUCTURES	PR	50	20	38	P
09. NETWORK AND POWER LAB.	TW	50	20	41	P C	19. COMMUNICATION THEORY	PP	100	40	65	P
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	41	P C	20. COMMUNICATION THEORY	OR	50	20	27	P
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	39	P

GRAND TOTAL = 871/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

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S8053059   MANE DEEPALI LIMBAJI   REKHA   , 71045517J   , S8053059 , PICT   , S8053059

01. SIGNAL AND SYSTEMS	PP	100	40	69	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	72	P
02. SIGNAL AND SYSTEMS	OR	50	20	40	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	21	P
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	56	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	51	P
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	32	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	38	P
05. NETWORK ANALYSIS	PP	100	40	52	P C	15. ELECTROMAGNETIC	PP	100	40	63	P
06. DIGITAL LOGIC DESIGN	PP	100	40	60	P C	16. ELECTROMAGNETIC	TW	25	10	21	P
07. DIGITAL LOGIC DESIGN	PR	50	20	29	P C	17. DATA STRUCTURES	PP	100	40	70	P

08. POWER DEVICES AND MACHINES	PP	100	40	54	P C	18. DATA STRUCTURES	PR	50	20	43	P	
09. NETWORK AND POWER LAB.	TW	50	20	37	P C	19. COMMUNICATION THEORY	PP	100	40	61	P	
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	43	P C	20. COMMUNICATION THEORY	OR	50	20	35	P	
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	35	P	

GRAND TOTAL = 982/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

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S8053060   MANE GAURANGI LALASAHEB   KALPANA   , 71045518G   , S8053060 ,   PICT   , S8053060

01. SIGNAL AND SYSTEMS	PP	100	40	52	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	69	P	
02. SIGNAL AND SYSTEMS	OR	50	20	26	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	23	P	
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	42	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	42	P	
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	35	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	35	P	
05. NETWORK ANALYSIS	PP	100	40	48	P C	15. ELECTROMAGNETIC	PP	100	40	45	P	
06. DIGITAL LOGIC DESIGN	PP	100	40	53	P C	16. ELECTROMAGNETIC	TW	25	10	20	P	
07. DIGITAL LOGIC DESIGN	PR	50	20	29	P C	17. DATA STRUCTURES	PP	100	40	64	P	
08. POWER DEVICES AND MACHINES	PP	100	40	40	P C	18. DATA STRUCTURES	PR	50	20	43	P	
09. NETWORK AND POWER LAB.	TW	50	20	40	P C	19. COMMUNICATION THEORY	PP	100	40	49	P	
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	40	P C	20. COMMUNICATION THEORY	OR	50	20	33	P	
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	42	P	

GRAND TOTAL = 870/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

. . . .
                    UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(ELECTRONICS & TELECOM.)
    DATE : 18 AUG. 2011   CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.   PAGE NO. 21 (345)
. . . .
  NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.
        OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
. . . .

S8053061   MANTRI SHRUTI BAJRANG   SUREKHA   , 71045519E   , S8053061 ,   PICT   , S8053061

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01. SIGNAL AND SYSTEMS              PP   100   40   79  P C    11. ENGINEERING MATHEMATICS III      PP   100   40   80  P
02. SIGNAL AND SYSTEMS              OR    50   20   45  P C    12. ENGINEERING MATHEMATICS III      TW    25   10   21  P
03. SOLID STATES DEVICES AND CIRCUITSPP  100   40   47  P C    13. INTEGRATED CIRCUITS APPLICATIONS PP   100   40   48  P
04. SOLID STATES DEVICES AND CIRCUITSPR   50   20   25  P C    14. INTEGRATED CIRCUITS APPLICATIONS PR    50   20   28  P
05. NETWORK ANALYSIS               PP   100   40   58  P C    15. ELECTROMAGNETIC                  PP   100   40   49  P
06. DIGITAL LOGIC DESIGN           PP   100   40   63  P C    16. ELECTROMAGNETIC                  TW    25   10   18  P
07. DIGITAL LOGIC DESIGN           PR    50   20   24  P C    17. DATA STRUCTURES                  PP   100   40   49  P
08. POWER DEVICES AND MACHINES     PP   100   40   57  P C    18. DATA STRUCTURES                  PR    50   20   34  P
09. NETWORK AND POWER LAB.         TW    50   20   41  P C    19. COMMUNICATION THEORY             PP   100   40   42  P
10. ELECTRONIC INSTRUMENTS AND TOOLS TW  50   20   43  P C    20. COMMUNICATION THEORY             OR    50   20   27  P
                                                              21. CIRCUIT SIMULATION AND TOOLS    TW    50   20   36  P
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GRAND TOTAL = 914/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

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S8053062   MERCHANT ALIAKBAR YAHAYA   MEELA   , 71129933B   , S8053062 ,   PICT   , S8053062

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01. SIGNAL AND SYSTEMS              PP   100   40   62  P C    11. ENGINEERING MATHEMATICS III      PP   100   40   66  P
02. SIGNAL AND SYSTEMS              OR    50   20   33  P C    12. ENGINEERING MATHEMATICS III      TW    25   10   21  P
03. SOLID STATES DEVICES AND CIRCUITSPP  100   40   52  P C    13. INTEGRATED CIRCUITS APPLICATIONS PP   100   40   70  P
04. SOLID STATES DEVICES AND CIRCUITSPR   50   20   40  P C    14. INTEGRATED CIRCUITS APPLICATIONS PR    50   20   35  P
05. NETWORK ANALYSIS               PP   100   40   52  P C    15. ELECTROMAGNETIC                  PP   100   40   76  P
06. DIGITAL LOGIC DESIGN           PP   100   40   50  P C    16. ELECTROMAGNETIC                  TW    25   10   17  P
07. DIGITAL LOGIC DESIGN           PR    50   20   39  P C    17. DATA STRUCTURES                  PP   100   40   62  P
08. POWER DEVICES AND MACHINES     PP   100   40   51  P C    18. DATA STRUCTURES                  PR    50   20   43  P
09. NETWORK AND POWER LAB.         TW    50   20   38  P C    19. COMMUNICATION THEORY             PP   100   40   65  P
10. ELECTRONIC INSTRUMENTS AND TOOLS TW  50   20   44  P C    20. COMMUNICATION THEORY             OR    50   20   29  P
                                                              21. CIRCUIT SIMULATION AND TOOLS    TW    50   20   38  P
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GRAND TOTAL = 983/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

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  S8053063   MISHRA ABHISHEK ASHUTOSH   MALA   , 71045525K   , S8053063 ,   PICT   , S8053063

  01. SIGNAL AND SYSTEMS   PP   100   40   40 P C   11. ENGINEERING MATHEMATICS III   PP   100   40   41 P
  02. SIGNAL AND SYSTEMS   OR   50   20   36 P C   12. ENGINEERING MATHEMATICS III   TW   25   10   17 P
  03. SOLID STATES DEVICES AND CIRCUITSPP   100   40   54 P C   13. INTEGRATED CIRCUITS APPLICATIONS PP   100   40   47 P
  04. SOLID STATES DEVICES AND CIRCUITSPR   50   20   40 P C   14. INTEGRATED CIRCUITS APPLICATIONS PR   50   20   33 P
  05. NETWORK ANALYSIS   PP   100   40   40 P   15. ELECTROMAGNETIC   PP   100   40   52 P
  06. DIGITAL LOGIC DESIGN   PP   100   40   48 P C   16. ELECTROMAGNETIC   TW   25   10   20 P
  07. DIGITAL LOGIC DESIGN   PR   50   20   29 P C   17. DATA STRUCTURES   PP   100   40   43 P
  08. POWER DEVICES AND MACHINES   PP   100   40   51 P C   18. DATA STRUCTURES   PR   50   20   46 P
  09. NETWORK AND POWER LAB.   TW   50   20   40 P C   19. COMMUNICATION THEORY   PP   100   40   51 P
  10. ELECTRONIC INSTRUMENTS AND TOOLS TW   50   20   39 P C   20. COMMUNICATION THEORY   OR   50   20   30 P
                                                                 21. CIRCUIT SIMULATION AND TOOLS   TW   50   20   38 P

GRAND TOTAL = 835/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

. . . . UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(ELECTRONICS & TELECOM.) . . . .
     DATE : 18 AUG. 2011   CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.   PAGE NO. 22 (346)
. . . .
  NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.
         OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
. . . .

  S8053064   MOHAMMED HABIBULLAH BAIG NASRULLAH BAIG   SADIYA BEGUM   , 71045673F   , S8053064 ,   PICT   , S8053064

  01. SIGNAL AND SYSTEMS   PP   100   40   94 P C   11. ENGINEERING MATHEMATICS III   PP   100   40   86 P
  02. SIGNAL AND SYSTEMS   OR   50   20   42 P C   12. ENGINEERING MATHEMATICS III   TW   25   10   18 P
  03. SOLID STATES DEVICES AND CIRCUITSPP   100   40   75 P C   13. INTEGRATED CIRCUITS APPLICATIONS PP   100   40   61 P

04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	35	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	38	P
05. NETWORK ANALYSIS	PP	100	40	47	P C	15. ELECTROMAGNETIC	PP	100	40	87	P
06. DIGITAL LOGIC DESIGN	PP	100	40	65	P C	16. ELECTROMAGNETIC	TW	25	10	22	P
07. DIGITAL LOGIC DESIGN	PR	50	20	38	P C	17. DATA STRUCTURES	PP	100	40	63	P
08. POWER DEVICES AND MACHINES	PP	100	40	52	P C	18. DATA STRUCTURES	PR	50	20	48	P
09. NETWORK AND POWER LAB.	TW	50	20	39	P C	19. COMMUNICATION THEORY	PP	100	40	64	P
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	35	P C	20. COMMUNICATION THEORY	OR	50	20	35	P
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	39	P

GRAND TOTAL = 1083/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

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S8053065   MORE VIKAS CHANDRABHAN   SUMAN   , 71045527F   , S8053065 ,   PICT   , S8053065

01. SIGNAL AND SYSTEMS	PP	100	40	44	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	40	P
02. SIGNAL AND SYSTEMS	OR	50	20	22	P	12. ENGINEERING MATHEMATICS III	TW	25	10	15	P
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	40	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	30	F
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	15	F	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	AA	F
05. NETWORK ANALYSIS	PP	100	40	27	F	15. ELECTROMAGNETIC	PP	100	40	22	F
06. DIGITAL LOGIC DESIGN	PP	100	40	40	P C	16. ELECTROMAGNETIC	TW	25	10	17	P
07. DIGITAL LOGIC DESIGN	PR	50	20	25	P C	17. DATA STRUCTURES	PP	100	40	27	F
08. POWER DEVICES AND MACHINES	PP	100	40	42	P C	18. DATA STRUCTURES	PR	50	20	32	P
09. NETWORK AND POWER LAB.	TW	50	20	39	P C	19. COMMUNICATION THEORY	PP	100	40	40	P
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	38	P C	20. COMMUNICATION THEORY	OR	50	20	29	P
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	33	P

GRAND TOTAL = 617/1500, RESULT: FAILS

ORDN. 1 MARKS :

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S8053066   MRIDUL NAGAR   MANJU   , 71045528D   , S8053066 ,   PICT   , S8053066

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01. SIGNAL AND SYSTEMS                 PP   100   40   52  P C     11. ENGINEERING MATHEMATICS III        PP   100   40   60  P
02. SIGNAL AND SYSTEMS                 OR    50   20   35  P C     12. ENGINEERING MATHEMATICS III        TW    25   10   18  P
03. SOLID STATES DEVICES AND CIRCUITSPP     100   40   46  P C     13. INTEGRATED CIRCUITS APPLICATIONS PP    100   40   26  F
04. SOLID STATES DEVICES AND CIRCUITSPR      50   20   30  P C     14. INTEGRATED CIRCUITS APPLICATIONS PR     50   20   30  P
05. NETWORK ANALYSIS                   PP   100   40   40  P C     15. ELECTROMAGNETIC                    PP   100   40   53  P
06. DIGITAL LOGIC DESIGN               PP   100   40   40  P C     16. ELECTROMAGNETIC                    TW    25   10   13  P
07. DIGITAL LOGIC DESIGN               PR    50   20   30  P C     17. DATA STRUCTURES                    PP   100   40   48  P
08. POWER DEVICES AND MACHINES         PP   100   40   44  P C     18. DATA STRUCTURES                    PR    50   20   28  P
09. NETWORK AND POWER LAB.             TW    50   20   39  P C     19. COMMUNICATION THEORY               PP   100   40   48  P
10. ELECTRONIC INSTRUMENTS AND TOOLS TW      50   20   35  P C     20. COMMUNICATION THEORY               OR    50   20   21  P
                                                                   21. CIRCUIT SIMULATION AND TOOLS      TW    50   20   26  P

GRAND TOTAL =  762/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :
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. . . . UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(ELECTRONICS & TELECOM.) . . . .

    DATE : 18 AUG. 2011   CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.   PAGE NO. 23 (347)
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NOTE: FIRST LINE : SEAT NO.,  NAME OF THE CANDIDATE,  MOTHER, PERMANENT REG. NO.,  PREVIOUS SEAT NO.,  COLLEGE,   SEAT NO.

      OTHER LINES: HEAD OF PASSING,  MAX. MARKS,  MIN. PASS MARKS,  MARKS OBTAINED,  P/F:PASS/FAIL,  C:PREVIOUS CARRY OVER
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S8053067   MUJAWAR SAMIR HANIF                    RUBAB              , 71045529B     , S8053067 ,  PICT      , S8053067

01. SIGNAL AND SYSTEMS                 PP   100   40   66  P C     11. ENGINEERING MATHEMATICS III        PP   100   40   49  P
02. SIGNAL AND SYSTEMS                 OR    50   20   36  P C     12. ENGINEERING MATHEMATICS III        TW    25   10   18  P
03. SOLID STATES DEVICES AND CIRCUITSPP     100   40   46  P C     13. INTEGRATED CIRCUITS APPLICATIONS PP    100   40   40  P
04. SOLID STATES DEVICES AND CIRCUITSPR      50   20   26  P C     14. INTEGRATED CIRCUITS APPLICATIONS PR     50   20   25  P
05. NETWORK ANALYSIS                   PP   100   40   40  P C     15. ELECTROMAGNETIC                    PP   100   40   59  P
06. DIGITAL LOGIC DESIGN               PP   100   40   40  P C     16. ELECTROMAGNETIC                    TW    25   10   18  P
07. DIGITAL LOGIC DESIGN               PR    50   20   28  P C     17. DATA STRUCTURES                    PP   100   40   50  P
08. POWER DEVICES AND MACHINES         PP   100   40   40  P C     18. DATA STRUCTURES                    PR    50   20   34  P
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09. NETWORK AND POWER LAB.	TW	50	20	39	P C	19. COMMUNICATION THEORY	PP	100	40	40	P
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	41	P C	20. COMMUNICATION THEORY	OR	50	20	22	P
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	34	P

GRAND TOTAL = 791/1500, RESULT: SECOND CLASS

ORDN. 1 MARKS :

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S8053068   MULLA SHARIQA AZAMALI   ARIFA   , 71045531D   , S8053068 ,   PICT   , S8053068

01. SIGNAL AND SYSTEMS	PP	100	40	67	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	73	P
02. SIGNAL AND SYSTEMS	OR	50	20	35	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	21	P
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	62	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	40	P
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	29	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	35	P
05. NETWORK ANALYSIS	PP	100	40	68	P C	15. ELECTROMAGNETIC	PP	100	40	56	P
06. DIGITAL LOGIC DESIGN	PP	100	40	46	P C	16. ELECTROMAGNETIC	TW	25	10	13	P
07. DIGITAL LOGIC DESIGN	PR	50	20	25	P C	17. DATA STRUCTURES	PP	100	40	63	P
08. POWER DEVICES AND MACHINES	PP	100	40	58	P C	18. DATA STRUCTURES	PR	50	20	43	P
09. NETWORK AND POWER LAB.	TW	50	20	37	P C	19. COMMUNICATION THEORY	PP	100	40	64	P
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	37	P C	20. COMMUNICATION THEORY	OR	50	20	40	P
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	34	P

GRAND TOTAL = 946/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

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S8053069   MUNDANKAR SUSHANT SANJAY   SHAILA   , 71045532B   , S8053069 ,   PICT   , S8053069

01. SIGNAL AND SYSTEMS	PP	100	40	83	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	86	P
02. SIGNAL AND SYSTEMS	OR	50	20	37	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	18	P
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	56	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	58	P
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	35	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	33	P

05. NETWORK ANALYSIS	PP	100	40	61	P C		15. ELECTROMAGNETIC	PP	100	40	65	P
06. DIGITAL LOGIC DESIGN	PP	100	40	62	P C		16. ELECTROMAGNETIC	TW	25	10	20	P
07. DIGITAL LOGIC DESIGN	PR	50	20	30	P C		17. DATA STRUCTURES	PP	100	40	59	P
08. POWER DEVICES AND MACHINES	PP	100	40	57	P C		18. DATA STRUCTURES	PR	50	20	36	P
09. NETWORK AND POWER LAB.	TW	50	20	37	P C		19. COMMUNICATION THEORY	PP	100	40	40	P
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	37	P C		20. COMMUNICATION THEORY	OR	50	20	35	P
							21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	35	P

GRAND TOTAL = 980/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

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UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(ELECTRONICS & TELECOM.)
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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S8053070   NAGHATE ANKIT VIJAY   MADHURI   , 71045537C   , S8053070 ,   PICT   , S8053070

01. SIGNAL AND SYSTEMS	PP	100	40	80	P C		11. ENGINEERING MATHEMATICS III	PP	100	40	72	P
02. SIGNAL AND SYSTEMS	OR	50	20	35	P C		12. ENGINEERING MATHEMATICS III	TW	25	10	21	P
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	40	P C		13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	48	P
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	20	P C		14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	40	P
05. NETWORK ANALYSIS	PP	100	40	49	P C		15. ELECTROMAGNETIC	PP	100	40	66	P
06. DIGITAL LOGIC DESIGN	PP	100	40	78	P C		16. ELECTROMAGNETIC	TW	25	10	20	P
07. DIGITAL LOGIC DESIGN	PR	50	20	44	P C		17. DATA STRUCTURES	PP	100	40	73	P
08. POWER DEVICES AND MACHINES	PP	100	40	45	P C		18. DATA STRUCTURES	PR	50	20	48	P
09. NETWORK AND POWER LAB.	TW	50	20	37	P C		19. COMMUNICATION THEORY	PP	100	40	51	P
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	39	P C		20. COMMUNICATION THEORY	OR	50	20	33	P
							21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	41	P

GRAND TOTAL = 980/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

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S8053071   NERLEKAR SHWETA PRAKASH   SUGANDHA   , 71045546B   , S8053071 ,   PICT   , S8053071

01. SIGNAL AND SYSTEMS	PP	100	40	46	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	66	P
02. SIGNAL AND SYSTEMS	OR	50	20	22	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	21	P
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	40	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	18	F
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	25	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	35	P
05. NETWORK ANALYSIS	PP	100	40	40	P C	15. ELECTROMAGNETIC	PP	100	40	20	F
06. DIGITAL LOGIC DESIGN	PP	100	40	24	F	16. ELECTROMAGNETIC	TW	25	10	19	P
07. DIGITAL LOGIC DESIGN	PR	50	20	25	P	17. DATA STRUCTURES	PP	100	40	50	P
08. POWER DEVICES AND MACHINES	PP	100	40	40	P C	18. DATA STRUCTURES	PR	50	20	36	P
09. NETWORK AND POWER LAB.	TW	50	20	40	P C	19. COMMUNICATION THEORY	PP	100	40	32	F
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	41	P C	20. COMMUNICATION THEORY	OR	50	20	27	P
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	37	P

GRAND TOTAL = 704/1500, RESULT: FAILS

ORDN. 1 MARKS :

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S8053072   NEVASE PRAJAKTA MARUTI   NANDA   , 71129934L   , S8053072 ,   PICT   , S8053072

01. SIGNAL AND SYSTEMS	PP	100	40	52	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	40	P
02. SIGNAL AND SYSTEMS	OR	50	20	16	F	12. ENGINEERING MATHEMATICS III	TW	25	10	23	P
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	40	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	57	P
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	26	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	34	P
05. NETWORK ANALYSIS	PP	100	40	29	F	15. ELECTROMAGNETIC	PP	100	40	43	P
06. DIGITAL LOGIC DESIGN	PP	100	40	68	P C	16. ELECTROMAGNETIC	TW	25	10	20	P
07. DIGITAL LOGIC DESIGN	PR	50	20	34	P C	17. DATA STRUCTURES	PP	100	40	63	P
08. POWER DEVICES AND MACHINES	PP	100	40	54	P C	18. DATA STRUCTURES	PR	50	20	35	P
09. NETWORK AND POWER LAB.	TW	50	20	41	P C	19. COMMUNICATION THEORY	PP	100	40	47	P

cols05

| 10. ELECTRONIC INSTRUMENTS AND TOOLS | TW | 50 | 20 | 37 | P C | | 20. COMMUNICATION THEORY | OR | 50 | 20 | 22 | P |
| | | | | | | | 21. CIRCUIT SIMULATION AND TOOLS | TW | 50 | 20 | 40 | P |

GRAND TOTAL = 821/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.

      OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S8053073   NIKAM JEEVAN SHANKAR   SANGITA   , 71045547L   , S8053073 , PICT   , S8053073

01. SIGNAL AND SYSTEMS	PP	100	40	19	F		11. ENGINEERING MATHEMATICS III	PP	100	40	25	F
02. SIGNAL AND SYSTEMS	OR	50	20	20	P		12. ENGINEERING MATHEMATICS III	TW	25	10	23	P
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	40	P		13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	25	F
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	20	P		14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	25	P
05. NETWORK ANALYSIS	PP	100	40	24	F		15. ELECTROMAGNETIC	PP	100	40	28	F
06. DIGITAL LOGIC DESIGN	PP	100	40	40	P C		16. ELECTROMAGNETIC	TW	25	10	15	P
07. DIGITAL LOGIC DESIGN	PR	50	20	29	P C		17. DATA STRUCTURES	PP	100	40	40	P
08. POWER DEVICES AND MACHINES	PP	100	40	40	P		18. DATA STRUCTURES	PR	50	20	28	P
09. NETWORK AND POWER LAB.	TW	50	20	37	P C		19. COMMUNICATION THEORY	PP	100	40	23	F
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	39	P C		20. COMMUNICATION THEORY	OR	50	20	23	P
							21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	37	P

GRAND TOTAL = 600/1500, RESULT: FAILS

ORDN. 1 MARKS :

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S8053074   NIRMAL MAYUR VINAYAKRAO   SUNITA   , 71045548J   , S8053074 , PICT   , S8053074

01. SIGNAL AND SYSTEMS	PP	100	40	86	P	C	11. ENGINEERING MATHEMATICS III	PP	100	40	79	P	
02. SIGNAL AND SYSTEMS	OR	50	20	40	P	C	12. ENGINEERING MATHEMATICS III	TW	25	10	23	P	
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	72	P	C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	69	P
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	34	P	C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	39	P
05. NETWORK ANALYSIS	PP	100	40	49	P	C	15. ELECTROMAGNETIC	PP	100	40	61	P	
06. DIGITAL LOGIC DESIGN	PP	100	40	66	P	C	16. ELECTROMAGNETIC	TW	25	10	21	P	
07. DIGITAL LOGIC DESIGN	PR	50	20	24	P	C	17. DATA STRUCTURES	PP	100	40	52	P	
08. POWER DEVICES AND MACHINES	PP	100	40	58	P	C	18. DATA STRUCTURES	PR	50	20	38	P	
09. NETWORK AND POWER LAB.	TW	50	20	40	P	C	19. COMMUNICATION THEORY	PP	100	40	66	P	
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	44	P	C	20. COMMUNICATION THEORY	OR	50	20	32	P
							21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	37	P	

GRAND TOTAL = 1030/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

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S8053075   PACHANGE ASHISH NARAYAN   KALPANA   , 71045551J   , S8053075 ,   PICT   , S8053075

01. SIGNAL AND SYSTEMS	PP	100	40	80	P	C	11. ENGINEERING MATHEMATICS III	PP	100	40	64	P	
02. SIGNAL AND SYSTEMS	OR	50	20	34	P	C	12. ENGINEERING MATHEMATICS III	TW	25	10	22	P	
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	53	P	C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	51	P
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	34	P	C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	30	P
05. NETWORK ANALYSIS	PP	100	40	64	P		15. ELECTROMAGNETIC	PP	100	40	43	P	
06. DIGITAL LOGIC DESIGN	PP	100	40	77	P	C	16. ELECTROMAGNETIC	TW	25	10	10	P	
07. DIGITAL LOGIC DESIGN	PR	50	20	39	P	C	17. DATA STRUCTURES	PP	100	40	64	P	
08. POWER DEVICES AND MACHINES	PP	100	40	58	P	C	18. DATA STRUCTURES	PR	50	20	46	P	
09. NETWORK AND POWER LAB.	TW	50	20	38	P	C	19. COMMUNICATION THEORY	PP	100	40	58	P	
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	26	P	C	20. COMMUNICATION THEORY	OR	50	20	34	P
							21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	34	P	

GRAND TOTAL = 959/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

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    DATE : 18 AUG. 2011              CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.              PAGE NO.  26  (  350)

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   NOTE: FIRST LINE : SEAT NO.,  NAME OF THE CANDIDATE,  MOTHER, PERMANENT REG. NO.,  PREVIOUS SEAT NO.,  COLLEGE,   SEAT NO.

         OTHER LINES: HEAD OF PASSING,  MAX. MARKS,  MIN. PASS MARKS,  MARKS OBTAINED,  P/F:PASS/FAIL,  C:PREVIOUS CARRY OVER

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S8053076   PARODE ASHWINI SANJAY   JAISHREE   , 71045559D   , S8053076 , PICT   , S8053076

01. SIGNAL AND SYSTEMS	PP	100	40	68	P C		11. ENGINEERING MATHEMATICS III	PP	100	40	67	P
02. SIGNAL AND SYSTEMS	OR	50	20	20	P C		12. ENGINEERING MATHEMATICS III	TW	25	10	25	P
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	40	P C		13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	40	P
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	30	P C		14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	35	P
05. NETWORK ANALYSIS	PP	100	40	40	P C		15. ELECTROMAGNETIC	PP	100	40	46	P
06. DIGITAL LOGIC DESIGN	PP	100	40	57	P C		16. ELECTROMAGNETIC	TW	25	10	20	P
07. DIGITAL LOGIC DESIGN	PR	50	20	33	P C		17. DATA STRUCTURES	PP	100	40	64	P
08. POWER DEVICES AND MACHINES	PP	100	40	45	P C		18. DATA STRUCTURES	PR	50	20	44	P
09. NETWORK AND POWER LAB.	TW	50	20	43	P C		19. COMMUNICATION THEORY	PP	100	40	43	P
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	42	P C		20. COMMUNICATION THEORY	OR	50	20	30	P
							21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	42	P

GRAND TOTAL = 874/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

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S8053077   PATIL GAURAV VISHRAM   PRAMILA   , 71045564L   , S8053077 , PICT   , S8053077

01. SIGNAL AND SYSTEMS	PP	100	40	53	P C		11. ENGINEERING MATHEMATICS III	PP	100	40	42	P
02. SIGNAL AND SYSTEMS	OR	50	20	20	P C		12. ENGINEERING MATHEMATICS III	TW	25	10	20	P
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	52	P		13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	51	P
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	28	P C		14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	27	P
05. NETWORK ANALYSIS	PP	100	40	40	P C		15. ELECTROMAGNETIC	PP	100	40	69	P

06. DIGITAL LOGIC DESIGN	PP	100	40	52	P C	16. ELECTROMAGNETIC	TW	25	10	17	P
07. DIGITAL LOGIC DESIGN	PR	50	20	32	P C	17. DATA STRUCTURES	PP	100	40	47	P
08. POWER DEVICES AND MACHINES	PP	100	40	59	P C	18. DATA STRUCTURES	PR	50	20	44	P
09. NETWORK AND POWER LAB.	TW	50	20	36	P C	19. COMMUNICATION THEORY	PP	100	40	53	P
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	39	P C	20. COMMUNICATION THEORY	OR	50	20	24	P
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	34	P

GRAND TOTAL = 839/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

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S8053078   PATIL OMKAR VISHWAS   VANITA   , 71045566G   , S8053078 ,   PICT   , S8053078

01. SIGNAL AND SYSTEMS	PP	100	40	85	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	79	P
02. SIGNAL AND SYSTEMS	OR	50	20	37	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	21	P
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	50	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	64	P
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	38	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	35	P
05. NETWORK ANALYSIS	PP	100	40	45	P C	15. ELECTROMAGNETIC	PP	100	40	64	P
06. DIGITAL LOGIC DESIGN	PP	100	40	57	P C	16. ELECTROMAGNETIC	TW	25	10	21	P
07. DIGITAL LOGIC DESIGN	PR	50	20	32	P C	17. DATA STRUCTURES	PP	100	40	60	P
08. POWER DEVICES AND MACHINES	PP	100	40	49	P C	18. DATA STRUCTURES	PR	50	20	38	P
09. NETWORK AND POWER LAB.	TW	50	20	40	P C	19. COMMUNICATION THEORY	PP	100	40	59	P
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	43	P C	20. COMMUNICATION THEORY	OR	50	20	32	P
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	42	P

GRAND TOTAL = 991/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

. . . .
                 UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(ELECTRONICS & TELECOM.)
    DATE : 18 AUG. 2011   CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.   PAGE NO. 27 (351)
. . . .
 NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S8053079   PATIL SONAM SHASHIKANT   SHUBHANGI   , 71129935J   , S8053079 , PICT   , S8053079

01. SIGNAL AND SYSTEMS	PP	100	40	73	P C		11. ENGINEERING MATHEMATICS III	PP	100	40	76	P
02. SIGNAL AND SYSTEMS	OR	50	20	32	P C		12. ENGINEERING MATHEMATICS III	TW	25	10	21	P
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	45	P C		13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	56	P
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	35	P C		14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	35	P
05. NETWORK ANALYSIS	PP	100	40	40	P C		15. ELECTROMAGNETIC	PP	100	40	64	P
06. DIGITAL LOGIC DESIGN	PP	100	40	60	P C		16. ELECTROMAGNETIC	TW	25	10	19	P
07. DIGITAL LOGIC DESIGN	PR	50	20	29	P C		17. DATA STRUCTURES	PP	100	40	64	P
08. POWER DEVICES AND MACHINES	PP	100	40	64	P C		18. DATA STRUCTURES	PR	50	20	42	P
09. NETWORK AND POWER LAB.	TW	50	20	38	P C		19. COMMUNICATION THEORY	PP	100	40	65	P
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	42	P C		20. COMMUNICATION THEORY	OR	50	20	35	P
							21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	41	P

GRAND TOTAL = 976/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

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S8053080   PATIL TEJAS LAXMAN   SUREKHA   , 71045568C   , S8053080 , PICT   , S8053080

01. SIGNAL AND SYSTEMS	PP	100	40	76	P C		11. ENGINEERING MATHEMATICS III	PP	100	40	66	P
02. SIGNAL AND SYSTEMS	OR	50	20	40	P C		12. ENGINEERING MATHEMATICS III	TW	25	10	21	P
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	64	P C		13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	32*	P
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	34	P C		14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	33	P
05. NETWORK ANALYSIS	PP	100	40	43	P C		15. ELECTROMAGNETIC	PP	100	40	50	P
06. DIGITAL LOGIC DESIGN	PP	100	40	63	P C		16. ELECTROMAGNETIC	TW	25	10	19	P
07. DIGITAL LOGIC DESIGN	PR	50	20	33	P C		17. DATA STRUCTURES	PP	100	40	52	P
08. POWER DEVICES AND MACHINES	PP	100	40	50	P C		18. DATA STRUCTURES	PR	50	20	44	P
09. NETWORK AND POWER LAB.	TW	50	20	40	P C		19. COMMUNICATION THEORY	PP	100	40	47	P
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	43	P C		20. COMMUNICATION THEORY	OR	50	20	40	P

                                                          21. CIRCUIT SIMULATION AND TOOLS   TW   50   20   37 P

GRAND TOTAL = 927/1500, RESULT: FIRST CLASS   * [O.4]

ORDN. 1 MARKS :

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  S8053081   PAWADE NITIN NARAYAN   RENUKA   , 71045569M   , S8053081 ,   PICT   , S8053081

  01. SIGNAL AND SYSTEMS   PP   100   40   71 P C   11. ENGINEERING MATHEMATICS III   PP   100   40   56 P
  02. SIGNAL AND SYSTEMS   OR   50   20   35 P C   12. ENGINEERING MATHEMATICS III   TW   25   10   23 P
  03. SOLID STATES DEVICES AND CIRCUITSPP   100   40   55 P C   13. INTEGRATED CIRCUITS APPLICATIONS PP   100   40   40 P
  04. SOLID STATES DEVICES AND CIRCUITSPR   50   20   38 P C   14. INTEGRATED CIRCUITS APPLICATIONS PR   50   20   30 P
  05. NETWORK ANALYSIS   PP   100   40   40 P C   15. ELECTROMAGNETIC   PP   100   40   67 P
  06. DIGITAL LOGIC DESIGN   PP   100   40   76 P C   16. ELECTROMAGNETIC   TW   25   10   20 P
  07. DIGITAL LOGIC DESIGN   PR   50   20   34 P C   17. DATA STRUCTURES   PP   100   40   52 P
  08. POWER DEVICES AND MACHINES   PP   100   40   49 P C   18. DATA STRUCTURES   PR   50   20   39 P
  09. NETWORK AND POWER LAB.   TW   50   20   44 P C   19. COMMUNICATION THEORY   PP   100   40   68 P
  10. ELECTRONIC INSTRUMENTS AND TOOLS TW   50   20   43 P C   20. COMMUNICATION THEORY   OR   50   20   27 P
                                                                    21. CIRCUIT SIMULATION AND TOOLS   TW   50   20   38 P

GRAND TOTAL = 945/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

. . . .
                    UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(ELECTRONICS & TELECOM.)
     DATE : 18 AUG. 2011   CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.   PAGE NO. 28 (352)
. . . .
  NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.
        OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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  S8053082   PAWAR PRADIPKUMAR RAOSAHEB   SANGITA   , 71045572M   , S8053082 ,   PICT   , S8053082

  01. SIGNAL AND SYSTEMS   PP   100   40   64 P C   11. ENGINEERING MATHEMATICS III   PP   100   40   70 P

02. SIGNAL AND SYSTEMS	OR	50	20	20	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	22	P	
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	46	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	51	P	
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	30	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	30	P	
05. NETWORK ANALYSIS	PP	100	40	49	P C	15. ELECTROMAGNETIC	PP	100	40	68	P	
06. DIGITAL LOGIC DESIGN	PP	100	40	59	P C	16. ELECTROMAGNETIC	TW	25	10	17	P	
07. DIGITAL LOGIC DESIGN	PR	50	20	34	P C	17. DATA STRUCTURES	PP	100	40	58	P	
08. POWER DEVICES AND MACHINES	PP	100	40	41	P C	18. DATA STRUCTURES	PR	50	20	32	P	
09. NETWORK AND POWER LAB.	TW	50	20	39	P C	19. COMMUNICATION THEORY	PP	100	40	46	P	
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	34	P C	20. COMMUNICATION THEORY	OR	50	20	27	P	
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	35	P	

GRAND TOTAL = 872/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

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S8053083   PAWAR SURAJ SUBHASH   VIDYA   , 71045573K   , S8053083 ,   PICT   , S8053083

01. SIGNAL AND SYSTEMS	PP	100	40	52	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	47	P	
02. SIGNAL AND SYSTEMS	OR	50	20	26	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	19	P	
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	44	P	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	28	F	
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	28	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	25	P	
05. NETWORK ANALYSIS	PP	100	40	40	P	15. ELECTROMAGNETIC	PP	100	40	40	P	
06. DIGITAL LOGIC DESIGN	PP	100	40	25	F	16. ELECTROMAGNETIC	TW	25	10	18	P	
07. DIGITAL LOGIC DESIGN	PR	50	20	24	P C	17. DATA STRUCTURES	PP	100	40	45	P	
08. POWER DEVICES AND MACHINES	PP	100	40	40	P C	18. DATA STRUCTURES	PR	50	20	43	P	
09. NETWORK AND POWER LAB.	TW	50	20	35	P C	19. COMMUNICATION THEORY	PP	100	40	20	F	
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	36	P C	20. COMMUNICATION THEORY	OR	50	20	27	P	
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	35	P	

GRAND TOTAL = 697/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

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S8053084   PITRODA UTSAV RAJENDRA   KAILASH   , 71129936G   , S8053084 , PICT   , S8053084

01. SIGNAL AND SYSTEMS	PP	100	40	40	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	18	F
02. SIGNAL AND SYSTEMS	OR	50	20	21	P	12. ENGINEERING MATHEMATICS III	TW	25	10	18	P
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	49	P	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	51	P
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	28	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	30	P
05. NETWORK ANALYSIS	PP	100	40	19	F	15. ELECTROMAGNETIC	PP	100	40	40	P
06. DIGITAL LOGIC DESIGN	PP	100	40	40	P C	16. ELECTROMAGNETIC	TW	25	10	16	P
07. DIGITAL LOGIC DESIGN	PR	50	20	24	P C	17. DATA STRUCTURES	PP	100	40	43	P
08. POWER DEVICES AND MACHINES	PP	100	40	48	P C	18. DATA STRUCTURES	PR	50	20	45	P
09. NETWORK AND POWER LAB.	TW	50	20	41	P C	19. COMMUNICATION THEORY	PP	100	40	40	P
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	38	P C	20. COMMUNICATION THEORY	OR	50	20	20	P
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	42	P

GRAND TOTAL = 711/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

. . . .
            UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(ELECTRONICS & TELECOM.)
     DATE : 18 AUG. 2011   CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.   PAGE NO. 29 (353)
. . . .
  NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.
        OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S8053085   POL AMIT KISHOR   DAYA   , 71045575F   , S8053085 , PICT   , S8053085

01. SIGNAL AND SYSTEMS	PP	100	40	80	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	95	P
02. SIGNAL AND SYSTEMS	OR	50	20	29	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	24	P
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	57	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	44	P
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	30	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	36	P
05. NETWORK ANALYSIS	PP	100	40	42	P C	15. ELECTROMAGNETIC	PP	100	40	65	P
06. DIGITAL LOGIC DESIGN	PP	100	40	82	P C	16. ELECTROMAGNETIC	TW	25	10	23	P

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07. DIGITAL LOGIC DESIGN          PR    50   20   29  P C    17. DATA STRUCTURES                PP   100   40   65  P
08. POWER DEVICES AND MACHINES    PP   100   40   47  P C    18. DATA STRUCTURES                PR    50   20   46  P
09. NETWORK AND POWER LAB.        TW    50   20   41  P C    19. COMMUNICATION THEORY           PP   100   40   48  P
10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50   20   38  P C    20. COMMUNICATION THEORY           OR    50   20   21  P
                                                             21. CIRCUIT SIMULATION AND TOOLS  TW    50   20   40  P
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GRAND TOTAL = 982/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

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S8053086   PRABHUDESAI PRATHAMESH PRAKASH   ADITI   , 71045579J   , S8053086 ,   PICT   , S8053086

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01. SIGNAL AND SYSTEMS            PP   100   40   78  P C    11. ENGINEERING MATHEMATICS III    PP   100   40   63  P
02. SIGNAL AND SYSTEMS            OR    50   20   42  P C    12. ENGINEERING MATHEMATICS III    TW    25   10   21  P
03. SOLID STATES DEVICES AND CIRCUITSPP 100  40   67  P C    13. INTEGRATED CIRCUITS APPLICATIONS PP 100  40   56  P
04. SOLID STATES DEVICES AND CIRCUITSPR  50  20   36  P C    14. INTEGRATED CIRCUITS APPLICATIONS PR  50  20   41  P
05. NETWORK ANALYSIS              PP   100   40   45  P C    15. ELECTROMAGNETIC                PP   100   40   70  P
06. DIGITAL LOGIC DESIGN          PP   100   40   69  P C    16. ELECTROMAGNETIC                TW    25   10   17  P
07. DIGITAL LOGIC DESIGN          PR    50   20   42  P C    17. DATA STRUCTURES                PP   100   40   62  P
08. POWER DEVICES AND MACHINES    PP   100   40   61  P C    18. DATA STRUCTURES                PR    50   20   46  P
09. NETWORK AND POWER LAB.        TW    50   20   42  P C    19. COMMUNICATION THEORY           PP   100   40   67  P
10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50   20   39  P C    20. COMMUNICATION THEORY           OR    50   20   35  P
                                                             21. CIRCUIT SIMULATION AND TOOLS  TW    50   20   40  P
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GRAND TOTAL = 1039/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

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S8053087   PRIYANKA TOMAR   SHASHI   , 71045583G   , S8053087 ,   PICT   , S8053087

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01. SIGNAL AND SYSTEMS            PP   100   40   77  P C    11. ENGINEERING MATHEMATICS III    PP   100   40   51  P
02. SIGNAL AND SYSTEMS            OR    50   20   32  P C    12. ENGINEERING MATHEMATICS III    TW    25   10   18  P
03. SOLID STATES DEVICES AND CIRCUITSPP 100  40   40  P C    13. INTEGRATED CIRCUITS APPLICATIONS PP 100  40   40  P
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04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	28	P C		14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	37	P
05. NETWORK ANALYSIS	PP	100	40	42	P C		15. ELECTROMAGNETIC	PP	100	40	54	P
06. DIGITAL LOGIC DESIGN	PP	100	40	49	P C		16. ELECTROMAGNETIC	TW	25	10	11	P
07. DIGITAL LOGIC DESIGN	PR	50	20	30	P C		17. DATA STRUCTURES	PP	100	40	61	P
08. POWER DEVICES AND MACHINES	PP	100	40	40	P C		18. DATA STRUCTURES	PR	50	20	46	P
09. NETWORK AND POWER LAB.	TW	50	20	38	P C		19. COMMUNICATION THEORY	PP	100	40	40	P
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	44	P C		20. COMMUNICATION THEORY	OR	50	20	34	P
							21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	26	P

GRAND TOTAL = 838/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

. . . . UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(ELECTRONICS & TELECOM.) . . . .

DATE : 18 AUG. 2011   CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.   PAGE NO. 30 (354)

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.

      OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S8053089   ROOPAL OBEROI   AMARJEET   , 71045591H   , S8053089 ,   PICT   , S8053089

01. SIGNAL AND SYSTEMS	PP	100	40	63	P C		11. ENGINEERING MATHEMATICS III	PP	100	40	60	P
02. SIGNAL AND SYSTEMS	OR	50	20	32	P C		12. ENGINEERING MATHEMATICS III	TW	25	10	21	P
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	48	P		13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	40	P
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	29	P C		14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	38	P
05. NETWORK ANALYSIS	PP	100	40	42	P		15. ELECTROMAGNETIC	PP	100	40	54	P
06. DIGITAL LOGIC DESIGN	PP	100	40	40	P C		16. ELECTROMAGNETIC	TW	25	10	17	P
07. DIGITAL LOGIC DESIGN	PR	50	20	24	P C		17. DATA STRUCTURES	PP	100	40	47	P
08. POWER DEVICES AND MACHINES	PP	100	40	43	P C		18. DATA STRUCTURES	PR	50	20	32	P
09. NETWORK AND POWER LAB.	TW	50	20	40	P C		19. COMMUNICATION THEORY	PP	100	40	42	P
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	37	P C		20. COMMUNICATION THEORY	OR	50	20	31	P
							21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	28	P

GRAND TOTAL = 808/1500, RESULT: SECOND CLASS

ORDN. 1 MARKS :

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S8053090   SAURABH SANJAY MANE   PALLAVI   , 71045601J   , S8053090 ,   PICT   , S8053090

01. SIGNAL AND SYSTEMS	PP	100	40	74	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	52	P
02. SIGNAL AND SYSTEMS	OR	50	20	35	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	17	P
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	47	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	51	P
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	33	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	34	P
05. NETWORK ANALYSIS	PP	100	40	47	P C	15. ELECTROMAGNETIC	PP	100	40	61	P
06. DIGITAL LOGIC DESIGN	PP	100	40	49	P C	16. ELECTROMAGNETIC	TW	25	10	18	P
07. DIGITAL LOGIC DESIGN	PR	50	20	29	P C	17. DATA STRUCTURES	PP	100	40	50	P
08. POWER DEVICES AND MACHINES	PP	100	40	52	P C	18. DATA STRUCTURES	PR	50	20	44	P
09. NETWORK AND POWER LAB.	TW	50	20	42	P C	19. COMMUNICATION THEORY	PP	100	40	63	P
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	42	P C	20. COMMUNICATION THEORY	OR	50	20	22	P
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	37	P

GRAND TOTAL = 899+01/1500, RESULT: FIRST CLASS [O.2]

ORDN. 1 MARKS :

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S8053091   SAYED SAIF IFTEKAR   SHAKILA   , 71045604C   , S8053091 ,   PICT   , S8053091

01. SIGNAL AND SYSTEMS	PP	100	40	48	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	43	P
02. SIGNAL AND SYSTEMS	OR	50	20	27	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	20	P
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	42	P	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	14	F
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	36	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	43	P
05. NETWORK ANALYSIS	PP	100	40	40	P	15. ELECTROMAGNETIC	PP	100	40	48	P
06. DIGITAL LOGIC DESIGN	PP	100	40	40	P C	16. ELECTROMAGNETIC	TW	25	10	17	P
07. DIGITAL LOGIC DESIGN	PR	50	20	30	P C	17. DATA STRUCTURES	PP	100	40	49	P
08. POWER DEVICES AND MACHINES	PP	100	40	40	P C	18. DATA STRUCTURES	PR	50	20	45	P

09. NETWORK AND POWER LAB.	TW	50	20	41	P C		19. COMMUNICATION THEORY	PP	100	40	40	P
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	41	P C		20. COMMUNICATION THEORY	OR	50	20	28	P
							21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	37	P

GRAND TOTAL = 769/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

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UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(ELECTRONICS & TELECOM.)

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S8053092   SHAIKH SHOAIB IQBAL   REHANA   , 71129937E   , S8053092 ,   PICT   , S8053092

01. SIGNAL AND SYSTEMS	PP	100	40	83	P C		11. ENGINEERING MATHEMATICS III	PP	100	40	72	P
02. SIGNAL AND SYSTEMS	OR	50	20	38	P C		12. ENGINEERING MATHEMATICS III	TW	25	10	21	P
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	62	P C		13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	53	P
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	39	P C		14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	37	P
05. NETWORK ANALYSIS	PP	100	40	42	P C		15. ELECTROMAGNETIC	PP	100	40	61	P
06. DIGITAL LOGIC DESIGN	PP	100	40	70	P C		16. ELECTROMAGNETIC	TW	25	10	22	P
07. DIGITAL LOGIC DESIGN	PR	50	20	32	P C		17. DATA STRUCTURES	PP	100	40	66	P
08. POWER DEVICES AND MACHINES	PP	100	40	53	P C		18. DATA STRUCTURES	PR	50	20	45	P
09. NETWORK AND POWER LAB.	TW	50	20	39	P C		19. COMMUNICATION THEORY	PP	100	40	61	P
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	43	P C		20. COMMUNICATION THEORY	OR	50	20	38	P
							21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	45	P

GRAND TOTAL = 1022/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

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S8053093   SHELKE MINAXI BHARAT   PUTULA   , 71129938C   , S8053093 ,   PICT   , S8053093

01. SIGNAL AND SYSTEMS	PP	100	40	49	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	53	P
02. SIGNAL AND SYSTEMS	OR	50	20	26	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	23	P
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	45	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	40	P
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	25	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	30	P
05. NETWORK ANALYSIS	PP	100	40	45	P C	15. ELECTROMAGNETIC	PP	100	40	66	P
06. DIGITAL LOGIC DESIGN	PP	100	40	40	P C	16. ELECTROMAGNETIC	TW	25	10	19	P
07. DIGITAL LOGIC DESIGN	PR	50	20	20	P C	17. DATA STRUCTURES	PP	100	40	51	P
08. POWER DEVICES AND MACHINES	PP	100	40	63	P C	18. DATA STRUCTURES	PR	50	20	36	P
09. NETWORK AND POWER LAB.	TW	50	20	40	P C	19. COMMUNICATION THEORY	PP	100	40	51	P
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	37	P C	20. COMMUNICATION THEORY	OR	50	20	30	P
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	41	P

GRAND TOTAL = 830/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

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S8053094   SHENDKAR SUSHMA HANUMANT   REKHA   , 71129939M   , S8053094 ,   PICT   , S8053094

01. SIGNAL AND SYSTEMS	PP	100	40	55	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	68	P
02. SIGNAL AND SYSTEMS	OR	50	20	28	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	20	P
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	40	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	43	P
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	21	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	30	P
05. NETWORK ANALYSIS	PP	100	40	48	P	15. ELECTROMAGNETIC	PP	100	40	45	P
06. DIGITAL LOGIC DESIGN	PP	100	40	43	P C	16. ELECTROMAGNETIC	TW	25	10	16	P
07. DIGITAL LOGIC DESIGN	PR	50	20	35	P	17. DATA STRUCTURES	PP	100	40	50	P
08. POWER DEVICES AND MACHINES	PP	100	40	53	P C	18. DATA STRUCTURES	PR	50	20	25	P
09. NETWORK AND POWER LAB.	TW	50	20	38	P C	19. COMMUNICATION THEORY	PP	100	40	40	P
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	43	P C	20. COMMUNICATION THEORY	OR	50	20	30	P
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	38	P

GRAND TOTAL = 809/1500, RESULT: SECOND CLASS

ORDN. 1 MARKS :

. . . . UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(ELECTRONICS & TELECOM.) . . . .

    DATE : 18 AUG. 2011   CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.   PAGE NO. 32 (356)

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  NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.

        OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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  S8053095   SHINDE MANOJ SURESH   MANJUSHRI   , 71045615J   , S8053095 , PICT   , S8053095

  01. SIGNAL AND SYSTEMS   PP   100   40   78 P C   11. ENGINEERING MATHEMATICS III   PP   100   40   76 P

  02. SIGNAL AND SYSTEMS   OR   50   20   33 P C   12. ENGINEERING MATHEMATICS III   TW   25   10   19 P

  03. SOLID STATES DEVICES AND CIRCUITSPP   100   40   64 P C   13. INTEGRATED CIRCUITS APPLICATIONS PP   100   40   44 P

  04. SOLID STATES DEVICES AND CIRCUITSPR   50   20   36 P C   14. INTEGRATED CIRCUITS APPLICATIONS PR   50   20   25 P

  05. NETWORK ANALYSIS   PP   100   40   41 P C   15. ELECTROMAGNETIC   PP   100   40   62 P

  06. DIGITAL LOGIC DESIGN   PP   100   40   64 P C   16. ELECTROMAGNETIC   TW   25   10   13 P

  07. DIGITAL LOGIC DESIGN   PR   50   20   29 P C   17. DATA STRUCTURES   PP   100   40   62 P

  08. POWER DEVICES AND MACHINES   PP   100   40   55 P C   18. DATA STRUCTURES   PR   50   20   42 P

  09. NETWORK AND POWER LAB.   TW   50   20   41 P C   19. COMMUNICATION THEORY   PP   100   40   50 P

  10. ELECTRONIC INSTRUMENTS AND TOOLS TW   50   20   34 P C   20. COMMUNICATION THEORY   OR   50   20   29 P

                                                                  21. CIRCUIT SIMULATION AND TOOLS   TW   50   20   31 P

GRAND TOTAL = 928/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

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  S8053096   SHITOLE JAYESH DASHRATH   SULOCHANA   , 71129940E   , S8053096 , PICT   , S8053096

  01. SIGNAL AND SYSTEMS   PP   100   40   40 P C   11. ENGINEERING MATHEMATICS III   PP   100   40   40 P

  02. SIGNAL AND SYSTEMS   OR   50   20   22 P   12. ENGINEERING MATHEMATICS III   TW   25   10   20 P

  03. SOLID STATES DEVICES AND CIRCUITSPP   100   40   56 P   13. INTEGRATED CIRCUITS APPLICATIONS PP   100   40   44 P

  04. SOLID STATES DEVICES AND CIRCUITSPR   50   20   20 P C   14. INTEGRATED CIRCUITS APPLICATIONS PR   50   20   30 P

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05. NETWORK ANALYSIS              PP  100  40  40  P      15. ELECTROMAGNETIC                  PP  100  40  47  P
06. DIGITAL LOGIC DESIGN          PP  100  40  45  P C    16. ELECTROMAGNETIC                  TW   25  10  16  P
07. DIGITAL LOGIC DESIGN          PR   50  20  30  P C    17. DATA STRUCTURES                  PP  100  40  40  P
08. POWER DEVICES AND MACHINES    PP  100  40  40  P C    18. DATA STRUCTURES                  PR   50  20  34  P
09. NETWORK AND POWER LAB.        TW   50  20  40  P C    19. COMMUNICATION THEORY             PP  100  40  40  P
10. ELECTRONIC INSTRUMENTS AND TOOLS TW  50  20  38  P C  20. COMMUNICATION THEORY             OR   50  20  29  P
                                                          21. CIRCUIT SIMULATION AND TOOLS    TW   50  20  41  P
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GRAND TOTAL = 752/1500, RESULT: SECOND CLASS

ORDN. 1 MARKS :

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S8053097   SIDDHARTHA BANGA   SUMEDHA RANI   , 71045625F   , S8053097 ,   PICT   , S8053097

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01. SIGNAL AND SYSTEMS            PP  100  40  40  P C    11. ENGINEERING MATHEMATICS III      PP  100  40  40  P
02. SIGNAL AND SYSTEMS            OR   50  20  22  P C    12. ENGINEERING MATHEMATICS III      TW   25  10  19  P
03. SOLID STATES DEVICES AND CIRCUITSPP  100  40  40  P   13. INTEGRATED CIRCUITS APPLICATIONS PP  100  40  40  P
04. SOLID STATES DEVICES AND CIRCUITSPR   50  20  34  P C 14. INTEGRATED CIRCUITS APPLICATIONS PR   50  20  34  P
05. NETWORK ANALYSIS              PP  100  40  40  P      15. ELECTROMAGNETIC                  PP  100  40  40  P
06. DIGITAL LOGIC DESIGN          PP  100  40  44  P C    16. ELECTROMAGNETIC                  TW   25  10  17  P
07. DIGITAL LOGIC DESIGN          PR   50  20  28  P C    17. DATA STRUCTURES                  PP  100  40  56  P
08. POWER DEVICES AND MACHINES    PP  100  40  40  P C    18. DATA STRUCTURES                  PR   50  20  35  P
09. NETWORK AND POWER LAB.        TW   50  20  34  P C    19. COMMUNICATION THEORY             PP  100  40  19  F
10. ELECTRONIC INSTRUMENTS AND TOOLS TW  50  20  34  P C  20. COMMUNICATION THEORY             OR   50  20  31  P
                                                          21. CIRCUIT SIMULATION AND TOOLS    TW   50  20  36  P
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GRAND TOTAL = 723/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

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                    UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(ELECTRONICS & TELECOM.)
   DATE : 18 AUG. 2011   CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.   PAGE NO. 33 (357)
. . . .

NOTE: FIRST LINE : SEAT NO.,   NAME OF THE CANDIDATE,   MOTHER, PERMANENT REG. NO.,   PREVIOUS SEAT NO.,   COLLEGE,   SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

. . . .

S8053098   SONAWANE NIKHIL BHIMRAO   SUNANDA   , 71045632J   , S8053098 , PICT   , S8053098

01. SIGNAL AND SYSTEMS	PP	100	40	28	F		11. ENGINEERING MATHEMATICS III	PP	100	40	AA	F	
02. SIGNAL AND SYSTEMS	OR	50	20	14	F		12. ENGINEERING MATHEMATICS III	TW	25	10	10	P	
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	AA	F		13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	AA	F
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	15	F		14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	AA	F
05. NETWORK ANALYSIS	PP	100	40	AA	F		15. ELECTROMAGNETIC	PP	100	40	AA	F
06. DIGITAL LOGIC DESIGN	PP	100	40	AA	F		16. ELECTROMAGNETIC	TW	25	10	10	P
07. DIGITAL LOGIC DESIGN	PR	50	20	30	P C		17. DATA STRUCTURES	PP	100	40	AA	F
08. POWER DEVICES AND MACHINES	PP	100	40	40	P C		18. DATA STRUCTURES	PR	50	20	AA	F
09. NETWORK AND POWER LAB.	TW	50	20	27	P C		19. COMMUNICATION THEORY	PP	100	40	AA	F
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	30	P C		20. COMMUNICATION THEORY	OR	50	20	AA	F
							21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	20	P

GRAND TOTAL = 224/1500, RESULT: FAILS   RESULT RESERVED FOR BKLG

ORDN. 1 MARKS :

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S8053099   SONU SINGH   USHA   , 71045633G   , S8053099 , PICT   , S8053099

01. SIGNAL AND SYSTEMS	PP	100	40	40	P C		11. ENGINEERING MATHEMATICS III	PP	100	40	40	P
02. SIGNAL AND SYSTEMS	OR	50	20	32	P C		12. ENGINEERING MATHEMATICS III	TW	25	10	17	P
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	31	F		13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	18	F
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	20	P C		14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	34	P
05. NETWORK ANALYSIS	PP	100	40	28	F		15. ELECTROMAGNETIC	PP	100	40	31	F
06. DIGITAL LOGIC DESIGN	PP	100	40	40	P C		16. ELECTROMAGNETIC	TW	25	10	16	P
07. DIGITAL LOGIC DESIGN	PR	50	20	29	P C		17. DATA STRUCTURES	PP	100	40	41	P
08. POWER DEVICES AND MACHINES	PP	100	40	31	F		18. DATA STRUCTURES	PR	50	20	35	P
09. NETWORK AND POWER LAB.	TW	50	20	35	P C		19. COMMUNICATION THEORY	PP	100	40	40	P

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10. ELECTRONIC INSTRUMENTS AND TOOLS TW    50    20    33  P C      20. COMMUNICATION THEORY           OR    50    20    29  P
                                                                    21. CIRCUIT SIMULATION AND TOOLS  TW    50    20    35  P
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GRAND TOTAL = 655/1500, RESULT: FAILS

ORDN. 1 MARKS :

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S8053100    SUBAGHYA MAHAJAN                    VIJAY                , 71045635C    , S8053100 ,  PICT     , S8053100

01. SIGNAL AND SYSTEMS               PP   100    40    49  P C      11. ENGINEERING MATHEMATICS III        PP   100    40    46  P
02. SIGNAL AND SYSTEMS               OR    50    20    43  P C      12. ENGINEERING MATHEMATICS III        TW    25    10    16  P
03. SOLID STATES DEVICES AND CIRCUITSPP   100    40    49  P        13. INTEGRATED CIRCUITS APPLICATIONS PP   100    40    40  P
04. SOLID STATES DEVICES AND CIRCUITSPR    50    20    32  P C      14. INTEGRATED CIRCUITS APPLICATIONS PR    50    20    45  P
05. NETWORK ANALYSIS                 PP   100    40    40  P C      15. ELECTROMAGNETIC                    PP   100    40    52  P
06. DIGITAL LOGIC DESIGN             PP   100    40    50  P C      16. ELECTROMAGNETIC                    TW    25    10    11  P
07. DIGITAL LOGIC DESIGN             PR    50    20    29  P C      17. DATA STRUCTURES                    PP   100    40    49  P
08. POWER DEVICES AND MACHINES       PP   100    40    52  P C      18. DATA STRUCTURES                    PR    50    20    47  P
09. NETWORK AND POWER LAB.           TW    50    20    35  P C      19. COMMUNICATION THEORY               PP   100    40    41  P
10. ELECTRONIC INSTRUMENTS AND TOOLS TW    50    20    38  P C      20. COMMUNICATION THEORY               OR    50    20    30  P
                                                                    21. CIRCUIT SIMULATION AND TOOLS      TW    50    20    30  P
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GRAND TOTAL = 824+01/1500, RESULT: HIGHER SECOND CLASS [O.2]

ORDN. 1 MARKS :

. . . .
               UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(ELECTRONICS & TELECOM.)
    DATE : 18 AUG. 2011   CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.   PAGE NO. 34 (358)
. . . .
 NOTE: FIRST LINE : SEAT NO.,   NAME OF THE CANDIDATE,   MOTHER, PERMANENT REG. NO.,   PREVIOUS SEAT NO.,   COLLEGE,   SEAT NO.
       OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
. . . .

 S8053101   SURVE SMIT BALASAHEB   PUSHPA   , 71045637K   , S8053101 , PICT   , S8053101
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01. SIGNAL AND SYSTEMS	PP	100	40	62	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	62	P
02. SIGNAL AND SYSTEMS	OR	50	20	40	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	17	P
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	40	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	42	P
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	32	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	31	P
05. NETWORK ANALYSIS	PP	100	40	49	P	15. ELECTROMAGNETIC	PP	100	40	42	P
06. DIGITAL LOGIC DESIGN	PP	100	40	68	P C	16. ELECTROMAGNETIC	TW	25	10	17	P
07. DIGITAL LOGIC DESIGN	PR	50	20	25	P C	17. DATA STRUCTURES	PP	100	40	48	P
08. POWER DEVICES AND MACHINES	PP	100	40	53	P C	18. DATA STRUCTURES	PR	50	20	34	P
09. NETWORK AND POWER LAB.	TW	50	20	37	P C	19. COMMUNICATION THEORY	PP	100	40	40	P
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	40	P C	20. COMMUNICATION THEORY	OR	50	20	30	P
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	37	P

GRAND TOTAL = 846/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

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S8053102   TELI REKHA MALLAPPA   KASTURI   , 71045643D   , S8053102 ,   PICT   , S8053102

01. SIGNAL AND SYSTEMS	PP	100	40	83	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	79	P
02. SIGNAL AND SYSTEMS	OR	50	20	40	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	22	P
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	40	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	47	P
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	35	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	31	P
05. NETWORK ANALYSIS	PP	100	40	40	P C	15. ELECTROMAGNETIC	PP	100	40	63	P
06. DIGITAL LOGIC DESIGN	PP	100	40	56	P C	16. ELECTROMAGNETIC	TW	25	10	19	P
07. DIGITAL LOGIC DESIGN	PR	50	20	30	P C	17. DATA STRUCTURES	PP	100	40	74	P
08. POWER DEVICES AND MACHINES	PP	100	40	40	P C	18. DATA STRUCTURES	PR	50	20	41	P
09. NETWORK AND POWER LAB.	TW	50	20	40	P C	19. COMMUNICATION THEORY	PP	100	40	50	P
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	40	P C	20. COMMUNICATION THEORY	OR	50	20	29	P
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	36	P

GRAND TOTAL = 935/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

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S8053103   TEMGIRE PRASAD SANTU   TAI   , 71045644B   , S8053103 ,   PICT   , S8053103

01. SIGNAL AND SYSTEMS	PP	100	40	87	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	84	P
02. SIGNAL AND SYSTEMS	OR	50	20	45	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	24	P
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	62	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	66	P
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	40	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	43	P
05. NETWORK ANALYSIS	PP	100	40	55	P C	15. ELECTROMAGNETIC	PP	100	40	80	P
06. DIGITAL LOGIC DESIGN	PP	100	40	83	P C	16. ELECTROMAGNETIC	TW	25	10	22	P
07. DIGITAL LOGIC DESIGN	PR	50	20	31	P C	17. DATA STRUCTURES	PP	100	40	50	P
08. POWER DEVICES AND MACHINES	PP	100	40	57	P C	18. DATA STRUCTURES	PR	50	20	44	P
09. NETWORK AND POWER LAB.	TW	50	20	44	P C	19. COMMUNICATION THEORY	PP	100	40	59	P
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	43	P C	20. COMMUNICATION THEORY	OR	50	20	35	P
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	43	P

GRAND TOTAL = 1097/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

. . . .
                       UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(ELECTRONICS & TELECOM.)
     DATE : 18 AUG. 2011   CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.   PAGE NO. 35 (359)
. . . .
  NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.
          OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S8053104   THAKARE BHAGYASHREE RAJENDRA   SUNITA   , 71045645L   , S8053104 ,   PICT   , S8053104

01. SIGNAL AND SYSTEMS	PP	100	40	23	F	11. ENGINEERING MATHEMATICS III	PP	100	40	40	P
02. SIGNAL AND SYSTEMS	OR	50	20	20	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	21	P
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	29	F	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	25	F
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	20	P	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	30	P
05. NETWORK ANALYSIS	PP	100	40	12	F	15. ELECTROMAGNETIC	PP	100	40	07	F

06. DIGITAL LOGIC DESIGN	PP	100	40	11	F		16. ELECTROMAGNETIC	TW	25	10	15 P
07. DIGITAL LOGIC DESIGN	PR	50	20	28	P C		17. DATA STRUCTURES	PP	100	40	57 P
08. POWER DEVICES AND MACHINES	PP	100	40	29	F		18. DATA STRUCTURES	PR	50	20	44 P
09. NETWORK AND POWER LAB.	TW	50	20	37	P C		19. COMMUNICATION THEORY	PP	100	40	23 F
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	39	P C		20. COMMUNICATION THEORY	OR	50	20	20 P
							21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	35 P

GRAND TOTAL = 565/1500, RESULT: FAILS   RESULT RESERVED FOR BKLG

ORDN. 1 MARKS :

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S8053105   THORAT TANVI TATYASAHEB   SULOCHANA   , 71045650G   , S8053105 ,   PICT   , S8053105

01. SIGNAL AND SYSTEMS	PP	100	40	55	P C		11. ENGINEERING MATHEMATICS III	PP	100	40	75 P
02. SIGNAL AND SYSTEMS	OR	50	20	30	P C		12. ENGINEERING MATHEMATICS III	TW	25	10	22 P
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	56	P C		13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	51 P
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	36	P C		14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	37 P
05. NETWORK ANALYSIS	PP	100	40	43	P C		15. ELECTROMAGNETIC	PP	100	40	54 P
06. DIGITAL LOGIC DESIGN	PP	100	40	52	P C		16. ELECTROMAGNETIC	TW	25	10	22 P
07. DIGITAL LOGIC DESIGN	PR	50	20	34	P C		17. DATA STRUCTURES	PP	100	40	47 P
08. POWER DEVICES AND MACHINES	PP	100	40	45	P C		18. DATA STRUCTURES	PR	50	20	46 P
09. NETWORK AND POWER LAB.	TW	50	20	37	P C		19. COMMUNICATION THEORY	PP	100	40	67 P
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	40	P C		20. COMMUNICATION THEORY	OR	50	20	35 P
							21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	37 P

GRAND TOTAL = 921/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

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S8053106   VYAS HARIKESH PRADEEP   BHAVANA   , 71045661B   , S8053106 ,   PICT   , S8053106

01. SIGNAL AND SYSTEMS	PP	100	40	78	P C		11. ENGINEERING MATHEMATICS III	PP	100	40	60 P

02. SIGNAL AND SYSTEMS	OR	50	20	35	P C		12. ENGINEERING MATHEMATICS III	TW	25	10	20	P
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	41	P C		13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	47	P
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	20	P C		14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	42	P
05. NETWORK ANALYSIS	PP	100	40	40	P C		15. ELECTROMAGNETIC	PP	100	40	41	P
06. DIGITAL LOGIC DESIGN	PP	100	40	49	P C		16. ELECTROMAGNETIC	TW	25	10	18	P
07. DIGITAL LOGIC DESIGN	PR	50	20	34	P C		17. DATA STRUCTURES	PP	100	40	60	P
08. POWER DEVICES AND MACHINES	PP	100	40	53	P C		18. DATA STRUCTURES	PR	50	20	47	P
09. NETWORK AND POWER LAB.	TW	50	20	41	P C		19. COMMUNICATION THEORY	PP	100	40	54	P
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	42	P C		20. COMMUNICATION THEORY	OR	50	20	29	P
							21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	40	P

GRAND TOTAL = 891+09/1500, RESULT: FIRST CLASS [O.2]

ORDN. 1 MARKS :

. . . .
                    UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(ELECTRONICS & TELECOM.)

. . . .

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.

       OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S8053107   WAGHMARE ABHISHEK SUBHASH   VIDYA   , 71045662L   , S8053107 ,   PICT   , S8053107

01. SIGNAL AND SYSTEMS	PP	100	40	41	P C		11. ENGINEERING MATHEMATICS III	PP	100	40	47	P
02. SIGNAL AND SYSTEMS	OR	50	20	20	P C		12. ENGINEERING MATHEMATICS III	TW	25	10	20	P
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	40	P C		13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	31	F
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	35	P C		14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	30	P
05. NETWORK ANALYSIS	PP	100	40	40	P		15. ELECTROMAGNETIC	PP	100	40	40	P
06. DIGITAL LOGIC DESIGN	PP	100	40	31	F		16. ELECTROMAGNETIC	TW	25	10	17	P
07. DIGITAL LOGIC DESIGN	PR	50	20	30	P C		17. DATA STRUCTURES	PP	100	40	40	P
08. POWER DEVICES AND MACHINES	PP	100	40	40	P C		18. DATA STRUCTURES	PR	50	20	38	P
09. NETWORK AND POWER LAB.	TW	50	20	43	P C		19. COMMUNICATION THEORY	PP	100	40	40	P
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	42	P C		20. COMMUNICATION THEORY	OR	50	20	27	P

21. CIRCUIT SIMULATION AND TOOLS   TW   50   20   41 P

GRAND TOTAL = 733/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

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S8053108   WATHORE SANKET UTTAMRAO   KAVITA   , 71045665E   , S8053108 ,   PICT   , S8053108

01. SIGNAL AND SYSTEMS	PP	100	40	41	P C	11. ENGINEERING MATHEMATICS III	PP 100 40 49 P
02. SIGNAL AND SYSTEMS	OR	50	20	31	P C	12. ENGINEERING MATHEMATICS III	TW 25 10 19 P
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	40	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP 100 40 40 P
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	30	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR 50 20 28 P
05. NETWORK ANALYSIS	PP	100	40	47	P	15. ELECTROMAGNETIC	PP 100 40 51 P
06. DIGITAL LOGIC DESIGN	PP	100	40	40	P	16. ELECTROMAGNETIC	TW 25 10 13 P
07. DIGITAL LOGIC DESIGN	PR	50	20	24	P C	17. DATA STRUCTURES	PP 100 40 56 P
08. POWER DEVICES AND MACHINES	PP	100	40	42	P C	18. DATA STRUCTURES	PR 50 20 36 P
09. NETWORK AND POWER LAB.	TW	50	20	37	P C	19. COMMUNICATION THEORY	PP 100 40 40 P
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	38	P C	20. COMMUNICATION THEORY	OR 50 20 34 P
						21. CIRCUIT SIMULATION AND TOOLS	TW 50 20 34 P

GRAND TOTAL = 770/1500, RESULT: SECOND CLASS

ORDN. 1 MARKS :

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S8053109   YADAV VIKAS PRABHURAO   SUREKHA   , 71045667M   , S8053109 ,   PICT   , S8053109

01. SIGNAL AND SYSTEMS	PP	100	40	71	P C	11. ENGINEERING MATHEMATICS III	PP 100 40 76 P
02. SIGNAL AND SYSTEMS	OR	50	20	39	P C	12. ENGINEERING MATHEMATICS III	TW 25 10 17 P
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	57	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP 100 40 51 P
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	37	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR 50 20 32 P
05. NETWORK ANALYSIS	PP	100	40	57	P C	15. ELECTROMAGNETIC	PP 100 40 52 P
06. DIGITAL LOGIC DESIGN	PP	100	40	54	P C	16. ELECTROMAGNETIC	TW 25 10 15 P

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                                                          cols05
07. DIGITAL LOGIC DESIGN            PR    50   20   34  P C      17. DATA STRUCTURES              PP   100   40   55  P
08. POWER DEVICES AND MACHINES      PP   100   40   49  P C      18. DATA STRUCTURES              PR    50   20   42  P
09. NETWORK AND POWER LAB.          TW    50   20   38  P C      19. COMMUNICATION THEORY         PP   100   40   45  P
10. ELECTRONIC INSTRUMENTS AND TOOLS TW  50   20   39  P C      20. COMMUNICATION THEORY         OR    50   20   30  P
                                                                21. CIRCUIT SIMULATION AND TOOLS TW   50   20   33  P
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GRAND TOTAL = 923/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

. . . . UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(ELECTRONICS & TELECOM.). . . . .

    DATE : 18 AUG. 2011   CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.   PAGE NO. 37 (361)
. . . .

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.

        OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

. . . .

S8053110   YARDI NIRANJAN DHANANJAY   KRANTI   , 71045668K   , S8053110 , PICT   , S8053110

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01. SIGNAL AND SYSTEMS              PP   100   40   79  P C      11. ENGINEERING MATHEMATICS III   PP   100   40   78  P
02. SIGNAL AND SYSTEMS              OR    50   20   34  P C      12. ENGINEERING MATHEMATICS III   TW    25   10   18  P
03. SOLID STATES DEVICES AND CIRCUITSPP  100   40   40  P C      13. INTEGRATED CIRCUITS APPLICATIONS PP  100   40   48  P
04. SOLID STATES DEVICES AND CIRCUITSPR   50   20   26  P C      14. INTEGRATED CIRCUITS APPLICATIONS PR   50   20   40  P
05. NETWORK ANALYSIS               PP   100   40   40  P C      15. ELECTROMAGNETIC              PP   100   40   66  P
06. DIGITAL LOGIC DESIGN            PP   100   40   40  P C      16. ELECTROMAGNETIC              TW    25   10   17  P
07. DIGITAL LOGIC DESIGN            PR    50   20   37  P C      17. DATA STRUCTURES              PP   100   40   55  P
08. POWER DEVICES AND MACHINES      PP   100   40   40  P C      18. DATA STRUCTURES              PR    50   20   46  P
09. NETWORK AND POWER LAB.          TW    50   20   39  P C      19. COMMUNICATION THEORY         PP   100   40   51  P
10. ELECTRONIC INSTRUMENTS AND TOOLS TW  50   20   41  P C      20. COMMUNICATION THEORY         OR    50   20   29  P
                                                                21. CIRCUIT SIMULATION AND TOOLS TW   50   20   37  P
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GRAND TOTAL = 901/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

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S8053120   JADHAV OMKAR FULCHAND   JYOTSNA   , 70701463J   , S8053120 ,   PICT   , S8053120

01. SIGNAL AND SYSTEMS	PP	100	40	40	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	40	P C
02. SIGNAL AND SYSTEMS	OR	50	20	20	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	16	P C
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	40	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	49	P C
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	24	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	AA	F
05. NETWORK ANALYSIS	PP	100	40	23	F	15. ELECTROMAGNETIC	PP	100	40	40	P C
06. DIGITAL LOGIC DESIGN	PP	100	40	53	P C	16. ELECTROMAGNETIC	TW	25	10	18	P C
07. DIGITAL LOGIC DESIGN	PR	50	20	20	P C	17. DATA STRUCTURES	PP	100	40	40	P C
08. POWER DEVICES AND MACHINES	PP	100	40	40	P C	18. DATA STRUCTURES	PR	50	20	25	P C
09. NETWORK AND POWER LAB.	TW	50	20	24	P C	19. COMMUNICATION THEORY	PP	100	40	54	P C
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	38	P C	20. COMMUNICATION THEORY	OR	50	20	32	P C
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	38	P C

GRAND TOTAL = 674/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

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S8053121   JAGTAP SAUDAMINI DHANANJAY   PRERANA   , 71070199D   , S8053121 ,   PICT   , S8053121

01. SIGNAL AND SYSTEMS	PP	100	40	40	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	50	P C
02. SIGNAL AND SYSTEMS	OR	50	20	29	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	20	P C
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	50	P	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	65	P C
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	36	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	28	P C
05. NETWORK ANALYSIS	PP	100	40	43	P C	15. ELECTROMAGNETIC	PP	100	40	51	P C
06. DIGITAL LOGIC DESIGN	PP	100	40	51	P C	16. ELECTROMAGNETIC	TW	25	10	21	P C
07. DIGITAL LOGIC DESIGN	PR	50	20	35	P C	17. DATA STRUCTURES	PP	100	40	57	P C
08. POWER DEVICES AND MACHINES	PP	100	40	43	P C	18. DATA STRUCTURES	PR	50	20	47	P C
09. NETWORK AND POWER LAB.	TW	50	20	44	P C	19. COMMUNICATION THEORY	PP	100	40	70	P C
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	39	P C	20. COMMUNICATION THEORY	OR	50	20	29	P C
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	39	P C

GRAND TOTAL = 887/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

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                    UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(ELECTRONICS & TELECOM.)

    DATE : 18 AUG. 2011   CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.   PAGE NO. 38 (362)

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  NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.

        OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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  S8053123   KESHAV SINGHAL   POONAM   , 70925472F   , S8053123 , PICT   , S8053123

  01. SIGNAL AND SYSTEMS   PP   100   40   69   P C   11. ENGINEERING MATHEMATICS III   PP   100   40   AA   F
  02. SIGNAL AND SYSTEMS   OR   50   20   27   P C   12. ENGINEERING MATHEMATICS III   TW   25   10   10   P
  03. SOLID STATES DEVICES AND CIRCUITSPP   100   40   25   F   13. INTEGRATED CIRCUITS APPLICATIONS PP   100   40   21   F
  04. SOLID STATES DEVICES AND CIRCUITSPR   50   20   20   P C   14. INTEGRATED CIRCUITS APPLICATIONS PR   50   20   42   P
  05. NETWORK ANALYSIS   PP   100   40   25   F   15. ELECTROMAGNETIC   PP   100   40   AA   F
  06. DIGITAL LOGIC DESIGN   PP   100   40   43   P C   16. ELECTROMAGNETIC   TW   25   10   11   P
  07. DIGITAL LOGIC DESIGN   PR   50   20   20   P C   17. DATA STRUCTURES   PP   100   40   28   F
  08. POWER DEVICES AND MACHINES   PP   100   40   40   P C   18. DATA STRUCTURES   PR   50   20   34   P
  09. NETWORK AND POWER LAB.   TW   50   20   21   P C   19. COMMUNICATION THEORY   PP   100   40   20   F
  10. ELECTRONIC INSTRUMENTS AND TOOLS TW   50   20   25   P C   20. COMMUNICATION THEORY   OR   50   20   29   P
                                                                    21. CIRCUIT SIMULATION AND TOOLS   TW   50   20   34   P

GRAND TOTAL = 544/1500, RESULT: FAILS

ORDN. 1 MARKS :

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  S8053127   MOTWANI NIKITA SUDAMA   KRISHNA   , 71070201K   , S8053127 , PICT   , S8053127

  01. SIGNAL AND SYSTEMS   PP   100   40   48   P C   11. ENGINEERING MATHEMATICS III   PP   100   40   44   P C
  02. SIGNAL AND SYSTEMS   OR   50   20   28   P C   12. ENGINEERING MATHEMATICS III   TW   25   10   18   P C

03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	40	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	46	P C
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	20	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	27	P C
05. NETWORK ANALYSIS	PP	100	40	43	P	15. ELECTROMAGNETIC	PP	100	40	40	P C
06. DIGITAL LOGIC DESIGN	PP	100	40	40	P C	16. ELECTROMAGNETIC	TW	25	10	20	P C
07. DIGITAL LOGIC DESIGN	PR	50	20	36	P C	17. DATA STRUCTURES	PP	100	40	45	P C
08. POWER DEVICES AND MACHINES	PP	100	40	45	P C	18. DATA STRUCTURES	PR	50	20	27	P C
09. NETWORK AND POWER LAB.	TW	50	20	33	P C	19. COMMUNICATION THEORY	PP	100	40	40	P C
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	40	P C	20. COMMUNICATION THEORY	OR	50	20	20	P C
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	43	P C

GRAND TOTAL = 743+07/1500, RESULT: SECOND CLASS [O.2]

ORDN. 1 MARKS :

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S8053130   PAWAR SACHIN VINAYAKRAO   SAVITA   , 70925557J   , S8053130 ,   PICT   , S8053130

01. SIGNAL AND SYSTEMS	PP	100	40	AA	F	11. ENGINEERING MATHEMATICS III	PP	100	40	04	F
02. SIGNAL AND SYSTEMS	OR	50	20	AA	F	12. ENGINEERING MATHEMATICS III	TW	25	10	15	P
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	AA	F	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	24	F
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	AA	F	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	40	P
05. NETWORK ANALYSIS	PP	100	40	AA	F	15. ELECTROMAGNETIC	PP	100	40	AA	F
06. DIGITAL LOGIC DESIGN	PP	100	40	AA	F	16. ELECTROMAGNETIC	TW	25	10	18	P
07. DIGITAL LOGIC DESIGN	PR	50	20	AA	F	17. DATA STRUCTURES	PP	100	40	AA	F
08. POWER DEVICES AND MACHINES	PP	100	40	AA	F	18. DATA STRUCTURES	PR	50	20	AA	F
09. NETWORK AND POWER LAB.	TW	50	20	33	P C	19. COMMUNICATION THEORY	PP	100	40	AA	F
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	20	P C	20. COMMUNICATION THEORY	OR	50	20	21	P
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	32	P

GRAND TOTAL = 207/1500, RESULT: FAILS   RESULT RESERVED FOR BKLG

ORDN. 1 MARKS :

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.

        OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S8053131   PAWAR VISHAL ALIAS DIGVIJAY BALASAHEB   VIJAYANTI   , 71070203F   , S8053131 ,   PICT   , S8053131

01. SIGNAL AND SYSTEMS	PP	100	40	40	P		11. ENGINEERING MATHEMATICS III	PP	100	40	AA	F
02. SIGNAL AND SYSTEMS	OR	50	20	20	P C		12. ENGINEERING MATHEMATICS III	TW	25	10	14	P C
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	50	P		13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	59	P C
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	20	P C		14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	20	P C
05. NETWORK ANALYSIS	PP	100	40	AA	F		15. ELECTROMAGNETIC	PP	100	40	40	P
06. DIGITAL LOGIC DESIGN	PP	100	40	29	F		16. ELECTROMAGNETIC	TW	25	10	12	P C
07. DIGITAL LOGIC DESIGN	PR	50	20	20	P C		17. DATA STRUCTURES	PP	100	40	40	P
08. POWER DEVICES AND MACHINES	PP	100	40	56	P C		18. DATA STRUCTURES	PR	50	20	28	P C
09. NETWORK AND POWER LAB.	TW	50	20	34	P C		19. COMMUNICATION THEORY	PP	100	40	53	P C
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	30	P C		20. COMMUNICATION THEORY	OR	50	20	24	P C
							21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	37	P C

GRAND TOTAL = 626/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

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S8053132   PRASHANT POPAT JAGTAP   MUKTA   , 70801568K   , S8053132 ,   PICT   , S8053132

01. SIGNAL AND SYSTEMS	PP	100	40	40	P C		11. ENGINEERING MATHEMATICS III	PP	100	40	58	P C
02. SIGNAL AND SYSTEMS	OR	50	20	20	P C		12. ENGINEERING MATHEMATICS III	TW	25	10	16	P C
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	31*	P		13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	48	P C
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	22	P C		14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	35	P
05. NETWORK ANALYSIS	PP	100	40	40	P C		15. ELECTROMAGNETIC	PP	100	40	40	P C
06. DIGITAL LOGIC DESIGN	PP	100	40	51	P C		16. ELECTROMAGNETIC	TW	25	10	10	P C
07. DIGITAL LOGIC DESIGN	PR	50	20	20	P C		17. DATA STRUCTURES	PP	100	40	56	P C

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08. POWER DEVICES AND MACHINES       PP  100  40  45  P C   18. DATA STRUCTURES               PR   50  20  26  P C
09. NETWORK AND POWER LAB.           TW   50  20  22  P C   19. COMMUNICATION THEORY          PP  100  40  40  P C
10. ELECTRONIC INSTRUMENTS AND TOOLS TW   50  20  21  P C   20. COMMUNICATION THEORY          OR   50  20  20  P C
                                                            21. CIRCUIT SIMULATION AND TOOLS  TW   50  20  31  P C

GRAND TOTAL =  692/1500, RESULT: PASS CLASS   * [O.4]

ORDN. 1 MARKS :


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S8053137    THOKALE PRIYANKA MADHAV              MANDAKINI        , 70925636B    , S8053137 ,  PICT     , S8053137

01. SIGNAL AND SYSTEMS               PP  100  40  53  P C   11. ENGINEERING MATHEMATICS III   PP  100  40  40  P C
02. SIGNAL AND SYSTEMS               OR   50  20  32  P C   12. ENGINEERING MATHEMATICS III   TW   25  10  18  P C
03. SOLID STATES DEVICES AND CIRCUITSPP  100  40  AA  F     13. INTEGRATED CIRCUITS APPLICATIONS PP  100  40  AA  F
04. SOLID STATES DEVICES AND CIRCUITSPR   50  20  29  P C   14. INTEGRATED CIRCUITS APPLICATIONS PR   50  20  AA  F
05. NETWORK ANALYSIS                 PP  100  40  AA  F     15. ELECTROMAGNETIC               PP  100  40  AA  F
06. DIGITAL LOGIC DESIGN             PP  100  40  43  P C   16. ELECTROMAGNETIC               TW   25  10  13  P C
07. DIGITAL LOGIC DESIGN             PR   50  20  20  P C   17. DATA STRUCTURES               PP  100  40  AA  F
08. POWER DEVICES AND MACHINES       PP  100  40  AA  F     18. DATA STRUCTURES               PR   50  20  AA  F
09. NETWORK AND POWER LAB.           TW   50  20  32  P C   19. COMMUNICATION THEORY          PP  100  40  AA  F
10. ELECTRONIC INSTRUMENTS AND TOOLS TW   50  20  32  P C   20. COMMUNICATION THEORY          OR   50  20  AA  F
                                                            21. CIRCUIT SIMULATION AND TOOLS  TW   50  20  41  P C

GRAND TOTAL =  353/1500, RESULT: FAILS

ORDN. 1 MARKS :


. . . . . . . . . . . . . . . . . . UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(ELECTRONICS & TELECOM.) . . . . . . . . . . . . . . . . . . . . . . . . . . .
    DATE : 18 AUG. 2011              CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.          PAGE NO.  40  (  364)
. . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . .
 NOTE: FIRST LINE : SEAT NO.,  NAME OF THE CANDIDATE,  MOTHER, PERMANENT REG. NO.,  PREVIOUS SEAT NO.,  COLLEGE,   SEAT NO.
       OTHER LINES: HEAD OF PASSING,  MAX. MARKS,  MIN. PASS MARKS,  MARKS OBTAINED,  P/F:PASS/FAIL,  C:PREVIOUS CARRY OVER
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S8053139   AGARWAL VINIT SHARAD   SANTOSH   , 70925321E   , S8053002 ,   PICT   , S8053139

01. SIGNAL AND SYSTEMS	PP	100	40	42	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	83	P
02. SIGNAL AND SYSTEMS	OR	50	20	33	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	25	P
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	54	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	61	P
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	27	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	38	P
05. NETWORK ANALYSIS	PP	100	40	44	P C	15. ELECTROMAGNETIC	PP	100	40	77	P
06. DIGITAL LOGIC DESIGN	PP	100	40	40	P C	16. ELECTROMAGNETIC	TW	25	10	24	P
07. DIGITAL LOGIC DESIGN	PR	50	20	25	P C	17. DATA STRUCTURES	PP	100	40	54	P
08. POWER DEVICES AND MACHINES	PP	100	40	40	P C	18. DATA STRUCTURES	PR	50	20	36	P
09. NETWORK AND POWER LAB.	TW	50	20	29	P C	19. COMMUNICATION THEORY	PP	100	40	65	P
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	30	P C	20. COMMUNICATION THEORY	OR	50	20	47	P
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	47	P

GRAND TOTAL = 921/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

. . . . UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(COMPUTER) EXAMINATION MAY 2011 . . . .

    DATE : 18 AUG. 2011   CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.   PAGE NO. 01 (365)

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 NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.

        OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S8054202   ABHIMANYU BHOSALE   SUJATA   , 71045355J   , S8054202 ,   PICT   , S8054202

01. DISCRETE STRUCTURES	PP	100	40	53	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	40	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	40	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	31	F
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	46	P C	13. DATA STRUCTURES	PP	100	40	26	F
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	59	P C	14. COMPUTER GRAPHICS	PP	100	40	59	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	44	P C	15. COMPUTER ORGANIZATION	PP	100	40	43	P
06. PROGRAMMING LABORATORY	TW	25	10	15	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	23	P
07. PROGRAMMING LABORATORY	PR	50	20	40	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	23	P

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08. DIGITAL ELECTRONICS LABORATORY   TW   25   10   16  P C    18. MICROPROCESSORS & INTERFACING LABTW    50   20   27  P
09. DIGITAL ELECTRONICS LABORATORY   PR   50   20   23  P C    19. MICROPROCESSORS & INTERFACING LABPR    50   20   10  F
10. SOFT SKILLS                      TW   50   20   32  P C    20. DATA STRUCTURES LABORATORY       TW    50   20   23  P
                                                               21. DATA STRUCTURES LABORATORY       PR    50   20   35  P
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GRAND TOTAL = 708/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

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S8054203   ABNAVE OMKAR RAJENDRA   SHOBHA   , 71045358C   , S8054203 , PICT   , S8054203

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01. DISCRETE STRUCTURES              PP  100   40   40  P C    11. ENGINEERING MATHEMATICS III      PP   100   40   42  P
02. PROGRAMMING & PROBLEM SOLVING    PP  100   40   40  P C    12. MICROPROC. & INTERFACING TECHNIQ.PP   100   40   43  P
03. DIGIT. ELECTRONICS & LOGIC DESIGNPP  100   40   40  P C    13. DATA STRUCTURES                  PP   100   40   40  P
04. DATA STRUCTURES AND ALGORITHMS   PP  100   40   40  P C    14. COMPUTER GRAPHICS                PP   100   40   57  P
05. HUMANITIES AND SOCIAL SCIENCE    PP  100   40   41  P C    15. COMPUTER ORGANIZATION            PP   100   40   45  P
06. PROGRAMMING LABORATORY           TW   25   10   13  P C    16. O. O. PROG. & COMP. GRAPH. LAB   TW    50   20   23  P
07. PROGRAMMING LABORATORY           PR   50   20   12  F      17. O. O. PROG. & COMP. GRAPH. LAB   PR    50   20   05  F
08. DIGITAL ELECTRONICS LABORATORY   TW   25   10   13  P C    18. MICROPROCESSORS & INTERFACING LABTW    50   20   21  P
09. DIGITAL ELECTRONICS LABORATORY   PR   50   20   23  P      19. MICROPROCESSORS & INTERFACING LABPR    50   20   20  P
10. SOFT SKILLS                      TW   50   20   22  P C    20. DATA STRUCTURES LABORATORY       TW    50   20   22  P
                                                               21. DATA STRUCTURES LABORATORY       PR    50   20   00  F
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GRAND TOTAL = 602/1500, RESULT: FAILS A.T.K.T.   RESULT RESERVED FOR BKLG

ORDN. 1 MARKS :

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S8054204   ADHAV ASHISH DNYANDEO   SUJATA   , 71045359M   , S8054204 , PICT   , S8054204

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01. DISCRETE STRUCTURES              PP  100   40   66  P C    11. ENGINEERING MATHEMATICS III      PP   100   40   63  P
02. PROGRAMMING & PROBLEM SOLVING    PP  100   40   57  P C    12. MICROPROC. & INTERFACING TECHNIQ.PP   100   40   50  P
03. DIGIT. ELECTRONICS & LOGIC DESIGNPP  100   40   56  P C    13. DATA STRUCTURES                  PP   100   40   62  P
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04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	72	P C	14. COMPUTER GRAPHICS		PP	100	40	66	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	57	P C	15. COMPUTER ORGANIZATION		PP	100	40	60	P
06. PROGRAMMING LABORATORY	TW	25	10	17	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW		50	20	32	P
07. PROGRAMMING LABORATORY	PR	50	20	41	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR		50	20	44	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	15	P C	18. MICROPROCESSORS & INTERFACING LAB	TW		50	20	35	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	30	P C	19. MICROPROCESSORS & INTERFACING LAB	PR		50	20	44	P
10. SOFT SKILLS	TW	50	20	39	P C	20. DATA STRUCTURES LABORATORY		TW	50	20	30	P
						21. DATA STRUCTURES LABORATORY		PR	50	20	33	P

GRAND TOTAL = 969/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

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UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(COMPUTER) EXAMINATION MAY 2011

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.

        OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S8054205   ADHIKARI DHANASHREE HEMCHANDRA   YAYUTA   , 71045360E   , S8054205 , PICT   , S8054205

01. DISCRETE STRUCTURES	PP	100	40	69	P C	11. ENGINEERING MATHEMATICS III		PP	100	40	71	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	62	P C	12. MICROPROC. & INTERFACING TECHNIQ.		PP	100	40	58	P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	79	P C	13. DATA STRUCTURES		PP	100	40	67	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	68	P C	14. COMPUTER GRAPHICS		PP	100	40	65	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	64	P C	15. COMPUTER ORGANIZATION		PP	100	40	57	P
06. PROGRAMMING LABORATORY	TW	25	10	22	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW		50	20	43	P
07. PROGRAMMING LABORATORY	PR	50	20	32	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR		50	20	43	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	23	P C	18. MICROPROCESSORS & INTERFACING LAB	TW		50	20	43	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	41	P C	19. MICROPROCESSORS & INTERFACING LAB	PR		50	20	46	P
10. SOFT SKILLS	TW	50	20	41	P C	20. DATA STRUCTURES LABORATORY		TW	50	20	45	P
						21. DATA STRUCTURES LABORATORY		PR	50	20	39	P

GRAND TOTAL = 1078/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

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S8054206   ADITI JOSHI   SURUCHI   , 71045361C   , S8054206 ,   PICT   , S8054206

01. DISCRETE STRUCTURES	PP	100	40	69	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	74	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	60	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	46	P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	83	P C	13. DATA STRUCTURES	PP	100	40	58	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	84	P C	14. COMPUTER GRAPHICS	PP	100	40	65	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	51	P C	15. COMPUTER ORGANIZATION	PP	100	40	54	P
06. PROGRAMMING LABORATORY	TW	25	10	18	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	29	P
07. PROGRAMMING LABORATORY	PR	50	20	35	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	44	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	19	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	31	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	40	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	28	P
10. SOFT SKILLS	TW	50	20	34	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	26	P
						21. DATA STRUCTURES LABORATORY	PR	50	20	37	P

GRAND TOTAL = 985/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

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S8054207   ADTANI VASHISHTHA MOHAN   BHAWANA   , 71045362M   , S8054207 ,   PICT   , S8054207

01. DISCRETE STRUCTURES	PP	100	40	81	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	71	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	57	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	40	P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	70	P C	13. DATA STRUCTURES	PP	100	40	53	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	77	P C	14. COMPUTER GRAPHICS	PP	100	40	69	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	54	P C	15. COMPUTER ORGANIZATION	PP	100	40	61	P
06. PROGRAMMING LABORATORY	TW	25	10	21	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	43	P
07. PROGRAMMING LABORATORY	PR	50	20	45	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	36	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	23	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	44	P

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09. DIGITAL ELECTRONICS LABORATORY    PR   50   20   40  P C    19. MICROPROCESSORS & INTERFACING LABPR   50   20   38  P

10. SOFT SKILLS                       TW   50   20   34  P C    20. DATA STRUCTURES LABORATORY        TW   50   20   40  P

                                                               21. DATA STRUCTURES LABORATORY        PR   50   20   41  P


GRAND TOTAL = 1038/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :
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                  UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(COMPUTER) EXAMINATION MAY 2011
     DATE : 18 AUG. 2011          CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.        PAGE NO.  03  (  367)
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  NOTE: FIRST LINE : SEAT NO.,  NAME OF THE CANDIDATE,  MOTHER,  PERMANENT REG. NO.,  PREVIOUS SEAT NO.,  COLLEGE,   SEAT NO.

         OTHER LINES: HEAD OF PASSING,  MAX. MARKS,  MIN. PASS MARKS,  MARKS OBTAINED,  P/F:PASS/FAIL,  C:PREVIOUS CARRY OVER

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  S8054208    AGRAWAL ANKIT UMESHKUMAR              SHAMADEVI          , 71045364H     , S8054208 ,   PICT     , S8054208

  01. DISCRETE STRUCTURES              PP  100   40   69  P C    11. ENGINEERING MATHEMATICS III       PP  100   40   68  P

  02. PROGRAMMING & PROBLEM SOLVING    PP  100   40   50  P C    12. MICROPROC. & INTERFACING TECHNIQ.PP  100   40   54  P

  03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100   40   64  P C    13. DATA STRUCTURES                   PP  100   40   66  P

  04. DATA STRUCTURES AND ALGORITHMS   PP  100   40   78  P C    14. COMPUTER GRAPHICS                 PP  100   40   65  P

  05. HUMANITIES AND SOCIAL SCIENCE    PP  100   40   60  P C    15. COMPUTER ORGANIZATION             PP  100   40   55  P

  06. PROGRAMMING LABORATORY           TW   25   10   17  P C    16. O. O. PROG. & COMP. GRAPH. LAB    TW   50   20   32  P

  07. PROGRAMMING LABORATORY           PR   50   20   30  P C    17. O. O. PROG. & COMP. GRAPH. LAB    PR   50   20   39  P

  08. DIGITAL ELECTRONICS LABORATORY   TW   25   10   17  P C    18. MICROPROCESSORS & INTERFACING LABTW   50   20   29  P

  09. DIGITAL ELECTRONICS LABORATORY   PR   50   20   27  P C    19. MICROPROCESSORS & INTERFACING LABPR   50   20   40  P

  10. SOFT SKILLS                      TW   50   20   27  P C    20. DATA STRUCTURES LABORATORY        TW   50   20   26  P

                                                               21. DATA STRUCTURES LABORATORY        PR   50   20   38  P


GRAND TOTAL =  951/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :


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  S8054209    AGRAWAL ANNU ARUN               MEENA             , 71045365F    , S8054209 ,   PICT     , S8054209
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01. DISCRETE STRUCTURES	PP	100	40	68	P C
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	47	P C
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	76	P C
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	67	P C
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	47	P C
06. PROGRAMMING LABORATORY	TW	25	10	21	P C
07. PROGRAMMING LABORATORY	PR	50	20	34	P C
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	19	P C
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	38	P C
10. SOFT SKILLS	TW	50	20	38	P C

11. ENGINEERING MATHEMATICS III	PP	100	40	88	P
12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	56	P
13. DATA STRUCTURES	PP	100	40	60	P
14. COMPUTER GRAPHICS	PP	100	40	71	P
15. COMPUTER ORGANIZATION	PP	100	40	61	P
16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	39	P
17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	35	P
18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	40	P
19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	38	P
20. DATA STRUCTURES LABORATORY	TW	50	20	35	P
21. DATA STRUCTURES LABORATORY	PR	50	20	37	P

GRAND TOTAL = 1015/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

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S8054210   AKASH DEEP AGRAWAL   KAUSALYA   , 71045366D   , S8054210 ,   PICT   , S8054210

01. DISCRETE STRUCTURES	PP	100	40	69	P C
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	48	P C
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	63	P C
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	72	P C
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	48	P C
06. PROGRAMMING LABORATORY	TW	25	10	16	P C
07. PROGRAMMING LABORATORY	PR	50	20	28	P C
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	17	P C
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	28	P C
10. SOFT SKILLS	TW	50	20	32	P C

11. ENGINEERING MATHEMATICS III	PP	100	40	64	P
12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	52	P
13. DATA STRUCTURES	PP	100	40	04	F
14. COMPUTER GRAPHICS	PP	100	40	68	P
15. COMPUTER ORGANIZATION	PP	100	40	56	P
16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	39	P
17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	43	P
18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	35	P
19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	37	P
20. DATA STRUCTURES LABORATORY	TW	50	20	33	P
21. DATA STRUCTURES LABORATORY	PR	50	20	32	P

GRAND TOTAL = 884/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

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. . . . . . . . . . . . . . . . . . . . . UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(COMPUTER) EXAMINATION MAY 2011  . . . . . . . . . . . . . . . . . . . . .
    DATE : 18 AUG. 2011            CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.          PAGE NO.  04  (  368)
. . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . .
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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.

      OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S8054211   ANIRUDH JODHA   ANJU   , 71045370B   , S8054211 , PICT   , S8054211

01. DISCRETE STRUCTURES	PP	100	40	49	P C		11. ENGINEERING MATHEMATICS III	PP	100	40	42	P	
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	40	P C		12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	40	P	
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	41	P C		13. DATA STRUCTURES	PP	100	40	32	F	
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	59	P C		14. COMPUTER GRAPHICS	PP	100	40	49	P	
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	46	P C		15. COMPUTER ORGANIZATION	PP	100	40	40	P	
06. PROGRAMMING LABORATORY	TW	25	10	16	P C		16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	33	P	
07. PROGRAMMING LABORATORY	PR	50	20	35	P C		17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	32	P	
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	15	P C		18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	29	P	
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	24	P C		19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	30	P	
10. SOFT SKILLS	TW	50	20	35	P C		20. DATA STRUCTURES LABORATORY	TW	50	20	30	P	
							21. DATA STRUCTURES LABORATORY	PR	50	20	15	F	

GRAND TOTAL = 732/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

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S8054212   AVHAD PRATIK GULAB   MAMTA   , 71045377K   , S8054212 , PICT   , S8054212

01. DISCRETE STRUCTURES	PP	100	40	68	P C		11. ENGINEERING MATHEMATICS III	PP	100	40	52	P	
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	41	P C		12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	31*	P	
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	55	P C		13. DATA STRUCTURES	PP	100	40	43	P	
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	72	P C		14. COMPUTER GRAPHICS	PP	100	40	53	P	

05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	44	P C	15. COMPUTER ORGANIZATION	PP	100	40	40	P
06. PROGRAMMING LABORATORY	TW	25	10	18	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	29	P
07. PROGRAMMING LABORATORY	PR	50	20	40	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	22	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	19	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	31	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	22	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	26	P
10. SOFT SKILLS	TW	50	20	35	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	30	P
						21. DATA STRUCTURES LABORATORY	PR	50	20	21	P

GRAND TOTAL = 792/1500, RESULT: SECOND CLASS   * [O.4]

ORDN. 1 MARKS :

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S8054213   AYACHIT DUSHYANT PRADEEPRAO   PRATIBHA   , 71132423K   , S8054213 , PICT   , S8054213

01. DISCRETE STRUCTURES	PP	100	40	66	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	46	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	59	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	57	P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	51	P C	13. DATA STRUCTURES	PP	100	40	66	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	58	P C	14. COMPUTER GRAPHICS	PP	100	40	69	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	40	P C	15. COMPUTER ORGANIZATION	PP	100	40	58	P
06. PROGRAMMING LABORATORY	TW	25	10	21	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	40	P
07. PROGRAMMING LABORATORY	PR	50	20	36	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	32	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	20	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	41	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	21	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	38	P
10. SOFT SKILLS	TW	50	20	39	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	36	P
						21. DATA STRUCTURES LABORATORY	PR	50	20	37	P

GRAND TOTAL = 931/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

. . . . UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(COMPUTER) EXAMINATION MAY 2011 . . . .

DATE : 18 AUG. 2011   CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.   PAGE NO. 05 (369)

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S8054214   BAHULIKAR CHAITALI SANJAY   SNEHA   , 71045383D   , S8054214 ,   PICT   , S8054214

01. DISCRETE STRUCTURES	PP	100	40	63	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	81	P	
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	55	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	65	P	
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	74	P C	13. DATA STRUCTURES	PP	100	40	73	P	
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	83	P C	14. COMPUTER GRAPHICS	PP	100	40	66	P	
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	57	P C	15. COMPUTER ORGANIZATION	PP	100	40	53	P	
06. PROGRAMMING LABORATORY	TW	25	10	23	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	44	P	
07. PROGRAMMING LABORATORY	PR	50	20	42	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	36	P	
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	22	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	44	P	
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	42	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	42	P	
10. SOFT SKILLS	TW	50	20	43	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	44	P	
						21. DATA STRUCTURES LABORATORY	PR	50	20	35	P	

GRAND TOTAL = 1087/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

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S8054215   BANG GAURAV SUNIL   SUNANDA   , 71045384B   , S8054215 ,   PICT   , S8054215

01. DISCRETE STRUCTURES	PP	100	40	64	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	92	P	
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	47	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	51	P	
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	66	P C	13. DATA STRUCTURES	PP	100	40	67	P	
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	67	P C	14. COMPUTER GRAPHICS	PP	100	40	69	P	
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	50	P C	15. COMPUTER ORGANIZATION	PP	100	40	48	P	
06. PROGRAMMING LABORATORY	TW	25	10	15	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	33	P	
07. PROGRAMMING LABORATORY	PR	50	20	28	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	41	P	
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	16	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	30	P	
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	25	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	45	P	

cols05

| 10. SOFT SKILLS | TW | 50 | 20 | 32 | P C | 20. DATA STRUCTURES LABORATORY | TW | 50 | 20 | 35 | P |
| | | | | | | 21. DATA STRUCTURES LABORATORY | PR | 50 | 20 | 31 | P |

GRAND TOTAL = 952/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

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S8054216   BHADADE MOHIT DNYANESH   SHOBHA   , 71045385L   , S8054216 ,   PICT   , S8054216

01. DISCRETE STRUCTURES	PP	100	40	76	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	83	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	57	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	59	P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	70	P C	13. DATA STRUCTURES	PP	100	40	73	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	68	P C	14. COMPUTER GRAPHICS	PP	100	40	63	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	57	P C	15. COMPUTER ORGANIZATION	PP	100	40	65	P
06. PROGRAMMING LABORATORY	TW	25	10	17	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	39	P
07. PROGRAMMING LABORATORY	PR	50	20	37	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	34	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	21	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	35	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	42	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	39	P
10. SOFT SKILLS	TW	50	20	35	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	35	P
						21. DATA STRUCTURES LABORATORY	PR	50	20	39	P

GRAND TOTAL = 1044/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

.. . . UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(COMPUTER) EXAMINATION MAY 2011 . . . .

DATE : 18 AUG. 2011   CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.   PAGE NO. 06 (370)

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.

        OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S8054217   BHANDARKAR PRATIK PRAVIN   KALPANA   , 71132424H   , S8054217 ,   PICT   , S8054217

01. DISCRETE STRUCTURES	PP	100	40	64	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	56	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	61	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	46	P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	78	P C	13. DATA STRUCTURES	PP	100	40	69	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	76	P C	14. COMPUTER GRAPHICS	PP	100	40	62	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	47	P C	15. COMPUTER ORGANIZATION	PP	100	40	50	P
06. PROGRAMMING LABORATORY	TW	25	10	20	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	44	P
07. PROGRAMMING LABORATORY	PR	50	20	37	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	42	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	21	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	41	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	38	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	41	P
10. SOFT SKILLS	TW	50	20	39	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	39	P
						21. DATA STRUCTURES LABORATORY	PR	50	20	41	P

GRAND TOTAL = 1012/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

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S8054218   BHANGALE PRATIK DIGAMBAR   MEENAKSHI   , 71132425F   , S8054218 ,   PICT   , S8054218

01. DISCRETE STRUCTURES	PP	100	40	60	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	55	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	70	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	59	P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	71	P C	13. DATA STRUCTURES	PP	100	40	78	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	62	P C	14. COMPUTER GRAPHICS	PP	100	40	59	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	50	P C	15. COMPUTER ORGANIZATION	PP	100	40	54	P
06. PROGRAMMING LABORATORY	TW	25	10	23	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	41	P
07. PROGRAMMING LABORATORY	PR	50	20	32	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	40	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	20	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	40	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	37	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	28	P
10. SOFT SKILLS	TW	50	20	41	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	33	P
						21. DATA STRUCTURES LABORATORY	PR	50	20	34	P

GRAND TOTAL = 987/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

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S8054219   BHAVSAR CHAITANYA SHYAM   BHARATI   , 71045389C   , S8054219 ,   PICT   , S8054219

01. DISCRETE STRUCTURES	PP	100	40	60	P C		11. ENGINEERING MATHEMATICS III	PP	100	40	41	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	45	P C		12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	43	P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	72	P C		13. DATA STRUCTURES	PP	100	40	60	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	65	P C		14. COMPUTER GRAPHICS	PP	100	40	67	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	47	P C		15. COMPUTER ORGANIZATION	PP	100	40	43	P
06. PROGRAMMING LABORATORY	TW	25	10	18	P C		16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	32	P
07. PROGRAMMING LABORATORY	PR	50	20	41	P C		17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	41	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	19	P C		18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	29	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	40	P C		19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	30	P
10. SOFT SKILLS	TW	50	20	34	P C		20. DATA STRUCTURES LABORATORY	TW	50	20	27	P
							21. DATA STRUCTURES LABORATORY	PR	50	20	35	P

GRAND TOTAL = 889/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

. . . .
                     UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(COMPUTER) EXAMINATION MAY 2011
    DATE : 18 AUG. 2011   CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.   PAGE NO. 07 (371)
. . . .

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.
        OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S8054220   BHOIR SARIKA DAMU   ANUSAYA   , 71045391E   , S8054220 ,   PICT   , S8054220

01. DISCRETE STRUCTURES	PP	100	40	53	P		11. ENGINEERING MATHEMATICS III	PP	100	40	40	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	40	P C		12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	26	F
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	60	P C		13. DATA STRUCTURES	PP	100	40	54	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	45	P C		14. COMPUTER GRAPHICS	PP	100	40	61	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	43	P		15. COMPUTER ORGANIZATION	PP	100	40	40	P

06. PROGRAMMING LABORATORY	TW	25	10	16	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	32	P
07. PROGRAMMING LABORATORY	PR	50	20	42	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	22	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	20	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	36	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	25	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	32	P
10. SOFT SKILLS	TW	50	20	33	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	35	P
						21. DATA STRUCTURES LABORATORY	PR	50	20	28	P

GRAND TOTAL = 783/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

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S8054221   BHOSALE SHITAL MANOHAR   JAYA   , 71132426D   , S8054221 ,   PICT   , S8054221

01. DISCRETE STRUCTURES	PP	100	40	40	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	11	F
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	44	P	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	40	P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	41	P C	13. DATA STRUCTURES	PP	100	40	40	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	47	P C	14. COMPUTER GRAPHICS	PP	100	40	46	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	40	P C	15. COMPUTER ORGANIZATION	PP	100	40	40	P
06. PROGRAMMING LABORATORY	TW	25	10	17	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	39	P
07. PROGRAMMING LABORATORY	PR	50	20	25	P	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	20$	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	19	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	34	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	20	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	08	F
10. SOFT SKILLS	TW	50	20	34	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	32	P
						21. DATA STRUCTURES LABORATORY	PR	50	20	10	F

GRAND TOTAL = 647/1500, RESULT: FAILS A.T.K.T.   [$ O.1]

ORDN. 1 MARKS : (17)2,

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S8054222   BHURKE POOJA MILIND   GEETANJALI   , 71045395H   , S8054222 ,   PICT   , S8054222

01. DISCRETE STRUCTURES	PP	100	40	81	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	60	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	53	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	48	P

03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	50	P C		13. DATA STRUCTURES	PP	100	40	58	P	
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	54	P C		14. COMPUTER GRAPHICS	PP	100	40	68	P	
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	56	P C		15. COMPUTER ORGANIZATION	PP	100	40	50	P	
06. PROGRAMMING LABORATORY	TW	25	10	21	P C		16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	30	P	
07. PROGRAMMING LABORATORY	PR	50	20	30	P C		17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	27	P	
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	20	P C		18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	33	P	
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	24	P C		19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	22	P	
10. SOFT SKILLS	TW	50	20	41	P C		20. DATA STRUCTURES LABORATORY	TW	50	20	34	P	
							21. DATA STRUCTURES LABORATORY	PR	50	20	08	F	

GRAND TOTAL = 868/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

. . . .
UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(COMPUTER) EXAMINATION MAY 2011
DATE : 18 AUG. 2011   CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.   PAGE NO. 08 (372)
. . . .
NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.
      OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
. . . .

S8054223   BISWAJIT DEY   TAPATI RANI   , 71045397D   , S8054223 , PICT   , S8054223

01. DISCRETE STRUCTURES	PP	100	40	60	P C		11. ENGINEERING MATHEMATICS III	PP	100	40	69	P	
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	64	P C		12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	46	P	
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	68	P C		13. DATA STRUCTURES	PP	100	40	65	P	
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	67	P C		14. COMPUTER GRAPHICS	PP	100	40	60	P	
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	53	P C		15. COMPUTER ORGANIZATION	PP	100	40	62	P	
06. PROGRAMMING LABORATORY	TW	25	10	20	P C		16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	45	P	
07. PROGRAMMING LABORATORY	PR	50	20	40	P C		17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	45	P	
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	22	P C		18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	40	P	
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	41	P C		19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	37	P	
10. SOFT SKILLS	TW	50	20	40	P C		20. DATA STRUCTURES LABORATORY	TW	50	20	42	P	

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                                              21. DATA STRUCTURES LABORATORY      PR   50   20   34  P
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GRAND TOTAL = 1020/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

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  S8054224    BOTHALE SANJANA ANIL               KALPANA              , 71045399L    , S8054224 ,   PICT      , S8054224

  01. DISCRETE STRUCTURES           PP   100   40   60  P C
  02. PROGRAMMING & PROBLEM SOLVING   PP   100   40   57  P C
  03. DIGIT. ELECTRONICS & LOGIC DESIGNPP   100   40   71  P C
  04. DATA STRUCTURES AND ALGORITHMS  PP   100   40   58  P C
  05. HUMANITIES AND SOCIAL SCIENCE   PP   100   40   54  P C
  06. PROGRAMMING LABORATORY         TW    25   10   14  P C
  07. PROGRAMMING LABORATORY         PR    50   20   41  P C
  08. DIGITAL ELECTRONICS LABORATORY  TW    25   10   15  P C
  09. DIGITAL ELECTRONICS LABORATORY  PR    50   20   30  P C
  10. SOFT SKILLS                    TW    50   20   31  P C
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  FIRST TERM TOTAL =   431/700.

ORDN. 1 MARKS :

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  S8054225    CHANDORKAR ANUJA ANIRUDDHA         ARUNA                , 71045401F    , S8054225 ,   PICT      , S8054225

  01. DISCRETE STRUCTURES           PP   100   40   60  P C      11. ENGINEERING MATHEMATICS III       PP   100   40   76  P
  02. PROGRAMMING & PROBLEM SOLVING   PP   100   40   51  P C      12. MICROPROC. & INTERFACING TECHNIQ.PP   100   40   43  P
  03. DIGIT. ELECTRONICS & LOGIC DESIGNPP   100   40   68  P C      13. DATA STRUCTURES                   PP   100   40   79  P
  04. DATA STRUCTURES AND ALGORITHMS  PP   100   40   76  P C      14. COMPUTER GRAPHICS                 PP   100   40   71  P
  05. HUMANITIES AND SOCIAL SCIENCE   PP   100   40   49  P C      15. COMPUTER ORGANIZATION             PP   100   40   58  P
  06. PROGRAMMING LABORATORY         TW    25   10   21  P C      16. O. O. PROG. & COMP. GRAPH. LAB    TW    50   20   42  P
  07. PROGRAMMING LABORATORY         PR    50   20   37  P C      17. O. O. PROG. & COMP. GRAPH. LAB    PR    50   20   36  P
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08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	22	P C		18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	42	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	40	P C		19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	42	P
10. SOFT SKILLS	TW	50	20	40	P C		20. DATA STRUCTURES LABORATORY	TW	50	20	45	P
							21. DATA STRUCTURES LABORATORY	PR	50	20	30	P

GRAND TOTAL = 1028/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

. . . .
UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(COMPUTER) EXAMINATION MAY 2011

DATE : 18 AUG. 2011   CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.   PAGE NO. 09 (373)
. . . .

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.

    OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S8054226   CHAUDHARI DIPTI KRISHNA   SHARDA   , 71045404L   , S8054226 ,   PICT   , S8054226

01. DISCRETE STRUCTURES	PP	100	40	67	P C		11. ENGINEERING MATHEMATICS III	PP	100	40	52	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	45	P C		12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	52	P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	67	P C		13. DATA STRUCTURES	PP	100	40	47	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	65	P C		14. COMPUTER GRAPHICS	PP	100	40	54	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	52	P C		15. COMPUTER ORGANIZATION	PP	100	40	55	P
06. PROGRAMMING LABORATORY	TW	25	10	17	P C		16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	30	P
07. PROGRAMMING LABORATORY	PR	50	20	38	P C		17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	34	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	17	P C		18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	29	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	25	P C		19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	23	P
10. SOFT SKILLS	TW	50	20	33	P C		20. DATA STRUCTURES LABORATORY	TW	50	20	30	P
							21. DATA STRUCTURES LABORATORY	PR	50	20	35	P

GRAND TOTAL = 867/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

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S8054227   CHAUDHARI RASHMI CHANDRAKANT   KARUNA   , 71045405J   , S8054227 , PICT   , S8054227

01. DISCRETE STRUCTURES	PP	100	40	65	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	62	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	49	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	51	P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	65	P C	13. DATA STRUCTURES	PP	100	40	53	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	74	P C	14. COMPUTER GRAPHICS	PP	100	40	51	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	40	P C	15. COMPUTER ORGANIZATION	PP	100	40	60	P
06. PROGRAMMING LABORATORY	TW	25	10	21	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	35	P
07. PROGRAMMING LABORATORY	PR	50	20	38	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	38	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	22	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	41	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	35	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	40	P
10. SOFT SKILLS	TW	50	20	41	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	39	P
						21. DATA STRUCTURES LABORATORY	PR	50	20	32	P

GRAND TOTAL = 952/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

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S8054228   CHAVAN ASHISH VIJAYKUMAR   RAJESHREE   , 70925380L   , S8054228 , PICT   , S8054228

01. DISCRETE STRUCTURES	PP	100	40	53	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	67	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	40	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	42	P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	45	P C	13. DATA STRUCTURES	PP	100	40	70	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	69	P C	14. COMPUTER GRAPHICS	PP	100	40	53	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	48	P C	15. COMPUTER ORGANIZATION	PP	100	40	52	P
06. PROGRAMMING LABORATORY	TW	25	10	18	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	37	P
07. PROGRAMMING LABORATORY	PR	50	20	37	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	34	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	19	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	39	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	25	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	39	P
10. SOFT SKILLS	TW	50	20	38	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	33	P
						21. DATA STRUCTURES LABORATORY	PR	50	20	33	P

GRAND TOTAL = 891+09/1500, RESULT: FIRST CLASS [O.2]

ORDN. 1 MARKS :

. . . . UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(COMPUTER) EXAMINATION MAY 2011 . . . .

DATE : 18 AUG. 2011   CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.   PAGE NO. 10 (374)
. . . .

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S8054229   CHHAJED DARSHAN RAJKUMAR   KUSUM   , 71045407E   , S8054229 ,   PICT   , S8054229

01. DISCRETE STRUCTURES	PP	100	40	65	P C		11. ENGINEERING MATHEMATICS III	PP	100	40	65	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	55	P C		12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	49	P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	72	P C		13. DATA STRUCTURES	PP	100	40	71	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	68	P C		14. COMPUTER GRAPHICS	PP	100	40	61	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	51	P C		15. COMPUTER ORGANIZATION	PP	100	40	48	P
06. PROGRAMMING LABORATORY	TW	25	10	19	P C		16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	36	P
07. PROGRAMMING LABORATORY	PR	50	20	35	P C		17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	28	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	21	P C		18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	35	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	37	P C		19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	37	P
10. SOFT SKILLS	TW	50	20	36	P C		20. DATA STRUCTURES LABORATORY	TW	50	20	37	P
							21. DATA STRUCTURES LABORATORY	PR	50	20	37	P

GRAND TOTAL = 963/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

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S8054230   CHHAJED NIKHIL VINOD   MANISHA   , 71045408C   , S8054230 ,   PICT   , S8054230

01. DISCRETE STRUCTURES	PP	100	40	64	P C		11. ENGINEERING MATHEMATICS III	PP	100	40	88	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	48	P C		12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	52	P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	70	P C		13. DATA STRUCTURES	PP	100	40	67	P

04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	78	P C	14. COMPUTER GRAPHICS		PP	100	40	70	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	53	P C	15. COMPUTER ORGANIZATION		PP	100	40	66	P
06. PROGRAMMING LABORATORY	TW	25	10	21	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	29	P
07. PROGRAMMING LABORATORY	PR	50	20	40	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	32	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	20	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	35	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	40	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	34	P
10. SOFT SKILLS	TW	50	20	39	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	30	P
						21. DATA STRUCTURES LABORATORY	PR	50	20	40	P

GRAND TOTAL = 1016/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

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S8054231   DESHMUKH MAYURAJ PRABHAKAR   ANURADHA   , 71045412M   , S8054231 ,   PICT   , S8054231

01. DISCRETE STRUCTURES	PP	100	40	53	P C	11. ENGINEERING MATHEMATICS III		PP	100	40	40	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	57	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	28	F
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	58	P C	13. DATA STRUCTURES		PP	100	40	40	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	73	P C	14. COMPUTER GRAPHICS		PP	100	40	44	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	52	P C	15. COMPUTER ORGANIZATION		PP	100	40	47	P
06. PROGRAMMING LABORATORY	TW	25	10	16	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	21	P
07. PROGRAMMING LABORATORY	PR	50	20	39	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	22	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	12	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	22	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	28	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	20	P
10. SOFT SKILLS	TW	50	20	26	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	21	P
						21. DATA STRUCTURES LABORATORY	PR	50	20	25	P

GRAND TOTAL = 744/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S8054232   DESHMUKH SMITA ARUN   ASHLESHA   , 71045413K   , S8054232 ,   PICT   , S8054232

01. DISCRETE STRUCTURES	PP	100	40	61	P C		11. ENGINEERING MATHEMATICS III	PP	100	40	66	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	52	P C		12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	47	P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	67	P C		13. DATA STRUCTURES	PP	100	40	57	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	67	P C		14. COMPUTER GRAPHICS	PP	100	40	52	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	42	P C		15. COMPUTER ORGANIZATION	PP	100	40	61	P
06. PROGRAMMING LABORATORY	TW	25	10	15	P C		16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	30	P
07. PROGRAMMING LABORATORY	PR	50	20	36	P C		17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	28	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	20	P C		18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	30	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	26	P C		19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	28	P
10. SOFT SKILLS	TW	50	20	37	P C		20. DATA STRUCTURES LABORATORY	TW	50	20	34	P
							21. DATA STRUCTURES LABORATORY	PR	50	20	33	P

GRAND TOTAL = 889/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

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S8054233   DISHA DAWANI   MEENA   , 71045419J   , S8054233 ,   PICT   , S8054233

01. DISCRETE STRUCTURES	PP	100	40	66	P C		11. ENGINEERING MATHEMATICS III	PP	100	40	65	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	53	P C		12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	42	P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	62	P C		13. DATA STRUCTURES	PP	100	40	64	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	76	P C		14. COMPUTER GRAPHICS	PP	100	40	68	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	45	P C		15. COMPUTER ORGANIZATION	PP	100	40	50	P
06. PROGRAMMING LABORATORY	TW	25	10	20	P C		16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	43	P
07. PROGRAMMING LABORATORY	PR	50	20	44	P C		17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	36	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	21	P C		18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	43	P

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09. DIGITAL ELECTRONICS LABORATORY    PR    50   20   45  P C    19. MICROPROCESSORS & INTERFACING LABPR    50   20   40  P
10. SOFT SKILLS                       TW    50   20   42  P C    20. DATA STRUCTURES LABORATORY       TW    50   20   45  P
                                                                21. DATA STRUCTURES LABORATORY       PR    50   20   39  P
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GRAND TOTAL = 1009/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

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S8054234   DUBEY AAKRITI MANOJ               ARCHANA          , 71045421L    , S8054234 ,   PICT     , S8054234

01. DISCRETE STRUCTURES               PP   100   40   60  P C    11. ENGINEERING MATHEMATICS III      PP   100   40   67  P
02. PROGRAMMING & PROBLEM SOLVING     PP   100   40   64  P C    12. MICROPROC. & INTERFACING TECHNIQ.PP   100   40   44  P
03. DIGIT. ELECTRONICS & LOGIC DESIGNPP   100   40   75  P C    13. DATA STRUCTURES                  PP   100   40   71  P
04. DATA STRUCTURES AND ALGORITHMS    PP   100   40   77  P C    14. COMPUTER GRAPHICS                PP   100   40   67  P
05. HUMANITIES AND SOCIAL SCIENCE     PP   100   40   52  P C    15. COMPUTER ORGANIZATION            PP   100   40   58  P
06. PROGRAMMING LABORATORY            TW    25   10   23  P C    16. O. O. PROG. & COMP. GRAPH. LAB   TW    50   20   44  P
07. PROGRAMMING LABORATORY            PR    50   20   40  P C    17. O. O. PROG. & COMP. GRAPH. LAB   PR    50   20   39  P
08. DIGITAL ELECTRONICS LABORATORY    TW    25   10   23  P C    18. MICROPROCESSORS & INTERFACING LABTW    50   20   45  P
09. DIGITAL ELECTRONICS LABORATORY    PR    50   20   32  P C    19. MICROPROCESSORS & INTERFACING LABPR    50   20   35  P
10. SOFT SKILLS                       TW    50   20   43  P C    20. DATA STRUCTURES LABORATORY       TW    50   20   45  P
                                                                21. DATA STRUCTURES LABORATORY       PR    50   20   38  P
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GRAND TOTAL = 1042/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.

      OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S8054235   DUBEY SHREYA CHANDRAMA   TARA   , 71045422J   , S8054235 ,   PICT   , S8054235

01. DISCRETE STRUCTURES	PP	100	40	75	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	66	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	66	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	59	P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	79	P C	13. DATA STRUCTURES	PP	100	40	59	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	70	P C	14. COMPUTER GRAPHICS	PP	100	40	72	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	56	P C	15. COMPUTER ORGANIZATION	PP	100	40	68	P
06. PROGRAMMING LABORATORY	TW	25	10	19	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	33	P
07. PROGRAMMING LABORATORY	PR	50	20	38	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	40	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	21	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	37	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	32	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	40	P
10. SOFT SKILLS	TW	50	20	41	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	35	P
						21. DATA STRUCTURES LABORATORY	PR	50	20	34	P

GRAND TOTAL = 1040/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

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S8054236   DURGESH CHAPORKAR   SUMEDHA   , 71045423G   , S8054236 ,   PICT   , S8054236

01. DISCRETE STRUCTURES	PP	100	40	63	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	42	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	45	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	25	F
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	75	P C	13. DATA STRUCTURES	PP	100	40	64	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	66	P C	14. COMPUTER GRAPHICS	PP	100	40	63	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	53	P C	15. COMPUTER ORGANIZATION	PP	100	40	45	P
06. PROGRAMMING LABORATORY	TW	25	10	15	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	26	P
07. PROGRAMMING LABORATORY	PR	50	20	32	P	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	38	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	15	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	25	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	21	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	22	P
10. SOFT SKILLS	TW	50	20	28	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	22	P
						21. DATA STRUCTURES LABORATORY	PR	50	20	28	P

GRAND TOTAL = 813/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

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S8054237   DURUGKAR SURAJ ANIL   NIRMALA   , 71045424E   , S8054237 ,   PICT   , S8054237

01. DISCRETE STRUCTURES	PP	100	40	61	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	40	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	45	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	29	F
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	44	P C	13. DATA STRUCTURES	PP	100	40	40	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	70	P C	14. COMPUTER GRAPHICS	PP	100	40	58	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	40	P	15. COMPUTER ORGANIZATION	PP	100	40	46	P
06. PROGRAMMING LABORATORY	TW	25	10	11	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	22	P
07. PROGRAMMING LABORATORY	PR	50	20	34	P	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	26	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	14	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	21	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	20	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	08	F
10. SOFT SKILLS	TW	50	20	26	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	21	P
						21. DATA STRUCTURES LABORATORY	PR	50	20	08	F

GRAND TOTAL = 684/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

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                      UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(COMPUTER) EXAMINATION MAY 2011

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 NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.

        OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S8054238   GADE ABHIJEET BHARAT   MANGAL   , 71132427B   , S8054238 ,   PICT   , S8054238

01. DISCRETE STRUCTURES	PP	100	40	56	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	42	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	66	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	47	P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	72	P C	13. DATA STRUCTURES	PP	100	40	52	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	82	P C	14. COMPUTER GRAPHICS	PP	100	40	67	P

05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	70	P C		15. COMPUTER ORGANIZATION	PP	100	40	67	P	
06. PROGRAMMING LABORATORY	TW	25	10	20	P C		16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	35	P	
07. PROGRAMMING LABORATORY	PR	50	20	28	P C		17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	30	P	
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	19	P C		18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	35	P	
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	20	P C		19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	38	P	
10. SOFT SKILLS	TW	50	20	37	P C		20. DATA STRUCTURES LABORATORY	TW	50	20	29	P	
							21. DATA STRUCTURES LABORATORY	PR	50	20	25	P	

GRAND TOTAL = 937/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

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S8054239   GADIA SAURABH PRASHANT   SUNITA   , 71045428H   , S8054239 ,   PICT   , S8054239

01. DISCRETE STRUCTURES	PP	100	40	76	P C		11. ENGINEERING MATHEMATICS III	PP	100	40	85	P	
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	57	P C		12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	46	P	
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	72	P C		13. DATA STRUCTURES	PP	100	40	69	P	
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	79	P C		14. COMPUTER GRAPHICS	PP	100	40	62	P	
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	58	P C		15. COMPUTER ORGANIZATION	PP	100	40	66	P	
06. PROGRAMMING LABORATORY	TW	25	10	17	P C		16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	31	P	
07. PROGRAMMING LABORATORY	PR	50	20	30	P C		17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	42	P	
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	17	P C		18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	36	P	
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	34	P C		19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	36	P	
10. SOFT SKILLS	TW	50	20	37	P C		20. DATA STRUCTURES LABORATORY	TW	50	20	33	P	
							21. DATA STRUCTURES LABORATORY	PR	50	20	38	P	

GRAND TOTAL = 1021/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

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S8054240   GADSING MAYURI LAXMAN   SAVITA   , 71045429F   , S8054240 ,   PICT   , S8054240

01. DISCRETE STRUCTURES	PP	100	40	73	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	67	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	49	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	40	P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	64	P C	13. DATA STRUCTURES	PP	100	40	56	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	67	P C	14. COMPUTER GRAPHICS	PP	100	40	56	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	55	P C	15. COMPUTER ORGANIZATION	PP	100	40	51	P
06. PROGRAMMING LABORATORY	TW	25	10	20	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	37	P
07. PROGRAMMING LABORATORY	PR	50	20	28	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	40	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	17	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	38	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	35	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	37	P
10. SOFT SKILLS	TW	50	20	36	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	35	P
						21. DATA STRUCTURES LABORATORY	PR	50	20	39	P

GRAND TOTAL = 940/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

. . . . UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(COMPUTER) EXAMINATION MAY 2011 . . . .

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 NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.

        OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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 S8054241   GAIKWAD MAHESH EKNATH   LATA   , 71045431H   , S8054241 ,   PICT   , S8054241

01. DISCRETE STRUCTURES	PP	100	40	40	P	11. ENGINEERING MATHEMATICS III	PP	100	40	40	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	32	F	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	32	F
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	47	P C	13. DATA STRUCTURES	PP	100	40	32	F
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	42	P C	14. COMPUTER GRAPHICS	PP	100	40	46	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	40	P C	15. COMPUTER ORGANIZATION	PP	100	40	40	P
06. PROGRAMMING LABORATORY	TW	25	10	14	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	27	P
07. PROGRAMMING LABORATORY	PR	50	20	12	F	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	04	F
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	16	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	26	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	20	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	22	P

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10. SOFT SKILLS                        TW    50    20    31  P C    20. DATA STRUCTURES LABORATORY       TW    50    20    26  P
                                                                    21. DATA STRUCTURES LABORATORY       PR    50    20    25  P
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GRAND TOTAL = 614/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

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S8054242    GAIKWAD PUSHAKAR KISHOR              VAISHALI          , 71045432F    , S8054242 ,  PICT      , S8054242

01. DISCRETE STRUCTURES              PP   100    40    64  P C    11. ENGINEERING MATHEMATICS III      PP   100    40    40  P
02. PROGRAMMING & PROBLEM SOLVING    PP   100    40    55  P C    12. MICROPROC. & INTERFACING TECHNIQ.PP   100    40    53  P
03. DIGIT. ELECTRONICS & LOGIC DESIGNPP   100    40    82  P C    13. DATA STRUCTURES                  PP   100    40    61  P
04. DATA STRUCTURES AND ALGORITHMS   PP   100    40    76  P C    14. COMPUTER GRAPHICS                PP   100    40    70  P
05. HUMANITIES AND SOCIAL SCIENCE    PP   100    40    65  P C    15. COMPUTER ORGANIZATION            PP   100    40    58  P
06. PROGRAMMING LABORATORY           TW    25    10    21  P C    16. O. O. PROG. & COMP. GRAPH. LAB   TW    50    20    35  P
07. PROGRAMMING LABORATORY           PR    50    20    45  P C    17. O. O. PROG. & COMP. GRAPH. LAB   PR    50    20    43  P
08. DIGITAL ELECTRONICS LABORATORY   TW    25    10    19  P C    18. MICROPROCESSORS & INTERFACING LABTW    50    20    40  P
09. DIGITAL ELECTRONICS LABORATORY   PR    50    20    44  P C    19. MICROPROCESSORS & INTERFACING LABPR    50    20    44  P
10. SOFT SKILLS                      TW    50    20    38  P C    20. DATA STRUCTURES LABORATORY       TW    50    20    38  P
                                                                  21. DATA STRUCTURES LABORATORY       PR    50    20    37  P
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GRAND TOTAL = 1028/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

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S8054243    GANDHI ANUJ VIDYUT                   SHARMILA          , 71045433D    , S8054243 ,  PICT      , S8054243

01. DISCRETE STRUCTURES              PP   100    40    69  P C    11. ENGINEERING MATHEMATICS III      PP   100    40    68  P
02. PROGRAMMING & PROBLEM SOLVING    PP   100    40    42  P C    12. MICROPROC. & INTERFACING TECHNIQ.PP   100    40    55  P
03. DIGIT. ELECTRONICS & LOGIC DESIGNPP   100    40    56  P C    13. DATA STRUCTURES                  PP   100    40    58  P
04. DATA STRUCTURES AND ALGORITHMS   PP   100    40    64  P C    14. COMPUTER GRAPHICS                PP   100    40    64  P
05. HUMANITIES AND SOCIAL SCIENCE    PP   100    40    40  P C    15. COMPUTER ORGANIZATION            PP   100    40    65  P
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06. PROGRAMMING LABORATORY	TW	25	10	15	P C		16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	34	P
07. PROGRAMMING LABORATORY	PR	50	20	32	P C		17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	34	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	17	P C		18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	40	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	22	P C		19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	46	P
10. SOFT SKILLS	TW	50	20	34	P C		20. DATA STRUCTURES LABORATORY	TW	50	20	34	P
							21. DATA STRUCTURES LABORATORY	PR	50	20	31	P

GRAND TOTAL = 920/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

. . . . UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(COMPUTER) EXAMINATION MAY 2011 . . . .

DATE : 18 AUG. 2011   CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.   PAGE NO. 15 (379)

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S8054244   GAUTAM KUMAR   BALA DEVI   , 71045434B   , S8054244 ,   PICT   , S8054244

01. DISCRETE STRUCTURES	PP	100	40	65	P C		11. ENGINEERING MATHEMATICS III	PP	100	40	82	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	51	P C		12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	60	P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	69	P C		13. DATA STRUCTURES	PP	100	40	69	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	72	P C		14. COMPUTER GRAPHICS	PP	100	40	71	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	62	P C		15. COMPUTER ORGANIZATION	PP	100	40	59	P
06. PROGRAMMING LABORATORY	TW	25	10	19	P C		16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	36	P
07. PROGRAMMING LABORATORY	PR	50	20	28	P C		17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	27	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	19	P C		18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	31	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	22	P C		19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	29	P
10. SOFT SKILLS	TW	50	20	36	P C		20. DATA STRUCTURES LABORATORY	TW	50	20	30	P
							21. DATA STRUCTURES LABORATORY	PR	50	20	40	P

GRAND TOTAL = 977/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

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S8054245    GOKHALE GAURAV SUHAS                SWATI              , 71132428L    , S8054245 ,  PICT     , S8054245
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01. DISCRETE STRUCTURES	PP	100	40	40	P C		11. ENGINEERING MATHEMATICS III	PP	100	40	40	P	
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	48	P C		12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	42	P	
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	40	P C		13. DATA STRUCTURES	PP	100	40	29	F	
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	66	P C		14. COMPUTER GRAPHICS	PP	100	40	57	P	
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	58	P C		15. COMPUTER ORGANIZATION	PP	100	40	50	P	
06. PROGRAMMING LABORATORY	TW	25	10	16	P C		16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	24	P	
07. PROGRAMMING LABORATORY	PR	50	20	22	P		17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	22	P	
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	18	P C		18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	20	P	
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	27	P C		19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	20	P	
10. SOFT SKILLS	TW	50	20	29	P C		20. DATA STRUCTURES LABORATORY	TW	50	20	20	P	
							21. DATA STRUCTURES LABORATORY	PR	50	20	04	F	

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GRAND TOTAL =  692/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :
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S8054246    GUPTA TARUN SATYABHUSHAN            KANAK              , 71045442C    , S8054246 ,  PICT     , S8054246
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01. DISCRETE STRUCTURES	PP	100	40	74	P C		11. ENGINEERING MATHEMATICS III	PP	100	40	55	P	
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	47	P C		12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	48	P	
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	68	P C		13. DATA STRUCTURES	PP	100	40	53	P	
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	73	P C		14. COMPUTER GRAPHICS	PP	100	40	69	P	
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	50	P C		15. COMPUTER ORGANIZATION	PP	100	40	63	P	
06. PROGRAMMING LABORATORY	TW	25	10	15	P C		16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	29	P	
07. PROGRAMMING LABORATORY	PR	50	20	30	P C		17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	37	P	
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	17	P C		18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	30	P	
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	23	P C		19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	32	P	
10. SOFT SKILLS	TW	50	20	34	P C		20. DATA STRUCTURES LABORATORY	TW	50	20	32	P	

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                                                  21. DATA STRUCTURES LABORATORY      PR   50   20   37  P
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GRAND TOTAL = 916/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

. . . . UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(COMPUTER) EXAMINATION MAY 2011 . . . .

      DATE : 18 AUG. 2011   CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.   PAGE NO. 16 (380)

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  NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.

         OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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  S8054247   HARSHIT SINGHANIA   SEEMA   , 71045445H   , S8054247 , PICT   , S8054247

  01. DISCRETE STRUCTURES   PP   100   40   54 P C   11. ENGINEERING MATHEMATICS III   PP   100   40   24 F
  02. PROGRAMMING & PROBLEM SOLVING PP   100   40   40 P C   12. MICROPROC. & INTERFACING TECHNIQ.PP   100   40   40 P
  03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 40   66 P C   13. DATA STRUCTURES   PP   100   40   48 P
  04. DATA STRUCTURES AND ALGORITHMS PP   100 40   58 P C   14. COMPUTER GRAPHICS   PP   100   40   56 P
  05. HUMANITIES AND SOCIAL SCIENCE PP   100   40   40 P C   15. COMPUTER ORGANIZATION   PP   100   40   48 P
  06. PROGRAMMING LABORATORY   TW   25   10   18 P C   16. O. O. PROG. & COMP. GRAPH. LAB TW   50   20   30 P
  07. PROGRAMMING LABORATORY   PR   50   20   41 P C   17. O. O. PROG. & COMP. GRAPH. LAB PR   50   20   31 P
  08. DIGITAL ELECTRONICS LABORATORY TW   25   10   15 P C   18. MICROPROCESSORS & INTERFACING LABTW   50   20   29 P
  09. DIGITAL ELECTRONICS LABORATORY PR   50   20   34 P C   19. MICROPROCESSORS & INTERFACING LABPR   50   20   20 P
  10. SOFT SKILLS   TW   50   20   35 P C   20. DATA STRUCTURES LABORATORY   TW   50   20   28 P
                                                               21. DATA STRUCTURES LABORATORY   PR   50   20   36 P

GRAND TOTAL = 791/1500, RESULT: FAILS A.T.K.T.   RESULT RESERVED FOR BKLG

ORDN. 1 MARKS :

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  S8054248   JADHAV PRASHANT UTTAM   ANITA   , 71045454G   , S8054248 , PICT   , S8054248

  01. DISCRETE STRUCTURES   PP   100   40   40 P C   11. ENGINEERING MATHEMATICS III   PP   100   40   40 P

02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	40	P		12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	AA	F
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	40	P	C	13. DATA STRUCTURES	PP	100	40	40	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	53	P	C	14. COMPUTER GRAPHICS	PP	100	40	49	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	40	P		15. COMPUTER ORGANIZATION	PP	100	40	40	P
06. PROGRAMMING LABORATORY	TW	25	10	13	P	C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	28	P
07. PROGRAMMING LABORATORY	PR	50	20	35	P		17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	20$	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	11	P	C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	22	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	21	P	C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	05	F
10. SOFT SKILLS	TW	50	20	30	P	C	20. DATA STRUCTURES LABORATORY	TW	50	20	21	P
							21. DATA STRUCTURES LABORATORY	PR	50	20	02	F

GRAND TOTAL = 590/1500, RESULT: FAILS A.T.K.T.   [$ O.1]

ORDN. 1 MARKS : (17)2,

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S8054249   JAIN PUJAN PRAFULLAKUMAR   JYOTI   , 71045456C   , S8054249 ,   PICT   , S8054249

01. DISCRETE STRUCTURES	PP	100	40	57	P	C	11. ENGINEERING MATHEMATICS III	PP	100	40	40	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	40	P	C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	40	P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	56	P	C	13. DATA STRUCTURES	PP	100	40	63	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	70	P	C	14. COMPUTER GRAPHICS	PP	100	40	61	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	40	P	C	15. COMPUTER ORGANIZATION	PP	100	40	59	P
06. PROGRAMMING LABORATORY	TW	25	10	17	P	C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	30	P
07. PROGRAMMING LABORATORY	PR	50	20	35	P	C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	10	F
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	15	P	C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	31	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	20	P	C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	37	P
10. SOFT SKILLS	TW	50	20	33	P	C	20. DATA STRUCTURES LABORATORY	TW	50	20	26	P
							21. DATA STRUCTURES LABORATORY	PR	50	20	37	P

GRAND TOTAL = 817/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.

       OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S8054250   JAIN SAKSHI PARAS   JYOTI   , 71045457M   , S8054250 , PICT   , S8054250

01. DISCRETE STRUCTURES	PP	100	40	69	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	79	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	49	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	56	P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	58	P C	13. DATA STRUCTURES	PP	100	40	60	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	71	P C	14. COMPUTER GRAPHICS	PP	100	40	65	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	51	P C	15. COMPUTER ORGANIZATION	PP	100	40	61	P
06. PROGRAMMING LABORATORY	TW	25	10	21	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	37	P
07. PROGRAMMING LABORATORY	PR	50	20	30	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	12	F
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	21	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	37	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	32	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	42	P
10. SOFT SKILLS	TW	50	20	38	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	34	P
						21. DATA STRUCTURES LABORATORY	PR	50	20	38	P

GRAND TOTAL = 961/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

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S8054251   JAISWAL PRATIK PRAKASH   KALPANA   , 71045458K   , S8054251 , PICT   , S8054251

01. DISCRETE STRUCTURES	PP	100	40	58	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	48	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	50	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	52	P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	50	P C	13. DATA STRUCTURES	PP	100	40	61	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	64	P C	14. COMPUTER GRAPHICS	PP	100	40	56	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	65	P C	15. COMPUTER ORGANIZATION	PP	100	40	52	P
06. PROGRAMMING LABORATORY	TW	25	10	19	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	39	P

07. PROGRAMMING LABORATORY	PR	50	20	37	P		17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	35	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	19	P	C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	39	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	20	P	C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	21	P
10. SOFT SKILLS	TW	50	20	36	P	C	20. DATA STRUCTURES LABORATORY	TW	50	20	32	P
							21. DATA STRUCTURES LABORATORY	PR	50	20	39	P

GRAND TOTAL = 892+08/1500, RESULT: FIRST CLASS [O.2]

ORDN. 1 MARKS :

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S8054252   JAMES DADO   ANIA   , 71045460M   , S8054252 ,   PICT   , S8054252

01. DISCRETE STRUCTURES	PP	100	40	40	P	C	11. ENGINEERING MATHEMATICS III	PP	100	40	40	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	40	P	C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	40	P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	57	P		13. DATA STRUCTURES	PP	100	40	47	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	65	P	C	14. COMPUTER GRAPHICS	PP	100	40	53	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	64	P	C	15. COMPUTER ORGANIZATION	PP	100	40	47	P
06. PROGRAMMING LABORATORY	TW	25	10	16	P	C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	28	P
07. PROGRAMMING LABORATORY	PR	50	20	25	P	C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	26	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	16	P	C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	26	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	24	P		19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	10	F
10. SOFT SKILLS	TW	50	20	37	P	C	20. DATA STRUCTURES LABORATORY	TW	50	20	22	P
							21. DATA STRUCTURES LABORATORY	PR	50	20	02	F

GRAND TOTAL = 725/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.

        OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S8054253   JASPREET SINGH HORA   BHUPENDER   , 71045461K   , S8054253 ,   PICT   , S8054253

01. DISCRETE STRUCTURES	PP	100	40	59	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	59	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	57	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	57	P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	58	P C	13. DATA STRUCTURES	PP	100	40	52	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	49	P C	14. COMPUTER GRAPHICS	PP	100	40	52	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	51	P C	15. COMPUTER ORGANIZATION	PP	100	40	49	P
06. PROGRAMMING LABORATORY	TW	25	10	20	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	42	P
07. PROGRAMMING LABORATORY	PR	50	20	26	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	08	F
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	18	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	43	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	36	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	34	P
10. SOFT SKILLS	TW	50	20	40	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	37	P
						21. DATA STRUCTURES LABORATORY	PR	50	20	20	P

GRAND TOTAL = 867/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

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S8054254   JONDHALE KANCHAN BABUNATH   ALKA   , 71132429J   , S8054254 ,   PICT   , S8054254

01. DISCRETE STRUCTURES	PP	100	40	62	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	50	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	41	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	59	P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	60	P C	13. DATA STRUCTURES	PP	100	40	49	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	79	P C	14. COMPUTER GRAPHICS	PP	100	40	59	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	47	P C	15. COMPUTER ORGANIZATION	PP	100	40	65	P
06. PROGRAMMING LABORATORY	TW	25	10	20	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	43	P
07. PROGRAMMING LABORATORY	PR	50	20	28	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	40	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	18	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	43	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	26	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	36	P
10. SOFT SKILLS	TW	50	20	39	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	37	P
						21. DATA STRUCTURES LABORATORY	PR	50	20	28	P

GRAND TOTAL = 929/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

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S8054255   JOSHI DHANANJAY SHRINIVAS   SAVITA   , 71132430B   , S8054255 ,   PICT   , S8054255

01. DISCRETE STRUCTURES	PP	100	40	49	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	51	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	47	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	58	P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	61	P C	13. DATA STRUCTURES	PP	100	40	66	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	74	P C	14. COMPUTER GRAPHICS	PP	100	40	69	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	69	P C	15. COMPUTER ORGANIZATION	PP	100	40	59	P
06. PROGRAMMING LABORATORY	TW	25	10	19	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	43	P
07. PROGRAMMING LABORATORY	PR	50	20	42	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	31	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	19	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	39	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	40	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	40	P
10. SOFT SKILLS	TW	50	20	37	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	39	P
						21. DATA STRUCTURES LABORATORY	PR	50	20	40	P

GRAND TOTAL = 992/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

. . . . UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(COMPUTER) EXAMINATION MAY 2011 . . . .

   DATE : 18 AUG. 2011   CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.   PAGE NO. 19 (383)

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 NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.

        OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S8054256   KANADE ROHIT RAJARAM   VIDYA   , 71132431L   , S8054256 ,   PICT   , S8054256

01. DISCRETE STRUCTURES	PP	100	40	76	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	58	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	66	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	62	P

03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	81	P C	13. DATA STRUCTURES	PP	100	40	75	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	79	P C	14. COMPUTER GRAPHICS	PP	100	40	74	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	62	P C	15. COMPUTER ORGANIZATION	PP	100	40	62	P
06. PROGRAMMING LABORATORY	TW	25	10	21	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	41	P
07. PROGRAMMING LABORATORY	PR	50	20	37	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	43	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	21	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	39	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	25	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	40	P
10. SOFT SKILLS	TW	50	20	39	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	36	P
						21. DATA STRUCTURES LABORATORY	PR	50	20	38	P

GRAND TOTAL = 1075/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

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S8054257   KANTROD SUKESHNI ANIL   SUNITA   , 71045468G   , S8054257 ,   PICT   , S8054257

01. DISCRETE STRUCTURES	PP	100	40	53	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	40	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	40	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	40	P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	60	P C	13. DATA STRUCTURES	PP	100	40	43	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	58	P C	14. COMPUTER GRAPHICS	PP	100	40	59	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	40	P C	15. COMPUTER ORGANIZATION	PP	100	40	61	P
06. PROGRAMMING LABORATORY	TW	25	10	19	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	39	P
07. PROGRAMMING LABORATORY	PR	50	20	27	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	34	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	22	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	34	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	35	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	20	P
10. SOFT SKILLS	TW	50	20	37	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	32	P
						21. DATA STRUCTURES LABORATORY	PR	50	20	33	P

GRAND TOTAL = 826/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

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S8054258   KAPLE MADHURA MUKESH   VANITA   , 71045469E   , S8054258 ,   PICT   , S8054258

01. DISCRETE STRUCTURES	PP	100	40	71	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	83	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	45	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	53	P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	75	P C	13. DATA STRUCTURES	PP	100	40	68	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	78	P C	14. COMPUTER GRAPHICS	PP	100	40	55	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	54	P C	15. COMPUTER ORGANIZATION	PP	100	40	66	P
06. PROGRAMMING LABORATORY	TW	25	10	21	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	39	P
07. PROGRAMMING LABORATORY	PR	50	20	35	P	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	36	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	20	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	36	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	40	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	39	P
10. SOFT SKILLS	TW	50	20	40	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	42	P
						21. DATA STRUCTURES LABORATORY	PR	50	20	30	P

GRAND TOTAL = 1026/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

. . . .
UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(COMPUTER) EXAMINATION MAY 2011

   DATE : 18 AUG. 2011   CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.   PAGE NO. 20 (384)
. . . .

 NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.

        OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

. . . .

S8054259   KARAD NEHA DASHRATH   USHA   , 71045471G   , S8054259 ,   PICT   , S8054259

01. DISCRETE STRUCTURES	PP	100	40	66	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	22	F
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	46	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	40	P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	60	P C	13. DATA STRUCTURES	PP	100	40	42	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	57	P C	14. COMPUTER GRAPHICS	PP	100	40	49	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	40	P C	15. COMPUTER ORGANIZATION	PP	100	40	50	P
06. PROGRAMMING LABORATORY	TW	25	10	19	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	33	P
07. PROGRAMMING LABORATORY	PR	50	20	38	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	33	P

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08. DIGITAL ELECTRONICS LABORATORY    TW    25    10    20  P C    18. MICROPROCESSORS & INTERFACING LABTW    50    20    29  P
09. DIGITAL ELECTRONICS LABORATORY    PR    50    20    21  P C    19. MICROPROCESSORS & INTERFACING LABPR    50    20    21  P
10. SOFT SKILLS                       TW    50    20    38  P C    20. DATA STRUCTURES LABORATORY        TW    50    20    26  P
                                                                   21. DATA STRUCTURES LABORATORY        PR    50    20    09  F
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GRAND TOTAL = 759/1500, RESULT: FAILS A.T.K.T.   RESULT RESERVED FOR BKLG

ORDN. 1 MARKS :

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S8054260   KARANDE RAHUL MADAN   SUNITA   , 71045472E   , S8054260 ,   PICT   , S8054260

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01. DISCRETE STRUCTURES               PP   100    40    49  P C    11. ENGINEERING MATHEMATICS III       PP   100    40    40  P
02. PROGRAMMING & PROBLEM SOLVING     PP   100    40    40  P C    12. MICROPROC. & INTERFACING TECHNIQ.PP   100    40    27  F
03. DIGIT. ELECTRONICS & LOGIC DESIGNPP   100    40    54  P C    13. DATA STRUCTURES                   PP   100    40    30  F
04. DATA STRUCTURES AND ALGORITHMS    PP   100    40    44  P C    14. COMPUTER GRAPHICS                 PP   100    40    60  P
05. HUMANITIES AND SOCIAL SCIENCE     PP   100    40    40  P C    15. COMPUTER ORGANIZATION             PP   100    40    41  P
06. PROGRAMMING LABORATORY            TW    25    10    15  P C    16. O. O. PROG. & COMP. GRAPH. LAB    TW    50    20    24  P
07. PROGRAMMING LABORATORY            PR    50    20    AA  F      17. O. O. PROG. & COMP. GRAPH. LAB    PR    50    20    AA  F
08. DIGITAL ELECTRONICS LABORATORY    TW    25    10    16  P C    18. MICROPROCESSORS & INTERFACING LABTW    50    20    30  P
09. DIGITAL ELECTRONICS LABORATORY    PR    50    20    24  P C    19. MICROPROCESSORS & INTERFACING LABPR    50    20    05  F
10. SOFT SKILLS                       TW    50    20    35  P C    20. DATA STRUCTURES LABORATORY        TW    50    20    30  P
                                                                   21. DATA STRUCTURES LABORATORY        PR    50    20    06  F
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GRAND TOTAL = 610/1500, RESULT: FAILS

ORDN. 1 MARKS :

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S8054261   KATARIYA RAHUL PADAMKUMAR   BHARATI   , 71045476H   , S8054261 ,   PICT   , S8054261

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01. DISCRETE STRUCTURES               PP   100    40    63  P C    11. ENGINEERING MATHEMATICS III       PP   100    40    41  P
02. PROGRAMMING & PROBLEM SOLVING     PP   100    40    40  P C    12. MICROPROC. & INTERFACING TECHNIQ.PP   100    40    40  P
03. DIGIT. ELECTRONICS & LOGIC DESIGNPP   100    40    62  P C    13. DATA STRUCTURES                   PP   100    40    57  P
04. DATA STRUCTURES AND ALGORITHMS    PP   100    40    61  P C    14. COMPUTER GRAPHICS                 PP   100    40    62  P
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05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	47	P C		15. COMPUTER ORGANIZATION	PP	100	40	61	P
06. PROGRAMMING LABORATORY	TW	25	10	21	P C		16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	41	P
07. PROGRAMMING LABORATORY	PR	50	20	35	P C		17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	42	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	21	P C		18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	41	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	43	P C		19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	44	P
10. SOFT SKILLS	TW	50	20	39	P C		20. DATA STRUCTURES LABORATORY	TW	50	20	40	P
							21. DATA STRUCTURES LABORATORY	PR	50	20	38	P

GRAND TOTAL = 939/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

. . . . UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(COMPUTER) EXAMINATION MAY 2011 . . . .

    DATE : 18 AUG. 2011   CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.   PAGE NO. 21 (385)

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  NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.

        OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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  S8054262   KAWADE SARANG JAIRAJ   KARUNA   , 71045477F   , S8054262 ,   PICT   , S8054262

01. DISCRETE STRUCTURES	PP	100	40	67	P C		11. ENGINEERING MATHEMATICS III	PP	100	40	74	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	53	P C		12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	49	P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	75	P C		13. DATA STRUCTURES	PP	100	40	63	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	68	P C		14. COMPUTER GRAPHICS	PP	100	40	76	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	57	P C		15. COMPUTER ORGANIZATION	PP	100	40	73	P
06. PROGRAMMING LABORATORY	TW	25	10	19	P C		16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	43	P
07. PROGRAMMING LABORATORY	PR	50	20	28	P C		17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	41	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	20	P C		18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	42	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	33	P C		19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	40	P
10. SOFT SKILLS	TW	50	20	43	P C		20. DATA STRUCTURES LABORATORY	TW	50	20	39	P
							21. DATA STRUCTURES LABORATORY	PR	50	20	35	P

GRAND TOTAL = 1038/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

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S8054263   KAZI SABIYA FARUK   SHAHEDA   , 71132432J   , S8054263 ,   PICT   , S8054263

01. DISCRETE STRUCTURES	PP	100	40	65	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	66 P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	49	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	48 P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	71	P C	13. DATA STRUCTURES	PP	100	40	66 P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	79	P C	14. COMPUTER GRAPHICS	PP	100	40	64 P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	54	P C	15. COMPUTER ORGANIZATION	PP	100	40	65 P
06. PROGRAMMING LABORATORY	TW	25	10	20	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	42 P
07. PROGRAMMING LABORATORY	PR	50	20	46	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	42 P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	21	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	43 P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	39	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	42 P
10. SOFT SKILLS	TW	50	20	34	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	40 P
						21. DATA STRUCTURES LABORATORY	PR	50	20	41 P

GRAND TOTAL = 1037/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

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S8054265   KHADE PRERANA BALASAHEB   TARABAI   , 71045481D   , S8054265 ,   PICT   , S8054265

01. DISCRETE STRUCTURES	PP	100	40	44	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	22 F
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	40	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	31 F
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	48	P C	13. DATA STRUCTURES	PP	100	40	48 P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	64	P C	14. COMPUTER GRAPHICS	PP	100	40	54 P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	42	P C	15. COMPUTER ORGANIZATION	PP	100	40	50 P
06. PROGRAMMING LABORATORY	TW	25	10	21	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	40 P
07. PROGRAMMING LABORATORY	PR	50	20	30	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	30 P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	20	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	40 P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	34	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	35 P

| 10. SOFT SKILLS | TW | 50 | 20 | 39 | P C | 20. DATA STRUCTURES LABORATORY | TW | 50 | 20 | 38 | P |
| | | | | | | 21. DATA STRUCTURES LABORATORY | PR | 50 | 20 | 33 | P |

GRAND TOTAL = 803/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

. . . . UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(COMPUTER) EXAMINATION MAY 2011 . . . .

DATE : 18 AUG. 2011   CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.   PAGE NO. 22 (386)

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.

        OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S8054266   KHARDE ABHIJIT DIGAMBER   JYOTI   , 71045483L   , S8054266 ,   PICT   , S8054266

01. DISCRETE STRUCTURES	PP	100	40	67	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	68	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	56	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	47	P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	70	P C	13. DATA STRUCTURES	PP	100	40	60	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	71	P C	14. COMPUTER GRAPHICS	PP	100	40	66	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	47	P C	15. COMPUTER ORGANIZATION	PP	100	40	60	P
06. PROGRAMMING LABORATORY	TW	25	10	22	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	38	P
07. PROGRAMMING LABORATORY	PR	50	20	45	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	35	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	21	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	36	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	23	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	41	P
10. SOFT SKILLS	TW	50	20	39	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	38	P
						21. DATA STRUCTURES LABORATORY	PR	50	20	40	P

GRAND TOTAL = 990/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

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S8054267   KHEDKAR PRITAM KISHOR   SUREKHA   , 71045484J   , S8054267 ,   PICT   , S8054267

01. DISCRETE STRUCTURES	PP	100	40	55	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	65	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	47	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	40	P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	73	P C	13. DATA STRUCTURES	PP	100	40	56	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	68	P C	14. COMPUTER GRAPHICS	PP	100	40	63	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	40	P C	15. COMPUTER ORGANIZATION	PP	100	40	40	P
06. PROGRAMMING LABORATORY	TW	25	10	18	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	25	P
07. PROGRAMMING LABORATORY	PR	50	20	41	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	37	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	17	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	25	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	22	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	05	F
10. SOFT SKILLS	TW	50	20	38	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	32	P
						21. DATA STRUCTURES LABORATORY	PR	50	20	36	P

GRAND TOTAL = 843/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

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S8054268   KSHATRIYA PURVA HEMANT   RAJESHRI   , 71045487C   , S8054268 ,   PICT   , S8054268

01. DISCRETE STRUCTURES	PP	100	40	50	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	72	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	44	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	48	P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	65	P C	13. DATA STRUCTURES	PP	100	40	56	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	66	P C	14. COMPUTER GRAPHICS	PP	100	40	66	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	64	P C	15. COMPUTER ORGANIZATION	PP	100	40	60	P
06. PROGRAMMING LABORATORY	TW	25	10	21	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	34	P
07. PROGRAMMING LABORATORY	PR	50	20	40	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	36	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	21	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	35	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	27	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	41	P
10. SOFT SKILLS	TW	50	20	40	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	31	P
						21. DATA STRUCTURES LABORATORY	PR	50	20	38	P

GRAND TOTAL = 955/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

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                    UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(COMPUTER) EXAMINATION MAY 2011
      DATE : 18 AUG. 2011           CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.         PAGE NO.  23  (  387)
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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S8054269   KSHITIZ DANGE   REKHA RANI   , 71045488M   , S8054269 ,   PICT   , S8054269

01. DISCRETE STRUCTURES	PP	100	40	65	P C		11. ENGINEERING MATHEMATICS III	PP	100	40	59	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	51	P C		12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	45	P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	65	P C		13. DATA STRUCTURES	PP	100	40	51	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	67	P C		14. COMPUTER GRAPHICS	PP	100	40	70	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	44	P C		15. COMPUTER ORGANIZATION	PP	100	40	64	P
06. PROGRAMMING LABORATORY	TW	25	10	20	P C		16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	38	P
07. PROGRAMMING LABORATORY	PR	50	20	42	P C		17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	42	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	16	P C		18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	40	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	45	P C		19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	42	P
10. SOFT SKILLS	TW	50	20	39	P C		20. DATA STRUCTURES LABORATORY	TW	50	20	37	P
							21. DATA STRUCTURES LABORATORY	PR	50	20	40	P

GRAND TOTAL = 982/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

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S8054270   KULKARNI AKASH ABHAY   ASHWINI   , 71045489K   , S8054270 ,   PICT   , S8054270

01. DISCRETE STRUCTURES	PP	100	40	54	P C		11. ENGINEERING MATHEMATICS III	PP	100	40	84	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	64	P C		12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	61	P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	83	P C		13. DATA STRUCTURES	PP	100	40	58	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	63	P C		14. COMPUTER GRAPHICS	PP	100	40	69	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	53	P C		15. COMPUTER ORGANIZATION	PP	100	40	65	P

06. PROGRAMMING LABORATORY	TW	25	10	22	P C		16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	41	P
07. PROGRAMMING LABORATORY	PR	50	20	43	P C		17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	41	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	22	P C		18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	38	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	39	P C		19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	45	P
10. SOFT SKILLS	TW	50	20	39	P C		20. DATA STRUCTURES LABORATORY	TW	50	20	41	P
							21. DATA STRUCTURES LABORATORY	PR	50	20	44	P

GRAND TOTAL = 1069/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

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S8054271   KULKARNI CHINMAYI AVINASH   SHOBHA   , 71045493H   , S8054271 ,   PICT   , S8054271

01. DISCRETE STRUCTURES	PP	100	40	46	P C		11. ENGINEERING MATHEMATICS III	PP	100	40	40	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	48	P C		12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	40	P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	63	P C		13. DATA STRUCTURES	PP	100	40	43	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	43	P C		14. COMPUTER GRAPHICS	PP	100	40	55	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	45	P		15. COMPUTER ORGANIZATION	PP	100	40	46	P
06. PROGRAMMING LABORATORY	TW	25	10	18	P C		16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	38	P
07. PROGRAMMING LABORATORY	PR	50	20	39	P		17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	23	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	16	P C		18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	33	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	22	P C		19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	34	P
10. SOFT SKILLS	TW	50	20	40	P C		20. DATA STRUCTURES LABORATORY	TW	50	20	36	P
							21. DATA STRUCTURES LABORATORY	PR	50	20	38	P

GRAND TOTAL = 806/1500, RESULT: SECOND CLASS

ORDN. 1 MARKS :

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UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(COMPUTER) EXAMINATION MAY 2011

DATE : 18 AUG. 2011   CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.   PAGE NO. 24 (388)

. . . .

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S8054272   KULKARNI DNYANESH VIJAYRAO   MANGAL   , 71045494F   , S8054272 ,   PICT   , S8054272

01. DISCRETE STRUCTURES	PP	100	40	67	P C		11. ENGINEERING MATHEMATICS III	PP	100	40	63	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	56	P C		12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	40	P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	77	P C		13. DATA STRUCTURES	PP	100	40	63	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	61	P C		14. COMPUTER GRAPHICS	PP	100	40	74	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	57	P C		15. COMPUTER ORGANIZATION	PP	100	40	60	P
06. PROGRAMMING LABORATORY	TW	25	10	21	P C		16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	32	P
07. PROGRAMMING LABORATORY	PR	50	20	38	P C		17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	40	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	20	P C		18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	35	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	37	P C		19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	40	P
10. SOFT SKILLS	TW	50	20	40	P C		20. DATA STRUCTURES LABORATORY	TW	50	20	30	P
							21. DATA STRUCTURES LABORATORY	PR	50	20	39	P

GRAND TOTAL = 990/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

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S8054273   KULKARNI MANASI MILIND   JYOTI   , 71045496B   , S8054273 ,   PICT   , S8054273

01. DISCRETE STRUCTURES	PP	100	40	44	P C		11. ENGINEERING MATHEMATICS III	PP	100	40	40	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	40	P C		12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	40	P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	55	P C		13. DATA STRUCTURES	PP	100	40	40	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	40	P C		14. COMPUTER GRAPHICS	PP	100	40	57	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	49	P C		15. COMPUTER ORGANIZATION	PP	100	40	58	P
06. PROGRAMMING LABORATORY	TW	25	10	19	P C		16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	36	P
07. PROGRAMMING LABORATORY	PR	50	20	32	P		17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	32	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	20	P C		18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	35	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	35	P C		19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	32	P
10. SOFT SKILLS	TW	50	20	41	P C		20. DATA STRUCTURES LABORATORY	TW	50	20	35	P

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                                      21. DATA STRUCTURES LABORATORY        PR     50    20    10   F
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GRAND TOTAL = 790/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

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S8054274   KULKARNI PRATHAMESH DEEPAK   VIDYA   , 71132433G   , S8054274 ,   PICT   , S8054274

01. DISCRETE STRUCTURES	PP	100	40	65	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	62	P		
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	52	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	53	P		
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	68	P C	13. DATA STRUCTURES	PP	100	40	62	P		
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	65	P C	14. COMPUTER GRAPHICS	PP	100	40	74	P		
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	55	P C	15. COMPUTER ORGANIZATION	PP	100	40	58	P		
06. PROGRAMMING LABORATORY	TW	25	10	20	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	40	P		
07. PROGRAMMING LABORATORY	PR	50	20	23	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	42	P		
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	20	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	39	P		
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	31	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	38	P		
10. SOFT SKILLS	TW	50	20	40	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	32	P		
						21. DATA STRUCTURES LABORATORY	PR	50	20	35	P		

GRAND TOTAL = 974/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

. . . . UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(COMPUTER) EXAMINATION MAY 2011 . . . .

   DATE : 18 AUG. 2011   CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.   PAGE NO. 25 (389)

. . . .

 NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO.,   PREVIOUS SEAT NO., COLLEGE,   SEAT NO.

        OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S8054275   KULKARNI SAURABH RAVINDRA   SWATI   , 71045498J   , S8054275 ,   PICT   , S8054275

01. DISCRETE STRUCTURES	PP	100	40	48	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	73	P

02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	63	P C		12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	50	P	
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	69	P C		13. DATA STRUCTURES	PP	100	40	50	P	
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	57	P C		14. COMPUTER GRAPHICS	PP	100	40	75	P	
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	53	P C		15. COMPUTER ORGANIZATION	PP	100	40	55	P	
06. PROGRAMMING LABORATORY	TW	25	10	23	P C		16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	42	P	
07. PROGRAMMING LABORATORY	PR	50	20	44	P C		17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	43	P	
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	22	P C		18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	37	P	
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	35	P C		19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	44	P	
10. SOFT SKILLS	TW	50	20	43	P C		20. DATA STRUCTURES LABORATORY	TW	50	20	42	P	
							21. DATA STRUCTURES LABORATORY	PR	50	20	40	P	

GRAND TOTAL = 1008/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

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S8054276   KUNTALWAD GNYANESH GAJJARAM   SARASWATHI   , 71045501B   , S8054276 ,   PICT   , S8054276

01. DISCRETE STRUCTURES	PP	100	40	40	P C		11. ENGINEERING MATHEMATICS III	PP	100	40	40	P	
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	51	P C		12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	40	P	
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	48	P C		13. DATA STRUCTURES	PP	100	40	41	P	
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	40	P C		14. COMPUTER GRAPHICS	PP	100	40	57	P	
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	41	P C		15. COMPUTER ORGANIZATION	PP	100	40	40	P	
06. PROGRAMMING LABORATORY	TW	25	10	16	P C		16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	22	P	
07. PROGRAMMING LABORATORY	PR	50	20	20	P		17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	23	P	
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	14	P C		18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	25	P	
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	30	P		19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	08	F	
10. SOFT SKILLS	TW	50	20	38	P C		20. DATA STRUCTURES LABORATORY	TW	50	20	34	P	
							21. DATA STRUCTURES LABORATORY	PR	50	20	15	F	

GRAND TOTAL = 683/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

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S8054277   LALHMANGAIHA   NGURTHANKIMI   , 71045503J   , S8054277 ,   PICT   , S8054277

01. DISCRETE STRUCTURES	PP	100	40	40	P C		11. ENGINEERING MATHEMATICS III	PP	100	40	01	F
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	AA	F		12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	05	F
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	40	P C		13. DATA STRUCTURES	PP	100	40	06	F
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	AA	F		14. COMPUTER GRAPHICS	PP	100	40	23	F
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	45	P C		15. COMPUTER ORGANIZATION	PP	100	40	20	F
06. PROGRAMMING LABORATORY	TW	25	10	14	P C		16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	20	P
07. PROGRAMMING LABORATORY	PR	50	20	AA	F		17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	02	F
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	10	P C		18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	20	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	20	P C		19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	05	F
10. SOFT SKILLS	TW	50	20	24	P C		20. DATA STRUCTURES LABORATORY	TW	50	20	20	P
							21. DATA STRUCTURES LABORATORY	PR	50	20	AA	F

GRAND TOTAL = 315/1500, RESULT: FAILS

ORDN. 1 MARKS :

. . . .
UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(COMPUTER) EXAMINATION MAY 2011
     DATE : 18 AUG. 2011   CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.   PAGE NO. 26 (390)
. . . .
 NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.
         OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S8054278   LOKHANDE VINAYA VISHWANATHRAO   PRATIBHA   , 71045504G   , S8054278 ,   PICT   , S8054278

01. DISCRETE STRUCTURES	PP	100	40	46	P C		11. ENGINEERING MATHEMATICS III	PP	100	40	87	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	40	P C		12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	42	P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	55	P C		13. DATA STRUCTURES	PP	100	40	60	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	52	P C		14. COMPUTER GRAPHICS	PP	100	40	69	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	45	P C		15. COMPUTER ORGANIZATION	PP	100	40	51	P
06. PROGRAMMING LABORATORY	TW	25	10	18	P C		16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	38	P

07. PROGRAMMING LABORATORY	PR	50	20	33	P		17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	35	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	18	P C		18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	37	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	23	P C		19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	36	P
10. SOFT SKILLS	TW	50	20	35	P C		20. DATA STRUCTURES LABORATORY	TW	50	20	38	P
							21. DATA STRUCTURES LABORATORY	PR	50	20	25	P

GRAND TOTAL = 883/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

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S8054279   MAHARANWAR NAMITA ANILRAO   RENUKA   , 71045510M   , S8054279 , PICT   , S8054279

01. DISCRETE STRUCTURES	PP	100	40	44	P C		11. ENGINEERING MATHEMATICS III	PP	100	40	44	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	58	P C		12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	44	P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	40	P C		13. DATA STRUCTURES	PP	100	40	42	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	52	P C		14. COMPUTER GRAPHICS	PP	100	40	62	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	54	P C		15. COMPUTER ORGANIZATION	PP	100	40	48	P
06. PROGRAMMING LABORATORY	TW	25	10	19	P C		16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	36	P
07. PROGRAMMING LABORATORY	PR	50	20	34	P		17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	34	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	20	P C		18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	33	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	25	P C		19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	24	P
10. SOFT SKILLS	TW	50	20	41	P C		20. DATA STRUCTURES LABORATORY	TW	50	20	31	P
							21. DATA STRUCTURES LABORATORY	PR	50	20	32	P

GRAND TOTAL = 817+08/1500, RESULT: HIGHER SECOND CLASS [O.2]

ORDN. 1 MARKS :

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S8054280   MALANI MUKUND PARMANAND   SUDHA   , 71045513F   , S8054280 , PICT   , S8054280

01. DISCRETE STRUCTURES	PP	100	40	68	P C		11. ENGINEERING MATHEMATICS III	PP	100	40	51	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	40	P C		12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	51	P

03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	66	P C		13. DATA STRUCTURES		PP	100	40	54	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	42	P C		14. COMPUTER GRAPHICS		PP	100	40	72	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	49	P C		15. COMPUTER ORGANIZATION		PP	100	40	57	P
06. PROGRAMMING LABORATORY	TW	25	10	18	P C		16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	42	P
07. PROGRAMMING LABORATORY	PR	50	20	37	P		17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	35	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	16	P C		18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	36	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	24	P C		19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	25	P
10. SOFT SKILLS	TW	50	20	33	P C		20. DATA STRUCTURES LABORATORY	TW	50	20	33	P
							21. DATA STRUCTURES LABORATORY	PR	50	20	31	P

GRAND TOTAL = 880/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

. . . .
UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(COMPUTER) EXAMINATION MAY 2011

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.

         OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S8054281   MALHOTRA VIJAY GULSHARAN   ANJU   , 71045515B   , S8054281 , PICT   , S8054281

01. DISCRETE STRUCTURES	PP	100	40	74	P C		11. ENGINEERING MATHEMATICS III		PP	100	40	71	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	44	P C		12. MICROPROC. & INTERFACING TECHNIQ.		PP	100	40	58	P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	71	P C		13. DATA STRUCTURES		PP	100	40	54	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	60	P C		14. COMPUTER GRAPHICS		PP	100	40	70	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	52	P C		15. COMPUTER ORGANIZATION		PP	100	40	50	P
06. PROGRAMMING LABORATORY	TW	25	10	21	P C		16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	37	P
07. PROGRAMMING LABORATORY	PR	50	20	37	P C		17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	36	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	21	P C		18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	34	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	30	P C		19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	36	P
10. SOFT SKILLS	TW	50	20	40	P C		20. DATA STRUCTURES LABORATORY	TW	50	20	31	P
							21. DATA STRUCTURES LABORATORY	PR	50	20	38	P

GRAND TOTAL = 965/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

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S8054282   MANDLIWALA NAFISA OANALI   RASHIDA   , 71045516L   , S8054282 ,   PICT   , S8054282

01. DISCRETE STRUCTURES	PP	100	40	43	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	69	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	40	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	60	P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	58	P C	13. DATA STRUCTURES	PP	100	40	55	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	59	P C	14. COMPUTER GRAPHICS	PP	100	40	72	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	47	P C	15. COMPUTER ORGANIZATION	PP	100	40	60	P
06. PROGRAMMING LABORATORY	TW	25	10	21	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	43	P
07. PROGRAMMING LABORATORY	PR	50	20	43	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	38	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	21	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	38	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	27	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	42	P
10. SOFT SKILLS	TW	50	20	43	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	40	P
						21. DATA STRUCTURES LABORATORY	PR	50	20	36	P

GRAND TOTAL = 955/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

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S8054283   MANE POOJA VASANT   VARSHA   , 71132434E   , S8054283 ,   PICT   , S8054283

01. DISCRETE STRUCTURES	PP	100	40	50	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	40	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	58	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	57	P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	41	P C	13. DATA STRUCTURES	PP	100	40	56	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	48	P C	14. COMPUTER GRAPHICS	PP	100	40	61	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	46	P C	15. COMPUTER ORGANIZATION	PP	100	40	54	P
06. PROGRAMMING LABORATORY	TW	25	10	19	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	43	P
07. PROGRAMMING LABORATORY	PR	50	20	34	P	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	36	P

cols05

08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	19	P C		18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	35	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	23	P C		19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	37	P
10. SOFT SKILLS	TW	50	20	37	P C		20. DATA STRUCTURES LABORATORY	TW	50	20	37	P
							21. DATA STRUCTURES LABORATORY	PR	50	20	31	P

GRAND TOTAL = 862/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

. . . . UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(COMPUTER) EXAMINATION MAY 2011 . . . .

DATE : 18 AUG. 2011   CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.   PAGE NO. 28 (392)

. . . .

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

        OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S8054284   MANVI BHATIA   ANJU   , 71045520J   , S8054284 ,   PICT   , S8054284

01. DISCRETE STRUCTURES	PP	100	40	69	P C		11. ENGINEERING MATHEMATICS III	PP	100	40	80	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	50	P C		12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	56	P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	82	P C		13. DATA STRUCTURES	PP	100	40	57	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	67	P C		14. COMPUTER GRAPHICS	PP	100	40	77	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	52	P C		15. COMPUTER ORGANIZATION	PP	100	40	66	P
06. PROGRAMMING LABORATORY	TW	25	10	22	P C		16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	41	P
07. PROGRAMMING LABORATORY	PR	50	20	40	P C		17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	44	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	20	P C		18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	36	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	34	P C		19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	40	P
10. SOFT SKILLS	TW	50	20	44	P C		20. DATA STRUCTURES LABORATORY	TW	50	20	41	P
							21. DATA STRUCTURES LABORATORY	PR	50	20	40	P

GRAND TOTAL = 1058/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

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S8054285   MASKE SHRUTI HIRALAL   SANGEETA   , 71045523C   , S8054285 ,   PICT   , S8054285

01. DISCRETE STRUCTURES	PP	100	40	40	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	52	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	40	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	31*	P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	58	P C	13. DATA STRUCTURES	PP	100	40	43	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	41	P C	14. COMPUTER GRAPHICS	PP	100	40	47	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	40	P C	15. COMPUTER ORGANIZATION	PP	100	40	40	P
06. PROGRAMMING LABORATORY	TW	25	10	18	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	38	P
07. PROGRAMMING LABORATORY	PR	50	20	25	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	33	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	18	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	34	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	27	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	30	P
10. SOFT SKILLS	TW	50	20	39	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	31	P
						21. DATA STRUCTURES LABORATORY	PR	50	20	26	P

GRAND TOTAL = 751/1500, RESULT: SECOND CLASS   * [O.4]

ORDN. 1 MARKS :

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S8054287   MUNESHWAR PRANALI PRAKASH   PRAMILA   , 71045533L   , S8054287 ,   PICT   , S8054287

01. DISCRETE STRUCTURES	PP	100	40	50	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	57	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	47	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	40	P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	53	P C	13. DATA STRUCTURES	PP	100	40	56	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	40	P C	14. COMPUTER GRAPHICS	PP	100	40	56	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	46	P C	15. COMPUTER ORGANIZATION	PP	100	40	40	P
06. PROGRAMMING LABORATORY	TW	25	10	18	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	38	P
07. PROGRAMMING LABORATORY	PR	50	20	37	P	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	30	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	18	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	30	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	27	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	10	F
10. SOFT SKILLS	TW	50	20	39	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	29	P
						21. DATA STRUCTURES LABORATORY	PR	50	20	20	P

GRAND TOTAL = 781/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

. . . . UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(COMPUTER) EXAMINATION MAY 2011 . . . .

DATE : 18 AUG. 2011   CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.   PAGE NO. 29 (393)

. . . .

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

        OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S8054288   MUSALE SAGAR RAJENDRA   KALPANA   , 71045534J   , S8054288 ,   PICT   , S8054288

01. DISCRETE STRUCTURES	PP	100	40	57	P C		11. ENGINEERING MATHEMATICS III	PP	100	40	51	P	
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	50	P C		12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	40	P	
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	64	P C		13. DATA STRUCTURES	PP	100	40	48	P	
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	50	P C		14. COMPUTER GRAPHICS	PP	100	40	49	P	
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	43	P C		15. COMPUTER ORGANIZATION	PP	100	40	55	P	
06. PROGRAMMING LABORATORY	TW	25	10	19	P C		16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	44	P	
07. PROGRAMMING LABORATORY	PR	50	20	42	P C		17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	36	P	
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	22	P C		18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	41	P	
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	30	P C		19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	40	P	
10. SOFT SKILLS	TW	50	20	41	P C		20. DATA STRUCTURES LABORATORY	TW	50	20	35	P	
							21. DATA STRUCTURES LABORATORY	PR	50	20	41	P	

GRAND TOTAL = 898+02/1500, RESULT: FIRST CLASS [O.2]

ORDN. 1 MARKS :

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S8054289   MUTALIK MADHUMITRA MADHAV   MAITREYEE   , 71045535G   , S8054289 ,   PICT   , S8054289

01. DISCRETE STRUCTURES	PP	100	40	74	P C		11. ENGINEERING MATHEMATICS III	PP	100	40	72	P	
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	69	P C		12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	53	P	
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	76	P C		13. DATA STRUCTURES	PP	100	40	77	P	

04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	61	P C	14. COMPUTER GRAPHICS	PP	100	40	66	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	57	P C	15. COMPUTER ORGANIZATION	PP	100	40	62	P
06. PROGRAMMING LABORATORY	TW	25	10	20	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	34	P
07. PROGRAMMING LABORATORY	PR	50	20	44	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	39	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	24	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	38	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	40	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	37	P
10. SOFT SKILLS	TW	50	20	37	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	35	P
						21. DATA STRUCTURES LABORATORY	PR	50	20	40	P

GRAND TOTAL = 1055/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

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S8054290   MUTHA SURABHI SHASHIKUMAR   UJJWALA   , 71045536E   , S8054290 ,   PICT   , S8054290

01. DISCRETE STRUCTURES	PP	100	40	68	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	79	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	62	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	50	P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	83	P C	13. DATA STRUCTURES	PP	100	40	71	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	59	P C	14. COMPUTER GRAPHICS	PP	100	40	68	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	53	P C	15. COMPUTER ORGANIZATION	PP	100	40	62	P
06. PROGRAMMING LABORATORY	TW	25	10	24	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	44	P
07. PROGRAMMING LABORATORY	PR	50	20	45	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	42	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	24	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	42	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	42	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	46	P
10. SOFT SKILLS	TW	50	20	38	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	46	P
						21. DATA STRUCTURES LABORATORY	PR	50	20	39	P

GRAND TOTAL = 1087/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

. . . . UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(COMPUTER) EXAMINATION MAY 2011 . . . .

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S8054291   NAIR AJAY RADHAKRISHNAN   GEETHA   , 71045539K   , S8054291 ,   PICT   , S8054291

01. DISCRETE STRUCTURES	PP	100	40	63	P C		11. ENGINEERING MATHEMATICS III	PP	100	40	50	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	55	P C		12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	51	P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	55	P C		13. DATA STRUCTURES	PP	100	40	48	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	51	P C		14. COMPUTER GRAPHICS	PP	100	40	72	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	52	P C		15. COMPUTER ORGANIZATION	PP	100	40	48	P
06. PROGRAMMING LABORATORY	TW	25	10	16	P C		16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	30	P
07. PROGRAMMING LABORATORY	PR	50	20	40	P C		17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	37	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	19	P C		18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	27	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	35	P C		19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	39	P
10. SOFT SKILLS	TW	50	20	31	P C		20. DATA STRUCTURES LABORATORY	TW	50	20	29	P
							21. DATA STRUCTURES LABORATORY	PR	50	20	33	P

GRAND TOTAL = 881/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

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S8054292   NAJAN KAUSTUBH GORAKSHANATH   MEERA   , 71045540C   , S8054292 ,   PICT   , S8054292

01. DISCRETE STRUCTURES	PP	100	40	42	P C		11. ENGINEERING MATHEMATICS III	PP	100	40	54	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	40	P C		12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	21	F
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	47	P C		13. DATA STRUCTURES	PP	100	40	40	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	48	P		14. COMPUTER GRAPHICS	PP	100	40	41	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	31	F		15. COMPUTER ORGANIZATION	PP	100	40	27	F
06. PROGRAMMING LABORATORY	TW	25	10	17	P C		16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	41	P
07. PROGRAMMING LABORATORY	PR	50	20	30	P C		17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	30	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	18	P C		18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	40	P

09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	20	P C		19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	36	P
10. SOFT SKILLS	TW	50	20	34	P C		20. DATA STRUCTURES LABORATORY	TW	50	20	35	P
							21. DATA STRUCTURES LABORATORY	PR	50	20	32	P

GRAND TOTAL = 724/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

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S8054293   NANWANI SHRUTI RAMESH   KAMLESH   , 71045541M   , S8054293 ,   PICT   , S8054293

01. DISCRETE STRUCTURES	PP	100	40	71	P C		11. ENGINEERING MATHEMATICS III	PP	100	40	61	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	67	P C		12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	46	P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	84	P C		13. DATA STRUCTURES	PP	100	40	75	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	56	P C		14. COMPUTER GRAPHICS	PP	100	40	67	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	40	P C		15. COMPUTER ORGANIZATION	PP	100	40	57	P
06. PROGRAMMING LABORATORY	TW	25	10	20	P C		16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	33	P
07. PROGRAMMING LABORATORY	PR	50	20	41	P C		17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	42	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	22	P C		18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	38	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	40	P C		19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	44	P
10. SOFT SKILLS	TW	50	20	41	P C		20. DATA STRUCTURES LABORATORY	TW	50	20	33	P
							21. DATA STRUCTURES LABORATORY	PR	50	20	33	P

GRAND TOTAL = 1011/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

. . . .
                    UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(COMPUTER) EXAMINATION MAY 2011
     DATE : 18 AUG. 2011   CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.   PAGE NO. 31 (395)
. . . .
  NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.
        OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
. . . .

S8054294   NARENDRA PAL SINGH   KADAM   , 71045542K   , S8054294 ,   PICT   , S8054294

01. DISCRETE STRUCTURES	PP	100	40	53	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	72	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	48	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	52	P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	66	P C	13. DATA STRUCTURES	PP	100	40	69	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	51	P C	14. COMPUTER GRAPHICS	PP	100	40	68	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	53	P C	15. COMPUTER ORGANIZATION	PP	100	40	58	P
06. PROGRAMMING LABORATORY	TW	25	10	19	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	46	P
07. PROGRAMMING LABORATORY	PR	50	20	30	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	27	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	20	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	45	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	40	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	39	P
10. SOFT SKILLS	TW	50	20	35	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	38	P
						21. DATA STRUCTURES LABORATORY	PR	50	20	33	P

GRAND TOTAL = 962/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

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S8054295   NEHA RANA   SUDHA   , 71045543H   , S8054295 ,   PICT   , S8054295

01. DISCRETE STRUCTURES	PP	100	40	72	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	89	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	64	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	63	P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	80	P C	13. DATA STRUCTURES	PP	100	40	66	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	58	P C	14. COMPUTER GRAPHICS	PP	100	40	74	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	46	P C	15. COMPUTER ORGANIZATION	PP	100	40	46	P
06. PROGRAMMING LABORATORY	TW	25	10	22	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	35	P
07. PROGRAMMING LABORATORY	PR	50	20	42	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	37	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	22	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	36	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	30	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	40	P
10. SOFT SKILLS	TW	50	20	40	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	38	P
						21. DATA STRUCTURES LABORATORY	PR	50	20	41	P

GRAND TOTAL = 1041/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

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S8054296   NEMADE AJAY VILAS   MANJUSHA   , 71045544F   , S8054296 ,   PICT   , S8054296

01. DISCRETE STRUCTURES	PP	100	40	58	P C		11. ENGINEERING MATHEMATICS III	PP	100	40	73	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	55	P C		12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	49	P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	59	P C		13. DATA STRUCTURES	PP	100	40	67	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	51	P C		14. COMPUTER GRAPHICS	PP	100	40	67	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	50	P C		15. COMPUTER ORGANIZATION	PP	100	40	56	P
06. PROGRAMMING LABORATORY	TW	25	10	20	P C		16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	38	P
07. PROGRAMMING LABORATORY	PR	50	20	38	P C		17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	38	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	20	P C		18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	35	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	32	P C		19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	31	P
10. SOFT SKILLS	TW	50	20	38	P C		20. DATA STRUCTURES LABORATORY	TW	50	20	33	P
							21. DATA STRUCTURES LABORATORY	PR	50	20	35	P

GRAND TOTAL = 943/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

. . . . UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(COMPUTER) EXAMINATION MAY 2011 . . . .
     DATE : 18 AUG. 2011   CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.   PAGE NO. 32 (396)
. . . .

  NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.

        OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S8054297   NERKAR NIKITA SUNIL   AMITA   , 71045545D   , S8054297 ,   PICT   , S8054297

01. DISCRETE STRUCTURES	PP	100	40	74	P C		11. ENGINEERING MATHEMATICS III	PP	100	40	78	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	54	P C		12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	53	P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	74	P C		13. DATA STRUCTURES	PP	100	40	73	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	56	P C		14. COMPUTER GRAPHICS	PP	100	40	75	P

05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	44	P C		15. COMPUTER ORGANIZATION	PP	100	40	54	P
06. PROGRAMMING LABORATORY	TW	25	10	23	P C		16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	46	P
07. PROGRAMMING LABORATORY	PR	50	20	38	P C		17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	43	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	24	P C		18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	41	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	42	P C		19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	44	P
10. SOFT SKILLS	TW	50	20	46	P C		20. DATA STRUCTURES LABORATORY	TW	50	20	45	P
							21. DATA STRUCTURES LABORATORY	PR	50	20	42	P

GRAND TOTAL = 1069/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

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S8054298   NITYA RAJ   SARASWATHY   , 71045550L   , S8054298 ,   PICT   , S8054298

01. DISCRETE STRUCTURES	PP	100	40	56	P C		11. ENGINEERING MATHEMATICS III	PP	100	40	45	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	54	P C		12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	48	P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	65	P C		13. DATA STRUCTURES	PP	100	40	41	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	51	P C		14. COMPUTER GRAPHICS	PP	100	40	72	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	43	P C		15. COMPUTER ORGANIZATION	PP	100	40	52	P
06. PROGRAMMING LABORATORY	TW	25	10	20	P C		16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	41	P
07. PROGRAMMING LABORATORY	PR	50	20	38	P C		17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	20$	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	19	P C		18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	44	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	25	P C		19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	36	P
10. SOFT SKILLS	TW	50	20	35	P C		20. DATA STRUCTURES LABORATORY	TW	50	20	38	P
							21. DATA STRUCTURES LABORATORY	PR	50	20	22	P

GRAND TOTAL = 865/1500, RESULT: HIGHER SECOND CLASS   [$ O.1]

ORDN. 1 MARKS : (17)2,

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S8054299   PANDYA PARTH VIPUL   ASHA   , 71045553E   , S8054299 ,   PICT   , S8054299

01. DISCRETE STRUCTURES	PP	100	40	51	P C		11. ENGINEERING MATHEMATICS III	PP	100	40	40	P

02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	40	P C		12. MICROPROC. & INTERFACING TECHNIQ.PP	100	40	40	P
03. DIGIT. ELECTRONICS & LOGIC DESIGNPP		100	40	40	P C		13. DATA STRUCTURES	PP	100	40	40	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	46	P C		14. COMPUTER GRAPHICS	PP	100	40	58	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	49	P C		15. COMPUTER ORGANIZATION	PP	100	40	50	P
06. PROGRAMMING LABORATORY	TW	25	10	19	P C		16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	33	P
07. PROGRAMMING LABORATORY	PR	50	20	22	P C		17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	20	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	16	P C		18. MICROPROCESSORS & INTERFACING LABTW	50	20	27	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	21	P C		19. MICROPROCESSORS & INTERFACING LABPR	50	20	05	F
10. SOFT SKILLS	TW	50	20	34	P C		20. DATA STRUCTURES LABORATORY	TW	50	20	33	P
							21. DATA STRUCTURES LABORATORY	PR	50	20	06	F

GRAND TOTAL = 690/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

. . . .
UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(COMPUTER) EXAMINATION MAY 2011
. . . .

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.

        OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S8054300   PATE MAYUR SATISH   MADHURI   , 71132435C   , S8054300 , PICT   , S8054300

01. DISCRETE STRUCTURES	PP	100	40	46	P C		11. ENGINEERING MATHEMATICS III	PP	100	40	48	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	53	P C		12. MICROPROC. & INTERFACING TECHNIQ.PP	100	40	55	P
03. DIGIT. ELECTRONICS & LOGIC DESIGNPP		100	40	66	P C		13. DATA STRUCTURES	PP	100	40	84	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	57	P C		14. COMPUTER GRAPHICS	PP	100	40	58	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	57	P C		15. COMPUTER ORGANIZATION	PP	100	40	52	P
06. PROGRAMMING LABORATORY	TW	25	10	21	P C		16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	41	P
07. PROGRAMMING LABORATORY	PR	50	20	38	P C		17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	41	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	20	P C		18. MICROPROCESSORS & INTERFACING LABTW	50	20	44	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	20	P C		19. MICROPROCESSORS & INTERFACING LABPR	50	20	39	P

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                                                 cols05
10. SOFT SKILLS                    TW   50   20   40  P C    20. DATA STRUCTURES LABORATORY        TW   50   20   37  P
                                                            21. DATA STRUCTURES LABORATORY        PR   50   20   40  P

GRAND TOTAL =  957/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :


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  S8054301    PATHAK AVINASH NARAYAN               NALINI             , 71132436M    , S8054301 ,   PICT     , S8054301

  01. DISCRETE STRUCTURES            PP  100   40   72  P C    11. ENGINEERING MATHEMATICS III      PP  100   40   58  P
  02. PROGRAMMING & PROBLEM SOLVING  PP  100   40   55  P C    12. MICROPROC. & INTERFACING TECHNIQ.PP  100   40   59  P
  03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100   40   72  P C    13. DATA STRUCTURES                  PP  100   40   70  P
  04. DATA STRUCTURES AND ALGORITHMS PP  100   40   60  P C    14. COMPUTER GRAPHICS                PP  100   40   76  P
  05. HUMANITIES AND SOCIAL SCIENCE  PP  100   40   50  P C    15. COMPUTER ORGANIZATION            PP  100   40   60  P
  06. PROGRAMMING LABORATORY         TW   25   10   21  P C    16. O. O. PROG. & COMP. GRAPH. LAB   TW   50   20   44  P
  07. PROGRAMMING LABORATORY         PR   50   20   43  P C    17. O. O. PROG. & COMP. GRAPH. LAB   PR   50   20   41  P
  08. DIGITAL ELECTRONICS LABORATORY TW   25   10   21  P C    18. MICROPROCESSORS & INTERFACING LABTW   50   20   38  P
  09. DIGITAL ELECTRONICS LABORATORY PR   50   20   28  P C    19. MICROPROCESSORS & INTERFACING LABPR   50   20   34  P
  10. SOFT SKILLS                    TW   50   20   39  P C    20. DATA STRUCTURES LABORATORY        TW   50   20   40  P
                                                              21. DATA STRUCTURES LABORATORY        PR   50   20   22  P

GRAND TOTAL = 1003/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :


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  S8054302    PATHAK VISHVAJIT HARISHCHANDRA        PRATIBHA           , 71045561F    , S8054302 ,   PICT     , S8054302

  01. DISCRETE STRUCTURES            PP  100   40   72  P C    11. ENGINEERING MATHEMATICS III      PP  100   40   55  P
  02. PROGRAMMING & PROBLEM SOLVING  PP  100   40   53  P C    12. MICROPROC. & INTERFACING TECHNIQ.PP  100   40   52  P
  03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100   40   55  P C    13. DATA STRUCTURES                  PP  100   40   44  P
  04. DATA STRUCTURES AND ALGORITHMS PP  100   40   56  P C    14. COMPUTER GRAPHICS                PP  100   40   59  P
  05. HUMANITIES AND SOCIAL SCIENCE  PP  100   40   41  P C    15. COMPUTER ORGANIZATION            PP  100   40   47  P
  06. PROGRAMMING LABORATORY         TW   25   10   19  P C    16. O. O. PROG. & COMP. GRAPH. LAB   TW   50   20   35  P
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07. PROGRAMMING LABORATORY	PR	50	20	30	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	22	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	18	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	39	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	28	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	38	P
10. SOFT SKILLS	TW	50	20	34	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	37	P
						21. DATA STRUCTURES LABORATORY	PR	50	20	32	P

GRAND TOTAL = 866/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

. . . .
UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(COMPUTER) EXAMINATION MAY 2011

DATE : 18 AUG. 2011   CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.   PAGE NO. 34 (398)

. . . .

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.

       OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S8054303   PATIL AKSHAY JAYANT   POOJA   , 71045562D   , S8054303 , PICT   , S8054303

01. DISCRETE STRUCTURES	PP	100	40	73	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	59	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	54	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	53	P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	75	P C	13. DATA STRUCTURES	PP	100	40	63	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	59	P C	14. COMPUTER GRAPHICS	PP	100	40	59	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	46	P C	15. COMPUTER ORGANIZATION	PP	100	40	53	P
06. PROGRAMMING LABORATORY	TW	25	10	18	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	33	P
07. PROGRAMMING LABORATORY	PR	50	20	35	P	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	36	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	18	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	28	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	37	P	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	26	P
10. SOFT SKILLS	TW	50	20	28	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	29	P
						21. DATA STRUCTURES LABORATORY	PR	50	20	12	F

GRAND TOTAL = 894/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

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S8054304   PATIL SAGAR RAJESH   USHA   , 71045567E   , S8054304 ,   PICT   , S8054304

01. DISCRETE STRUCTURES	PP	100	40	73	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	59	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	50	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	57	P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	72	P C	13. DATA STRUCTURES	PP	100	40	62	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	62	P C	14. COMPUTER GRAPHICS	PP	100	40	70	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	49	P C	15. COMPUTER ORGANIZATION	PP	100	40	63	P
06. PROGRAMMING LABORATORY	TW	25	10	21	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	43	P
07. PROGRAMMING LABORATORY	PR	50	20	42	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	43	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	21	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	46	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	40	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	45	P
10. SOFT SKILLS	TW	50	20	41	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	42	P
						21. DATA STRUCTURES LABORATORY	PR	50	20	35	P

GRAND TOTAL = 1036/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

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S8054305   PATIL SHALAKA VIJAY   SHAILAJA   , 71132437K   , S8054305 ,   PICT   , S8054305

01. DISCRETE STRUCTURES	PP	100	40	61	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	78	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	57	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	61	P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	84	P C	13. DATA STRUCTURES	PP	100	40	78	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	68	P C	14. COMPUTER GRAPHICS	PP	100	40	71	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	48	P C	15. COMPUTER ORGANIZATION	PP	100	40	57	P
06. PROGRAMMING LABORATORY	TW	25	10	20	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	40	P
07. PROGRAMMING LABORATORY	PR	50	20	46	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	44	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	22	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	40	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	36	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	32	P
10. SOFT SKILLS	TW	50	20	39	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	40	P
						21. DATA STRUCTURES LABORATORY	PR	50	20	38	P

GRAND TOTAL = 1060/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

. . . . UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(COMPUTER) EXAMINATION MAY 2011 . . . .

DATE : 18 AUG. 2011   CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.   PAGE NO. 35 (399)

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.

          OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S8054306   PAWAR AJINKYA NANDU   VIDYA   , 71045570E   , S8054306 ,   PICT   , S8054306

01. DISCRETE STRUCTURES	PP	100	40	51	P	C	11. ENGINEERING MATHEMATICS III	PP	100	40	40	P	
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	54	P	C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	40	P	
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	54	P	C	13. DATA STRUCTURES	PP	100	40	40	P	
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	55	P	C	14. COMPUTER GRAPHICS	PP	100	40	60	P	
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	57	P	C	15. COMPUTER ORGANIZATION	PP	100	40	50	P	
06. PROGRAMMING LABORATORY	TW	25	10	15	P	C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	22	P	
07. PROGRAMMING LABORATORY	PR	50	20	37	P	C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	32	P	
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	13	P	C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	20	P	
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	28	P		19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	24	P	
10. SOFT SKILLS	TW	50	20	36	P	C	20. DATA STRUCTURES LABORATORY	TW	50	20	33	P	
							21. DATA STRUCTURES LABORATORY	PR	50	20	12	F	

GRAND TOTAL = 773/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

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S8054307   PAWAR HARISH MANGESH   RENUKA   , 71132438H   , S8054307 ,   PICT   , S8054307

01. DISCRETE STRUCTURES	PP	100	40	63	P	C	11. ENGINEERING MATHEMATICS III	PP	100	40	44	P	
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	59	P	C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	51	P	

03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	57	P C		13. DATA STRUCTURES		PP	100	40	55	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	48	P C		14. COMPUTER GRAPHICS		PP	100	40	54	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	57	P C		15. COMPUTER ORGANIZATION		PP	100	40	48	P
06. PROGRAMMING LABORATORY	TW	25	10	21	P C		16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	42	P
07. PROGRAMMING LABORATORY	PR	50	20	45	P C		17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	30	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	21	P C		18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	38	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	37	P C		19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	30	P
10. SOFT SKILLS	TW	50	20	38	P C		20. DATA STRUCTURES LABORATORY	TW	50	20	35	P
							21. DATA STRUCTURES LABORATORY	PR	50	20	11	F

GRAND TOTAL = 884/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

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S8054308   PILAJI KEDARNATH BALAJI   ANUJA   , 71045574H   , S8054308 ,   PICT   , S8054308

01. DISCRETE STRUCTURES	PP	100	40	62	P C		11. ENGINEERING MATHEMATICS III		PP	100	40	46	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	63	P C		12. MICROPROC. & INTERFACING TECHNIQ.		PP	100	40	57	P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	78	P C		13. DATA STRUCTURES		PP	100	40	69	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	48	P C		14. COMPUTER GRAPHICS		PP	100	40	61	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	58	P C		15. COMPUTER ORGANIZATION		PP	100	40	58	P
06. PROGRAMMING LABORATORY	TW	25	10	16	P C		16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	34	P
07. PROGRAMMING LABORATORY	PR	50	20	24	P C		17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	30	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	16	P C		18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	35	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	30	P C		19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	40	P
10. SOFT SKILLS	TW	50	20	33	P C		20. DATA STRUCTURES LABORATORY	TW	50	20	35	P
							21. DATA STRUCTURES LABORATORY	PR	50	20	30	P

GRAND TOTAL = 923/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.

       OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S8054309   PORE DIVYA KISHOR   KALPANA   , 71132439F   , S8054309 ,   PICT   , S8054309

01. DISCRETE STRUCTURES	PP	100	40	62	P C		11. ENGINEERING MATHEMATICS III	PP	100	40	49	P	
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	58	P C		12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	58	P	
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	76	P C		13. DATA STRUCTURES	PP	100	40	62	P	
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	67	P C		14. COMPUTER GRAPHICS	PP	100	40	52	P	
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	69	P C		15. COMPUTER ORGANIZATION	PP	100	40	59	P	
06. PROGRAMMING LABORATORY	TW	25	10	20	P C		16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	43	P	
07. PROGRAMMING LABORATORY	PR	50	20	35	P C		17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	28	P	
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	22	P C		18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	43	P	
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	33	P C		19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	38	P	
10. SOFT SKILLS	TW	50	20	39	P C		20. DATA STRUCTURES LABORATORY	TW	50	20	37	P	
							21. DATA STRUCTURES LABORATORY	PR	50	20	21	P	

GRAND TOTAL = 971/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

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S8054310   POTDAR SHANTANU RATNAKAR   ANITA   , 71132440K   , S8054310 ,   PICT   , S8054310

01. DISCRETE STRUCTURES	PP	100	40	77	P C		11. ENGINEERING MATHEMATICS III	PP	100	40	78	P	
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	62	P C		12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	50	P	
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	84	P C		13. DATA STRUCTURES	PP	100	40	73	P	
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	74	P C		14. COMPUTER GRAPHICS	PP	100	40	65	P	
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	59	P C		15. COMPUTER ORGANIZATION	PP	100	40	63	P	
06. PROGRAMMING LABORATORY	TW	25	10	22	P C		16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	46	P	
07. PROGRAMMING LABORATORY	PR	50	20	42	P C		17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	44	P	

08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	22	P	C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	45	P			
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	42	P	C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	44	P			
10. SOFT SKILLS	TW	50	20	38	P	C	20. DATA STRUCTURES LABORATORY	TW	50	20	43	P			
							21. DATA STRUCTURES LABORATORY	PR	50	20	44	P			

GRAND TOTAL = 1117/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

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S8054311   PRANIDHYA KHANDELWAL   NISHA   , 71045580B   , S8054311 ,   PICT   , S8054311

01. DISCRETE STRUCTURES	PP	100	40	62	P	C	11. ENGINEERING MATHEMATICS III	PP	100	40	50	P	
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	40	P	C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	43	P	
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	53	P	C	13. DATA STRUCTURES	PP	100	40	52	P	
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	40	P	C	14. COMPUTER GRAPHICS	PP	100	40	64	P	
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	49	P	C	15. COMPUTER ORGANIZATION	PP	100	40	52	P	
06. PROGRAMMING LABORATORY	TW	25	10	17	P	C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	37	P	
07. PROGRAMMING LABORATORY	PR	50	20	39	P		17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	28	P	
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	17	P	C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	38	P	
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	23	P	C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	30	P	
10. SOFT SKILLS	TW	50	20	35	P	C	20. DATA STRUCTURES LABORATORY	TW	50	20	34	P	
							21. DATA STRUCTURES LABORATORY	PR	50	20	32	P	

GRAND TOTAL = 835/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

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 NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.

        OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S8054312   PRATIVINDHYA MISHRA   ANURAG   , 71045581L   , S8054312 , PICT   , S8054312

01. DISCRETE STRUCTURES	PP	100	40	69	P C		11. ENGINEERING MATHEMATICS III	PP	100	40	61	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	62	P C		12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	54	P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	81	P C		13. DATA STRUCTURES	PP	100	40	67	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	65	P C		14. COMPUTER GRAPHICS	PP	100	40	67	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	65	P C		15. COMPUTER ORGANIZATION	PP	100	40	70	P
06. PROGRAMMING LABORATORY	TW	25	10	23	P C		16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	45	P
07. PROGRAMMING LABORATORY	PR	50	20	43	P C		17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	41	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	23	P C		18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	43	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	37	P C		19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	45	P
10. SOFT SKILLS	TW	50	20	46	P C		20. DATA STRUCTURES LABORATORY	TW	50	20	45	P
							21. DATA STRUCTURES LABORATORY	PR	50	20	26	P

GRAND TOTAL = 1078/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

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S8054313   RAHEJA ROHAN VINOD   POOJA   , 71045585C   , S8054313 , PICT   , S8054313

01. DISCRETE STRUCTURES	PP	100	40	67	P C		11. ENGINEERING MATHEMATICS III	PP	100	40	52	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	52	P C		12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	40	P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	67	P C		13. DATA STRUCTURES	PP	100	40	66	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	49	P C		14. COMPUTER GRAPHICS	PP	100	40	48	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	56	P C		15. COMPUTER ORGANIZATION	PP	100	40	40	P
06. PROGRAMMING LABORATORY	TW	25	10	22	P C		16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	36	P
07. PROGRAMMING LABORATORY	PR	50	20	42	P C		17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	37	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	21	P C		18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	36	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	41	P C		19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	42	P
10. SOFT SKILLS	TW	50	20	38	P C		20. DATA STRUCTURES LABORATORY	TW	50	20	40	P
							21. DATA STRUCTURES LABORATORY	PR	50	20	38	P

GRAND TOTAL = 930/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

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S8054314   RANE RAHUL DILIP   VIDYA   , 71045586M   , S8054314 ,   PICT   , S8054314

01. DISCRETE STRUCTURES	PP	100	40	54	P C		11. ENGINEERING MATHEMATICS III	PP	100	40	67	P	
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	56	P C		12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	58	P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	79	P C		13. DATA STRUCTURES	PP	100	40	71	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	56	P C		14. COMPUTER GRAPHICS	PP	100	40	52	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	64	P C		15. COMPUTER ORGANIZATION	PP	100	40	57	P
06. PROGRAMMING LABORATORY	TW	25	10	20	P C		16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	39	P
07. PROGRAMMING LABORATORY	PR	50	20	35	P C		17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	42	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	20	P C		18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	41	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	35	P C		19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	40	P
10. SOFT SKILLS	TW	50	20	38	P C		20. DATA STRUCTURES LABORATORY	TW	50	20	37	P
							21. DATA STRUCTURES LABORATORY	PR	50	20	30	P

GRAND TOTAL = 991/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

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UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(COMPUTER) EXAMINATION MAY 2011
    DATE : 18 AUG. 2011   CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.   PAGE NO. 38 (402)
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  NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.
        OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S8054315   RAWALE SUHAS SUDAMRAO   RATNMALA   , 71045588H   , S8054315 ,   PICT   , S8054315

01. DISCRETE STRUCTURES	PP	100	40	49	P C		11. ENGINEERING MATHEMATICS III	PP	100	40	22	F	
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	52	P C		12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	24	F
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	54	P C		13. DATA STRUCTURES	PP	100	40	49	P

04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	53	P C		14. COMPUTER GRAPHICS	PP	100	40	40	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	40	P C		15. COMPUTER ORGANIZATION	PP	100	40	44	P
06. PROGRAMMING LABORATORY	TW	25	10	20	P C		16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	32	P
07. PROGRAMMING LABORATORY	PR	50	20	24	P C		17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	29	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	19	P C		18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	32	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	22	P C		19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	28	P
10. SOFT SKILLS	TW	50	20	34	P C		20. DATA STRUCTURES LABORATORY	TW	50	20	27	P
							21. DATA STRUCTURES LABORATORY	PR	50	20	08	F

GRAND TOTAL = 702/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

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S8054316   SAKORE MITHILA RAMNATH   MADHURI   , 71045593D   , S8054316 ,   PICT   , S8054316

01. DISCRETE STRUCTURES	PP	100	40	61	P C		11. ENGINEERING MATHEMATICS III	PP	100	40	40	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	60	P C		12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	51	P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	71	P C		13. DATA STRUCTURES	PP	100	40	80	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	55	P C		14. COMPUTER GRAPHICS	PP	100	40	71	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	59	P C		15. COMPUTER ORGANIZATION	PP	100	40	60	P
06. PROGRAMMING LABORATORY	TW	25	10	22	P C		16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	39	P
07. PROGRAMMING LABORATORY	PR	50	20	35	P		17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	31	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	20	P C		18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	38	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	30	P C		19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	10	F
10. SOFT SKILLS	TW	50	20	36	P C		20. DATA STRUCTURES LABORATORY	TW	50	20	41	P
							21. DATA STRUCTURES LABORATORY	PR	50	20	28	P

GRAND TOTAL = 938/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

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S8054317   SANGHVI PALAK RAJESH   NEHA   , 71045595L   , S8054317 ,   PICT   , S8054317

01. DISCRETE STRUCTURES	PP	100	40	69	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	47	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	46	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	52	P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	75	P C	13. DATA STRUCTURES	PP	100	40	59	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	61	P C	14. COMPUTER GRAPHICS	PP	100	40	55	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	48	P C	15. COMPUTER ORGANIZATION	PP	100	40	55	P
06. PROGRAMMING LABORATORY	TW	25	10	23	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	46	P
07. PROGRAMMING LABORATORY	PR	50	20	38	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	37	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	24	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	46	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	44	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	40	P
10. SOFT SKILLS	TW	50	20	40	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	45	P
						21. DATA STRUCTURES LABORATORY	PR	50	20	40	P

GRAND TOTAL = 990/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

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UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(COMPUTER) EXAMINATION MAY 2011

DATE : 18 AUG. 2011   CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.   PAGE NO. 39 (403)

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S8054318   SANGLIKAR PRATIK SANTOSH   ANJALI   , 71045597G   , S8054318 , PICT   , S8054318

01. DISCRETE STRUCTURES	PP	100	40	57	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	40	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	53	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	43	P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	40	P C	13. DATA STRUCTURES	PP	100	40	58	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	48	P C	14. COMPUTER GRAPHICS	PP	100	40	47	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	53	P C	15. COMPUTER ORGANIZATION	PP	100	40	61	P
06. PROGRAMMING LABORATORY	TW	25	10	20	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	30	P
07. PROGRAMMING LABORATORY	PR	50	20	29	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	33	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	20	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	37	P

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09. DIGITAL ELECTRONICS LABORATORY    PR   50   20   23  P C    19. MICROPROCESSORS & INTERFACING LABPR   50   20   45  P
10. SOFT SKILLS                       TW   50   20   41  P C    20. DATA STRUCTURES LABORATORY       TW   50   20   38  P
                                                               21. DATA STRUCTURES LABORATORY       PR   50   20   22  P
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GRAND TOTAL = 838/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

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S8054319   SAVLA SANKET HARISH   JYOTI   , 71045602G   , S8054319 ,   PICT   , S8054319

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01. DISCRETE STRUCTURES               PP  100   40   56  P C    11. ENGINEERING MATHEMATICS III     PP  100   40   56  P
02. PROGRAMMING & PROBLEM SOLVING     PP  100   40   57  P C    12. MICROPROC. & INTERFACING TECHNIQ.PP  100   40   40  P
03. DIGIT. ELECTRONICS & LOGIC DESIGNPP  100   40   62  P C    13. DATA STRUCTURES                 PP  100   40   54  P
04. DATA STRUCTURES AND ALGORITHMS    PP  100   40   49  P C    14. COMPUTER GRAPHICS               PP  100   40   49  P
05. HUMANITIES AND SOCIAL SCIENCE     PP  100   40   53  P C    15. COMPUTER ORGANIZATION           PP  100   40   49  P
06. PROGRAMMING LABORATORY            TW   25   10   20  P C    16. O. O. PROG. & COMP. GRAPH. LAB  TW   50   20   35  P
07. PROGRAMMING LABORATORY            PR   50   20   41  P C    17. O. O. PROG. & COMP. GRAPH. LAB  PR   50   20   38  P
08. DIGITAL ELECTRONICS LABORATORY    TW   25   10   17  P C    18. MICROPROCESSORS & INTERFACING LABTW   50   20   34  P
09. DIGITAL ELECTRONICS LABORATORY    PR   50   20   41  P C    19. MICROPROCESSORS & INTERFACING LABPR   50   20   30  P
10. SOFT SKILLS                       TW   50   20   40  P C    20. DATA STRUCTURES LABORATORY       TW   50   20   30  P
                                                               21. DATA STRUCTURES LABORATORY       PR   50   20   34  P
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GRAND TOTAL = 885/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

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S8054320   SHAH DIMPLE PIYUSH   BINAL   , 71045606K   , S8054320 ,   PICT   , S8054320

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01. DISCRETE STRUCTURES               PP  100   40   64  P C    11. ENGINEERING MATHEMATICS III     PP  100   40   80  P
02. PROGRAMMING & PROBLEM SOLVING     PP  100   40   43  P C    12. MICROPROC. & INTERFACING TECHNIQ.PP  100   40   44  P
03. DIGIT. ELECTRONICS & LOGIC DESIGNPP  100   40   64  P C    13. DATA STRUCTURES                 PP  100   40   69  P
04. DATA STRUCTURES AND ALGORITHMS    PP  100   40   54  P C    14. COMPUTER GRAPHICS               PP  100   40   60  P
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05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	52	P C	15. COMPUTER ORGANIZATION	PP	100	40	60	P	
06. PROGRAMMING LABORATORY	TW	25	10	19	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	40	P	
07. PROGRAMMING LABORATORY	PR	50	20	29	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	36	P	
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	20	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	43	P	
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	38	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	42	P	
10. SOFT SKILLS	TW	50	20	40	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	40	P	
						21. DATA STRUCTURES LABORATORY	PR	50	20	38	P	

GRAND TOTAL = 975/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

. . . .
UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(COMPUTER) EXAMINATION MAY 2011

DATE : 18 AUG. 2011   CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.   PAGE NO. 40 (404)

. . . .

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.

       OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

. . . .

S8054321   SHAH YASH MANESH   RAJASHRI   , 71045608F   , S8054321 ,   PICT   , S8054321

01. DISCRETE STRUCTURES	PP	100	40	68	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	63	P	
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	60	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	52	P	
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	86	P C	13. DATA STRUCTURES	PP	100	40	67	P	
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	56	P C	14. COMPUTER GRAPHICS	PP	100	40	70	P	
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	52	P C	15. COMPUTER ORGANIZATION	PP	100	40	64	P	
06. PROGRAMMING LABORATORY	TW	25	10	22	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	40	P	
07. PROGRAMMING LABORATORY	PR	50	20	43	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	45	P	
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	20	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	40	P	
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	30	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	45	P	
10. SOFT SKILLS	TW	50	20	41	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	39	P	
						21. DATA STRUCTURES LABORATORY	PR	50	20	25	P	

GRAND TOTAL = 1028/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

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S8054322   SHEKHAR SAMIR SHAILESH KUMAR GUNJAN   PUSHPA RANI GUNJAN   , 71045609D   , S8054322 ,   PICT   , S8054322

01. DISCRETE STRUCTURES	PP	100	40	52	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	56	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	47	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	40	P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	60	P C	13. DATA STRUCTURES	PP	100	40	66	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	46	P C	14. COMPUTER GRAPHICS	PP	100	40	47	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	53	P C	15. COMPUTER ORGANIZATION	PP	100	40	56	P
06. PROGRAMMING LABORATORY	TW	25	10	17	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	34	P
07. PROGRAMMING LABORATORY	PR	50	20	37	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	41	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	18	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	35	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	20	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	45	P
10. SOFT SKILLS	TW	50	20	36	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	37	P
						21. DATA STRUCTURES LABORATORY	PR	50	20	34	P

GRAND TOTAL = 877/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

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S8054323   SHINDE AMIT KEDARNATH   SANGEETA   , 71045614L   , S8054323 ,   PICT   , S8054323

01. DISCRETE STRUCTURES	PP	100	40	53	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	40	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	40	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	AA	F
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	49	P C	13. DATA STRUCTURES	PP	100	40	54	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	40	P C	14. COMPUTER GRAPHICS	PP	100	40	46	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	40	P C	15. COMPUTER ORGANIZATION	PP	100	40	54	P
06. PROGRAMMING LABORATORY	TW	25	10	17	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	32	P
07. PROGRAMMING LABORATORY	PR	50	20	35	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	32	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	16	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	36	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	21	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	38	P

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                                                          cols05
10. SOFT SKILLS                     TW   50   20   34  P C     20. DATA STRUCTURES LABORATORY      TW   50   20   30  P
                                                              21. DATA STRUCTURES LABORATORY      PR   50   20   05  F
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GRAND TOTAL = 712/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

. . . . UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(COMPUTER) EXAMINATION MAY 2011 . . . .

   DATE : 18 AUG. 2011   CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.   PAGE NO. 41 (405)
. . . .

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.

       OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

. . . .

S8054324   SHREYAS KUMAR PANIGRAHI   MINATI   , 71045618C   , S8054324 , PICT   , S8054324

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01. DISCRETE STRUCTURES             PP  100   40   56  P C     11. ENGINEERING MATHEMATICS III     PP  100   40   40  P
02. PROGRAMMING & PROBLEM SOLVING   PP  100   40   52  P C     12. MICROPROC. & INTERFACING TECHNIQ.PP  100   40   49  P
03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100   40   56  P C     13. DATA STRUCTURES                 PP  100   40   49  P
04. DATA STRUCTURES AND ALGORITHMS  PP  100   40   49  P C     14. COMPUTER GRAPHICS               PP  100   40   60  P
05. HUMANITIES AND SOCIAL SCIENCE   PP  100   40   62  P C     15. COMPUTER ORGANIZATION           PP  100   40   62  P
06. PROGRAMMING LABORATORY          TW   25   10   15  P C     16. O. O. PROG. & COMP. GRAPH. LAB  TW   50   20   28  P
07. PROGRAMMING LABORATORY          PR   50   20   40  P C     17. O. O. PROG. & COMP. GRAPH. LAB  PR   50   20   38  P
08. DIGITAL ELECTRONICS LABORATORY  TW   25   10   16  P C     18. MICROPROCESSORS & INTERFACING LABTW   50   20   30  P
09. DIGITAL ELECTRONICS LABORATORY  PR   50   20   30  P C     19. MICROPROCESSORS & INTERFACING LABPR   50   20   28  P
10. SOFT SKILLS                     TW   50   20   35  P C     20. DATA STRUCTURES LABORATORY      TW   50   20   32  P
                                                              21. DATA STRUCTURES LABORATORY      PR   50   20   42  P
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GRAND TOTAL = 869/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

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S8054325   SHRUTI SWAGATIKA   SUJATA   , 71045619M   , S8054325 , PICT   , S8054325

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01. DISCRETE STRUCTURES             PP  100  40  70  P C    11. ENGINEERING MATHEMATICS III      PP  100  40  40  P
02. PROGRAMMING & PROBLEM SOLVING   PP  100  40  60  P C    12. MICROPROC. & INTERFACING TECHNIQ.PP  100  40  53  P
03. DIGIT. ELECTRONICS & LOGIC DESIGNPP  100  40  65  P C   13. DATA STRUCTURES                  PP  100  40  59  P
04. DATA STRUCTURES AND ALGORITHMS  PP  100  40  53  P C    14. COMPUTER GRAPHICS                PP  100  40  64  P
05. HUMANITIES AND SOCIAL SCIENCE   PP  100  40  60  P C    15. COMPUTER ORGANIZATION            PP  100  40  60  P
06. PROGRAMMING LABORATORY          TW   25  10  20  P C    16. O. O. PROG. & COMP. GRAPH. LAB   TW   50  20  36  P
07. PROGRAMMING LABORATORY          PR   50  20  39  P C    17. O. O. PROG. & COMP. GRAPH. LAB   PR   50  20  37  P
08. DIGITAL ELECTRONICS LABORATORY  TW   25  10  20  P C    18. MICROPROCESSORS & INTERFACING LABTW   50  20  39  P
09. DIGITAL ELECTRONICS LABORATORY  PR   50  20  25  P C    19. MICROPROCESSORS & INTERFACING LABPR   50  20  40  P
10. SOFT SKILLS                     TW   50  20  38  P C    20. DATA STRUCTURES LABORATORY       TW   50  20  37  P
                                                            21. DATA STRUCTURES LABORATORY       PR   50  20  28  P
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GRAND TOTAL = 943/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

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S8054326   SHUBHRANK GUPTA   INDIRA   , 71045621C   , S8054326 ,   PICT   , S8054326

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01. DISCRETE STRUCTURES             PP  100  40  40  P
02. PROGRAMMING & PROBLEM SOLVING   PP  100  40  40  P C
03. DIGIT. ELECTRONICS & LOGIC DESIGNPP  100  40  AA  F
04. DATA STRUCTURES AND ALGORITHMS  PP  100  40  17  F
05. HUMANITIES AND SOCIAL SCIENCE   PP  100  40  40  P C
06. PROGRAMMING LABORATORY          TW   25  10  13  P C
07. PROGRAMMING LABORATORY          PR   50  20  AA  F
08. DIGITAL ELECTRONICS LABORATORY  TW   25  10  10  P C
09. DIGITAL ELECTRONICS LABORATORY  PR   50  20  AA  F
10. SOFT SKILLS                     TW   50  20  24  P C
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FIRST TERM TOTAL = 184/700.

ORDN. 1 MARKS :

cols05

. . . .   UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(COMPUTER) EXAMINATION MAY 2011   . . . .

DATE : 18 AUG. 2011   CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.   PAGE NO. 42 (406)

. . . .

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

. . . .

S8054327   SIDDHA MONIKA SUNIL   JAYASHREE   , 71045623K   , S8054327 , PICT   , S8054327

01. DISCRETE STRUCTURES	PP	100	40	56	P C		11. ENGINEERING MATHEMATICS III	PP	100	40	54	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	63	P C		12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	44	P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	67	P C		13. DATA STRUCTURES	PP	100	40	59	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	47	P C		14. COMPUTER GRAPHICS	PP	100	40	45	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	59	P C		15. COMPUTER ORGANIZATION	PP	100	40	55	P
06. PROGRAMMING LABORATORY	TW	25	10	19	P C		16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	37	P
07. PROGRAMMING LABORATORY	PR	50	20	39	P C		17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	37	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	16	P C		18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	41	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	30	P C		19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	40	P
10. SOFT SKILLS	TW	50	20	39	P C		20. DATA STRUCTURES LABORATORY	TW	50	20	41	P
							21. DATA STRUCTURES LABORATORY	PR	50	20	34	P

GRAND TOTAL = 922/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

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S8054328   SIDDHARTH BATRA   SANGEETA   , 71045624H   , S8054328 , PICT   , S8054328

01. DISCRETE STRUCTURES	PP	100	40	71	P C
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	40	P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	40	P C
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	AA	F
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	43	P C

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06. PROGRAMMING LABORATORY          TW    25    10    14   P C
07. PROGRAMMING LABORATORY          PR    50    20    24   P C
08. DIGITAL ELECTRONICS LABORATORY  TW    25    10    10   P C
09. DIGITAL ELECTRONICS LABORATORY  PR    50    20    20   P
10. SOFT SKILLS                     TW    50    20    23   P C


   FIRST TERM TOTAL =  285/700.

ORDN. 1 MARKS :
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S8054329    SINARE NUPOOR SANJAY              KALPANA          , 71045626D    , S8054329 ,   PICT      , S8054329

01. DISCRETE STRUCTURES             PP   100    40    63   P C   11. ENGINEERING MATHEMATICS III     PP   100    40    49   P
02. PROGRAMMING & PROBLEM SOLVING   PP   100    40    68   P C   12. MICROPROC. & INTERFACING TECHNIQ.PP 100    40    46   P
03. DIGIT. ELECTRONICS & LOGIC DESIGNPP  100    40    82   P C   13. DATA STRUCTURES                 PP   100    40    71   P
04. DATA STRUCTURES AND ALGORITHMS  PP   100    40    63   P C   14. COMPUTER GRAPHICS               PP   100    40    66   P
05. HUMANITIES AND SOCIAL SCIENCE   PP   100    40    63   P C   15. COMPUTER ORGANIZATION           PP   100    40    68   P
06. PROGRAMMING LABORATORY          TW    25    10    22   P C   16. O. O. PROG. & COMP. GRAPH. LAB  TW    50    20    38   P
07. PROGRAMMING LABORATORY          PR    50    20    37   P C   17. O. O. PROG. & COMP. GRAPH. LAB  PR    50    20    34   P
08. DIGITAL ELECTRONICS LABORATORY  TW    25    10    22   P C   18. MICROPROCESSORS & INTERFACING LABTW  50    20    41   P
09. DIGITAL ELECTRONICS LABORATORY  PR    50    20    38   P C   19. MICROPROCESSORS & INTERFACING LABPR  50    20    40   P
10. SOFT SKILLS                     TW    50    20    43   P C   20. DATA STRUCTURES LABORATORY      TW    50    20    43   P
                                                                21. DATA STRUCTURES LABORATORY      PR    50    20    42   P

GRAND TOTAL = 1039/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :
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.... UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(COMPUTER) EXAMINATION MAY 2011 ....

DATE : 18 AUG. 2011   CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.   PAGE NO. 43 (407)

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.

                                                Page 150

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

. . . .

S8054330   SINGHAL PANKAJ SHRIBHAGWAN   ANITA   , 71045627B   , S8054330 , PICT   , S8054330

01. DISCRETE STRUCTURES	PP	100	40	68	P C		11. ENGINEERING MATHEMATICS III	PP	100	40	87	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	52	P C		12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	43	P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	83	P C		13. DATA STRUCTURES	PP	100	40	65	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	50	P C		14. COMPUTER GRAPHICS	PP	100	40	65	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	56	P C		15. COMPUTER ORGANIZATION	PP	100	40	64	P
06. PROGRAMMING LABORATORY	TW	25	10	21	P C		16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	36	P
07. PROGRAMMING LABORATORY	PR	50	20	40	P C		17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	25	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	21	P C		18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	37	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	30	P C		19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	40	P
10. SOFT SKILLS	TW	50	20	39	P C		20. DATA STRUCTURES LABORATORY	TW	50	20	39	P
							21. DATA STRUCTURES LABORATORY	PR	50	20	32	P

GRAND TOTAL = 993/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

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S8054331   SONAWANE KALINDI DATTATRAYA   MAYA   , 71045631L   , S8054331 , PICT   , S8054331

01. DISCRETE STRUCTURES	PP	100	40	56	P C		11. ENGINEERING MATHEMATICS III	PP	100	40	43	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	46	P C		12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	45	P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	63	P C		13. DATA STRUCTURES	PP	100	40	63	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	54	P C		14. COMPUTER GRAPHICS	PP	100	40	55	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	50	P C		15. COMPUTER ORGANIZATION	PP	100	40	59	P
06. PROGRAMMING LABORATORY	TW	25	10	20	P C		16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	39	P
07. PROGRAMMING LABORATORY	PR	50	20	27	P		17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	27	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	20	P C		18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	38	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	30	P C		19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	08	F
10. SOFT SKILLS	TW	50	20	36	P C		20. DATA STRUCTURES LABORATORY	TW	50	20	36	P

                                                      21. DATA STRUCTURES LABORATORY   PR   50   20   25   P

GRAND TOTAL = 840/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

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 S8054332   SOUNDANKAR KOMAL RAJENDRA   KALPANA   , 71045634E   , S8054332 ,   PICT   , S8054332

 01. DISCRETE STRUCTURES   PP   100   40   65   P C   11. ENGINEERING MATHEMATICS III   PP   100   40   44   P
 02. PROGRAMMING & PROBLEM SOLVING PP   100   40   45   P C   12. MICROPROC. & INTERFACING TECHNIQ.PP   100   40   32   F
 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100   40   41   P C   13. DATA STRUCTURES   PP   100   40   46   P
 04. DATA STRUCTURES AND ALGORITHMS PP   100   40   44   P C   14. COMPUTER GRAPHICS   PP   100   40   40   P
 05. HUMANITIES AND SOCIAL SCIENCE PP   100   40   49   P C   15. COMPUTER ORGANIZATION   PP   100   40   42   P
 06. PROGRAMMING LABORATORY   TW   25   10   19   P C   16. O. O. PROG. & COMP. GRAPH. LAB   TW   50   20   30   P
 07. PROGRAMMING LABORATORY   PR   50   20   28   P   17. O. O. PROG. & COMP. GRAPH. LAB   PR   50   20   31   P
 08. DIGITAL ELECTRONICS LABORATORY TW   25   10   19   P C   18. MICROPROCESSORS & INTERFACING LABTW   50   20   32   P
 09. DIGITAL ELECTRONICS LABORATORY PR   50   20   28   P C   19. MICROPROCESSORS & INTERFACING LABPR   50   20   10   F
 10. SOFT SKILLS   TW   50   20   37   P C   20. DATA STRUCTURES LABORATORY   TW   50   20   35   P
                                                                 21. DATA STRUCTURES LABORATORY   PR   50   20   10   F

GRAND TOTAL = 727/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

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                          UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(COMPUTER) EXAMINATION MAY 2011
     DATE : 18 AUG. 2011   CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.   PAGE NO. 44 (408)
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 NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.
        OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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 S8054333   SUKHANI KUMAR DAYARAM   ARTI   , 71045636M   , S8054333 ,   PICT   , S8054333

 01. DISCRETE STRUCTURES   PP   100   40   68   P C   11. ENGINEERING MATHEMATICS III   PP   100   40   52   P

02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	51	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	45	P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	70	P C	13. DATA STRUCTURES	PP	100	40	64	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	45	P C	14. COMPUTER GRAPHICS	PP	100	40	59	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	49	P C	15. COMPUTER ORGANIZATION	PP	100	40	60	P
06. PROGRAMMING LABORATORY	TW	25	10	18	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	30	P
07. PROGRAMMING LABORATORY	PR	50	20	38	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	46	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	17	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	33	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	42	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	46	P
10. SOFT SKILLS	TW	50	20	37	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	30	P
						21. DATA STRUCTURES LABORATORY	PR	50	20	31	P

GRAND TOTAL = 931/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

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S8054334   TAHAKIK AVINASH ABASAHEB   SHAKUNTALA   , 71045638H   , S8054334 ,   PICT   , S8054334

01. DISCRETE STRUCTURES	PP	100	40	66	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	81	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	61	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	49	P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	66	P C	13. DATA STRUCTURES	PP	100	40	73	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	51	P C	14. COMPUTER GRAPHICS	PP	100	40	61	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	48	P C	15. COMPUTER ORGANIZATION	PP	100	40	70	P
06. PROGRAMMING LABORATORY	TW	25	10	18	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	25	P
07. PROGRAMMING LABORATORY	PR	50	20	35	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	43	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	18	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	28	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	27	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	42	P
10. SOFT SKILLS	TW	50	20	32	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	24	P
						21. DATA STRUCTURES LABORATORY	PR	50	20	33	P

GRAND TOTAL = 951/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

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S8054335   TANPURE AJAY PRAKASH   SUNITA   , 71045639F   , S8054335 , PICT   , S8054335

01. DISCRETE STRUCTURES   PP   100   40   54 P C   11. ENGINEERING MATHEMATICS III   PP   100   40   10 F
02. PROGRAMMING & PROBLEM SOLVING PP   100   40   43 P C   12. MICROPROC. & INTERFACING TECHNIQ.PP   100   40   30 F
03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100   40   78 P C   13. DATA STRUCTURES   PP   100   40   64 P
04. DATA STRUCTURES AND ALGORITHMS PP   100   40   60 P C   14. COMPUTER GRAPHICS   PP   100   40   60 P
05. HUMANITIES AND SOCIAL SCIENCE PP   100   40   50 P C   15. COMPUTER ORGANIZATION   PP   100   40   51 P
06. PROGRAMMING LABORATORY   TW   25   10   19 P C   16. O. O. PROG. & COMP. GRAPH. LAB TW   50   20   33 P
07. PROGRAMMING LABORATORY   PR   50   20   37 P C   17. O. O. PROG. & COMP. GRAPH. LAB PR   50   20   35 P
08. DIGITAL ELECTRONICS LABORATORY TW   25   10   19 P C   18. MICROPROCESSORS & INTERFACING LABTW   50   20   36 P
09. DIGITAL ELECTRONICS LABORATORY PR   50   20   31 P C   19. MICROPROCESSORS & INTERFACING LABPR   50   20   32 P
10. SOFT SKILLS   TW   50   20   38 P C   20. DATA STRUCTURES LABORATORY   TW   50   20   40 P
                                                              21. DATA STRUCTURES LABORATORY   PR   50   20   11 F

GRAND TOTAL = 831/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

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                    UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(COMPUTER) EXAMINATION MAY 2011
     DATE : 18 AUG. 2011   CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.   PAGE NO. 45 (409)
. . . .
 NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.
       OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S8054336   TARGE SOURABH SURESH   MANISHA   , 71132441H   , S8054336 , PICT   , S8054336

01. DISCRETE STRUCTURES   PP   100   40   63 P C   11. ENGINEERING MATHEMATICS III   PP   100   40   56 P
02. PROGRAMMING & PROBLEM SOLVING PP   100   40   56 P C   12. MICROPROC. & INTERFACING TECHNIQ.PP   100   40   45 P
03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100   40   70 P C   13. DATA STRUCTURES   PP   100   40   65 P
04. DATA STRUCTURES AND ALGORITHMS PP   100   40   49 P C   14. COMPUTER GRAPHICS   PP   100   40   62 P
05. HUMANITIES AND SOCIAL SCIENCE PP   100   40   66 P C   15. COMPUTER ORGANIZATION   PP   100   40   57 P
06. PROGRAMMING LABORATORY   TW   25   10   21 P C   16. O. O. PROG. & COMP. GRAPH. LAB TW   50   20   42 P

07. PROGRAMMING LABORATORY	PR	50	20	38	P C		17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	40	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	22	P C		18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	42	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	40	P C		19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	44	P
10. SOFT SKILLS	TW	50	20	39	P C		20. DATA STRUCTURES LABORATORY	TW	50	20	36	P
							21. DATA STRUCTURES LABORATORY	PR	50	20	32	P

GRAND TOTAL = 985/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

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S8054337   TATTI ADITI ANIL   ASHWINI   , 71045641H   , S8054337 ,   PICT   , S8054337

01. DISCRETE STRUCTURES	PP	100	40	73	P C		11. ENGINEERING MATHEMATICS III	PP	100	40	56	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	61	P C		12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	42	P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	91	P C		13. DATA STRUCTURES	PP	100	40	57	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	67	P C		14. COMPUTER GRAPHICS	PP	100	40	47	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	58	P C		15. COMPUTER ORGANIZATION	PP	100	40	59	P
06. PROGRAMMING LABORATORY	TW	25	10	19	P C		16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	38	P
07. PROGRAMMING LABORATORY	PR	50	20	45	P C		17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	40	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	20	P C		18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	37	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	37	P C		19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	45	P
10. SOFT SKILLS	TW	50	20	31	P C		20. DATA STRUCTURES LABORATORY	TW	50	20	37	P
							21. DATA STRUCTURES LABORATORY	PR	50	20	41	P

GRAND TOTAL = 1001/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

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S8054338   THAKARE DHANRAJSINGH SURESH   JAYA   , 71045646J   , S8054338 ,   PICT   , S8054338

01. DISCRETE STRUCTURES	PP	100	40	55	P C		11. ENGINEERING MATHEMATICS III	PP	100	40	40	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	52	P C		12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	40	P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	59	P C		13. DATA STRUCTURES	PP	100	40	59	P

04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	53	P C		14. COMPUTER GRAPHICS	PP	100	40	60	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	40	P C		15. COMPUTER ORGANIZATION	PP	100	40	49	P
06. PROGRAMMING LABORATORY	TW	25	10	17	P C		16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	34	P
07. PROGRAMMING LABORATORY	PR	50	20	30	P C		17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	24	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	19	P C		18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	40	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	20	P C		19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	30	P
10. SOFT SKILLS	TW	50	20	38	P C		20. DATA STRUCTURES LABORATORY	TW	50	20	40	P
							21. DATA STRUCTURES LABORATORY	PR	50	20	32	P

GRAND TOTAL = 831/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

. . . . UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(COMPUTER) EXAMINATION MAY 2011 . . . .

DATE : 18 AUG. 2011   CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.   PAGE NO. 46 (410)

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S8054339   THAKKAR BHAVIN PRANAY   VARSHA   , 71045647G   , S8054339 ,   PICT   , S8054339

01. DISCRETE STRUCTURES	PP	100	40	62	P C		11. ENGINEERING MATHEMATICS III	PP	100	40	40	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	47	P C		12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	40	P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	62	P C		13. DATA STRUCTURES	PP	100	40	69	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	49	P C		14. COMPUTER GRAPHICS	PP	100	40	64	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	63	P C		15. COMPUTER ORGANIZATION	PP	100	40	67	P
06. PROGRAMMING LABORATORY	TW	25	10	17	P C		16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	34	P
07. PROGRAMMING LABORATORY	PR	50	20	35	P C		17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	45	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	18	P C		18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	32	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	42	P C		19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	42	P
10. SOFT SKILLS	TW	50	20	28	P C		20. DATA STRUCTURES LABORATORY	TW	50	20	28	P
							21. DATA STRUCTURES LABORATORY	PR	50	20	34	P

GRAND TOTAL = 918/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

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S8054340   THAKUR DHIRAJSING UDAYSING   SAROJADEVI   , 71045648E   , S8054340 ,   PICT   , S8054340

01. DISCRETE STRUCTURES	PP	100	40	48	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	AA	F
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	40	P	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	09	F
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	40	P	13. DATA STRUCTURES	PP	100	40	40	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	45	P	14. COMPUTER GRAPHICS	PP	100	40	28	F
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	40	P C	15. COMPUTER ORGANIZATION	PP	100	40	40	P
06. PROGRAMMING LABORATORY	TW	25	10	13	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	21	P
07. PROGRAMMING LABORATORY	PR	50	20	37	P	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	07	F
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	12	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	25	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	20	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	AA	F
10. SOFT SKILLS	TW	50	20	29	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	28	P
						21. DATA STRUCTURES LABORATORY	PR	50	20	13	F

GRAND TOTAL = 535/1500, RESULT: FAILS

ORDN. 1 MARKS :

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S8054341   UTKARSH MISHRA   MAMTA   , 71045654K   , S8054341 ,   PICT   , S8054341

01. DISCRETE STRUCTURES	PP	100	40	71	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	54	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	47	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	30	F
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	51	P C	13. DATA STRUCTURES	PP	100	40	53	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	40	P C	14. COMPUTER GRAPHICS	PP	100	40	58	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	53	P C	15. COMPUTER ORGANIZATION	PP	100	40	48	P
06. PROGRAMMING LABORATORY	TW	25	10	15	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	24	P
07. PROGRAMMING LABORATORY	PR	50	20	10	F	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	06	F
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	15	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	25	P

09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	30	P C		19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	10	F	
10. SOFT SKILLS	TW	50	20	22	P C		20. DATA STRUCTURES LABORATORY	TW	50	20	28	P	
							21. DATA STRUCTURES LABORATORY	PR	50	20	30	P	

GRAND TOTAL = 720/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

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UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(COMPUTER) EXAMINATION MAY 2011

DATE : 18 AUG. 2011   CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.   PAGE NO. 47 (411)

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NOS., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S8054342   VAYKOLE TEJAS ANIL   KALPANA   , 71045656F   , S8054342 ,   PICT   , S8054342

01. DISCRETE STRUCTURES	PP	100	40	51	P C		11. ENGINEERING MATHEMATICS III	PP	100	40	40	P	
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	60	P C		12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	31*	P	
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	40	P C		13. DATA STRUCTURES	PP	100	40	50	P	
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	55	P C		14. COMPUTER GRAPHICS	PP	100	40	48	P	
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	51	P C		15. COMPUTER ORGANIZATION	PP	100	40	54	P	
06. PROGRAMMING LABORATORY	TW	25	10	15	P C		16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	32	P	
07. PROGRAMMING LABORATORY	PR	50	20	37	P		17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	42	P	
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	17	P C		18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	30	P	
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	25	P C		19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	32	P	
10. SOFT SKILLS	TW	50	20	29	P C		20. DATA STRUCTURES LABORATORY	TW	50	20	30	P	
							21. DATA STRUCTURES LABORATORY	PR	50	20	34	P	

GRAND TOTAL = 803/1500, RESULT: SECOND CLASS   * [O.4]

ORDN. 1 MARKS :

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S8054343   VEER ANJALI SUNDARDAS   URMILA   , 71132442F   , S8054343 ,   PICT   , S8054343

01. DISCRETE STRUCTURES	PP	100	40	53	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	60	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	51	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	54	P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	48	P C	13. DATA STRUCTURES	PP	100	40	77	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	73	P C	14. COMPUTER GRAPHICS	PP	100	40	55	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	72	P C	15. COMPUTER ORGANIZATION	PP	100	40	61	P
06. PROGRAMMING LABORATORY	TW	25	10	21	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	46	P
07. PROGRAMMING LABORATORY	PR	50	20	38	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	36	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	21	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	36	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	35	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	36	P
10. SOFT SKILLS	TW	50	20	40	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	43	P
						21. DATA STRUCTURES LABORATORY	PR	50	20	35	P

GRAND TOTAL = 991/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

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S8054344   VEERKAR PARTHA PRABODH   MEDHA   , 71045657D   , S8054344 ,   PICT   , S8054344

01. DISCRETE STRUCTURES	PP	100	40	63	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	25	F
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	47	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	31	F
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	58	P C	13. DATA STRUCTURES	PP	100	40	42	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	62	P C	14. COMPUTER GRAPHICS	PP	100	40	56	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	40	P C	15. COMPUTER ORGANIZATION	PP	100	40	40	P
06. PROGRAMMING LABORATORY	TW	25	10	22	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	42	P
07. PROGRAMMING LABORATORY	PR	50	20	42	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	38	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	19	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	42	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	27	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	36	P
10. SOFT SKILLS	TW	50	20	44	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	36	P
						21. DATA STRUCTURES LABORATORY	PR	50	20	42	P

GRAND TOTAL = 854/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

. . . . UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(COMPUTER) EXAMINATION MAY 2011 . . . .

DATE : 18 AUG. 2011   CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.   PAGE NO. 48 (412)

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.

        OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S8054345   WAJE ROHIT GITARAM   PARVATI   , 71045664G   , S8054345 , PICT   , S8054345

01. DISCRETE STRUCTURES	PP	100	40	73	P C		11. ENGINEERING MATHEMATICS III	PP	100	40	59	P	
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	61	P C		12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	40	P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	73	P C		13. DATA STRUCTURES	PP	100	40	60	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	75	P C		14. COMPUTER GRAPHICS	PP	100	40	49	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	62	P C		15. COMPUTER ORGANIZATION	PP	100	40	58	P
06. PROGRAMMING LABORATORY	TW	25	10	20	P C		16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	40	P
07. PROGRAMMING LABORATORY	PR	50	20	41	P C		17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	38	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	22	P C		18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	45	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	40	P C		19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	40	P
10. SOFT SKILLS	TW	50	20	38	P C		20. DATA STRUCTURES LABORATORY	TW	50	20	38	P
							21. DATA STRUCTURES LABORATORY	PR	50	20	35	P

GRAND TOTAL = 1007/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

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S8054346   ABNAVE VINAY PANDURANG   RANJANA   , 70925317G   , S8054346 , PICT   , S8054346

01. DISCRETE STRUCTURES	PP	100	40	51	P C		11. ENGINEERING MATHEMATICS III	PP	100	40	40	P C
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	56	P C		12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	40	P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	57	P C		13. DATA STRUCTURES	PP	100	40	55	P C
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	47	P C		14. COMPUTER GRAPHICS	PP	100	40	45	P C

05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	51	P C	15. COMPUTER ORGANIZATION	PP	100	40	44	P C
06. PROGRAMMING LABORATORY	TW	25	10	23	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	42	P C
07. PROGRAMMING LABORATORY	PR	50	20	39	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	44	P C
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	19	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	40	P C
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	34	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	36	P C
10. SOFT SKILLS	TW	50	20	40	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	41	P C
						21. DATA STRUCTURES LABORATORY	PR	50	20	37	P C

GRAND TOTAL = 881/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

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S8054347   AMLER VIPUL DHARMA   MEENA   , 71072123E   , S8054347 ,   PICT   , S8054347

01. DISCRETE STRUCTURES	PP	100	40	56	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	58	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	67	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	52	P C
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	76	P C	13. DATA STRUCTURES	PP	100	40	59	P C
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	62	P C	14. COMPUTER GRAPHICS	PP	100	40	64	P C
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	58	P C	15. COMPUTER ORGANIZATION	PP	100	40	47	P C
06. PROGRAMMING LABORATORY	TW	25	10	21	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	43	P C
07. PROGRAMMING LABORATORY	PR	50	20	40	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	43	P C
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	19	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	43	P C
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	35	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	40	P C
10. SOFT SKILLS	TW	50	20	41	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	43	P C
						21. DATA STRUCTURES LABORATORY	PR	50	20	43	P C

GRAND TOTAL = 1010/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

. . . .
UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(COMPUTER) EXAMINATION MAY 2011

cols05

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S8054349   ARASHDEEP SINGH HEIR   RUPINDER   , 70925338K   , S8054349 ,   PICT   , S8054349

01. DISCRETE STRUCTURES	PP	100	40	46	P C		11. ENGINEERING MATHEMATICS III	PP	100	40	40	P C
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	44	P C		12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	18	F
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	40	P C		13. DATA STRUCTURES	PP	100	40	40	P C
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	40	P C		14. COMPUTER GRAPHICS	PP	100	40	43	P C
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	40	P C		15. COMPUTER ORGANIZATION	PP	100	40	40	P
06. PROGRAMMING LABORATORY	TW	25	10	17	P C		16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	22	P C
07. PROGRAMMING LABORATORY	PR	50	20	25	P C		17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	35	P C
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	14	P C		18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	24	P C
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	28	P C		19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	30	P C
10. SOFT SKILLS	TW	50	20	31	P C		20. DATA STRUCTURES LABORATORY	TW	50	20	22	P C
							21. DATA STRUCTURES LABORATORY	PR	50	20	28	P C

GRAND TOTAL = 667/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

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S8054355   GADODIA ANUSHREE RAJENDRA   GEETA   , 71072126K   , S8054355 ,   PICT   , S8054355

01. DISCRETE STRUCTURES	PP	100	40	40	P C		11. ENGINEERING MATHEMATICS III	PP	100	40	27	F
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	40	P C		12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	42	P C
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	49	P C		13. DATA STRUCTURES	PP	100	40	49	P C
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	43	P C		14. COMPUTER GRAPHICS	PP	100	40	49	P C
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	43	P C		15. COMPUTER ORGANIZATION	PP	100	40	41	P C
06. PROGRAMMING LABORATORY	TW	25	10	19	P C		16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	36	P C
07. PROGRAMMING LABORATORY	PR	50	20	30	P C		17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	28	P C
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	19	P C		18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	33	P C
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	29	P C		19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	20	P C

10. SOFT SKILLS	TW	50	20	42	P C		20. DATA STRUCTURES LABORATORY	TW	50	20	32	P C
							21. DATA STRUCTURES LABORATORY	PR	50	20	10	F

GRAND TOTAL = 721/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

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S8054360   HARKARAN SINGH ANAND   HARPREET   , 70925435M   , S8054360 ,   PICT   , S8054360

01. DISCRETE STRUCTURES	PP	100	40	43	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	AA	F
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	47	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	40	P C
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	40	P C	13. DATA STRUCTURES	PP	100	40	40	P C
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	47	P C	14. COMPUTER GRAPHICS	PP	100	40	57	P C
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	40	P C	15. COMPUTER ORGANIZATION	PP	100	40	40	P C
06. PROGRAMMING LABORATORY	TW	25	10	16	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	28	P C
07. PROGRAMMING LABORATORY	PR	50	20	38	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	37	P C
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	13	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	23	P C
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	28	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	25	P C
10. SOFT SKILLS	TW	50	20	32	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	25	P C
						21. DATA STRUCTURES LABORATORY	PR	50	20	AA	F

GRAND TOTAL = 659/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

. . . . UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(COMPUTER) EXAMINATION MAY 2011 . . . .

DATE : 18 AUG. 2011   CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.   PAGE NO. 50 (414)

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.

         OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S8054361   JACHAK SURAJ GODHIRAM   SHOBHA   , 70925444L   , S8054361 ,   PICT   , S8054361

01. DISCRETE STRUCTURES	PP	100	40	46	P C		11. ENGINEERING MATHEMATICS III	PP	100	40	56	P C
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	40	P C		12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	46	P C
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	49	P C		13. DATA STRUCTURES	PP	100	40	49	P C
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	40	P C		14. COMPUTER GRAPHICS	PP	100	40	65	P C
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	50	P C		15. COMPUTER ORGANIZATION	PP	100	40	40	P C
06. PROGRAMMING LABORATORY	TW	25	10	19	P C		16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	38	P C
07. PROGRAMMING LABORATORY	PR	50	20	35	P C		17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	40	P C
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	19	P C		18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	38	P C
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	27	P C		19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	35	P
10. SOFT SKILLS	TW	50	20	38	P C		20. DATA STRUCTURES LABORATORY	TW	50	20	40	P C
							21. DATA STRUCTURES LABORATORY	PR	50	20	33	P C

GRAND TOTAL = 843/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

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S8054364   KADAM SHRINIWAS MUKUNDRAO   URMILA   , 70925458L   , S8054364 ,   PICT   , S8054364

01. DISCRETE STRUCTURES	PP	100	40	57	P C		11. ENGINEERING MATHEMATICS III	PP	100	40	41	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	52	P C		12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	40	P C
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	46	P C		13. DATA STRUCTURES	PP	100	40	52	P C
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	40	P C		14. COMPUTER GRAPHICS	PP	100	40	40	P C
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	51	P C		15. COMPUTER ORGANIZATION	PP	100	40	47	P
06. PROGRAMMING LABORATORY	TW	25	10	19	P C		16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	34	P C
07. PROGRAMMING LABORATORY	PR	50	20	27	P C		17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	35	P C
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	19	P C		18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	37	P C
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	20	P C		19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	21	P C
10. SOFT SKILLS	TW	50	20	39	P C		20. DATA STRUCTURES LABORATORY	TW	50	20	28	P C
							21. DATA STRUCTURES LABORATORY	PR	50	20	20	P C

GRAND TOTAL = 765/1500, RESULT: SECOND CLASS

ORDN. 1 MARKS :

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S8054366   KANDEKAR AKSHAY POPATRAO   MANGAL   , 71072129D   , S8054366 ,   PICT   , S8054366

01. DISCRETE STRUCTURES	PP	100	40	40	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	12	F
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	52	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	52	P C
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	61	P C	13. DATA STRUCTURES	PP	100	40	64	P C
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	64	P C	14. COMPUTER GRAPHICS	PP	100	40	54	P C
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	57	P C	15. COMPUTER ORGANIZATION	PP	100	40	50	P C
06. PROGRAMMING LABORATORY	TW	25	10	21	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	40	P C
07. PROGRAMMING LABORATORY	PR	50	20	38	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	42	P C
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	19	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	42	P C
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	36	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	35	P C
10. SOFT SKILLS	TW	50	20	41	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	39	P C
						21. DATA STRUCTURES LABORATORY	PR	50	20	37	P C

GRAND TOTAL = 896/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

. . . .
UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(COMPUTER) EXAMINATION MAY 2011
    DATE : 18 AUG. 2011   CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.   PAGE NO. 51 (415)
. . . .
NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.
        OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S8054367   KATDARE SANCHIT VINAYAK   ALKA   , 70925470K   , S8054367 ,   PICT   , S8054367

01. DISCRETE STRUCTURES	PP	100	40	70	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	40	P C
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	40	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	40	P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	56	P C	13. DATA STRUCTURES	PP	100	40	58	P C
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	40	P C	14. COMPUTER GRAPHICS	PP	100	40	65	P C
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	47	P C	15. COMPUTER ORGANIZATION	PP	100	40	44	P C

06. PROGRAMMING LABORATORY	TW	25	10	18	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	43	P C
07. PROGRAMMING LABORATORY	PR	50	20	38	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	38	P C
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	21	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	40	P C
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	40	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	40	P C
10. SOFT SKILLS	TW	50	20	42	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	44	P C
						21. DATA STRUCTURES LABORATORY	PR	50	20	37	P C

GRAND TOTAL = 901/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

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S8054368   KAVITKE INDRAJEET ARVIND   SUREKHA   , 70925471H   , S8054368 ,   PICT   , S8054368

01. DISCRETE STRUCTURES	PP	100	40	57	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	20	F
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	40	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	46	P C
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	56	P C	13. DATA STRUCTURES	PP	100	40	52	P C
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	51	P C	14. COMPUTER GRAPHICS	PP	100	40	64	P C
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	66	P C	15. COMPUTER ORGANIZATION	PP	100	40	44	P C
06. PROGRAMMING LABORATORY	TW	25	10	16	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	36	P C
07. PROGRAMMING LABORATORY	PR	50	20	39	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	44	P C
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	21	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	38	P C
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	34	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	35	P C
10. SOFT SKILLS	TW	50	20	35	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	36	P C
						21. DATA STRUCTURES LABORATORY	PR	50	20	36	P C

GRAND TOTAL = 866/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

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S8054370   KHAIRNAR PANKAJ VASUDEO   PATNAMALA   , 70715931J   , S8054370 ,   PICT   , S8054370

01. DISCRETE STRUCTURES	PP	100	40	AA	F	11. ENGINEERING MATHEMATICS III	PP	100	40	55	P

02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	AA	F		12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	40	P C
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	AA	F		13. DATA STRUCTURES	PP	100	40	53	P C
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	AA	F		14. COMPUTER GRAPHICS	PP	100	40	48	P C
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	AA	F		15. COMPUTER ORGANIZATION	PP	100	40	56	P C
06. PROGRAMMING LABORATORY	TW	25	10	AA	F		16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	23	P C
07. PROGRAMMING LABORATORY	PR	50	20	AA	F		17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	37	P C
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	AA	F		18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	25	P C
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	AA	F		19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	AA	F
10. SOFT SKILLS	TW	50	20	AA	F		20. DATA STRUCTURES LABORATORY	TW	50	20	20	P C
							21. DATA STRUCTURES LABORATORY	PR	50	20	AA	F

GRAND TOTAL = 357/1500, RESULT: FAILS   RESULT RESERVED FOR OTHR

ORDN. 1 MARKS :

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.

       OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S8054371   KHALID RAZA KHAN   AYESHA   , 70925473D   , S8054371 , PICT   , S8054371

01. DISCRETE STRUCTURES	PP	100	40	40	P C	
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	AA	F	
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	AA	F	
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	40	P C	
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	AA	F	
06. PROGRAMMING LABORATORY	TW	25	10	10	P C	
07. PROGRAMMING LABORATORY	PR	50	20	25	P	
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	10	P C	
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	23	P	
10. SOFT SKILLS	TW	50	20	20	P C	

FIRST TERM TOTAL = 168/700.

ORDN. 1 MARKS :

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S8054372   KHUPASE SANJANA UDAYRAO   ALKA   , 71072131F   , S8054372 ,   PICT   , S8054372

01. DISCRETE STRUCTURES	PP	100	40	40	P C		11. ENGINEERING MATHEMATICS III	PP	100	40	40	P	
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	49	P C		12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	54	P C
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	48	P C		13. DATA STRUCTURES	PP	100	40	55	P C
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	62	P C		14. COMPUTER GRAPHICS	PP	100	40	62	P C
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	58	P C		15. COMPUTER ORGANIZATION	PP	100	40	44	P C
06. PROGRAMMING LABORATORY	TW	25	10	20	P C		16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	37	P C
07. PROGRAMMING LABORATORY	PR	50	20	33	P C		17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	38	P C
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	21	P C		18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	40	P C
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	35	P C		19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	30	P C
10. SOFT SKILLS	TW	50	20	38	P C		20. DATA STRUCTURES LABORATORY	TW	50	20	39	P C
							21. DATA STRUCTURES LABORATORY	PR	50	20	28	P C

GRAND TOTAL = 871/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

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S8054374   KONDHAWALE PRASHANT MHATARABA   MANGAL   , 70925487D   , S8054374 ,   PICT   , S8054374

01. DISCRETE STRUCTURES	PP	100	40	40	P C		11. ENGINEERING MATHEMATICS III	PP	100	40	43	P C
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	42	P C		12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	40	P C
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	40	P C		13. DATA STRUCTURES	PP	100	40	40	P C
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	40	P		14. COMPUTER GRAPHICS	PP	100	40	40	P C
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	54	P C		15. COMPUTER ORGANIZATION	PP	100	40	46	P C
06. PROGRAMMING LABORATORY	TW	25	10	19	P C		16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	32	P C

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07. PROGRAMMING LABORATORY          PR   50   20   26  P C   17. O. O. PROG. & COMP. GRAPH. LAB   PR   50   20   32  P C

08. DIGITAL ELECTRONICS LABORATORY  TW   25   10   19  P C   18. MICROPROCESSORS & INTERFACING LABTW   50   20   31  P C

09. DIGITAL ELECTRONICS LABORATORY  PR   50   20   25  P C   19. MICROPROCESSORS & INTERFACING LABPR   50   20   38  P

10. SOFT SKILLS                     TW   50   20   40  P C   20. DATA STRUCTURES LABORATORY       TW   50   20   24  P C

                                                            21. DATA STRUCTURES LABORATORY       PR   50   20   34  P
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GRAND TOTAL = 745+05/1500, RESULT: SECOND CLASS [O.2]

ORDN. 1 MARKS :

. . . . UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(COMPUTER) EXAMINATION MAY 2011 . . . .

    DATE : 18 AUG. 2011   CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.   PAGE NO. 53 (417)
. . . .

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.

        OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S8054376   LATIFI AAMASH IMTIYAZ AHMED   SHAKERA   , 70925492L   , S8054376 ,   PICT   , S8054376

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01. DISCRETE STRUCTURES             PP  100   40   40  P C   11. ENGINEERING MATHEMATICS III      PP  100   40   40  P C

02. PROGRAMMING & PROBLEM SOLVING   PP  100   40   43  P C   12. MICROPROC. & INTERFACING TECHNIQ.PP  100   40   24  F

03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100   40   50  P C   13. DATA STRUCTURES                  PP  100   40   48  P C

04. DATA STRUCTURES AND ALGORITHMS  PP  100   40   40  P C   14. COMPUTER GRAPHICS                PP  100   40   54  P C

05. HUMANITIES AND SOCIAL SCIENCE   PP  100   40   46  P C   15. COMPUTER ORGANIZATION            PP  100   40   44  P

06. PROGRAMMING LABORATORY          TW   25   10   12  P C   16. O. O. PROG. & COMP. GRAPH. LAB   TW   50   20   26  P C

07. PROGRAMMING LABORATORY          PR   50   20   35  P C   17. O. O. PROG. & COMP. GRAPH. LAB   PR   50   20   35  P C

08. DIGITAL ELECTRONICS LABORATORY  TW   25   10   10  P C   18. MICROPROCESSORS & INTERFACING LABTW   50   20   23  P C

09. DIGITAL ELECTRONICS LABORATORY  PR   50   20   20  P C   19. MICROPROCESSORS & INTERFACING LABPR   50   20   28  P

10. SOFT SKILLS                     TW   50   20   32  P C   20. DATA STRUCTURES LABORATORY       TW   50   20   22  P C

                                                            21. DATA STRUCTURES LABORATORY       PR   50   20   30  P C
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GRAND TOTAL = 702/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

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S8054377   MAHAJAN VAIBHAV ANUP   ARTI   , 71072133B   , S8054377 ,   PICT   , S8054377

01. DISCRETE STRUCTURES	PP	100	40	40	P C	11. ENGINEERING MATHEMATICS III	PP 100 40 40 P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	42	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP 100 40 40 P C
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	65	P C	13. DATA STRUCTURES	PP 100 40 60 P C
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	48	P C	14. COMPUTER GRAPHICS	PP 100 40 58 P C
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	57	P C	15. COMPUTER ORGANIZATION	PP 100 40 40 P C
06. PROGRAMMING LABORATORY	TW	25	10	19	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW 50 20 36 P C
07. PROGRAMMING LABORATORY	PR	50	20	45	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR 50 20 38 P C
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	19	P C	18. MICROPROCESSORS & INTERFACING LAB	TW 50 20 40 P C
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	25	P C	19. MICROPROCESSORS & INTERFACING LAB	PR 50 20 20 P C
10. SOFT SKILLS	TW	50	20	40	P C	20. DATA STRUCTURES LABORATORY	TW 50 20 34 P C
						21. DATA STRUCTURES LABORATORY	PR 50 20 21 P C

GRAND TOTAL = 827/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

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S8054378   MANE OM ARUN   RAJANI   , 70925499H   , S8054378 ,   PICT   , S8054378

01. DISCRETE STRUCTURES	PP	100	40	40	P C	11. ENGINEERING MATHEMATICS III	PP 100 40 56 P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	40	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP 100 40 26 F
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	43	P C	13. DATA STRUCTURES	PP 100 40 40 P C
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	40	P C	14. COMPUTER GRAPHICS	PP 100 40 40 P C
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	50	P C	15. COMPUTER ORGANIZATION	PP 100 40 40 P C
06. PROGRAMMING LABORATORY	TW	25	10	14	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW 50 20 20 P C
07. PROGRAMMING LABORATORY	PR	50	20	39	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR 50 20 30 P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	12	P C	18. MICROPROCESSORS & INTERFACING LAB	TW 50 20 20 P C
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	20	P C	19. MICROPROCESSORS & INTERFACING LAB	PR 50 20 32 P
10. SOFT SKILLS	TW	50	20	29	P C	20. DATA STRUCTURES LABORATORY	TW 50 20 20 P C
						21. DATA STRUCTURES LABORATORY	PR 50 20 22 P C

GRAND TOTAL = 673/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

. . . . UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(COMPUTER) EXAMINATION MAY 2011 . . . .

   DATE : 18 AUG. 2011   CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.   PAGE NO. 54 (418)

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 NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.

        OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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 S8054379   NANDKAR NEHA VIJAY   DIPALI   , 70925523D   , S8054379 , PICT   , S8054379

 01. DISCRETE STRUCTURES   PP   100   40   48 P C   11. ENGINEERING MATHEMATICS III   PP   100   40   42 P C

 02. PROGRAMMING & PROBLEM SOLVING PP   100   40   40 P C   12. MICROPROC. & INTERFACING TECHNIQ.PP   100   40   48 P C

 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP   100   40   58 P C   13. DATA STRUCTURES   PP   100   40   61 P C

 04. DATA STRUCTURES AND ALGORITHMS PP   100   40   52 P C   14. COMPUTER GRAPHICS   PP   100   40   65 P C

 05. HUMANITIES AND SOCIAL SCIENCE PP   100   40   62 P C   15. COMPUTER ORGANIZATION   PP   100   40   41 P C

 06. PROGRAMMING LABORATORY   TW   25   10   17 P C   16. O. O. PROG. & COMP. GRAPH. LAB TW   50   20   29 P C

 07. PROGRAMMING LABORATORY   PR   50   20   25 P C   17. O. O. PROG. & COMP. GRAPH. LAB PR   50   20   29 P C

 08. DIGITAL ELECTRONICS LABORATORY TW   25   10   17 P C   18. MICROPROCESSORS & INTERFACING LABTW   50   20   38 P C

 09. DIGITAL ELECTRONICS LABORATORY PR   50   20   30 P C   19. MICROPROCESSORS & INTERFACING LABPR   50   20   AA F

 10. SOFT SKILLS   TW   50   20   39 P C   20. DATA STRUCTURES LABORATORY   TW   50   20   24 P C

                                                               21. DATA STRUCTURES LABORATORY   PR   50   20   30 P C

GRAND TOTAL = 795/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

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 S8054380   PATIL HARSHAL PRAVINSING   SANDHYA   , 70925546C   , S8054380 , PICT   , S8054380

 01. DISCRETE STRUCTURES   PP   100   40   52 P C   11. ENGINEERING MATHEMATICS III   PP   100   40   40 P C

 02. PROGRAMMING & PROBLEM SOLVING PP   100   40   40 P C   12. MICROPROC. & INTERFACING TECHNIQ.PP   100   40   40 P C

03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	54	P C		13. DATA STRUCTURES	PP	100	40	52	P C
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	53	P C		14. COMPUTER GRAPHICS	PP	100	40	52	P C
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	42	P C		15. COMPUTER ORGANIZATION	PP	100	40	40	P C
06. PROGRAMMING LABORATORY	TW	25	10	13	P C		16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	24	P C
07. PROGRAMMING LABORATORY	PR	50	20	29	P C		17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	39	P C
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	18	P C		18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	28	P C
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	24	P C		19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	30	P
10. SOFT SKILLS	TW	50	20	38	P C		20. DATA STRUCTURES LABORATORY	TW	50	20	25	P C
							21. DATA STRUCTURES LABORATORY	PR	50	20	20	P C

GRAND TOTAL = 753/1500, RESULT: SECOND CLASS

ORDN. 1 MARKS :

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S8054381   PATIL VINEET JAYANT   PRAJAKTA   , 70601438D   , S8054381 ,   PICT   , S8054381

01. DISCRETE STRUCTURES	PP	100	40	AA	F		11. ENGINEERING MATHEMATICS III	PP	100	40	27	F
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	AA	F		12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	40	P C
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	AA	F		13. DATA STRUCTURES	PP	100	40	51	P C
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	AA	F		14. COMPUTER GRAPHICS	PP	100	40	59	P C
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	AA	F		15. COMPUTER ORGANIZATION	PP	100	40	48	P
06. PROGRAMMING LABORATORY	TW	25	10	AA	F		16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	30	P C
07. PROGRAMMING LABORATORY	PR	50	20	AA	F		17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	34	P C
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	AA	F		18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	30	P C
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	AA	F		19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	40	P C
10. SOFT SKILLS	TW	50	20	AA	F		20. DATA STRUCTURES LABORATORY	TW	50	20	29	P C
							21. DATA STRUCTURES LABORATORY	PR	50	20	20	P C

GRAND TOTAL = 408/1500, RESULT: FAILS   RESULT RESERVED FOR OTHR

ORDN. 1 MARKS :

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S8054382   RATHOD NIKHIL PRAVEEN   ASHA   , 70925576E   , S8054382 ,   PICT   , S8054382

01. DISCRETE STRUCTURES	PP	100	40	78	P C		11. ENGINEERING MATHEMATICS III	PP	100	40	43	P C
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	40	P C		12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	40	P C
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	52	P C		13. DATA STRUCTURES	PP	100	40	51	P C
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	40	P C		14. COMPUTER GRAPHICS	PP	100	40	43	P C
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	62	P C		15. COMPUTER ORGANIZATION	PP	100	40	40	P C
06. PROGRAMMING LABORATORY	TW	25	10	15	P C		16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	38	P C
07. PROGRAMMING LABORATORY	PR	50	20	25	P C		17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	35	P C
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	21	P C		18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	35	P C
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	27	P C		19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	36	P C
10. SOFT SKILLS	TW	50	20	36	P C		20. DATA STRUCTURES LABORATORY	TW	50	20	37	P C
							21. DATA STRUCTURES LABORATORY	PR	50	20	32	P

GRAND TOTAL = 826/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

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S8054383   ROKADE VISHALSAGAR DEVIDAS   VANDANA   , 70701610L   , S8054383 ,   PICT   , S8054383

01. DISCRETE STRUCTURES	PP	100	40	AA	F		11. ENGINEERING MATHEMATICS III	PP	100	40	51	P C
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	AA	F		12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	53	P C
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	AA	F		13. DATA STRUCTURES	PP	100	40	52	P C
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	AA	F		14. COMPUTER GRAPHICS	PP	100	40	51	P C
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	AA	F		15. COMPUTER ORGANIZATION	PP	100	40	45	P C
06. PROGRAMMING LABORATORY	TW	25	10	AA	F		16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	23	P C
07. PROGRAMMING LABORATORY	PR	50	20	AA	F		17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	20	P

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08. DIGITAL ELECTRONICS LABORATORY   TW   25   10   AA  F    18. MICROPROCESSORS & INTERFACING LABTW   50   20   28   P C
09. DIGITAL ELECTRONICS LABORATORY   PR   50   20   AA  F    19. MICROPROCESSORS & INTERFACING LABPR   50   20   30   P C
10. SOFT SKILLS                      TW   50   20   AA  F    20. DATA STRUCTURES LABORATORY      TW   50   20   22   P C
                                                             21. DATA STRUCTURES LABORATORY      PR   50   20   30   P
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GRAND TOTAL = 405/1500, RESULT: FAILS   RESULT RESERVED FOR OTHR

ORDN. 1 MARKS :

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S8054384   SARNAIK ABHISHEK MANOJ   KAVITA   , 70925592G   , S8054384 ,   PICT   , S8054384

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01. DISCRETE STRUCTURES              PP  100   40   52  P C   11. ENGINEERING MATHEMATICS III     PP  100   40   26   F
02. PROGRAMMING & PROBLEM SOLVING     PP  100   40   40  P C   12. MICROPROC. & INTERFACING TECHNIQ.PP  100   40   32   F
03. DIGIT. ELECTRONICS & LOGIC DESIGNPP  100   40   40  P C   13. DATA STRUCTURES                 PP  100   40   40   P C
04. DATA STRUCTURES AND ALGORITHMS    PP  100   40   41  P C   14. COMPUTER GRAPHICS               PP  100   40   41   P C
05. HUMANITIES AND SOCIAL SCIENCE     PP  100   40   48  P C   15. COMPUTER ORGANIZATION           PP  100   40   40   P C
06. PROGRAMMING LABORATORY            TW   25   10   17  P C   16. O. O. PROG. & COMP. GRAPH. LAB   TW   50   20   26   P C
07. PROGRAMMING LABORATORY            PR   50   20   26  P C   17. O. O. PROG. & COMP. GRAPH. LAB   PR   50   20   43   P C
08. DIGITAL ELECTRONICS LABORATORY    TW   25   10   18  P C   18. MICROPROCESSORS & INTERFACING LABTW   50   20   28   P C
09. DIGITAL ELECTRONICS LABORATORY    PR   50   20   30  P C   19. MICROPROCESSORS & INTERFACING LABPR   50   20   22   P C
10. SOFT SKILLS                      TW   50   20   34  P C   20. DATA STRUCTURES LABORATORY      TW   50   20   25   P C
                                                             21. DATA STRUCTURES LABORATORY      PR   50   20   28   P C
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GRAND TOTAL = 697/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.

       OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S8054385   SAURAV JAUHARI   POONAM   , 70925593E   , S8054385 ,   PICT   , S8054385

01. DISCRETE STRUCTURES	PP	100	40	AA	F	11. ENGINEERING MATHEMATICS III	PP	100	40	AA	F
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	AA	F	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	AA	F
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	AA	F	13. DATA STRUCTURES	PP	100	40	AA	F
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	AA	F	14. COMPUTER GRAPHICS	PP	100	40	AA	F
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	AA	F	15. COMPUTER ORGANIZATION	PP	100	40	AA	F
06. PROGRAMMING LABORATORY	TW	25	10	13	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	20	P C
07. PROGRAMMING LABORATORY	PR	50	20	05	F	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	20	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	10	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	20	P C
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	AA	F	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	00	F
10. SOFT SKILLS	TW	50	20	26	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	20	P C
						21. DATA STRUCTURES LABORATORY	PR	50	20	00	F

GRAND TOTAL = 134/1500, RESULT: FAILS

ORDN. 1 MARKS :

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S8054386   SHANTANU SINGHAL SHIKHA   SHIKHA   , 70504046B   , S8054386 ,   PICT   , S8054386

01. DISCRETE STRUCTURES	PP	100	40	AA	F	11. ENGINEERING MATHEMATICS III	PP	100	40	40	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	AA	F	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	40	P C
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	AA	F	13. DATA STRUCTURES	PP	100	40	51	P C
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	AA	F	14. COMPUTER GRAPHICS	PP	100	40	48	P C
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	AA	F	15. COMPUTER ORGANIZATION	PP	100	40	40	P C
06. PROGRAMMING LABORATORY	TW	25	10	AA	F	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	20	P C
07. PROGRAMMING LABORATORY	PR	50	20	AA	F	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	36	P C
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	AA	F	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	20	P C
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	AA	F	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	30	P C
10. SOFT SKILLS	TW	50	20	AA	F	20. DATA STRUCTURES LABORATORY	TW	50	20	20	P C
						21. DATA STRUCTURES LABORATORY	PR	50	20	36	P C

GRAND TOTAL = 381/1500, RESULT: FAILS   RESULT RESERVED FOR OTHR

ORDN. 1 MARKS :

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S8054387   SHASHANK KUMAR GUPTA   ANJANI   , 70925598F   , S8054387 , PICT   , S8054387

01. DISCRETE STRUCTURES	PP	100	40	47	P C		11. ENGINEERING MATHEMATICS III	PP	100	40	42	P C
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	40	P C		12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	AA	F
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	AA	F		13. DATA STRUCTURES	PP	100	40	47	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	40	P C		14. COMPUTER GRAPHICS	PP	100	40	48	P C
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	62	P C		15. COMPUTER ORGANIZATION	PP	100	40	49	P
06. PROGRAMMING LABORATORY	TW	25	10	12	P C		16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	23	P C
07. PROGRAMMING LABORATORY	PR	50	20	32	P C		17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	35	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	10	P C		18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	20	P C
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	22	P C		19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	34	P C
10. SOFT SKILLS	TW	50	20	21	P C		20. DATA STRUCTURES LABORATORY	TW	50	20	20	P C
							21. DATA STRUCTURES LABORATORY	PR	50	20	09	F

GRAND TOTAL = 613/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

. . . . UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(COMPUTER) EXAMINATION MAY 2011 . . . .

    DATE : 18 AUG. 2011   CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.   PAGE NO. 57 (421)

. . . .

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.

        OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

. . . .

S8054388   SHERKAR ANIRUDHA UMAKANT   SMITA   , 70925601K   , S8054388 , PICT   , S8054388

01. DISCRETE STRUCTURES	PP	100	40	40	P C		11. ENGINEERING MATHEMATICS III	PP	100	40	19	F
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	40	P C		12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	26	F
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	49	P C		13. DATA STRUCTURES	PP	100	40	40	P C

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04. DATA STRUCTURES AND ALGORITHMS   PP   100   40   40  P C      14. COMPUTER GRAPHICS                  PP   100   40   40  P C
05. HUMANITIES AND SOCIAL SCIENCE    PP   100   40   50  P C      15. COMPUTER ORGANIZATION             PP   100   40   26  F
06. PROGRAMMING LABORATORY           TW    25   10   16  P C      16. O. O. PROG. & COMP. GRAPH. LAB    TW    50   20   39  P C
07. PROGRAMMING LABORATORY           PR    50   20   41  P C      17. O. O. PROG. & COMP. GRAPH. LAB    PR    50   20   36  P C
08. DIGITAL ELECTRONICS LABORATORY   TW    25   10   18  P C      18. MICROPROCESSORS & INTERFACING LABTW    50   20   39  P C
09. DIGITAL ELECTRONICS LABORATORY   PR    50   20   27  P C      19. MICROPROCESSORS & INTERFACING LABPR    50   20   20  P C
10. SOFT SKILLS                      TW    50   20   38  P C      20. DATA STRUCTURES LABORATORY        TW    50   20   36  P C
                                                                  21. DATA STRUCTURES LABORATORY        PR    50   20   30  P C
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GRAND TOTAL = 710/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

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S8054394   THAKARE SAGAR SATISH   JAYASHRI   , 70925632K   , S8054394 ,   PICT   , S8054394

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01. DISCRETE STRUCTURES              PP   100   40   40  P C      11. ENGINEERING MATHEMATICS III       PP   100   40   20  F
02. PROGRAMMING & PROBLEM SOLVING    PP   100   40   40  P C      12. MICROPROC. & INTERFACING TECHNIQ.PP   100   40   40  P C
03. DIGIT. ELECTRONICS & LOGIC DESIGNPP   100   40   26  F        13. DATA STRUCTURES                   PP   100   40   40  P C
04. DATA STRUCTURES AND ALGORITHMS   PP   100   40   40  P C      14. COMPUTER GRAPHICS                  PP   100   40   40  P C
05. HUMANITIES AND SOCIAL SCIENCE    PP   100   40   48  P C      15. COMPUTER ORGANIZATION             PP   100   40   40  P
06. PROGRAMMING LABORATORY           TW    25   10   17  P C      16. O. O. PROG. & COMP. GRAPH. LAB    TW    50   20   32  P C
07. PROGRAMMING LABORATORY           PR    50   20   20  P C      17. O. O. PROG. & COMP. GRAPH. LAB    PR    50   20   28  P C
08. DIGITAL ELECTRONICS LABORATORY   TW    25   10   19  P C      18. MICROPROCESSORS & INTERFACING LABTW    50   20   33  P C
09. DIGITAL ELECTRONICS LABORATORY   PR    50   20   21  P C      19. MICROPROCESSORS & INTERFACING LABPR    50   20   25  P C
10. SOFT SKILLS                      TW    50   20   25  P C      20. DATA STRUCTURES LABORATORY        TW    50   20   28  P C
                                                                  21. DATA STRUCTURES LABORATORY        PR    50   20   04  F
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GRAND TOTAL = 626/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

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S8054395   UIKEY ANURAG VINAYAK   JYOTI   , 70925640L   , S8054395 ,   PICT   , S8054395

01. DISCRETE STRUCTURES	PP	100	40	40	P C		11. ENGINEERING MATHEMATICS III	PP	100	40	AA	F
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	AA	F		12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	AA	F
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	47	P C		13. DATA STRUCTURES	PP	100	40	AA	F
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	40	P C		14. COMPUTER GRAPHICS	PP	100	40	AA	F
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	62	P C		15. COMPUTER ORGANIZATION	PP	100	40	AA	F
06. PROGRAMMING LABORATORY	TW	25	10	15	P C		16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	24	P
07. PROGRAMMING LABORATORY	PR	50	20	15	F		17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	06	F
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	11	P C		18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	30	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	24	P		19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	30	P
10. SOFT SKILLS	TW	50	20	38	P C		20. DATA STRUCTURES LABORATORY	TW	50	20	20	P
							21. DATA STRUCTURES LABORATORY	PR	50	20	10	F

GRAND TOTAL = 412/1500, RESULT: FAILS   RESULT RESERVED FOR BKLG + OTHR

ORDN. 1 MARKS :

. . . . UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(COMPUTER) EXAMINATION MAY 2011 . . . .

   DATE : 18 AUG. 2011   CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.   PAGE NO. 58 (422)

. . . .

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.

        OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S8054397   VAIRAGE ROHIT VISHNU   RAJASHRI   , 70925643E   , S8054397 ,   PICT   , S8054397

01. DISCRETE STRUCTURES	PP	100	40	51	P C		11. ENGINEERING MATHEMATICS III	PP	100	40	AA	F
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	40	P C		12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	AA	F
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	48	P C		13. DATA STRUCTURES	PP	100	40	41	P C
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	40	P C		14. COMPUTER GRAPHICS	PP	100	40	46	P C
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	55	P C		15. COMPUTER ORGANIZATION	PP	100	40	AA	F
06. PROGRAMMING LABORATORY	TW	25	10	16	P C		16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	34	P C
07. PROGRAMMING LABORATORY	PR	50	20	24	P C		17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	20	P C
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	16	P C		18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	20	P C

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09. DIGITAL ELECTRONICS LABORATORY   PR   50   20   31  P C    19. MICROPROCESSORS & INTERFACING LABPR   50   20   AA  F
10. SOFT SKILLS                      TW   50   20   40  P C    20. DATA STRUCTURES LABORATORY       TW   50   20   20  P C
                                                              21. DATA STRUCTURES LABORATORY       PR   50   20   AA  F
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GRAND TOTAL = 542/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

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S8054399   SANDEEP AGARWAL                  RITA                 , 70925587L    , S8054315 ,   PICT      , S8054399

01. DISCRETE STRUCTURES              PP  100   40   59  P C    11. ENGINEERING MATHEMATICS III      PP  100   40   53  P C
02. PROGRAMMING & PROBLEM SOLVING    PP  100   40   40  P C    12. MICROPROC. & INTERFACING TECHNIQ.PP  100   40   12  F
03. DIGIT. ELECTRONICS & LOGIC DESIGNPP  100   40   47  P C    13. DATA STRUCTURES                  PP  100   40   43  P C
04. DATA STRUCTURES AND ALGORITHMS   PP  100   40   40  P C    14. COMPUTER GRAPHICS                PP  100   40   01  F
05. HUMANITIES AND SOCIAL SCIENCE    PP  100   40   52  P C    15. COMPUTER ORGANIZATION            PP  100   40   15  F
06. PROGRAMMING LABORATORY           TW   25   10   20  P C    16. O. O. PROG. & COMP. GRAPH. LAB   TW   50   20   30  P C
07. PROGRAMMING LABORATORY           PR   50   20   30  P C    17. O. O. PROG. & COMP. GRAPH. LAB   PR   50   20   24  P C
08. DIGITAL ELECTRONICS LABORATORY   TW   25   10   17  P C    18. MICROPROCESSORS & INTERFACING LABTW   50   20   32  P C
09. DIGITAL ELECTRONICS LABORATORY   PR   50   20   25  P C    19. MICROPROCESSORS & INTERFACING LABPR   50   20   20  P
10. SOFT SKILLS                      TW   50   20   40  P C    20. DATA STRUCTURES LABORATORY       TW   50   20   22  P C
                                                              21. DATA STRUCTURES LABORATORY       PR   50   20   12  F
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GRAND TOTAL = 634/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

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S8054400   POORVI ARVIND DHARWAD            ROHINI               , 70925566H    , S8054306 ,   PICT      , S8054400

01. DISCRETE STRUCTURES              PP  100   40   40  P      11. ENGINEERING MATHEMATICS III      PP  100   40   11  F
02. PROGRAMMING & PROBLEM SOLVING    PP  100   40   40  P C    12. MICROPROC. & INTERFACING TECHNIQ.PP  100   40   31  F
03. DIGIT. ELECTRONICS & LOGIC DESIGNPP  100   40   40  P C    13. DATA STRUCTURES                  PP  100   40   40  P C
04. DATA STRUCTURES AND ALGORITHMS   PP  100   40   31  F      14. COMPUTER GRAPHICS                PP  100   40   40  P C
05. HUMANITIES AND SOCIAL SCIENCE    PP  100   40   40  P C    15. COMPUTER ORGANIZATION            PP  100   40   41  P
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06. PROGRAMMING LABORATORY	TW	25	10	14	P C		16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	23	P C
07. PROGRAMMING LABORATORY	PR	50	20	28	P C		17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	24	P C
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	19	P C		18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	25	P C
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	26	P C		19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	30	P
10. SOFT SKILLS	TW	50	20	32	P C		20. DATA STRUCTURES LABORATORY	TW	50	20	22	P C
							21. DATA STRUCTURES LABORATORY	PR	50	20	11	F

GRAND TOTAL = 608/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

. . . .
                        UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(INFORMATION TECHNOLOGY)
     DATE : 18 AUG. 2011   CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.   PAGE NO. 01 (423)
. . . .
 NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.
          OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
. . . .

 S8058501   AGHAV ISHWARI JAGANNATH   VAISHALI   , 71045363K   , S8058501 , PICT   , S8058501

01. DISCRETE STRUCTURES	PP	100	40	40	P C		11. ENG MATHS III	PP	100	40	40	P
02. COMPUTER ORGANIZATION	PP	100	40	54	P C		12. COMPUTER GRAPHICS	PP	100	40	63	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	54	P C		13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	55	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	59	P C		14. DATA STRUCTURES AND FILES	PP	100	40	64	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	61	P C		15. DATA COMMUNICATION	PP	100	40	66	P
06. DIGITAL LABORATORY	TW	50	20	33	P C		16. PROCESSOR INTERFACING LABORATORY	TW	25	10	20	P
07. DIGITAL LABORATORY	PR	50	20	35	P C		17. PROCESSOR INTERFACING LABORATORY	OR	50	20	35	P
08. PROGRAMMING LABORATORY	TW	50	20	38	P C		18. DATA STRUCTURES AND FILES LAB	TW	25	10	16	P
09. PROGRAMMING LABORATORY	PR	50	20	39	P C		19. DATA STRUCTURES AND FILES LAB	PR	50	20	35	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	36	P C		20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	42	P
							21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	20	P

GRAND TOTAL = 905+05/1500, RESULT: FIRST CLASS[O.163]

ORDN. 1 MARKS :

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S8058502   AGRAWAL NILIMA OMPRAKASH   LATA   , 71134941L   , S8058502 , PICT   , S8058502

01. DISCRETE STRUCTURES	PP	100	40	46	P C	11. ENG MATHS III	PP	100	40	40	P
02. COMPUTER ORGANIZATION	PP	100	40	40	P C	12. COMPUTER GRAPHICS	PP	100	40	56	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	43	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	54	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	52	P C	14. DATA STRUCTURES AND FILES	PP	100	40	51	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	51	P C	15. DATA COMMUNICATION	PP	100	40	55	P
06. DIGITAL LABORATORY	TW	50	20	30	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	21	P
07. DIGITAL LABORATORY	PR	50	20	25	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	38	P
08. PROGRAMMING LABORATORY	TW	50	20	41	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	20	P
09. PROGRAMMING LABORATORY	PR	50	20	27	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	15*	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	40	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	44	P
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	32	P

GRAND TOTAL = 821/1500, RESULT: SECOND CLASS   * [O.4]

ORDN. 1 MARKS :

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S8058503   AHER SHUBHAM SURESH   LATA   , 71134942J   , S8058503 , PICT   , S8058503

01. DISCRETE STRUCTURES	PP	100	40	52	P	11. ENG MATHS III	PP	100	40	40	P
02. COMPUTER ORGANIZATION	PP	100	40	50	P C	12. COMPUTER GRAPHICS	PP	100	40	60	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	65	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	61	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	53	P C	14. DATA STRUCTURES AND FILES	PP	100	40	63	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	58	P C	15. DATA COMMUNICATION	PP	100	40	72	P
06. DIGITAL LABORATORY	TW	50	20	37	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	23	P
07. DIGITAL LABORATORY	PR	50	20	27	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	40	P
08. PROGRAMMING LABORATORY	TW	50	20	39	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	20	P
09. PROGRAMMING LABORATORY	PR	50	20	38	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	40	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	41	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	45	P

21. OBJECT ORIENTED PROGRAMMING LAB PR   50   20   42 P

GRAND TOTAL = 966/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

. . . .
UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(INFORMATION TECHNOLOGY)
DATE : 18 AUG. 2011   CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.   PAGE NO. 02 (424)
. . . .

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S8058504   ALWAL ANNAPURNA RAMESH   RENUKA   , 71134943G   , S8058504 , PICT   , S8058504

01. DISCRETE STRUCTURES	PP	100	40	40	P C	11. ENG MATHS III	PP	100	40	40	P
02. COMPUTER ORGANIZATION	PP	100	40	54	P C	12. COMPUTER GRAPHICS	PP	100	40	61	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	60	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	51	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	46	P C	14. DATA STRUCTURES AND FILES	PP	100	40	55	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	59	P C	15. DATA COMMUNICATION	PP	100	40	77	P
06. DIGITAL LABORATORY	TW	50	20	28	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	22	P
07. DIGITAL LABORATORY	PR	50	20	30	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	35	P
08. PROGRAMMING LABORATORY	TW	50	20	36	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	17	P
09. PROGRAMMING LABORATORY	PR	50	20	40	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	39	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	33	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	43	P
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	40	P

GRAND TOTAL = 906/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

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S8058505   ANKIT SHARMA   ANITA   , 71045372J   , S8058505 , PICT   , S8058505

| 01. DISCRETE STRUCTURES | PP | 100 | 40 | 60 | P C | 11. ENG MATHS III | PP | 100 | 40 | 40 | P |

02. COMPUTER ORGANIZATION	PP	100	40	40	P C	12. COMPUTER GRAPHICS	PP	100	40	45	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	52	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	40	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	40	P C	14. DATA STRUCTURES AND FILES	PP	100	40	56	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	40	P C	15. DATA COMMUNICATION	PP	100	40	41	P
06. DIGITAL LABORATORY	TW	50	20	39	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	15	P
07. DIGITAL LABORATORY	PR	50	20	42	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	34	P
08. PROGRAMMING LABORATORY	TW	50	20	41	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	14	P
09. PROGRAMMING LABORATORY	PR	50	20	37	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	35	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	42	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	34	P
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	25	P

GRAND TOTAL = 812/1500, RESULT: SECOND CLASS

ORDN. 1 MARKS :

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S8058506   ANUJA WANGNOO   AMBICA   , 71045373G   , S8058506 ,   PICT   , S8058506

01. DISCRETE STRUCTURES	PP	100	40	60	P C	11. ENG MATHS III	PP	100	40	27	F
02. COMPUTER ORGANIZATION	PP	100	40	48	P C	12. COMPUTER GRAPHICS	PP	100	40	49	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	55	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	32	F
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	59	P C	14. DATA STRUCTURES AND FILES	PP	100	40	45	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	41	P C	15. DATA COMMUNICATION	PP	100	40	32	F
06. DIGITAL LABORATORY	TW	50	20	32	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	12	P
07. DIGITAL LABORATORY	PR	50	20	42	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	32	P
08. PROGRAMMING LABORATORY	TW	50	20	33	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	15	P
09. PROGRAMMING LABORATORY	PR	50	20	44	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	25	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	34	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	36	P
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	30	P

GRAND TOTAL = 783/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

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UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(INFORMATION TECHNOLOGY)

DATE : 18 AUG. 2011   CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.   PAGE NO. 03 (425)

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.

     OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S8058507   ASHISH SANADHYA   MADHU   , 71045374E   , S8058507 , PICT   , S8058507

01. DISCRETE STRUCTURES	PP	100	40	62	P C	11. ENG MATHS III	PP	100	40	76	P
02. COMPUTER ORGANIZATION	PP	100	40	40	P C	12. COMPUTER GRAPHICS	PP	100	40	61	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	65	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	68	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	68	P C	14. DATA STRUCTURES AND FILES	PP	100	40	63	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	50	P C	15. DATA COMMUNICATION	PP	100	40	63	P
06. DIGITAL LABORATORY	TW	50	20	40	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	20	P
07. DIGITAL LABORATORY	PR	50	20	35	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	44	P
08. PROGRAMMING LABORATORY	TW	50	20	43	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	19	P
09. PROGRAMMING LABORATORY	PR	50	20	39	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	35	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	44	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	45	P
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	44	P

GRAND TOTAL = 1024/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

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S8058508   ASHTEKAR KRUTTIKA CHANDRAKANT   SUREKHA   , 71045375C   , S8058508 , PICT   , S8058508

01. DISCRETE STRUCTURES	PP	100	40	55	P C	11. ENG MATHS III	PP	100	40	26	F
02. COMPUTER ORGANIZATION	PP	100	40	40	P C	12. COMPUTER GRAPHICS	PP	100	40	40	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	56	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	43	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	41	P C	14. DATA STRUCTURES AND FILES	PP	100	40	47	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	53	P C	15. DATA COMMUNICATION	PP	100	40	59	P
06. DIGITAL LABORATORY	TW	50	20	38	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	19	P

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07. DIGITAL LABORATORY	PR	50	20	36	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	32	P
08. PROGRAMMING LABORATORY	TW	50	20	38	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	15	P
09. PROGRAMMING LABORATORY	PR	50	20	34	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	37	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	38	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	43	P
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	32	P

GRAND TOTAL = 822/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

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S8058509   AUTADE SAGAR ASHOK   INDIRA   , 71045376M   , S8058509 ,   PICT   , S8058509

01. DISCRETE STRUCTURES	PP	100	40	40	P C	11. ENG MATHS III	PP	100	40	23	F
02. COMPUTER ORGANIZATION	PP	100	40	26	F	12. COMPUTER GRAPHICS	PP	100	40	22	F
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	44	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	16	F
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	40#	P	14. DATA STRUCTURES AND FILES	PP	100	40	40	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	28	F	15. DATA COMMUNICATION	PP	100	40	22	F
06. DIGITAL LABORATORY	TW	50	20	24	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	13	P
07. DIGITAL LABORATORY	PR	50	20	22	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	29	P
08. PROGRAMMING LABORATORY	TW	50	20	26	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	12	P
09. PROGRAMMING LABORATORY	PR	50	20	27	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	36	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	30	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	23	P
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	28	P

GRAND TOTAL = 571/1500, RESULT: FAILS   #[O.163+O.1]

ORDN. 1 MARKS : (04)3,

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 NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.

      OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S8058511   BADGUJAR PRATIKSHA BHASKAR   ANITA   , 71045380K   , S8058511 ,   PICT   , S8058511

01. DISCRETE STRUCTURES	PP	100	40	45	P C	11. ENG MATHS III	PP	100	40	40	P
02. COMPUTER ORGANIZATION	PP	100	40	46	P	12. COMPUTER GRAPHICS	PP	100	40	57	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	50	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	47	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	40	P C	14. DATA STRUCTURES AND FILES	PP	100	40	62	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	48	P C	15. DATA COMMUNICATION	PP	100	40	45	P
06. DIGITAL LABORATORY	TW	50	20	32	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	19	P
07. DIGITAL LABORATORY	PR	50	20	31	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	23	P
08. PROGRAMMING LABORATORY	TW	50	20	29	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	16	P
09. PROGRAMMING LABORATORY	PR	50	20	37	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	24	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	29	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	38	P
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	30	P

GRAND TOTAL = 788/1500, RESULT: SECOND CLASS

ORDN. 1 MARKS :

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S8058512   BHARTENDU BHARTI   MANISHA DEVI   , 71045387G   , S8058512 ,   PICT   , S8058512

01. DISCRETE STRUCTURES	PP	100	40	46	P C	11. ENG MATHS III	PP	100	40	51	P
02. COMPUTER ORGANIZATION	PP	100	40	43	P C	12. COMPUTER GRAPHICS	PP	100	40	48	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	64	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	49	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	41	P C	14. DATA STRUCTURES AND FILES	PP	100	40	63	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	43	P C	15. DATA COMMUNICATION	PP	100	40	58	P
06. DIGITAL LABORATORY	TW	50	20	20	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	14	P
07. DIGITAL LABORATORY	PR	50	20	24	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	34	P
08. PROGRAMMING LABORATORY	TW	50	20	28	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	15	P
09. PROGRAMMING LABORATORY	PR	50	20	27	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	39	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	20	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	34	P
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	30	P

GRAND TOTAL = 791/1500, RESULT: SECOND CLASS

ORDN. 1 MARKS :

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  S8058513   BHINTADE VIVEK ASHOK   SANGEETA   , 71134944E   , S8058513 ,   PICT   , S8058513

  01. DISCRETE STRUCTURES   PP   100   40   61 P C   11. ENG MATHS III   PP   100   40   47 P
  02. COMPUTER ORGANIZATION   PP   100   40   53 P C   12. COMPUTER GRAPHICS   PP   100   40   63 P
  03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100   40   68 P C   13. PROCESSOR ARCHITECTURE & INTER.   PP   100   40   76 P
  04. FUNDAMENTAL OF DATA STRUCTURES   PP   100   40   71 P C   14. DATA STRUCTURES AND FILES   PP   100   40   60 P
  05. HUMANITIES AND SOCIAL SCIENCES   PP   100   40   51 P C   15. DATA COMMUNICATION   PP   100   40   65 P
  06. DIGITAL LABORATORY   TW   50   20   38 P C   16. PROCESSOR INTERFACING LABORATORY TW   25   10   17 P
  07. DIGITAL LABORATORY   PR   50   20   31 P C   17. PROCESSOR INTERFACING LABORATORY OR   50   20   30 P
  08. PROGRAMMING LABORATORY   TW   50   20   34 P C   18. DATA STRUCTURES AND FILES LAB   TW   25   10   17 P
  09. PROGRAMMING LABORATORY   PR   50   20   45 P C   19. DATA STRUCTURES AND FILES LAB   PR   50   20   36 P
  10. COMMUNICATION AND LANGUAGE LAB. TW   50   20   28 P C   20. OBJECT ORIENTED PROGRAMMING LAB   TW   50   20   42 P
                                                                   21. OBJECT ORIENTED PROGRAMMING LAB   PR   50   20   37 P

GRAND TOTAL = 970/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

. . . . UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(INFORMATION TECHNOLOGY) . . . .
    DATE : 18 AUG. 2011   CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.   PAGE NO. 05 (427)

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 NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NOS., COLLEGE,   SEAT NO.
        OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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  S8058514   BHOSALE SHREEYA DEEPAKRAO   JYOTSNA   , 71045394K   , S8058514 ,   PICT   , S8058514

  01. DISCRETE STRUCTURES   PP   100   40   59 P C   11. ENG MATHS III   PP   100   40   54 P
  02. COMPUTER ORGANIZATION   PP   100   40   54 P C   12. COMPUTER GRAPHICS   PP   100   40   68 P

03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	65	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	64	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	60	P C	14. DATA STRUCTURES AND FILES	PP	100	40	54	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	53	P C	15. DATA COMMUNICATION	PP	100	40	70	P
06. DIGITAL LABORATORY	TW	50	20	37	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	19	P
07. DIGITAL LABORATORY	PR	50	20	33	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	35	P
08. PROGRAMMING LABORATORY	TW	50	20	38	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	14	P
09. PROGRAMMING LABORATORY	PR	50	20	42	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	12	F
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	41	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	33	P
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	35	P

GRAND TOTAL = 940/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

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S8058515   BORDE ROHAN SATISHRAO   UJWALA   , 71045398B   , S8058515 ,   PICT   , S8058515

01. DISCRETE STRUCTURES	PP	100	40	63	P C	11. ENG MATHS III	PP	100	40	52	P
02. COMPUTER ORGANIZATION	PP	100	40	49	P C	12. COMPUTER GRAPHICS	PP	100	40	58	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	52	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	56	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	64	P C	14. DATA STRUCTURES AND FILES	PP	100	40	73	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	49	P C	15. DATA COMMUNICATION	PP	100	40	60	P
06. DIGITAL LABORATORY	TW	50	20	20	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	17	P
07. DIGITAL LABORATORY	PR	50	20	30	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	28	P
08. PROGRAMMING LABORATORY	TW	50	20	33	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	16	P
09. PROGRAMMING LABORATORY	PR	50	20	30	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	31	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	27	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	42	P
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	34	P

GRAND TOTAL = 884/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

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S8058516   BRAHME NACHIKET SHRIKANT   VIDULA   , 71134945C   , S8058516 ,   PICT   , S8058516

01. DISCRETE STRUCTURES	PP	100	40	50	P C	11. ENG MATHS III		PP	100	40	47	P
02. COMPUTER ORGANIZATION	PP	100	40	48	P C	12. COMPUTER GRAPHICS		PP	100	40	52	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	51	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	59	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	48	P C	14. DATA STRUCTURES AND FILES		PP	100	40	56	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	45	P C	15. DATA COMMUNICATION		PP	100	40	58	P
06. DIGITAL LABORATORY	TW	50	20	33	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	18	P
07. DIGITAL LABORATORY	PR	50	20	32	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	25	P
08. PROGRAMMING LABORATORY	TW	50	20	42	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	15	P
09. PROGRAMMING LABORATORY	PR	50	20	37	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	43	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	38	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	39	P
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	38	P

GRAND TOTAL = 874/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

. . . .
                    UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(INFORMATION TECHNOLOGY)
    DATE : 18 AUG. 2011   CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.   PAGE NO. 06 (428)
. . . .
  NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.
          OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
. . . .

S8058517   CHAKRAVARTHY ROHAN KUMAR   MONICA   , 71045400H   , S8058517 ,   PICT   , S8058517

01. DISCRETE STRUCTURES	PP	100	40	53	P C	11. ENG MATHS III		PP	100	40	49	P
02. COMPUTER ORGANIZATION	PP	100	40	58	P C	12. COMPUTER GRAPHICS		PP	100	40	51	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	55	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	52	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	73	P C	14. DATA STRUCTURES AND FILES		PP	100	40	48	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	49	P C	15. DATA COMMUNICATION		PP	100	40	46	P
06. DIGITAL LABORATORY	TW	50	20	34	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	12	P
07. DIGITAL LABORATORY	PR	50	20	25	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	24	P

08. PROGRAMMING LABORATORY	TW	50	20	32	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	14	P
09. PROGRAMMING LABORATORY	PR	50	20	48	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	41	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	27	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	34	P
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	43	P

GRAND TOTAL = 868/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

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S8058518   CHANDAK RASHMI VIJAYKUMARJI   SHYAMA   , 71134946M   , S8058518 ,   PICT   , S8058518

01. DISCRETE STRUCTURES	PP	100	40	50	P C	11. ENG MATHS III	PP	100	40	44	P
02. COMPUTER ORGANIZATION	PP	100	40	46	P C	12. COMPUTER GRAPHICS	PP	100	40	58	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	66	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	61	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	69	P C	14. DATA STRUCTURES AND FILES	PP	100	40	73	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	40	P C	15. DATA COMMUNICATION	PP	100	40	49	P
06. DIGITAL LABORATORY	TW	50	20	38	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	20	P
07. DIGITAL LABORATORY	PR	50	20	28	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	44	P
08. PROGRAMMING LABORATORY	TW	50	20	40	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	20	P
09. PROGRAMMING LABORATORY	PR	50	20	40	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	38	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	46	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	42	P
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	30	P

GRAND TOTAL = 942/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

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S8058519   CHANGEDIYA SUNNY SANJAY   ASHA   , 71058517K   , S8058519 ,   PICT   , S8058519

01. DISCRETE STRUCTURES	PP	100	40	43	P C	11. ENG MATHS III	PP	100	40	40	P
02. COMPUTER ORGANIZATION	PP	100	40	41	P C	12. COMPUTER GRAPHICS	PP	100	40	63	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	47	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	52	P

04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	49	P C	14. DATA STRUCTURES AND FILES	PP	100	40	52	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	42	P C	15. DATA COMMUNICATION	PP	100	40	53	P
06. DIGITAL LABORATORY	TW	50	20	30	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	19	P
07. DIGITAL LABORATORY	PR	50	20	22	P	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	22	P
08. PROGRAMMING LABORATORY	TW	50	20	40	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	17	P
09. PROGRAMMING LABORATORY	PR	50	20	47	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	38	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	41	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	43	P
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	25	P

GRAND TOTAL = 826+05/1500, RESULT: HIGHER SECOND CLASS[O.163]

ORDN. 1 MARKS :

. . . .
                    UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(INFORMATION TECHNOLOGY)
   DATE : 18 AUG. 2011   CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.   PAGE NO. 07 (429)
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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.
        OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S8058520   CHAUDHAR SATISH RAOSAHEB   SANGITA   , 71045402D   , S8058520 , PICT   , S8058520

01. DISCRETE STRUCTURES	PP	100	40	63	P C	11. ENG MATHS III	PP	100	40	74	P
02. COMPUTER ORGANIZATION	PP	100	40	41	P C	12. COMPUTER GRAPHICS	PP	100	40	57	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	56	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	65	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	59	P C	14. DATA STRUCTURES AND FILES	PP	100	40	66	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	42	P C	15. DATA COMMUNICATION	PP	100	40	72	P
06. DIGITAL LABORATORY	TW	50	20	33	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	18	P
07. DIGITAL LABORATORY	PR	50	20	29	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	38	P
08. PROGRAMMING LABORATORY	TW	50	20	37	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	15	P
09. PROGRAMMING LABORATORY	PR	50	20	41	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	22	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	34	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	38	P
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	25	P

GRAND TOTAL = 925/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

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  S8058521   CHAUDHARI AMAR SHRIKANT   SHUBHANGI   , 71045403B   , S8058521 ,   PICT   , S8058521

  01. DISCRETE STRUCTURES   PP   100   40   52 P C   11. ENG MATHS III   PP   100   40   40 P
  02. COMPUTER ORGANIZATION   PP   100   40   44 P C   12. COMPUTER GRAPHICS   PP   100   40   51 P
  03. DIGITAL ELECTRONICS & LOGIC DESIGPP   100   40   58 P C   13. PROCESSOR ARCHITECTURE & INTER. PP   100   40   54 P
  04. FUNDAMENTAL OF DATA STRUCTURES PP   100   40   59 P C   14. DATA STRUCTURES AND FILES   PP   100   40   64 P
  05. HUMANITIES AND SOCIAL SCIENCES PP   100   40   49 P C   15. DATA COMMUNICATION   PP   100   40   61 P
  06. DIGITAL LABORATORY   TW   50   20   36 P C   16. PROCESSOR INTERFACING LABORATORY TW   25   10   21 P
  07. DIGITAL LABORATORY   PR   50   20   35 P C   17. PROCESSOR INTERFACING LABORATORY OR   50   20   44 P
  08. PROGRAMMING LABORATORY   TW   50   20   38 P C   18. DATA STRUCTURES AND FILES LAB   TW   25   10   15 P
  09. PROGRAMMING LABORATORY   PR   50   20   45 P C   19. DATA STRUCTURES AND FILES LAB   PR   50   20   39 P
  10. COMMUNICATION AND LANGUAGE LAB. TW   50   20   38 P C   20. OBJECT ORIENTED PROGRAMMING LAB TW   50   20   38 P
                                                                 21. OBJECT ORIENTED PROGRAMMING LAB PR   50   20   32 P

GRAND TOTAL = 913/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

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  S8058522   CHAVAN PUSHPAK VILAS   SUREKHA   , 71134947K   , S8058522 ,   PICT   , S8058522

  01. DISCRETE STRUCTURES   PP   100   40   58 P C   11. ENG MATHS III   PP   100   40   40 P
  02. COMPUTER ORGANIZATION   PP   100   40   62 P C   12. COMPUTER GRAPHICS   PP   100   40   58 P
  03. DIGITAL ELECTRONICS & LOGIC DESIGPP   100   40   57 P C   13. PROCESSOR ARCHITECTURE & INTER. PP   100   40   54 P
  04. FUNDAMENTAL OF DATA STRUCTURES PP   100   40   52 P C   14. DATA STRUCTURES AND FILES   PP   100   40   56 P
  05. HUMANITIES AND SOCIAL SCIENCES PP   100   40   56 P C   15. DATA COMMUNICATION   PP   100   40   66 P
  06. DIGITAL LABORATORY   TW   50   20   36 P C   16. PROCESSOR INTERFACING LABORATORY TW   25   10   16 P
  07. DIGITAL LABORATORY   PR   50   20   23 P C   17. PROCESSOR INTERFACING LABORATORY OR   50   20   35 P
  08. PROGRAMMING LABORATORY   TW   50   20   38 P C   18. DATA STRUCTURES AND FILES LAB   TW   25   10   18 P

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09. PROGRAMMING LABORATORY	PR	50	20	33	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	12	F
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	31	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	43	P
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	20	P

GRAND TOTAL = 864/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

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UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(INFORMATION TECHNOLOGY)

DATE : 18 AUG. 2011   CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.   PAGE NO. 08 (430)
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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S8058524   DARP YOGESH RAMCHANDRA   SANDHYA   , 71134948H   , S8058524 ,   PICT   , S8058524

01. DISCRETE STRUCTURES	PP	100	40	48	P C	11. ENG MATHS III	PP	100	40	20	F
02. COMPUTER ORGANIZATION	PP	100	40	46	P C	12. COMPUTER GRAPHICS	PP	100	40	46	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	43	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	56	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	48	P C	14. DATA STRUCTURES AND FILES	PP	100	40	51	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	45	P C	15. DATA COMMUNICATION	PP	100	40	53	P
06. DIGITAL LABORATORY	TW	50	20	32	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	17	P
07. DIGITAL LABORATORY	PR	50	20	24	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	35	P
08. PROGRAMMING LABORATORY	TW	50	20	39	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	15	P
09. PROGRAMMING LABORATORY	PR	50	20	30	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	30	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	37	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	38	P
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	25	P

GRAND TOTAL = 778/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

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S8058525   DAWANGE VISHAL SANJAY   KALPANA   , 71134949F   , S8058525 ,   PICT   , S8058525

01. DISCRETE STRUCTURES	PP	100	40	49	P C
02. COMPUTER ORGANIZATION	PP	100	40	53	P C
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	49	P C
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	65	P C
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	52	P C
06. DIGITAL LABORATORY	TW	50	20	37	P C
07. DIGITAL LABORATORY	PR	50	20	27	P C
08. PROGRAMMING LABORATORY	TW	50	20	39	P C
09. PROGRAMMING LABORATORY	PR	50	20	44	P C
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	29	P C

11. ENG MATHS III	PP	100	40	42	P
12. COMPUTER GRAPHICS	PP	100	40	57	P
13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	63	P
14. DATA STRUCTURES AND FILES	PP	100	40	73	P
15. DATA COMMUNICATION	PP	100	40	68	P
16. PROCESSOR INTERFACING LABORATORY	TW	25	10	19	P
17. PROCESSOR INTERFACING LABORATORY	OR	50	20	30	P
18. DATA STRUCTURES AND FILES LAB	TW	25	10	18	P
19. DATA STRUCTURES AND FILES LAB	PR	50	20	37	P
20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	41	P
21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	20	P

GRAND TOTAL = 912/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

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S8058526   DESHPANDE KIRTI VINOD   SHALINI   , 71134950K   , S8058526 ,   PICT   , S8058526

01. DISCRETE STRUCTURES	PP	100	40	44	P C
02. COMPUTER ORGANIZATION	PP	100	40	49	P C
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	60	P C
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	55	P C
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	51	P C
06. DIGITAL LABORATORY	TW	50	20	29	P C
07. DIGITAL LABORATORY	PR	50	20	22	P C
08. PROGRAMMING LABORATORY	TW	50	20	37	P C
09. PROGRAMMING LABORATORY	PR	50	20	27	P C
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	30	P C

11. ENG MATHS III	PP	100	40	23	F
12. COMPUTER GRAPHICS	PP	100	40	58	P
13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	53	P
14. DATA STRUCTURES AND FILES	PP	100	40	54	P
15. DATA COMMUNICATION	PP	100	40	69	P
16. PROCESSOR INTERFACING LABORATORY	TW	25	10	19	P
17. PROCESSOR INTERFACING LABORATORY	OR	50	20	27	P
18. DATA STRUCTURES AND FILES LAB	TW	25	10	16	P
19. DATA STRUCTURES AND FILES LAB	PR	50	20	10	F
20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	41	P
21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	31	P

GRAND TOTAL = 805+05/1500, RESULT: FAILS A.T.K.T.[O.163]

ORDN. 1 MARKS :

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    DATE : 18 AUG. 2011              CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.           PAGE NO.  09  (  431)
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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.

        OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S8058527   DESHPANDE NACHIKET CHANDRAKANT   KISHORI   , 71134951H   , S8058527 , PICT   , S8058527

01. DISCRETE STRUCTURES	PP	100	40	43	P C	11. ENG MATHS III		PP	100	40	12	F
02. COMPUTER ORGANIZATION	PP	100	40	41	P C	12. COMPUTER GRAPHICS		PP	100	40	51	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	51	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	61	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	53	P C	14. DATA STRUCTURES AND FILES		PP	100	40	61	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	53	P C	15. DATA COMMUNICATION		PP	100	40	59	P
06. DIGITAL LABORATORY	TW	50	20	34	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	18	P
07. DIGITAL LABORATORY	PR	50	20	35	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	29	P
08. PROGRAMMING LABORATORY	TW	50	20	39	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	19	P
09. PROGRAMMING LABORATORY	PR	50	20	36	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	30	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	38	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	43	P
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	30	P

GRAND TOTAL = 836/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

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S8058528   DHADIWAL KOMAL AJIT   ASHA   , 71134952F   , S8058528 , PICT   , S8058528

01. DISCRETE STRUCTURES	PP	100	40	41	P C	11. ENG MATHS III		PP	100	40	40	P
02. COMPUTER ORGANIZATION	PP	100	40	48	P C	12. COMPUTER GRAPHICS		PP	100	40	55	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	49	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	44	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	59	P C	14. DATA STRUCTURES AND FILES		PP	100	40	62	P

05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	54	P C	15. DATA COMMUNICATION	PP	100	40	54	P	
06. DIGITAL LABORATORY	TW	50	20	37	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	19	P	
07. DIGITAL LABORATORY	PR	50	20	22	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	28	P	
08. PROGRAMMING LABORATORY	TW	50	20	43	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	19	P	
09. PROGRAMMING LABORATORY	PR	50	20	37	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	43	P	
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	38	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	45	P	
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	39	P	

GRAND TOTAL = 876/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

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S8058529   DHULE AKSHAYKUMAR MAHADEVAPPA   SHIVANI   , 71045416D   , S8058529 , PICT   , S8058529

01. DISCRETE STRUCTURES	PP	100	40	75	P C	11. ENG MATHS III	PP	100	40	51	P	
02. COMPUTER ORGANIZATION	PP	100	40	50	P C	12. COMPUTER GRAPHICS	PP	100	40	56	P	
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	40	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	58	P	
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	43	P C	14. DATA STRUCTURES AND FILES	PP	100	40	64	P	
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	50	P C	15. DATA COMMUNICATION	PP	100	40	69	P	
06. DIGITAL LABORATORY	TW	50	20	33	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	16	P	
07. DIGITAL LABORATORY	PR	50	20	24	P	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	12	F	
08. PROGRAMMING LABORATORY	TW	50	20	40	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	14	P	
09. PROGRAMMING LABORATORY	PR	50	20	34	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	30	P	
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	36	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	33	P	
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	10	F	

GRAND TOTAL = 838/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

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UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(INFORMATION TECHNOLOGY)

   DATE : 18 AUG. 2011   CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.   PAGE NO. 10 (432)

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.

        OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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 S8058530   DIKE ASHISH VIJAY   RAJANI   , 71045418L   , S8058530 , PICT   , S8058530

 01. DISCRETE STRUCTURES   PP   100   40   51 P C   11. ENG MATHS III   PP   100   40   10 F
 02. COMPUTER ORGANIZATION   PP   100   40   40 P C   12. COMPUTER GRAPHICS   PP   100   40   41 P
 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100   40   43 P C   13. PROCESSOR ARCHITECTURE & INTER. PP   100   40   13 F
 04. FUNDAMENTAL OF DATA STRUCTURES   PP   100   40   40 P C   14. DATA STRUCTURES AND FILES   PP   100   40   45 P
 05. HUMANITIES AND SOCIAL SCIENCES   PP   100   40   50 P C   15. DATA COMMUNICATION   PP   100   40   29 F
 06. DIGITAL LABORATORY   TW   50   20   38 P C   16. PROCESSOR INTERFACING LABORATORY TW   25   10   16 P
 07. DIGITAL LABORATORY   PR   50   20   36 P C   17. PROCESSOR INTERFACING LABORATORY OR   50   20   24 P
 08. PROGRAMMING LABORATORY   TW   50   20   35 P C   18. DATA STRUCTURES AND FILES LAB   TW   25   10   12 P
 09. PROGRAMMING LABORATORY   PR   50   20   27 P   19. DATA STRUCTURES AND FILES LAB   PR   50   20   AA F
 10. COMMUNICATION AND LANGUAGE LAB. TW   50   20   38 P C   20. OBJECT ORIENTED PROGRAMMING LAB   TW   50   20   34 P
                                                                   21. OBJECT ORIENTED PROGRAMMING LAB   PR   50   20   30 P

GRAND TOTAL = 652/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

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 S8058531   DIVEKAR SANDIP LAXMAN   SHOBHA   , 71134953D   , S8058531 , PICT   , S8058531

 01. DISCRETE STRUCTURES   PP   100   40   69 P C   11. ENG MATHS III   PP   100   40   46 P
 02. COMPUTER ORGANIZATION   PP   100   40   44 P C   12. COMPUTER GRAPHICS   PP   100   40   62 P
 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100   40   67 P C   13. PROCESSOR ARCHITECTURE & INTER. PP   100   40   64 P
 04. FUNDAMENTAL OF DATA STRUCTURES   PP   100   40   67 P C   14. DATA STRUCTURES AND FILES   PP   100   40   70 P
 05. HUMANITIES AND SOCIAL SCIENCES   PP   100   40   51 P C   15. DATA COMMUNICATION   PP   100   40   64 P
 06. DIGITAL LABORATORY   TW   50   20   38 P C   16. PROCESSOR INTERFACING LABORATORY TW   25   10   21 P
 07. DIGITAL LABORATORY   PR   50   20   22 P C   17. PROCESSOR INTERFACING LABORATORY OR   50   20   39 P
 08. PROGRAMMING LABORATORY   TW   50   20   43 P C   18. DATA STRUCTURES AND FILES LAB   TW   25   10   21 P
 09. PROGRAMMING LABORATORY   PR   50   20   43 P C   19. DATA STRUCTURES AND FILES LAB   PR   50   20   35 P

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10. COMMUNICATION AND LANGUAGE LAB.  TW   50   20   39  P C      20. OBJECT ORIENTED PROGRAMMING LAB  TW   50   20   42  P
                                                                 21. OBJECT ORIENTED PROGRAMMING LAB  PR   50   20   44  P

GRAND TOTAL =  991/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :


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 S8058532    DUMAVAT KUNAL BHAVARLAL                 VIDYA            , 71134954B    , S8058532 ,   PICT     , S8058532

 01. DISCRETE STRUCTURES             PP   100   40   40  P C      11. ENG MATHS III                   PP   100   40   40  P
 02. COMPUTER ORGANIZATION           PP   100   40   43  P C      12. COMPUTER GRAPHICS               PP   100   40   57  P
 03. DIGITAL ELECTRONICS & LOGIC DESIGPP  100   40   62  P C      13. PROCESSOR ARCHITECTURE & INTER.  PP   100   40   59  P
 04. FUNDAMENTAL OF DATA STRUCTURES   PP   100   40   46  P C      14. DATA STRUCTURES AND FILES        PP   100   40   41  P
 05. HUMANITIES AND SOCIAL SCIENCES   PP   100   40   45  P C      15. DATA COMMUNICATION              PP   100   40   58  P
 06. DIGITAL LABORATORY              TW    50   20   36  P C      16. PROCESSOR INTERFACING LABORATORY TW    25   10   17  P
 07. DIGITAL LABORATORY              PR    50   20   32  P C      17. PROCESSOR INTERFACING LABORATORY OR    50   20   38  P
 08. PROGRAMMING LABORATORY          TW    50   20   41  P C      18. DATA STRUCTURES AND FILES LAB    TW    25   10   21  P
 09. PROGRAMMING LABORATORY          PR    50   20   28  P C      19. DATA STRUCTURES AND FILES LAB    PR    50   20   25  P
 10. COMMUNICATION AND LANGUAGE LAB.  TW   50   20   35  P C      20. OBJECT ORIENTED PROGRAMMING LAB  TW   50   20   43  P
                                                                 21. OBJECT ORIENTED PROGRAMMING LAB  PR   50   20   27  P

GRAND TOTAL =  834/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :


. . . . . . . . . . . . . . . . . . . UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(INFORMATION TECHNOLOGY) . . . . . . . . . . . . . . . . . . . . . . . . . . .
    DATE : 18 AUG. 2011              CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.           PAGE NO.  11  (  433)
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 NOTE: FIRST LINE : SEAT NO.,  NAME OF THE CANDIDATE,  MOTHER, PERMANENT REG. NO.,  PREVIOUS SEAT NO.,  COLLEGE,   SEAT NO.
       OTHER LINES: HEAD OF PASSING,  MAX. MARKS,  MIN. PASS MARKS,  MARKS OBTAINED,  P/F:PASS/FAIL,  C:PREVIOUS CARRY OVER
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 S8058533    EKHANDE DNYANESHWAR KACHARU             VITHABAI         , 71134955L    , S8058533 ,   PICT     , S8058533
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01. DISCRETE STRUCTURES	PP	100	40	52	P C		11. ENG MATHS III	PP	100	40	43	P
02. COMPUTER ORGANIZATION	PP	100	40	68	P C		12. COMPUTER GRAPHICS	PP	100	40	65	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	69	P C		13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	67	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	74	P C		14. DATA STRUCTURES AND FILES	PP	100	40	45	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	57	P C		15. DATA COMMUNICATION	PP	100	40	65	P
06. DIGITAL LABORATORY	TW	50	20	39	P C		16. PROCESSOR INTERFACING LABORATORY	TW	25	10	20	P
07. DIGITAL LABORATORY	PR	50	20	32	P C		17. PROCESSOR INTERFACING LABORATORY	OR	50	20	38	P
08. PROGRAMMING LABORATORY	TW	50	20	41	P C		18. DATA STRUCTURES AND FILES LAB	TW	25	10	17	P
09. PROGRAMMING LABORATORY	PR	50	20	45	P C		19. DATA STRUCTURES AND FILES LAB	PR	50	20	40	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	29	P C		20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	41	P
							21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	38	P

GRAND TOTAL = 985/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

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S8058534   GADKARI ASHISH VASANTRAO   SANGITA   , 71134956J   , S8058534 ,   PICT   , S8058534

01. DISCRETE STRUCTURES	PP	100	40	61	P C		11. ENG MATHS III	PP	100	40	42	P
02. COMPUTER ORGANIZATION	PP	100	40	52	P C		12. COMPUTER GRAPHICS	PP	100	40	63	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	60	P C		13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	67	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	63	P C		14. DATA STRUCTURES AND FILES	PP	100	40	63	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	45	P C		15. DATA COMMUNICATION	PP	100	40	60	P
06. DIGITAL LABORATORY	TW	50	20	38	P C		16. PROCESSOR INTERFACING LABORATORY	TW	25	10	18	P
07. DIGITAL LABORATORY	PR	50	20	33	P C		17. PROCESSOR INTERFACING LABORATORY	OR	50	20	29	P
08. PROGRAMMING LABORATORY	TW	50	20	44	P C		18. DATA STRUCTURES AND FILES LAB	TW	25	10	20	P
09. PROGRAMMING LABORATORY	PR	50	20	38	P C		19. DATA STRUCTURES AND FILES LAB	PR	50	20	35	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	39	P C		20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	40	P
							21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	43	P

GRAND TOTAL = 953/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

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S8058535   GAIKWAD AKSHAY SHAHAJI   MAHANANDA   , 71045430K   , S8058535 , PICT   , S8058535

01. DISCRETE STRUCTURES	PP	100	40	60	P C	11. ENG MATHS III	PP	100	40	71	P
02. COMPUTER ORGANIZATION	PP	100	40	56	P C	12. COMPUTER GRAPHICS	PP	100	40	52	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	77	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	47	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	54	P C	14. DATA STRUCTURES AND FILES	PP	100	40	52	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	40	P C	15. DATA COMMUNICATION	PP	100	40	59	P
06. DIGITAL LABORATORY	TW	50	20	22	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	10	P
07. DIGITAL LABORATORY	PR	50	20	40	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	13	F
08. PROGRAMMING LABORATORY	TW	50	20	21	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	12	P
09. PROGRAMMING LABORATORY	PR	50	20	40	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	40	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	27	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	20	P
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	22	P

GRAND TOTAL = 835/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

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                    UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(INFORMATION TECHNOLOGY)
     DATE : 18 AUG. 2011   CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.   PAGE NO. 12 (434)
. . . .

 NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.

         OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S8058536   GAIKWAD SAGAR ANAND   SUREKHA   , 71045773B   , S8058536 , PICT   , S8058536

01. DISCRETE STRUCTURES	PP	100	40	51	P C	11. ENG MATHS III	PP	100	40	44	P
02. COMPUTER ORGANIZATION	PP	100	40	49	P C	12. COMPUTER GRAPHICS	PP	100	40	52	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	61	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	69	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	57	P C	14. DATA STRUCTURES AND FILES	PP	100	40	66	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	40	P C	15. DATA COMMUNICATION	PP	100	40	55	P

06. DIGITAL LABORATORY	TW	50	20	25	P C		16. PROCESSOR INTERFACING LABORATORY	TW	25	10	17	P
07. DIGITAL LABORATORY	PR	50	20	27	P C		17. PROCESSOR INTERFACING LABORATORY	OR	50	20	24	P
08. PROGRAMMING LABORATORY	TW	50	20	39	P C		18. DATA STRUCTURES AND FILES LAB	TW	25	10	15	P
09. PROGRAMMING LABORATORY	PR	50	20	45	P C		19. DATA STRUCTURES AND FILES LAB	PR	50	20	42	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	39	P C		20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	39	P
							21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	28	P

GRAND TOTAL = 884/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

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S8058537   GEHANI ASHISH MOHAN   BHARTI   , 71134957G   , S8058537 ,   PICT   , S8058537

01. DISCRETE STRUCTURES	PP	100	40	72	P C		11. ENG MATHS III	PP	100	40	73	P
02. COMPUTER ORGANIZATION	PP	100	40	73	P C		12. COMPUTER GRAPHICS	PP	100	40	62	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	73	P C		13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	70	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	79	P C		14. DATA STRUCTURES AND FILES	PP	100	40	76	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	50	P C		15. DATA COMMUNICATION	PP	100	40	76	P
06. DIGITAL LABORATORY	TW	50	20	40	P C		16. PROCESSOR INTERFACING LABORATORY	TW	25	10	24	P
07. DIGITAL LABORATORY	PR	50	20	35	P C		17. PROCESSOR INTERFACING LABORATORY	OR	50	20	47	P
08. PROGRAMMING LABORATORY	TW	50	20	43	P C		18. DATA STRUCTURES AND FILES LAB	TW	25	10	24	P
09. PROGRAMMING LABORATORY	PR	50	20	47	P C		19. DATA STRUCTURES AND FILES LAB	PR	50	20	45	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	42	P C		20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	43	P
							21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	46	P

GRAND TOTAL = 1140/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

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S8058538   GHADGE TEJAL RAJENDRA   ROHINI   , 71134958E   , S8058538 ,   PICT   , S8058538

01. DISCRETE STRUCTURES	PP	100	40	59	P C		11. ENG MATHS III	PP	100	40	40	P
02. COMPUTER ORGANIZATION	PP	100	40	63	P C		12. COMPUTER GRAPHICS	PP	100	40	59	P

03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	60	P C		13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	58	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	58	P C		14. DATA STRUCTURES AND FILES	PP	100	40	73	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	53	P C		15. DATA COMMUNICATION	PP	100	40	66	P
06. DIGITAL LABORATORY	TW	50	20	39	P C		16. PROCESSOR INTERFACING LABORATORY	TW	25	10	20	P
07. DIGITAL LABORATORY	PR	50	20	36	P C		17. PROCESSOR INTERFACING LABORATORY	OR	50	20	42	P
08. PROGRAMMING LABORATORY	TW	50	20	43	P C		18. DATA STRUCTURES AND FILES LAB	TW	25	10	22	P
09. PROGRAMMING LABORATORY	PR	50	20	41	P C		19. DATA STRUCTURES AND FILES LAB	PR	50	20	38	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	38	P C		20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	44	P
							21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	29	P

GRAND TOTAL = 981/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

. . . .
                    UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(INFORMATION TECHNOLOGY)
     DATE : 18 AUG. 2011   CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.   PAGE NO. 13 (435)
. . . .

 NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.

        OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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 S8058539   GHODE AMAR PRADEEPKUMAR   SINDHU   , 71045436J   , S8058539 ,   PICT   , S8058539

01. DISCRETE STRUCTURES	PP	100	40	52	P C		11. ENG MATHS III	PP	100	40	15	F
02. COMPUTER ORGANIZATION	PP	100	40	42	P C		12. COMPUTER GRAPHICS	PP	100	40	42	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	44	P C		13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	22	F
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	40	P C		14. DATA STRUCTURES AND FILES	PP	100	40	49	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	41	P C		15. DATA COMMUNICATION	PP	100	40	45	P
06. DIGITAL LABORATORY	TW	50	20	20	P C		16. PROCESSOR INTERFACING LABORATORY	TW	25	10	11	P
07. DIGITAL LABORATORY	PR	50	20	33	P C		17. PROCESSOR INTERFACING LABORATORY	OR	50	20	22	P
08. PROGRAMMING LABORATORY	TW	50	20	30	P C		18. DATA STRUCTURES AND FILES LAB	TW	25	10	12	P
09. PROGRAMMING LABORATORY	PR	50	20	33	P C		19. DATA STRUCTURES AND FILES LAB	PR	50	20	11	F
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	25	P C		20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	26	P

                                           21. OBJECT ORIENTED PROGRAMMING LAB PR   50   20   20 P

GRAND TOTAL = 635/1500, RESULT: FAILS A.T.K.T.   RESULT RESERVED FOR BKLG

ORDN. 1 MARKS :

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  S8058540   GUNDECHA ANIKET PRAKASH   UJWALA   , 71134959C   , S8058540 ,   PICT   , S8058540

  01. DISCRETE STRUCTURES   PP   100   40   45 P C   11. ENG MATHS III   PP   100   40   10 F
  02. COMPUTER ORGANIZATION   PP   100   40   52 P C   12. COMPUTER GRAPHICS   PP   100   40   49 P
  03. DIGITAL ELECTRONICS & LOGIC DESIGPP   100   40   40 P C   13. PROCESSOR ARCHITECTURE & INTER. PP   100   40   40 P
  04. FUNDAMENTAL OF DATA STRUCTURES PP   100   40   53 P C   14. DATA STRUCTURES AND FILES   PP   100   40   41 P
  05. HUMANITIES AND SOCIAL SCIENCES PP   100   40   57 P C   15. DATA COMMUNICATION   PP   100   40   69 P
  06. DIGITAL LABORATORY   TW   50   20   35 P C   16. PROCESSOR INTERFACING LABORATORY TW   25   10   10 P
  07. DIGITAL LABORATORY   PR   50   20   21 P C   17. PROCESSOR INTERFACING LABORATORY OR   50   20   24 P
  08. PROGRAMMING LABORATORY   TW   50   20   33 P C   18. DATA STRUCTURES AND FILES LAB   TW   25   10   11 P
  09. PROGRAMMING LABORATORY   PR   50   20   37 P C   19. DATA STRUCTURES AND FILES LAB   PR   50   20   35 P
  10. COMMUNICATION AND LANGUAGE LAB. TW   50   20   36 P C   20. OBJECT ORIENTED PROGRAMMING LAB TW   50   20   27 P
                                                               21. OBJECT ORIENTED PROGRAMMING LAB PR   50   20   20 P

GRAND TOTAL = 745/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

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  S8058541   GUPTA VANI JITENDRA   RITU   , 71045443M   , S8058541 ,   PICT   , S8058541

  01. DISCRETE STRUCTURES   PP   100   40   56 P C   11. ENG MATHS III   PP   100   40   72 P
  02. COMPUTER ORGANIZATION   PP   100   40   60 P C   12. COMPUTER GRAPHICS   PP   100   40   61 P
  03. DIGITAL ELECTRONICS & LOGIC DESIGPP   100   40   79 P C   13. PROCESSOR ARCHITECTURE & INTER. PP   100   40   59 P
  04. FUNDAMENTAL OF DATA STRUCTURES PP   100   40   72 P C   14. DATA STRUCTURES AND FILES   PP   100   40   74 P
  05. HUMANITIES AND SOCIAL SCIENCES PP   100   40   44 P C   15. DATA COMMUNICATION   PP   100   40   65 P
  06. DIGITAL LABORATORY   TW   50   20   41 P C   16. PROCESSOR INTERFACING LABORATORY TW   25   10   23 P
  07. DIGITAL LABORATORY   PR   50   20   41 P C   17. PROCESSOR INTERFACING LABORATORY OR   50   20   42 P

08. PROGRAMMING LABORATORY	TW	50	20	43	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	19	P
09. PROGRAMMING LABORATORY	PR	50	20	44	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	43	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	45	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	45	P
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	40	P

GRAND TOTAL = 1068/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

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UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(INFORMATION TECHNOLOGY)

DATE : 18 AUG. 2011   CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.   PAGE NO. 14 (436)
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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.

    OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S8058543   HIRAN PRANAV PRAMODKUMAR   REKHA   , 71045447D   , S8058543 ,   PICT   , S8058543

01. DISCRETE STRUCTURES	PP	100	40	65	P C	11. ENG MATHS III	PP	100	40	74	P
02. COMPUTER ORGANIZATION	PP	100	40	56	P C	12. COMPUTER GRAPHICS	PP	100	40	52	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	59	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	47	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	56	P C	14. DATA STRUCTURES AND FILES	PP	100	40	51	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	40	P C	15. DATA COMMUNICATION	PP	100	40	57	P
06. DIGITAL LABORATORY	TW	50	20	36	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	19	P
07. DIGITAL LABORATORY	PR	50	20	32	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	35	P
08. PROGRAMMING LABORATORY	TW	50	20	36	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	18	P
09. PROGRAMMING LABORATORY	PR	50	20	34	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	33	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	36	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	44	P
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	15*	P

GRAND TOTAL = 895/1500, RESULT: HIGHER SECOND CLASS   * [O.4]

ORDN. 1 MARKS :

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S8058544   HIRE PRATIK PARASHURAM   MANGALA   , 71045448B   , S8058544 , PICT   , S8058544

01. DISCRETE STRUCTURES	PP	100	40	63	P C	11. ENG MATHS III	PP	100	40	65	P
02. COMPUTER ORGANIZATION	PP	100	40	49	P C	12. COMPUTER GRAPHICS	PP	100	40	51	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	57	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	32*	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	76	P C	14. DATA STRUCTURES AND FILES	PP	100	40	71	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	45	P C	15. DATA COMMUNICATION	PP	100	40	55	P
06. DIGITAL LABORATORY	TW	50	20	37	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	17	P
07. DIGITAL LABORATORY	PR	50	20	29	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	22	P
08. PROGRAMMING LABORATORY	TW	50	20	37	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	18	P
09. PROGRAMMING LABORATORY	PR	50	20	32	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	23	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	30	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	38	P
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	44	P

GRAND TOTAL = 891/1500, RESULT: HIGHER SECOND CLASS   * [O.4]

ORDN. 1 MARKS :

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S8058545   INAMDAR MOHSIN IMTIYAZ   SHAHENAZ   , 71045449L   , S8058545 , PICT   , S8058545

01. DISCRETE STRUCTURES	PP	100	40	44	P C	11. ENG MATHS III	PP	100	40	48	P
02. COMPUTER ORGANIZATION	PP	100	40	27	F	12. COMPUTER GRAPHICS	PP	100	40	40	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	53	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	27	F
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	46	P C	14. DATA STRUCTURES AND FILES	PP	100	40	59	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	40	P C	15. DATA COMMUNICATION	PP	100	40	40	P
06. DIGITAL LABORATORY	TW	50	20	28	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	19	P
07. DIGITAL LABORATORY	PR	50	20	22	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	27	P
08. PROGRAMMING LABORATORY	TW	50	20	37	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	17	P
09. PROGRAMMING LABORATORY	PR	50	20	37	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	40	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	37	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	41	P
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	32	P

GRAND TOTAL = 761/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

. . . . UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(INFORMATION TECHNOLOGY) . . . .

DATE : 18 AUG. 2011   CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.   PAGE NO. 15 (437)

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.

          OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S8058546   INGALE KOMAL RAJENDRA   USHA   , 71045450D   , S8058546 ,   PICT   , S8058546

01. DISCRETE STRUCTURES	PP	100	40	54	P C	11. ENG MATHS III	PP	100	40	65	P
02. COMPUTER ORGANIZATION	PP	100	40	43	P C	12. COMPUTER GRAPHICS	PP	100	40	46	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	55	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	30*	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	46	P C	14. DATA STRUCTURES AND FILES	PP	100	40	68	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	46	P C	15. DATA COMMUNICATION	PP	100	40	52	P
06. DIGITAL LABORATORY	TW	50	20	37	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	21	P
07. DIGITAL LABORATORY	PR	50	20	34	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	37	P
08. PROGRAMMING LABORATORY	TW	50	20	42	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	19	P
09. PROGRAMMING LABORATORY	PR	50	20	42	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	32	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	39	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	43	P
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	28	P

GRAND TOTAL = 879/1500, RESULT: HIGHER SECOND CLASS   * [O.4]

ORDN. 1 MARKS :

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S8058547   INGOLE PRACHI VASANT   NILIMA   , 71045451B   , S8058547 ,   PICT   , S8058547

01. DISCRETE STRUCTURES	PP	100	40	59	P C	11. ENG MATHS III	PP	100	40	68	P
02. COMPUTER ORGANIZATION	PP	100	40	40	P C	12. COMPUTER GRAPHICS	PP	100	40	67	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	45	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	44	P

04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	42	P C	14. DATA STRUCTURES AND FILES		PP	100	40	70	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	40	P C	15. DATA COMMUNICATION		PP	100	40	63	P
06. DIGITAL LABORATORY	TW	50	20	20	P C	16. PROCESSOR INTERFACING LABORATORY	TW		25	10	20	P
07. DIGITAL LABORATORY	PR	50	20	34	P C	17. PROCESSOR INTERFACING LABORATORY	OR		50	20	40	P
08. PROGRAMMING LABORATORY	TW	50	20	29	P C	18. DATA STRUCTURES AND FILES LAB		TW	25	10	19	P
09. PROGRAMMING LABORATORY	PR	50	20	38	P C	19. DATA STRUCTURES AND FILES LAB		PR	50	20	37	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	33	P C	20. OBJECT ORIENTED PROGRAMMING LAB		TW	50	20	44	P
						21. OBJECT ORIENTED PROGRAMMING LAB		PR	50	20	20#	P

GRAND TOTAL = 872+02/1500, RESULT: HIGHER SECOND CLASS #[O.163+O.1][O.163]

ORDN. 1 MARKS : (21)2,

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S8058548   ISHAN AGARWAL   MAMTA   , 71045452L   , S8058548 ,   PICT   , S8058548

01. DISCRETE STRUCTURES	PP	100	40	67	P C	11. ENG MATHS III		PP	100	40	54	P
02. COMPUTER ORGANIZATION	PP	100	40	45	P C	12. COMPUTER GRAPHICS		PP	100	40	50	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	45	P C	13. PROCESSOR ARCHITECTURE & INTER.		PP	100	40	44	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	64	P C	14. DATA STRUCTURES AND FILES		PP	100	40	59	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	44	P C	15. DATA COMMUNICATION		PP	100	40	40	P
06. DIGITAL LABORATORY	TW	50	20	30	P C	16. PROCESSOR INTERFACING LABORATORY	TW		25	10	18	P
07. DIGITAL LABORATORY	PR	50	20	33	P C	17. PROCESSOR INTERFACING LABORATORY	OR		50	20	38	P
08. PROGRAMMING LABORATORY	TW	50	20	33	P C	18. DATA STRUCTURES AND FILES LAB		TW	25	10	13	P
09. PROGRAMMING LABORATORY	PR	50	20	40	P C	19. DATA STRUCTURES AND FILES LAB		PR	50	20	30	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	30	P C	20. OBJECT ORIENTED PROGRAMMING LAB		TW	50	20	31	P
						21. OBJECT ORIENTED PROGRAMMING LAB		PR	50	20	22	P

GRAND TOTAL = 830/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

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UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(INFORMATION TECHNOLOGY)

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S8058549   JADHAV DHANRAJ KHANDERAO   CHHAYA   , 71134960G   , S8058549 ,   PICT   , S8058549

01. DISCRETE STRUCTURES	PP	100	40	49	P C	11. ENG MATHS III	PP	100	40	18	F
02. COMPUTER ORGANIZATION	PP	100	40	65	P C	12. COMPUTER GRAPHICS	PP	100	40	53	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	59	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	53	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	75	P C	14. DATA STRUCTURES AND FILES	PP	100	40	71	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	53	P C	15. DATA COMMUNICATION	PP	100	40	68	P
06. DIGITAL LABORATORY	TW	50	20	39	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	20	P
07. DIGITAL LABORATORY	PR	50	20	30	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	43	P
08. PROGRAMMING LABORATORY	TW	50	20	42	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	23	P
09. PROGRAMMING LABORATORY	PR	50	20	46	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	35	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	38	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	46	P
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	32	P

GRAND TOTAL = 958/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

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S8058550   JADHAV MAYUR JAGAHNATH   JANABAI   , 71134961E   , S8058550 ,   PICT   , S8058550

01. DISCRETE STRUCTURES	PP	100	40	65	P C	11. ENG MATHS III	PP	100	40	42	P
02. COMPUTER ORGANIZATION	PP	100	40	65	P C	12. COMPUTER GRAPHICS	PP	100	40	62	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	81	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	46	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	74	P C	14. DATA STRUCTURES AND FILES	PP	100	40	76	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	45	P C	15. DATA COMMUNICATION	PP	100	40	58	P
06. DIGITAL LABORATORY	TW	50	20	36	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	21	P
07. DIGITAL LABORATORY	PR	50	20	30	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	42	P
08. PROGRAMMING LABORATORY	TW	50	20	42	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	20	P

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09. PROGRAMMING LABORATORY          PR   50   20   46  P C      19. DATA STRUCTURES AND FILES LAB    PR   50   20   39  P
10. COMMUNICATION AND LANGUAGE LAB. TW   50   20   40  P C      20. OBJECT ORIENTED PROGRAMMING LAB  TW   50   20   38  P
                                                               21. OBJECT ORIENTED PROGRAMMING LAB  PR   50   20   46  P
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GRAND TOTAL = 1014/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

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S8058551   JODHWANI SAHIL PRADEEP              BABITA          , 71045462H    , S8058551 ,   PICT     , S8058551

01. DISCRETE STRUCTURES             PP  100   40   47  P C      11. ENG MATHS III                    PP  100   40   85  P
02. COMPUTER ORGANIZATION           PP  100   40   40  P C      12. COMPUTER GRAPHICS                PP  100   40   48  P
03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100   40   58  P C      13. PROCESSOR ARCHITECTURE & INTER.  PP  100   40   40  P
04. FUNDAMENTAL OF DATA STRUCTURES   PP  100   40   59  P C      14. DATA STRUCTURES AND FILES        PP  100   40   63  P
05. HUMANITIES AND SOCIAL SCIENCES   PP  100   40   40  P C      15. DATA COMMUNICATION               PP  100   40   48  P
06. DIGITAL LABORATORY               TW   50   20   34  P C      16. PROCESSOR INTERFACING LABORATORY TW   25   10   19  P
07. DIGITAL LABORATORY               PR   50   20   39  P C      17. PROCESSOR INTERFACING LABORATORY OR   50   20   40  P
08. PROGRAMMING LABORATORY           TW   50   20   41  P C      18. DATA STRUCTURES AND FILES LAB    TW   25   10   18  P
09. PROGRAMMING LABORATORY           PR   50   20   48  P C      19. DATA STRUCTURES AND FILES LAB    PR   50   20   43  P
10. COMMUNICATION AND LANGUAGE LAB. TW   50   20   36  P C      20. OBJECT ORIENTED PROGRAMMING LAB  TW   50   20   42  P
                                                               21. OBJECT ORIENTED PROGRAMMING LAB  PR   50   20   35  P
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GRAND TOTAL = 923/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

.. . . UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(INFORMATION TECHNOLOGY) . . . .

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.

        OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S8058552   JUVVADI MANOBHIRAM   J VIJAYASHRI   , 71057070J   , S8058552 ,   PICT   , S8058552

01. DISCRETE STRUCTURES	PP	100	40	58	P C	11. ENG MATHS III	PP	100	40	50	P	
02. COMPUTER ORGANIZATION	PP	100	40	62	P C	12. COMPUTER GRAPHICS	PP	100	40	49	P	
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	40	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	50	P	
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	48	P C	14. DATA STRUCTURES AND FILES	PP	100	40	72	P	
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	48	P C	15. DATA COMMUNICATION	PP	100	40	63	P	
06. DIGITAL LABORATORY	TW	50	20	26	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	18	P	
07. DIGITAL LABORATORY	PR	50	20	21	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	15*	P	
08. PROGRAMMING LABORATORY	TW	50	20	32	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	20	P	
09. PROGRAMMING LABORATORY	PR	50	20	33	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	37	P	
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	22	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	42	P	
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	25	P	

GRAND TOTAL = 831/1500, RESULT: HIGHER SECOND CLASS   *[O.163+O.4]   [& O.16

ORDN. 1 MARKS :

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S8058553   KADAM AMIT ASHOKRAO   MANDAKINI   , 71134962C   , S8058553 ,   PICT   , S8058553

01. DISCRETE STRUCTURES	PP	100	40	40	P C	11. ENG MATHS III	PP	100	40	40	P	
02. COMPUTER ORGANIZATION	PP	100	40	51	P C	12. COMPUTER GRAPHICS	PP	100	40	47	P	
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	64	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	43	P	
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	50	P C	14. DATA STRUCTURES AND FILES	PP	100	40	60	P	
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	45	P C	15. DATA COMMUNICATION	PP	100	40	54	P	
06. DIGITAL LABORATORY	TW	50	20	38	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	22	P	
07. DIGITAL LABORATORY	PR	50	20	22	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	39	P	
08. PROGRAMMING LABORATORY	TW	50	20	43	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	22	P	
09. PROGRAMMING LABORATORY	PR	50	20	30	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	39	P	
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	39	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	44	P	
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	38	P	

GRAND TOTAL = 870/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

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S8058554   KADAM PRADNYA PRADEEP   PRANITA   , 71134963M   , S8058554 ,   PICT   , S8058554

01. DISCRETE STRUCTURES	PP	100	40	54	P		11. ENG MATHS III	PP	100	40	40	P
02. COMPUTER ORGANIZATION	PP	100	40	40	P	C	12. COMPUTER GRAPHICS	PP	100	40	41	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	46	P	C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	40	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	49	P	C	14. DATA STRUCTURES AND FILES	PP	100	40	66	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	46	P	C	15. DATA COMMUNICATION	PP	100	40	67	P
06. DIGITAL LABORATORY	TW	50	20	36	P	C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	18	P
07. DIGITAL LABORATORY	PR	50	20	34	P	C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	24	P
08. PROGRAMMING LABORATORY	TW	50	20	37	P	C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	15	P
09. PROGRAMMING LABORATORY	PR	50	20	27	P	C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	20	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	33	P	C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	32	P
							21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	37	P

GRAND TOTAL = 802/1500, RESULT: SECOND CLASS

ORDN. 1 MARKS :

. . . .
                UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(INFORMATION TECHNOLOGY)
    DATE : 18 AUG. 2011   CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.   PAGE NO. 18 (440)
. . . .
 NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.
        OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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S8058555   KADAM RAHUL DNYANESHWAR   INDUBAI   , 71134964K   , S8058555 ,   PICT   , S8058555

01. DISCRETE STRUCTURES	PP	100	40	59	P	C	11. ENG MATHS III	PP	100	40	54	P
02. COMPUTER ORGANIZATION	PP	100	40	68	P	C	12. COMPUTER GRAPHICS	PP	100	40	62	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	62	P	C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	51	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	66	P	C	14. DATA STRUCTURES AND FILES	PP	100	40	74	P

05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	56	P C	15. DATA COMMUNICATION	PP	100	40	60	P
06. DIGITAL LABORATORY	TW	50	20	37	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	21	P
07. DIGITAL LABORATORY	PR	50	20	36	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	32	P
08. PROGRAMMING LABORATORY	TW	50	20	42	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	22	P
09. PROGRAMMING LABORATORY	PR	50	20	41	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	39	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	37	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	43	P
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	40	P

GRAND TOTAL = 1002/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

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S8058556   KAKADE POOJA DILIPRAO   SUNITA   , 71134965H   , S8058556 , PICT   , S8058556

01. DISCRETE STRUCTURES	PP	100	40	41	P C	11. ENG MATHS III	PP	100	40	52	P
02. COMPUTER ORGANIZATION	PP	100	40	52	P C	12. COMPUTER GRAPHICS	PP	100	40	59	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	76	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	43	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	58	P C	14. DATA STRUCTURES AND FILES	PP	100	40	71	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	46	P C	15. DATA COMMUNICATION	PP	100	40	63	P
06. DIGITAL LABORATORY	TW	50	20	37	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	21	P
07. DIGITAL LABORATORY	PR	50	20	33	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	38	P
08. PROGRAMMING LABORATORY	TW	50	20	39	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	21	P
09. PROGRAMMING LABORATORY	PR	50	20	42	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	40	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	35	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	47	P
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	27	P

GRAND TOTAL = 941/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

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S8058557   KALBANDE GAURI RAJENDRA   PRABHA   , 71134966F   , S8058557 , PICT   , S8058557

cols05

01. DISCRETE STRUCTURES	PP	100	40	47	P C		11. ENG MATHS III	PP	100	40	41	P
02. COMPUTER ORGANIZATION	PP	100	40	58	P C		12. COMPUTER GRAPHICS	PP	100	40	51	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	62	P C		13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	50	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	79	P C		14. DATA STRUCTURES AND FILES	PP	100	40	78	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	40	P C		15. DATA COMMUNICATION	PP	100	40	61	P
06. DIGITAL LABORATORY	TW	50	20	39	P C		16. PROCESSOR INTERFACING LABORATORY	TW	25	10	21	P
07. DIGITAL LABORATORY	PR	50	20	32	P C		17. PROCESSOR INTERFACING LABORATORY	OR	50	20	39	P
08. PROGRAMMING LABORATORY	TW	50	20	40	P C		18. DATA STRUCTURES AND FILES LAB	TW	25	10	22	P
09. PROGRAMMING LABORATORY	PR	50	20	42	P C		19. DATA STRUCTURES AND FILES LAB	PR	50	20	36	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	35	P C		20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	43	P
							21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	44	P

GRAND TOTAL = 960/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

. . . . UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(INFORMATION TECHNOLOGY). . . . .

   DATE : 18 AUG. 2011   CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.   PAGE NO. 19 (441)
. . . .

 NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.

        OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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 S8058558   KALRA JASBIRKAUR AMARJIT SINGH   MEENA   , 71134967D   , S8058558 , PICT   , S8058558

01. DISCRETE STRUCTURES	PP	100	40	46	P C		11. ENG MATHS III	PP	100	40	40	P
02. COMPUTER ORGANIZATION	PP	100	40	48	P C		12. COMPUTER GRAPHICS	PP	100	40	64	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	47	P C		13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	40	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	58	P C		14. DATA STRUCTURES AND FILES	PP	100	40	57	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	40	P C		15. DATA COMMUNICATION	PP	100	40	59	P
06. DIGITAL LABORATORY	TW	50	20	35	P C		16. PROCESSOR INTERFACING LABORATORY	TW	25	10	22	P
07. DIGITAL LABORATORY	PR	50	20	20	P C		17. PROCESSOR INTERFACING LABORATORY	OR	50	20	44	P
08. PROGRAMMING LABORATORY	TW	50	20	39	P C		18. DATA STRUCTURES AND FILES LAB	TW	25	10	21	P
09. PROGRAMMING LABORATORY	PR	50	20	44	P C		19. DATA STRUCTURES AND FILES LAB	PR	50	20	39	P

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10. COMMUNICATION AND LANGUAGE LAB.  TW   50   20   40  P C      20. OBJECT ORIENTED PROGRAMMING LAB  TW   50   20   47  P
                                                                 21. OBJECT ORIENTED PROGRAMMING LAB  PR   50   20   28  P
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GRAND TOTAL = 878/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

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S8058559   KAMBLE JITESH SHAMRAO                PUSHPA              , 71045466L    , S8058559 ,  PICT     , S8058559

01. DISCRETE STRUCTURES            PP   100   40   69  P C      11. ENG MATHS III                    PP   100   40   45  P
02. COMPUTER ORGANIZATION          PP   100   40   42  P C      12. COMPUTER GRAPHICS                PP   100   40   52  P
03. DIGITAL ELECTRONICS & LOGIC DESIGPP  100   40   56  P C      13. PROCESSOR ARCHITECTURE & INTER.  PP   100   40   45  P
04. FUNDAMENTAL OF DATA STRUCTURES  PP   100   40   67  P C      14. DATA STRUCTURES AND FILES        PP   100   40   64  P
05. HUMANITIES AND SOCIAL SCIENCES  PP   100   40   40  P C      15. DATA COMMUNICATION               PP   100   40   57  P
06. DIGITAL LABORATORY             TW    50   20   38  P C      16. PROCESSOR INTERFACING LABORATORY TW   25   10   24  P
07. DIGITAL LABORATORY             PR    50   20   35  P C      17. PROCESSOR INTERFACING LABORATORY OR   50   20   44  P
08. PROGRAMMING LABORATORY         TW    50   20   43  P C      18. DATA STRUCTURES AND FILES LAB     TW   25   10   20  P
09. PROGRAMMING LABORATORY         PR    50   20   45  P C      19. DATA STRUCTURES AND FILES LAB     PR   50   20   43  P
10. COMMUNICATION AND LANGUAGE LAB.  TW   50   20   39  P C      20. OBJECT ORIENTED PROGRAMMING LAB  TW   50   20   40  P
                                                                 21. OBJECT ORIENTED PROGRAMMING LAB  PR   50   20   45  P
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GRAND TOTAL = 953/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

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S8058560   KARACHIWALA HAMZA SHABBIR            DURRIYA             , 71045470J    , S8058560 ,  PICT     , S8058560

01. DISCRETE STRUCTURES            PP   100   40   52  P C      11. ENG MATHS III                    PP   100   40   22  F
02. COMPUTER ORGANIZATION          PP   100   40   50  P        12. COMPUTER GRAPHICS                PP   100   40   46  P
03. DIGITAL ELECTRONICS & LOGIC DESIGPP  100   40   52  P        13. PROCESSOR ARCHITECTURE & INTER.  PP   100   40   47  P
04. FUNDAMENTAL OF DATA STRUCTURES  PP   100   40   51  P C      14. DATA STRUCTURES AND FILES        PP   100   40   69  P
05. HUMANITIES AND SOCIAL SCIENCES  PP   100   40   45  P C      15. DATA COMMUNICATION               PP   100   40   45  P
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06. DIGITAL LABORATORY	TW	50	20	30	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	19	P
07. DIGITAL LABORATORY	PR	50	20	34	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	39	P
08. PROGRAMMING LABORATORY	TW	50	20	36	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	15	P
09. PROGRAMMING LABORATORY	PR	50	20	45	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	38	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	37	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	39	P
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	44	P

GRAND TOTAL = 855/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

. . . . UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(INFORMATION TECHNOLOGY) . . . .

   DATE : 18 AUG. 2011   CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.   PAGE NO. 20 (442)

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 NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.

       OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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 S8058561   KARISHMA GARG   KAVITA   , 71045474M   , S8058561 ,   PICT   , S8058561

01. DISCRETE STRUCTURES	PP	100	40	53	P C	11. ENG MATHS III	PP	100	40	59	P
02. COMPUTER ORGANIZATION	PP	100	40	56	P C	12. COMPUTER GRAPHICS	PP	100	40	62	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	63	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	40	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	76	P C	14. DATA STRUCTURES AND FILES	PP	100	40	69	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	44	P C	15. DATA COMMUNICATION	PP	100	40	48	P
06. DIGITAL LABORATORY	TW	50	20	38	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	17	P
07. DIGITAL LABORATORY	PR	50	20	33	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	37	P
08. PROGRAMMING LABORATORY	TW	50	20	39	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	13	P
09. PROGRAMMING LABORATORY	PR	50	20	35	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	28	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	40	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	32	P
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	42	P

GRAND TOTAL = 924/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

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S8058562   KAZI SANA NAFIS   FARJANA   , 71045478D   , S8058562 ,   PICT   , S8058562

01. DISCRETE STRUCTURES	PP	100	40	65	P C	11. ENG MATHS III	PP	100	40	65	P
02. COMPUTER ORGANIZATION	PP	100	40	49	P C	12. COMPUTER GRAPHICS	PP	100	40	64	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	75	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	45	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	75	P C	14. DATA STRUCTURES AND FILES	PP	100	40	64	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	40	P C	15. DATA COMMUNICATION	PP	100	40	61	P
06. DIGITAL LABORATORY	TW	50	20	38	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	19	P
07. DIGITAL LABORATORY	PR	50	20	32	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	45	P
08. PROGRAMMING LABORATORY	TW	50	20	43	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	20	P
09. PROGRAMMING LABORATORY	PR	50	20	39	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	39	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	40	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	46	P
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	40	P

GRAND TOTAL = 1004/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

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S8058563   KELKAR ANUJA MILIND   SUPRIYA   , 71045480F   , S8058563 ,   PICT   , S8058563

01. DISCRETE STRUCTURES	PP	100	40	82	P C	11. ENG MATHS III	PP	100	40	75	P
02. COMPUTER ORGANIZATION	PP	100	40	40	P C	12. COMPUTER GRAPHICS	PP	100	40	58	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	56	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	47	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	76	P C	14. DATA STRUCTURES AND FILES	PP	100	40	75	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	49	P C	15. DATA COMMUNICATION	PP	100	40	72	P
06. DIGITAL LABORATORY	TW	50	20	45	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	24	P
07. DIGITAL LABORATORY	PR	50	20	38	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	45	P
08. PROGRAMMING LABORATORY	TW	50	20	46	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	20	P
09. PROGRAMMING LABORATORY	PR	50	20	47	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	32	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	46	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	46	P

                                        21. OBJECT ORIENTED PROGRAMMING LAB PR   50   20   40 P

GRAND TOTAL = 1059+05/1500, RESULT: FIRST CLASS WITH DISTINCTION[O.163]

ORDN. 1 MARKS :

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                  UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(INFORMATION TECHNOLOGY)

    DATE : 18 AUG. 2011   CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.   PAGE NO. 21 (443)

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  NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.

        OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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  S8058564   KOTHAWALE AKASH SANJAY   ANITA   , 71045486E   , S8058564 , PICT   , S8058564

  01. DISCRETE STRUCTURES   PP   100   40   58 P C   11. ENG MATHS III   PP   100   40   40 P
  02. COMPUTER ORGANIZATION   PP   100   40   29 F   12. COMPUTER GRAPHICS   PP   100   40   50 P
  03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100   40   74 P C   13. PROCESSOR ARCHITECTURE & INTER.   PP   100   40   31 F
  04. FUNDAMENTAL OF DATA STRUCTURES PP   100   40   55 P C   14. DATA STRUCTURES AND FILES   PP   100   40   66 P
  05. HUMANITIES AND SOCIAL SCIENCES PP   100   40   40 P C   15. DATA COMMUNICATION   PP   100   40   41 P
  06. DIGITAL LABORATORY   TW   50   20   37 P C   16. PROCESSOR INTERFACING LABORATORY TW   25   10   23 P
  07. DIGITAL LABORATORY   PR   50   20   36 P C   17. PROCESSOR INTERFACING LABORATORY OR   50   20   37 P
  08. PROGRAMMING LABORATORY   TW   50   20   40 P C   18. DATA STRUCTURES AND FILES LAB   TW   25   10   17 P
  09. PROGRAMMING LABORATORY   PR   50   20   27 P C   19. DATA STRUCTURES AND FILES LAB   PR   50   20   40 P
  10. COMMUNICATION AND LANGUAGE LAB. TW   50   20   40 P C   20. OBJECT ORIENTED PROGRAMMING LAB   TW   50   20   38 P
                                                                 21. OBJECT ORIENTED PROGRAMMING LAB   PR   50   20   38 P

GRAND TOTAL = 857/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

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  S8058565   KULKARNI ALOK DIPAK   GEETA   , 71045491M   , S8058565 , PICT   , S8058565

  01. DISCRETE STRUCTURES   PP   100   40   AA F   11. ENG MATHS III   PP   100   40   00 F

02. COMPUTER ORGANIZATION	PP	100	40	03	F		12. COMPUTER GRAPHICS	PP	100	40	05	F
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	19	F		13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	02	F
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	45	P		14. DATA STRUCTURES AND FILES	PP	100	40	07	F
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	40	P C		15. DATA COMMUNICATION	PP	100	40	04	F
06. DIGITAL LABORATORY	TW	50	20	22	P C		16. PROCESSOR INTERFACING LABORATORY	TW	25	10	15	P
07. DIGITAL LABORATORY	PR	50	20	33	P C		17. PROCESSOR INTERFACING LABORATORY	OR	50	20	13	F
08. PROGRAMMING LABORATORY	TW	50	20	35	P C		18. DATA STRUCTURES AND FILES LAB	TW	25	10	15	P
09. PROGRAMMING LABORATORY	PR	50	20	35	P C		19. DATA STRUCTURES AND FILES LAB	PR	50	20	29	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	37	P C		20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	35	P
							21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	40	P

GRAND TOTAL = 434/1500, RESULT: FAILS   RESULT RESERVED FOR BKLG

ORDN. 1 MARKS :

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S8058566   KULKARNI AMITA DILIP   MEGHA   , 71045492K   , S8058566 ,   PICT   , S8058566

01. DISCRETE STRUCTURES	PP	100	40	52	P C		11. ENG MATHS III	PP	100	40	56	P
02. COMPUTER ORGANIZATION	PP	100	40	49	P C		12. COMPUTER GRAPHICS	PP	100	40	55	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	56	P C		13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	40	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	71	P C		14. DATA STRUCTURES AND FILES	PP	100	40	68	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	40	P C		15. DATA COMMUNICATION	PP	100	40	47	P
06. DIGITAL LABORATORY	TW	50	20	39	P C		16. PROCESSOR INTERFACING LABORATORY	TW	25	10	20	P
07. DIGITAL LABORATORY	PR	50	20	42	P C		17. PROCESSOR INTERFACING LABORATORY	OR	50	20	38	P
08. PROGRAMMING LABORATORY	TW	50	20	42	P C		18. DATA STRUCTURES AND FILES LAB	TW	25	10	20	P
09. PROGRAMMING LABORATORY	PR	50	20	32	P C		19. DATA STRUCTURES AND FILES LAB	PR	50	20	39	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	42	P C		20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	46	P
							21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	35	P

GRAND TOTAL = 929/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.

       OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S8058567   KULKARNI PRAMOD SHIVAJIRAO   SUMATI   , 71045672H   , S8058567 ,   PICT   , S8058567

01. DISCRETE STRUCTURES	PP	100	40	40	P	C	11. ENG MATHS III		PP	100	40	40	P
02. COMPUTER ORGANIZATION	PP	100	40	52	P	C	12. COMPUTER GRAPHICS		PP	100	40	43	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	40	P	C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	22	F
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	42	P	C	14. DATA STRUCTURES AND FILES		PP	100	40	50	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	43	P	C	15. DATA COMMUNICATION		PP	100	40	40	P
06. DIGITAL LABORATORY	TW	50	20	20	P	C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	19	P
07. DIGITAL LABORATORY	PR	50	20	28	P	C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	10	F
08. PROGRAMMING LABORATORY	TW	50	20	27	P	C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	12	P
09. PROGRAMMING LABORATORY	PR	50	20	27	P	C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	10	F
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	23	P	C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	32	P
							21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	10	F

GRAND TOTAL = 630/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

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S8058568   LAKHEPATIL AJINKYA RAMRAJE   HEMLATA   , 71045502L   , S8058568 ,   PICT   , S8058568

01. DISCRETE STRUCTURES	PP	100	40	54	P	C	11. ENG MATHS III		PP	100	40	40	P
02. COMPUTER ORGANIZATION	PP	100	40	46	P	C	12. COMPUTER GRAPHICS		PP	100	40	55	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	67	P	C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	40	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	49	P	C	14. DATA STRUCTURES AND FILES		PP	100	40	73	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	44	P	C	15. DATA COMMUNICATION		PP	100	40	50	P
06. DIGITAL LABORATORY	TW	50	20	35	P	C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	17	P

07. DIGITAL LABORATORY	PR	50	20	27	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	42	P
08. PROGRAMMING LABORATORY	TW	50	20	38	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	19	P
09. PROGRAMMING LABORATORY	PR	50	20	35	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	40	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	35	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	43	P
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	44	P

GRAND TOTAL = 893+07/1500, RESULT: FIRST CLASS [O.2]

ORDN. 1 MARKS :

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S8058569   LONKAR GORAKH SUBHASH   SHOBHA   , 71134968B   , S8058569 ,   PICT   , S8058569

01. DISCRETE STRUCTURES	PP	100	40	59	P C	11. ENG MATHS III	PP	100	40	43	P
02. COMPUTER ORGANIZATION	PP	100	40	71	P C	12. COMPUTER GRAPHICS	PP	100	40	54	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	57	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	44	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	70	P C	14. DATA STRUCTURES AND FILES	PP	100	40	75	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	41	P C	15. DATA COMMUNICATION	PP	100	40	68	P
06. DIGITAL LABORATORY	TW	50	20	38	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	23	P
07. DIGITAL LABORATORY	PR	50	20	38	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	39	P
08. PROGRAMMING LABORATORY	TW	50	20	44	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	23	P
09. PROGRAMMING LABORATORY	PR	50	20	44	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	39	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	38	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	46	P
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	38	P

GRAND TOTAL = 992/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

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UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(INFORMATION TECHNOLOGY)

DATE : 18 AUG. 2011   CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.   PAGE NO. 23 (445)

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 NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.

      OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S8058570   MAHAJAN AKSHATA DEVENDRA   JAISHREE   , 71045507M   , S8058570 ,   PICT   , S8058570

01. DISCRETE STRUCTURES	PP	100	40	74	P C		11. ENG MATHS III	PP	100	40	71	P
02. COMPUTER ORGANIZATION	PP	100	40	59	P C		12. COMPUTER GRAPHICS	PP	100	40	60	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	73	P C		13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	52	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	74	P C		14. DATA STRUCTURES AND FILES	PP	100	40	86	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	40	P C		15. DATA COMMUNICATION	PP	100	40	53	P
06. DIGITAL LABORATORY	TW	50	20	35	P C		16. PROCESSOR INTERFACING LABORATORY	TW	25	10	24	P
07. DIGITAL LABORATORY	PR	50	20	42	P C		17. PROCESSOR INTERFACING LABORATORY	OR	50	20	47	P
08. PROGRAMMING LABORATORY	TW	50	20	41	P C		18. DATA STRUCTURES AND FILES LAB	TW	25	10	20	P
09. PROGRAMMING LABORATORY	PR	50	20	44	P C		19. DATA STRUCTURES AND FILES LAB	PR	50	20	42	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	38	P C		20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	45	P
							21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	41	P

GRAND TOTAL = 1061/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

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S8058571   MAHALPURE SHRUTI SATISH   SUSHMA   , 71045509H   , S8058571 ,   PICT   , S8058571

01. DISCRETE STRUCTURES	PP	100	40	53	P C		11. ENG MATHS III	PP	100	40	76	P
02. COMPUTER ORGANIZATION	PP	100	40	46	P C		12. COMPUTER GRAPHICS	PP	100	40	55	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	52	P C		13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	41	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	69	P C		14. DATA STRUCTURES AND FILES	PP	100	40	66	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	49	P C		15. DATA COMMUNICATION	PP	100	40	57	P
06. DIGITAL LABORATORY	TW	50	20	41	P C		16. PROCESSOR INTERFACING LABORATORY	TW	25	10	18	P
07. DIGITAL LABORATORY	PR	50	20	38	P C		17. PROCESSOR INTERFACING LABORATORY	OR	50	20	44	P
08. PROGRAMMING LABORATORY	TW	50	20	43	P C		18. DATA STRUCTURES AND FILES LAB	TW	25	10	20	P
09. PROGRAMMING LABORATORY	PR	50	20	28	P C		19. DATA STRUCTURES AND FILES LAB	PR	50	20	25	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	44	P C		20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	45	P
							21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	32	P

GRAND TOTAL = 942/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

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S8058572   MARDA SAKSHI BRIJMOHAN   JYOTI   , 71045521G   , S8058572 ,   PICT   , S8058572

01. DISCRETE STRUCTURES	PP	100	40	66	P C		11. ENG MATHS III	PP	100	40	54	P
02. COMPUTER ORGANIZATION	PP	100	40	46	P C		12. COMPUTER GRAPHICS	PP	100	40	56	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	59	P C		13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	43	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	59	P C		14. DATA STRUCTURES AND FILES	PP	100	40	65	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	45	P C		15. DATA COMMUNICATION	PP	100	40	47	P
06. DIGITAL LABORATORY	TW	50	20	38	P C		16. PROCESSOR INTERFACING LABORATORY	TW	25	10	19	P
07. DIGITAL LABORATORY	PR	50	20	37	P C		17. PROCESSOR INTERFACING LABORATORY	OR	50	20	30	P
08. PROGRAMMING LABORATORY	TW	50	20	36	P C		18. DATA STRUCTURES AND FILES LAB	TW	25	10	16	P
09. PROGRAMMING LABORATORY	PR	50	20	30	P C		19. DATA STRUCTURES AND FILES LAB	PR	50	20	25	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	34	P C		20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	33	P
							21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	38	P

GRAND TOTAL = 876/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

. . . . UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(INFORMATION TECHNOLOGY) . . . .

DATE : 18 AUG. 2011   CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.   PAGE NO. 24 (446)

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S8058573   MARWA MAYUR RAJESH   ASHA   , 71045522E   , S8058573 ,   PICT   , S8058573

01. DISCRETE STRUCTURES	PP	100	40	65	P C		11. ENG MATHS III	PP	100	40	43	P
02. COMPUTER ORGANIZATION	PP	100	40	40	P C		12. COMPUTER GRAPHICS	PP	100	40	42	P

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03. DIGITAL ELECTRONICS & LOGIC DESIGPP  100  40  48  P C    13. PROCESSOR ARCHITECTURE & INTER.  PP  100  40  40  P
04. FUNDAMENTAL OF DATA STRUCTURES    PP  100  40  48  P C    14. DATA STRUCTURES AND FILES        PP  100  40  59  P
05. HUMANITIES AND SOCIAL SCIENCES    PP  100  40  54  P C    15. DATA COMMUNICATION               PP  100  40  42  P
06. DIGITAL LABORATORY                TW   50  20  20  P C    16. PROCESSOR INTERFACING LABORATORY TW   25  10  17  P
07. DIGITAL LABORATORY                PR   50  20  25  P C    17. PROCESSOR INTERFACING LABORATORY OR   50  20  29  P
08. PROGRAMMING LABORATORY            TW   50  20  30  P C    18. DATA STRUCTURES AND FILES LAB    TW   25  10  13  P
09. PROGRAMMING LABORATORY            PR   50  20  40  P C    19. DATA STRUCTURES AND FILES LAB    PR   50  20  39  P
10. COMMUNICATION AND LANGUAGE LAB.   TW   50  20  29  P C    20. OBJECT ORIENTED PROGRAMMING LAB  TW   50  20  33  P
                                                              21. OBJECT ORIENTED PROGRAMMING LAB  PR   50  20  20  P
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GRAND TOTAL = 776/1500, RESULT: SECOND CLASS

ORDN. 1 MARKS :

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S8058574   MAYANK SINGH   ARCHANA   , 71045524M   , S8058574 ,   PICT   , S8058574

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01. DISCRETE STRUCTURES               PP  100  40  52  P C    11. ENG MATHS III                    PP  100  40  44  P
02. COMPUTER ORGANIZATION             PP  100  40  56  P C    12. COMPUTER GRAPHICS                PP  100  40  40  P
03. DIGITAL ELECTRONICS & LOGIC DESIGPP  100  40  56  P C    13. PROCESSOR ARCHITECTURE & INTER.  PP  100  40  55  P
04. FUNDAMENTAL OF DATA STRUCTURES    PP  100  40  55  P C    14. DATA STRUCTURES AND FILES        PP  100  40  56  P
05. HUMANITIES AND SOCIAL SCIENCES    PP  100  40  50  P C    15. DATA COMMUNICATION               PP  100  40  53  P
06. DIGITAL LABORATORY                TW   50  20  26  P C    16. PROCESSOR INTERFACING LABORATORY TW   25  10  17  P
07. DIGITAL LABORATORY                PR   50  20  27  P C    17. PROCESSOR INTERFACING LABORATORY OR   50  20  40  P
08. PROGRAMMING LABORATORY            TW   50  20  32  P C    18. DATA STRUCTURES AND FILES LAB    TW   25  10  20  P
09. PROGRAMMING LABORATORY            PR   50  20  44  P C    19. DATA STRUCTURES AND FILES LAB    PR   50  20  36  P
10. COMMUNICATION AND LANGUAGE LAB.   TW   50  20  26  P C    20. OBJECT ORIENTED PROGRAMMING LAB  TW   50  20  37  P
                                                              21. OBJECT ORIENTED PROGRAMMING LAB  PR   50  20  43  P
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GRAND TOTAL = 865/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

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S8058575   MOHAMMED AAQUIB ANSARI MOHAMMED YUSUF   SUFIYA   , 71045526H   , S8058575 ,   PICT   , S8058575

01. DISCRETE STRUCTURES	PP	100	40	43	P		11. ENG MATHS III	PP	100	40	14 F
02. COMPUTER ORGANIZATION	PP	100	40	41	P C		12. COMPUTER GRAPHICS	PP	100	40	50 P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	45	P C		13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	30 F
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	40	P C		14. DATA STRUCTURES AND FILES	PP	100	40	73 P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	40	P C		15. DATA COMMUNICATION	PP	100	40	45 P
06. DIGITAL LABORATORY	TW	50	20	20	P C		16. PROCESSOR INTERFACING LABORATORY	TW	25	10	10 P
07. DIGITAL LABORATORY	PR	50	20	06	F		17. PROCESSOR INTERFACING LABORATORY	OR	50	20	08 F
08. PROGRAMMING LABORATORY	TW	50	20	20	P C		18. DATA STRUCTURES AND FILES LAB	TW	25	10	13 P
09. PROGRAMMING LABORATORY	PR	50	20	35	P C		19. DATA STRUCTURES AND FILES LAB	PR	50	20	10 F
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	20	P C		20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	20 P
							21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	20 P

GRAND TOTAL = 603/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

. . . .
              UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(INFORMATION TECHNOLOGY)
      DATE : 18 AUG. 2011   CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.   PAGE NO. 25 (447)
. . . .
  NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.
        OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
. . . .

S8058576   MULAY AMIT YESHWANT   SNEHAL   , 71045530F   , S8058576 ,   PICT   , S8058576

01. DISCRETE STRUCTURES	PP	100	40	61	P C		11. ENG MATHS III	PP	100	40	27 F
02. COMPUTER ORGANIZATION	PP	100	40	44	P C		12. COMPUTER GRAPHICS	PP	100	40	56 P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	44	P C		13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	15 F
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	41	P C		14. DATA STRUCTURES AND FILES	PP	100	40	51 P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	53	P C		15. DATA COMMUNICATION	PP	100	40	40 P
06. DIGITAL LABORATORY	TW	50	20	20	P C		16. PROCESSOR INTERFACING LABORATORY	TW	25	10	10 P
07. DIGITAL LABORATORY	PR	50	20	38	P C		17. PROCESSOR INTERFACING LABORATORY	OR	50	20	07 F

08. PROGRAMMING LABORATORY	TW	50	20	25	P C		18. DATA STRUCTURES AND FILES LAB	TW	25	10	12	P
09. PROGRAMMING LABORATORY	PR	50	20	29	P		19. DATA STRUCTURES AND FILES LAB	PR	50	20	AA	F
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	25	P C		20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	23	P
							21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	25	P

GRAND TOTAL = 646/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

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S8058577   MULEY PRASAD MUKUNDRAO   SUSHMA   , 71134969L   , S8058577 , PICT   , S8058577

01. DISCRETE STRUCTURES	PP	100	40	44	P C		11. ENG MATHS III	PP	100	40	26	F
02. COMPUTER ORGANIZATION	PP	100	40	52	P C		12. COMPUTER GRAPHICS	PP	100	40	48	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	58	P C		13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	54	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	52	P C		14. DATA STRUCTURES AND FILES	PP	100	40	63	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	47	P C		15. DATA COMMUNICATION	PP	100	40	56	P
06. DIGITAL LABORATORY	TW	50	20	38	P C		16. PROCESSOR INTERFACING LABORATORY	TW	25	10	18	P
07. DIGITAL LABORATORY	PR	50	20	27	P C		17. PROCESSOR INTERFACING LABORATORY	OR	50	20	34	P
08. PROGRAMMING LABORATORY	TW	50	20	43	P C		18. DATA STRUCTURES AND FILES LAB	TW	25	10	19	P
09. PROGRAMMING LABORATORY	PR	50	20	38	P C		19. DATA STRUCTURES AND FILES LAB	PR	50	20	41	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	34	P C		20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	42	P
							21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	28	P

GRAND TOTAL = 862/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

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S8058578   MULLA ASMA MUSA   RABIYA   , 71134970D   , S8058578 , PICT   , S8058578

01. DISCRETE STRUCTURES	PP	100	40	49	P		11. ENG MATHS III	PP	100	40	24	F
02. COMPUTER ORGANIZATION	PP	100	40	40	P C		12. COMPUTER GRAPHICS	PP	100	40	32	F
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	18	F		13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	30	F
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	48	P		14. DATA STRUCTURES AND FILES	PP	100	40	54	P

05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	44	P C	15. DATA COMMUNICATION	PP	100	40	43	P
06. DIGITAL LABORATORY	TW	50	20	38	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	21	P
07. DIGITAL LABORATORY	PR	50	20	22	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	29	P
08. PROGRAMMING LABORATORY	TW	50	20	42	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	22	P
09. PROGRAMMING LABORATORY	PR	50	20	33	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	22	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	37	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	43	P
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	20	P

GRAND TOTAL = 711/1500, RESULT: FAILS

ORDN. 1 MARKS :

. . . . UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(INFORMATION TECHNOLOGY) . . . .

DATE : 18 AUG. 2011   CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.   PAGE NO. 26 (448)

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.

          OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

. . . .

S8058579   MURKUTE DEVYANI BALASAHEB   KALPANA   , 71134971B   , S8058579 ,   PICT   , S8058579

01. DISCRETE STRUCTURES	PP	100	40	45	P C	11. ENG MATHS III	PP	100	40	49	P
02. COMPUTER ORGANIZATION	PP	100	40	47	P C	12. COMPUTER GRAPHICS	PP	100	40	60	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	40	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	49	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	48	P C	14. DATA STRUCTURES AND FILES	PP	100	40	76	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	40	P C	15. DATA COMMUNICATION	PP	100	40	65	P
06. DIGITAL LABORATORY	TW	50	20	36	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	19	P
07. DIGITAL LABORATORY	PR	50	20	28	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	37	P
08. PROGRAMMING LABORATORY	TW	50	20	37	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	20	P
09. PROGRAMMING LABORATORY	PR	50	20	31	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	30	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	40	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	46	P
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	25	P

GRAND TOTAL = 868/1500, RESULT: HIGHER SECOND CLASS

cols05

ORDN. 1 MARKS :

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S8058580   NAIKNAWARE UTKARSH MARUTI   MAHANANDA   , 71045538M   , S8058580 ,   PICT   , S8058580

01. DISCRETE STRUCTURES	PP	100	40	54	P C	11. ENG MATHS III	PP	100	40	45	P
02. COMPUTER ORGANIZATION	PP	100	40	46	P C	12. COMPUTER GRAPHICS	PP	100	40	51	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	45	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	26	F
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	40	P C	14. DATA STRUCTURES AND FILES	PP	100	40	66	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	52	P C	15. DATA COMMUNICATION	PP	100	40	40	P
06. DIGITAL LABORATORY	TW	50	20	42	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	21	P
07. DIGITAL LABORATORY	PR	50	20	32	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	25	P
08. PROGRAMMING LABORATORY	TW	50	20	42	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	20	P
09. PROGRAMMING LABORATORY	PR	50	20	37	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	32	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	40	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	43	P
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	42	P

GRAND TOTAL = 841/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

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S8058581   NIKAM SONALI JIVAN   ARUNA   , 71134972L   , S8058581 ,   PICT   , S8058581

01. DISCRETE STRUCTURES	PP	100	40	41	P	11. ENG MATHS III	PP	100	40	40	P
02. COMPUTER ORGANIZATION	PP	100	40	53	P C	12. COMPUTER GRAPHICS	PP	100	40	64	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	53	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	44	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	54	P C	14. DATA STRUCTURES AND FILES	PP	100	40	64	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	49	P C	15. DATA COMMUNICATION	PP	100	40	62	P
06. DIGITAL LABORATORY	TW	50	20	38	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	20	P
07. DIGITAL LABORATORY	PR	50	20	25	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	28	P
08. PROGRAMMING LABORATORY	TW	50	20	41	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	21	P
09. PROGRAMMING LABORATORY	PR	50	20	42	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	20	P

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10. COMMUNICATION AND LANGUAGE LAB.  TW   50   20   39  P C     20. OBJECT ORIENTED PROGRAMMING LAB  TW   50   20   41  P
                                                                21. OBJECT ORIENTED PROGRAMMING LAB  PR   50   20   22  P
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GRAND TOTAL = 861/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

. . . . UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(INFORMATION TECHNOLOGY) . . . .

    DATE : 18 AUG. 2011   CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.   PAGE NO. 27 (449)

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  NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.

         OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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  S8058582   NILESH D PHADTARE   KUNDA   , 70925526J   , S8058582 ,   PICT   , S8058582

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01. DISCRETE STRUCTURES            PP  100  40   43  P C     11. ENG MATHS III                    PP  100  40   40  P
02. COMPUTER ORGANIZATION          PP  100  40   50  P       12. COMPUTER GRAPHICS                PP  100  40   43  P
03. DIGITAL ELECTRONICS & LOGIC DESIGPP  100  40   50  P C   13. PROCESSOR ARCHITECTURE & INTER.  PP  100  40   18  F
04. FUNDAMENTAL OF DATA STRUCTURES  PP  100  40   40  P C    14. DATA STRUCTURES AND FILES        PP  100  40   72  P
05. HUMANITIES AND SOCIAL SCIENCES  PP  100  40   40  P C    15. DATA COMMUNICATION               PP  100  40   43  P
06. DIGITAL LABORATORY             TW   50  20   35  P C     16. PROCESSOR INTERFACING LABORATORY TW   25  10   20  P
07. DIGITAL LABORATORY             PR   50  20   31  P C     17. PROCESSOR INTERFACING LABORATORY OR   50  20   12  F
08. PROGRAMMING LABORATORY         TW   50  20   43  P C     18. DATA STRUCTURES AND FILES LAB    TW   25  10   23  P
09. PROGRAMMING LABORATORY         PR   50  20   32  P C     19. DATA STRUCTURES AND FILES LAB    PR   50  20   10  F
10. COMMUNICATION AND LANGUAGE LAB. TW   50  20   36  P C    20. OBJECT ORIENTED PROGRAMMING LAB  TW   50  20   44  P
                                                            21. OBJECT ORIENTED PROGRAMMING LAB  PR   50  20   16  F
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GRAND TOTAL = 741/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

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  S8058583   NIMBALKAR ABHIJIT SHIRISH   SUREKHA   , 71057179J   , S8058583 ,   PICT   , S8058583

01. DISCRETE STRUCTURES	PP	100	40	58	P C	11. ENG MATHS III	PP	100	40	83	P
02. COMPUTER ORGANIZATION	PP	100	40	48	P C	12. COMPUTER GRAPHICS	PP	100	40	57	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	62	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	49	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	65	P C	14. DATA STRUCTURES AND FILES	PP	100	40	74	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	46	P C	15. DATA COMMUNICATION	PP	100	40	58	P
06. DIGITAL LABORATORY	TW	50	20	28	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	21	P
07. DIGITAL LABORATORY	PR	50	20	36	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	40	P
08. PROGRAMMING LABORATORY	TW	50	20	36	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	22	P
09. PROGRAMMING LABORATORY	PR	50	20	42	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	43	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	28	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	46	P
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	38	P

GRAND TOTAL = 980/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

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S8058584   PANDIT DIVYA SYAMANTAKMANI   VEENA   , 71045552G   , S8058584 ,   PICT   , S8058584

01. DISCRETE STRUCTURES	PP	100	40	63	P C	11. ENG MATHS III	PP	100	40	46	P
02. COMPUTER ORGANIZATION	PP	100	40	47	P C	12. COMPUTER GRAPHICS	PP	100	40	45	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	61	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	46	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	59	P C	14. DATA STRUCTURES AND FILES	PP	100	40	60	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	45	P C	15. DATA COMMUNICATION	PP	100	40	53	P
06. DIGITAL LABORATORY	TW	50	20	27	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	18	P
07. DIGITAL LABORATORY	PR	50	20	34	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	38	P
08. PROGRAMMING LABORATORY	TW	50	20	38	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	15	P
09. PROGRAMMING LABORATORY	PR	50	20	45	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	37	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	35	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	39	P
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	32	P

GRAND TOTAL = 883+05/1500, RESULT: HIGHER SECOND CLASS[O.163]

ORDN. 1 MARKS :

cols05

. . . . UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(INFORMATION TECHNOLOGY) . . . .

DATE : 18 AUG. 2011   CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.   PAGE NO. 28 (450)

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

. . . .

S8058585   PANSARE ROHAN SAMPAT   MANGAL   , 71045554C   , S8058585 , PICT   , S8058585

01. DISCRETE STRUCTURES	PP	100	40	59	P C	11. ENG MATHS III		PP	100	40	45	P
02. COMPUTER ORGANIZATION	PP	100	40	53	P C	12. COMPUTER GRAPHICS		PP	100	40	58	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	56	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	60	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	55	P C	14. DATA STRUCTURES AND FILES		PP	100	40	73	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	52	P C	15. DATA COMMUNICATION		PP	100	40	58	P
06. DIGITAL LABORATORY	TW	50	20	31	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	15	P
07. DIGITAL LABORATORY	PR	50	20	34	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	25	P
08. PROGRAMMING LABORATORY	TW	50	20	37	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	19	P
09. PROGRAMMING LABORATORY	PR	50	20	35	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	30	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	35	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	39	P
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	36	P

GRAND TOTAL = 905/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

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S8058586   PARADKAR SUPRIYA SUNIL   CHITRA   , 71045555M   , S8058586 , PICT   , S8058586

01. DISCRETE STRUCTURES	PP	100	40	57	P C	11. ENG MATHS III		PP	100	40	44	P
02. COMPUTER ORGANIZATION	PP	100	40	45	P C	12. COMPUTER GRAPHICS		PP	100	40	55	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	51	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	45	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	49	P C	14. DATA STRUCTURES AND FILES		PP	100	40	77	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	40	P C	15. DATA COMMUNICATION		PP	100	40	49	P

06. DIGITAL LABORATORY	TW	50	20	38	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	20	P
07. DIGITAL LABORATORY	PR	50	20	30	P C	17. PROCESSOR INTERFACING LABORATORY	OR		50	20	39	P
08. PROGRAMMING LABORATORY	TW	50	20	44	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	19	P
09. PROGRAMMING LABORATORY	PR	50	20	40	P C	19. DATA STRUCTURES AND FILES LAB	PR		50	20	28	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	40	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW		50	20	43	P
						21. OBJECT ORIENTED PROGRAMMING LAB	PR		50	20	20#	P

GRAND TOTAL = 873+04/1500, RESULT: HIGHER SECOND CLASS #[O.163+O.1][O.163]

ORDN. 1 MARKS : (21)2,

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S8058587   PARAKH KUNAL KAILAS   MEENA   , 71045556K   , S8058587 ,   PICT   , S8058587

01. DISCRETE STRUCTURES	PP	100	40	58	P C	11. ENG MATHS III	PP	100	40	43	P
02. COMPUTER ORGANIZATION	PP	100	40	67	P C	12. COMPUTER GRAPHICS	PP	100	40	69	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	65	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	52	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	62	P C	14. DATA STRUCTURES AND FILES	PP	100	40	76	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	57	P C	15. DATA COMMUNICATION	PP	100	40	63	P
06. DIGITAL LABORATORY	TW	50	20	38	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	19	P
07. DIGITAL LABORATORY	PR	50	20	42	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	39	P
08. PROGRAMMING LABORATORY	TW	50	20	41	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	18	P
09. PROGRAMMING LABORATORY	PR	50	20	45	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	22	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	30	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	35	P
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	40	P

GRAND TOTAL = 981/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

. . . .
                    UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(INFORMATION TECHNOLOGY)

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 NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S8058588   PAREKH SONALI SUNIL   REKHA   , 71045558F   , S8058588 , PICT   , S8058588

01. DISCRETE STRUCTURES	PP	100	40	52	P	C	11. ENG MATHS III	PP	100	40	82	P	
02. COMPUTER ORGANIZATION	PP	100	40	62	P	C	12. COMPUTER GRAPHICS	PP	100	40	53	P	
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	51	P	C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	40	P	
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	68	P	C	14. DATA STRUCTURES AND FILES	PP	100	40	77	P	
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	56	P	C	15. DATA COMMUNICATION	PP	100	40	56	P	
06. DIGITAL LABORATORY	TW	50	20	33	P	C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	20	P	
07. DIGITAL LABORATORY	PR	50	20	39	P	C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	44	P	
08. PROGRAMMING LABORATORY	TW	50	20	37	P	C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	18	P	
09. PROGRAMMING LABORATORY	PR	50	20	41	P	C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	40	P	
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	38	P	C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	42	P	
							21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	42	P	

GRAND TOTAL = 991+05/1500, RESULT: FIRST CLASS WITH DISTINCTION[O.163]

ORDN. 1 MARKS :

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S8058589   PARTH SARTHI PIPLANI   SUREKHA   , 71045560H   , S8058589 , PICT   , S8058589

01. DISCRETE STRUCTURES	PP	100	40	56	P	C	11. ENG MATHS III	PP	100	40	40	P	
02. COMPUTER ORGANIZATION	PP	100	40	43	P	C	12. COMPUTER GRAPHICS	PP	100	40	54	P	
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	40	P	C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	16	F	
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	45	P	C	14. DATA STRUCTURES AND FILES	PP	100	40	72	P	
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	44	P	C	15. DATA COMMUNICATION	PP	100	40	40	P	
06. DIGITAL LABORATORY	TW	50	20	22	P	C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	10	P	
07. DIGITAL LABORATORY	PR	50	20	23	P	C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	05	F	
08. PROGRAMMING LABORATORY	TW	50	20	23	P	C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	19	P	
09. PROGRAMMING LABORATORY	PR	50	20	25	P	C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	41	P	
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	35	P	C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	37	P	

21. OBJECT ORIENTED PROGRAMMING LAB PR   50   20   22 P

GRAND TOTAL = 712/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

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  S8058590   PATEL NAZNIN JAVED   MAHERUNISSA   , 71134973J   , S8058590 ,   PICT   , S8058590

  01. DISCRETE STRUCTURES   PP   100   40   40 P C   11. ENG MATHS III   PP   100   40   24 F
  02. COMPUTER ORGANIZATION   PP   100   40   40 P C   12. COMPUTER GRAPHICS   PP   100   40   48 P
  03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 40   45 P C   13. PROCESSOR ARCHITECTURE & INTER. PP   100   40   31 F
  04. FUNDAMENTAL OF DATA STRUCTURES PP 100   40   44 P C   14. DATA STRUCTURES AND FILES   PP   100   40   64 P
  05. HUMANITIES AND SOCIAL SCIENCES PP 100   40   46 P C   15. DATA COMMUNICATION   PP   100   40   53 P
  06. DIGITAL LABORATORY   TW   50   20   35 P C   16. PROCESSOR INTERFACING LABORATORY TW   25   10   14 P
  07. DIGITAL LABORATORY   PR   50   20   40 P C   17. PROCESSOR INTERFACING LABORATORY OR   50   20   06 F
  08. PROGRAMMING LABORATORY   TW   50   20   42 P C   18. DATA STRUCTURES AND FILES LAB   TW   25   10   19 P
  09. PROGRAMMING LABORATORY   PR   50   20   22 P C   19. DATA STRUCTURES AND FILES LAB   PR   50   20   35 P
  10. COMMUNICATION AND LANGUAGE LAB. TW 50   20   43 P C   20. OBJECT ORIENTED PROGRAMMING LAB TW   50   20   41 P
                                                                21. OBJECT ORIENTED PROGRAMMING LAB PR   50   20   35 P

GRAND TOTAL = 767/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

. . . . UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(INFORMATION TECHNOLOGY) . . . .

   DATE : 18 AUG. 2011   CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.   PAGE NO. 30 (452)

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 NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.

       OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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  S8058591   PATIL DNYANESHWARI PRAKASH   SANGITA   , 71045563B   , S8058591 ,   PICT   , S8058591

  01. DISCRETE STRUCTURES   PP   100   40   56 P C   11. ENG MATHS III   PP   100   40   57 P

02. COMPUTER ORGANIZATION	PP	100	40	40	P C		12. COMPUTER GRAPHICS	PP	100	40	52	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	63	P C		13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	64	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	64	P C		14. DATA STRUCTURES AND FILES	PP	100	40	79	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	44	P C		15. DATA COMMUNICATION	PP	100	40	59	P
06. DIGITAL LABORATORY	TW	50	20	40	P C		16. PROCESSOR INTERFACING LABORATORY	TW	25	10	22	P
07. DIGITAL LABORATORY	PR	50	20	40	P C		17. PROCESSOR INTERFACING LABORATORY	OR	50	20	44	P
08. PROGRAMMING LABORATORY	TW	50	20	41	P C		18. DATA STRUCTURES AND FILES LAB	TW	25	10	22	P
09. PROGRAMMING LABORATORY	PR	50	20	43	P C		19. DATA STRUCTURES AND FILES LAB	PR	50	20	42	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	37	P C		20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	44	P
							21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	32	P

GRAND TOTAL = 985/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

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S8058592   PATIL KHUSHBU PRAKASH   CHHAYA   , 71045565J   , S8058592 ,   PICT   , S8058592

01. DISCRETE STRUCTURES	PP	100	40	55	P C		11. ENG MATHS III	PP	100	40	55	P
02. COMPUTER ORGANIZATION	PP	100	40	61	P C		12. COMPUTER GRAPHICS	PP	100	40	59	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	59	P C		13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	51	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	64	P C		14. DATA STRUCTURES AND FILES	PP	100	40	76	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	48	P C		15. DATA COMMUNICATION	PP	100	40	57	P
06. DIGITAL LABORATORY	TW	50	20	33	P C		16. PROCESSOR INTERFACING LABORATORY	TW	25	10	19	P
07. DIGITAL LABORATORY	PR	50	20	34	P C		17. PROCESSOR INTERFACING LABORATORY	OR	50	20	37	P
08. PROGRAMMING LABORATORY	TW	50	20	39	P C		18. DATA STRUCTURES AND FILES LAB	TW	25	10	15	P
09. PROGRAMMING LABORATORY	PR	50	20	41	P C		19. DATA STRUCTURES AND FILES LAB	PR	50	20	35	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	39	P C		20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	37	P
							21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	36	P

GRAND TOTAL = 950+05/1500, RESULT: FIRST CLASS[O.163]

ORDN. 1 MARKS :

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S8058593   PAWAR BHANUPRIYA VITTHAL   SHAKUNTALA   , 71045571C   , S8058593 , PICT   , S8058593

01. DISCRETE STRUCTURES	PP	100	40	47	P C	11. ENG MATHS III	PP	100	40	52	P
02. COMPUTER ORGANIZATION	PP	100	40	51	P C	12. COMPUTER GRAPHICS	PP	100	40	43	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	54	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	40	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	40	P C	14. DATA STRUCTURES AND FILES	PP	100	40	86	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	43	P C	15. DATA COMMUNICATION	PP	100	40	52	P
06. DIGITAL LABORATORY	TW	50	20	33	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	21	P
07. DIGITAL LABORATORY	PR	50	20	34	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	20	P
08. PROGRAMMING LABORATORY	TW	50	20	35	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	16	P
09. PROGRAMMING LABORATORY	PR	50	20	42	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	37	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	33	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	41	P
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	40	P

GRAND TOTAL = 860/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

. . . .
              UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(INFORMATION TECHNOLOGY)
     DATE : 18 AUG. 2011   CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.   PAGE NO. 31 (453)
. . . .
 NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.

       OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S8058594   PHUTANE AMEY UDAY   SARITA   , 71054577M   , S8058594 , PICT   , S8058594

01. DISCRETE STRUCTURES	PP	100	40	67	P C	11. ENG MATHS III	PP	100	40	50	P
02. COMPUTER ORGANIZATION	PP	100	40	55	P C	12. COMPUTER GRAPHICS	PP	100	40	61	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	60	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	60	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	56	P C	14. DATA STRUCTURES AND FILES	PP	100	40	78	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	56	P C	15. DATA COMMUNICATION	PP	100	40	66	P
06. DIGITAL LABORATORY	TW	50	20	28	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	21	P

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07. DIGITAL LABORATORY              PR   50   20   38  P C    17. PROCESSOR INTERFACING LABORATORY OR     50   20   39  P
08. PROGRAMMING LABORATORY          TW   50   20   37  P C    18. DATA STRUCTURES AND FILES LAB    TW    25   10   15  P
09. PROGRAMMING LABORATORY          PR   50   20   48  P C    19. DATA STRUCTURES AND FILES LAB    PR    50   20   44  P
10. COMMUNICATION AND LANGUAGE LAB. TW   50   20   35  P C    20. OBJECT ORIENTED PROGRAMMING LAB  TW    50   20   37  P
                                                              21. OBJECT ORIENTED PROGRAMMING LAB  PR    50   20   40  P
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GRAND TOTAL = 991/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

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S8058595    PINGLE ADITYA ULHAS                ARCHANA          , 71134974G    , S8058595 ,  PICT      , S8058595

01. DISCRETE STRUCTURES             PP  100   40   59  P C    11. ENG MATHS III                    PP   100   40   40  P
02. COMPUTER ORGANIZATION           PP  100   40   40  P C    12. COMPUTER GRAPHICS                PP   100   40   55  P
03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100   40   40  P C    13. PROCESSOR ARCHITECTURE & INTER.  PP   100   40   40  P
04. FUNDAMENTAL OF DATA STRUCTURES   PP  100   40   49  P C    14. DATA STRUCTURES AND FILES        PP   100   40   61  P
05. HUMANITIES AND SOCIAL SCIENCES   PP  100   40   53  P C    15. DATA COMMUNICATION               PP   100   40   54  P
06. DIGITAL LABORATORY              TW   50   20   39  P C    16. PROCESSOR INTERFACING LABORATORY TW    25   10   19  P
07. DIGITAL LABORATORY              PR   50   20   34  P C    17. PROCESSOR INTERFACING LABORATORY OR     50   20   40  P
08. PROGRAMMING LABORATORY          TW   50   20   42  P C    18. DATA STRUCTURES AND FILES LAB    TW    25   10   20  P
09. PROGRAMMING LABORATORY          PR   50   20   37  P C    19. DATA STRUCTURES AND FILES LAB    PR    50   20   38  P
10. COMMUNICATION AND LANGUAGE LAB. TW   50   20   38  P C    20. OBJECT ORIENTED PROGRAMMING LAB  TW    50   20   41  P
                                                              21. OBJECT ORIENTED PROGRAMMING LAB  PR    50   20   42  P
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GRAND TOTAL = 881/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

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S8058596    POLE AKSHAYKUMAR RAHUL             SHEELA           , 71045576D    , S8058596 ,  PICT      , S8058596

01. DISCRETE STRUCTURES             PP  100   40   43  P       11. ENG MATHS III                    PP   100   40   40  P
02. COMPUTER ORGANIZATION           PP  100   40   41  P       12. COMPUTER GRAPHICS                PP   100   40   44  P
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03. DIGITAL ELECTRONICS & LOGIC DESIGPP    100   40   40  P C    13. PROCESSOR ARCHITECTURE & INTER.   PP   100   40   40  P
04. FUNDAMENTAL OF DATA STRUCTURES   PP    100   40   49  P C    14. DATA STRUCTURES AND FILES         PP   100   40   46  P
05. HUMANITIES AND SOCIAL SCIENCES   PP    100   40   47  P C    15. DATA COMMUNICATION                PP   100   40   26  F
06. DIGITAL LABORATORY               TW     50   20   30  P C    16. PROCESSOR INTERFACING LABORATORY  TW    25   10   12  P
07. DIGITAL LABORATORY               PR     50   20   20  P      17. PROCESSOR INTERFACING LABORATORY  OR    50   20   AA  F
08. PROGRAMMING LABORATORY           TW     50   20   35  P C    18. DATA STRUCTURES AND FILES LAB     TW    25   10   14  P
09. PROGRAMMING LABORATORY           PR     50   20   29  P C    19. DATA STRUCTURES AND FILES LAB     PR    50   20   AA  F
10. COMMUNICATION AND LANGUAGE LAB.  TW     50   20   38  P C    20. OBJECT ORIENTED PROGRAMMING LAB   TW    50   20   27  P
                                                                 21. OBJECT ORIENTED PROGRAMMING LAB   PR    50   20   32  P
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GRAND TOTAL = 653+05/1500, RESULT: FAILS A.T.K.T.[O.163]

ORDN. 1 MARKS :

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             UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(INFORMATION TECHNOLOGY)
    DATE : 18 AUG. 2011   CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.   PAGE NO. 32 (454)
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NOTE: FIRST LINE : SEAT NO.,  NAME OF THE CANDIDATE,  MOTHER, PERMANENT REG. NO.,  PREVIOUS SEAT NO.,  COLLEGE,   SEAT NO.
         OTHER LINES: HEAD OF PASSING,  MAX. MARKS,  MIN. PASS MARKS,  MARKS OBTAINED,  P/F:PASS/FAIL,  C:PREVIOUS CARRY OVER
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S8058597   POTE SMITA RAGHUNATH              VIJAYA            , 71045577B    , S8058597 ,  PICT     , S8058597

01. DISCRETE STRUCTURES              PP    100   40   49  P C    11. ENG MATHS III                     PP   100   40   60  P
02. COMPUTER ORGANIZATION            PP    100   40   50  P C    12. COMPUTER GRAPHICS                 PP   100   40   54  P
03. DIGITAL ELECTRONICS & LOGIC DESIGPP    100   40   44  P C    13. PROCESSOR ARCHITECTURE & INTER.   PP   100   40   26  F
04. FUNDAMENTAL OF DATA STRUCTURES   PP    100   40   42  P C    14. DATA STRUCTURES AND FILES         PP   100   40   59  P
05. HUMANITIES AND SOCIAL SCIENCES   PP    100   40   41  P C    15. DATA COMMUNICATION                PP   100   40   40  P
06. DIGITAL LABORATORY               TW     50   20   35  P C    16. PROCESSOR INTERFACING LABORATORY  TW    25   10   19  P
07. DIGITAL LABORATORY               PR     50   20   28  P C    17. PROCESSOR INTERFACING LABORATORY  OR    50   20   35  P
08. PROGRAMMING LABORATORY           TW     50   20   42  P C    18. DATA STRUCTURES AND FILES LAB     TW    25   10   20  P
09. PROGRAMMING LABORATORY           PR     50   20   44  P C    19. DATA STRUCTURES AND FILES LAB     PR    50   20   38  P
10. COMMUNICATION AND LANGUAGE LAB.  TW     50   20   41  P C    20. OBJECT ORIENTED PROGRAMMING LAB   TW    50   20   48  P
                                                                 21. OBJECT ORIENTED PROGRAMMING LAB   PR    50   20   25  P
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GRAND TOTAL = 840/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

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S8058598   POTEY AKSHAY MADHUKAR   NILIMA   , 71045578L   , S8058598 ,   PICT   , S8058598

01. DISCRETE STRUCTURES	PP	100	40	51	P C	11. ENG MATHS III	PP	100	40	40	P
02. COMPUTER ORGANIZATION	PP	100	40	41	P C	12. COMPUTER GRAPHICS	PP	100	40	48	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	40	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	AA	F
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	49	P C	14. DATA STRUCTURES AND FILES	PP	100	40	41	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	45	P C	15. DATA COMMUNICATION	PP	100	40	53	P
06. DIGITAL LABORATORY	TW	50	20	33	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	10	P
07. DIGITAL LABORATORY	PR	50	20	42	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	AA	F
08. PROGRAMMING LABORATORY	TW	50	20	35	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	12	P
09. PROGRAMMING LABORATORY	PR	50	20	45	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	40	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	37	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	20	P
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	32	P

GRAND TOTAL = 714/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

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S8058599   PRIYA KASHYAP   REETA   , 71045582J   , S8058599 ,   PICT   , S8058599

01. DISCRETE STRUCTURES	PP	100	40	62	P C	11. ENG MATHS III	PP	100	40	48	P
02. COMPUTER ORGANIZATION	PP	100	40	45	P C	12. COMPUTER GRAPHICS	PP	100	40	50	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	58	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	51	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	58	P C	14. DATA STRUCTURES AND FILES	PP	100	40	73	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	53	P C	15. DATA COMMUNICATION	PP	100	40	54	P
06. DIGITAL LABORATORY	TW	50	20	38	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	18	P
07. DIGITAL LABORATORY	PR	50	20	42	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	34	P

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08. PROGRAMMING LABORATORY          TW   50   20   39  P C    18. DATA STRUCTURES AND FILES LAB    TW   25   10   16  P

09. PROGRAMMING LABORATORY          PR   50   20   40  P C    19. DATA STRUCTURES AND FILES LAB    PR   50   20   28  P

10. COMMUNICATION AND LANGUAGE LAB. TW   50   20   35  P C    20. OBJECT ORIENTED PROGRAMMING LAB  TW   50   20   36  P

                                                             21. OBJECT ORIENTED PROGRAMMING LAB  PR   50   20   20  P
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GRAND TOTAL = 898+02/1500, RESULT: FIRST CLASS [O.2]

ORDN. 1 MARKS :


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. . . . . . . . . . . . . . . . . UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(INFORMATION TECHNOLOGY) . . . . . . . . . . . . . . . . . . . . . . . . . . .

    DATE : 18 AUG. 2011           CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.         PAGE NO. 33  (  455)

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 NOTE: FIRST LINE : SEAT NO.,  NAME OF THE CANDIDATE,  MOTHER, PERMANENT REG. NO.,  PREVIOUS SEAT NO.,  COLLEGE,   SEAT NO.

       OTHER LINES: HEAD OF PASSING,  MAX. MARKS,  MIN. PASS MARKS,  MARKS OBTAINED,  P/F:PASS/FAIL,  C:PREVIOUS CARRY OVER

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 S8058600   PUNTAMBEKAR SHREYA SHAILESH          SEEMA              , 71045584E    , S8058600 ,   PICT      , S8058600

01. DISCRETE STRUCTURES             PP  100   40   40  P C    11. ENG MATHS III                    PP  100   40   18  F

02. COMPUTER ORGANIZATION           PP  100   40   44  P C    12. COMPUTER GRAPHICS                PP  100   40   40  P

03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100   40   40  P C    13. PROCESSOR ARCHITECTURE & INTER.  PP  100   40   28  F

04. FUNDAMENTAL OF DATA STRUCTURES  PP  100   40   40  P C    14. DATA STRUCTURES AND FILES        PP  100   40   40  P

05. HUMANITIES AND SOCIAL SCIENCES  PP  100   40   48  P C    15. DATA COMMUNICATION               PP  100   40   40  P

06. DIGITAL LABORATORY              TW   50   20   27  P C    16. PROCESSOR INTERFACING LABORATORY TW   25   10   19  P

07. DIGITAL LABORATORY              PR   50   20   22  P      17. PROCESSOR INTERFACING LABORATORY OR   50   20   10  F

08. PROGRAMMING LABORATORY          TW   50   20   27  P C    18. DATA STRUCTURES AND FILES LAB    TW   25   10   13  P

09. PROGRAMMING LABORATORY          PR   50   20   33  P C    19. DATA STRUCTURES AND FILES LAB    PR   50   20   17  F

10. COMMUNICATION AND LANGUAGE LAB. TW   50   20   23  P C    20. OBJECT ORIENTED PROGRAMMING LAB  TW   50   20   35  P

                                                             21. OBJECT ORIENTED PROGRAMMING LAB  PR   50   20   16  F
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GRAND TOTAL = 620/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

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S8058601   RAIJADE DHANVANTARI BALKRISHANA   RATNAPRABHA   , 71134975E   , S8058601 ,   PICT   , S8058601

01. DISCRETE STRUCTURES	PP	100	40	43	P C	11. ENG MATHS III	PP	100	40	40	P
02. COMPUTER ORGANIZATION	PP	100	40	40	P C	12. COMPUTER GRAPHICS	PP	100	40	61	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	55	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	54	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	63	P C	14. DATA STRUCTURES AND FILES	PP	100	40	63	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	41	P C	15. DATA COMMUNICATION	PP	100	40	51	P
06. DIGITAL LABORATORY	TW	50	20	39	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	19	P
07. DIGITAL LABORATORY	PR	50	20	33	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	32	P
08. PROGRAMMING LABORATORY	TW	50	20	43	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	21	P
09. PROGRAMMING LABORATORY	PR	50	20	43	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	40	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	39	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	42	P
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	36	P

GRAND TOTAL = 898+02/1500, RESULT: FIRST CLASS [O.2]

ORDN. 1 MARKS :

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S8058602   RAJOLE VISHAL VILASRAO   SUNITA   , 71134976C   , S8058602 ,   PICT   , S8058602

01. DISCRETE STRUCTURES	PP	100	40	40	P C	11. ENG MATHS III	PP	100	40	67	P
02. COMPUTER ORGANIZATION	PP	100	40	52	P C	12. COMPUTER GRAPHICS	PP	100	40	62	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	55	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	54	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	45	P C	14. DATA STRUCTURES AND FILES	PP	100	40	78	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	48	P C	15. DATA COMMUNICATION	PP	100	40	61	P
06. DIGITAL LABORATORY	TW	50	20	35	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	20	P
07. DIGITAL LABORATORY	PR	50	20	29	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	42	P
08. PROGRAMMING LABORATORY	TW	50	20	41	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	20	P
09. PROGRAMMING LABORATORY	PR	50	20	44	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	40	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	41	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	46	P
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	43	P

GRAND TOTAL = 963/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

. . . .
                UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(INFORMATION TECHNOLOGY)
      DATE : 18 AUG. 2011   CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.   PAGE NO. 34 (456)
. . . .

  NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.

        OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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  S8058603   RISHABH PANDITA   USHA   , 71045589F   , S8058603 ,   PICT   , S8058603

  01. DISCRETE STRUCTURES   PP   100   40   49 P C   11. ENG MATHS III   PP   100   40   40 P
  02. COMPUTER ORGANIZATION   PP   100   40   58 P C   12. COMPUTER GRAPHICS   PP   100   40   60 P
  03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 40   62 P C   13. PROCESSOR ARCHITECTURE & INTER. PP 100   40   45 P
  04. FUNDAMENTAL OF DATA STRUCTURES PP 100   40   52 P C   14. DATA STRUCTURES AND FILES   PP   100   40   66 P
  05. HUMANITIES AND SOCIAL SCIENCES PP 100   40   49 P C   15. DATA COMMUNICATION   PP   100   40   61 P
  06. DIGITAL LABORATORY   TW   50   20   30 P C   16. PROCESSOR INTERFACING LABORATORY TW   25   10   16 P
  07. DIGITAL LABORATORY   PR   50   20   33 P C   17. PROCESSOR INTERFACING LABORATORY OR   50   20   25 P
  08. PROGRAMMING LABORATORY   TW   50   20   31 P C   18. DATA STRUCTURES AND FILES LAB TW   25   10   15 P
  09. PROGRAMMING LABORATORY   PR   50   20   44 P C   19. DATA STRUCTURES AND FILES LAB PR   50   20   28 P
  10. COMMUNICATION AND LANGUAGE LAB. TW 50   20   40 P C   20. OBJECT ORIENTED PROGRAMMING LAB TW   50   20   30 P
                                                                21. OBJECT ORIENTED PROGRAMMING LAB PR   50   20   40 P

GRAND TOTAL = 874/1500, RESULT: HIGHER SECOND CLASS
ORDN. 1 MARKS :

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  S8058604   ROHAN MARWADI   MEENAKSHI   , 71045590K   , S8058604 ,   PICT   , S8058604

  01. DISCRETE STRUCTURES   PP   100   40   54 P C   11. ENG MATHS III   PP   100   40   45 P
  02. COMPUTER ORGANIZATION   PP   100   40   40 P C   12. COMPUTER GRAPHICS   PP   100   40   41 P
  03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 40   58 P C   13. PROCESSOR ARCHITECTURE & INTER. PP 100   40   19 F

04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	45	P C	14. DATA STRUCTURES AND FILES	PP	100	40	68	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	51	P C	15. DATA COMMUNICATION	PP	100	40	49	P
06. DIGITAL LABORATORY	TW	50	20	30	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	18	P
07. DIGITAL LABORATORY	PR	50	20	28	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	22	P
08. PROGRAMMING LABORATORY	TW	50	20	40	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	17	P
09. PROGRAMMING LABORATORY	PR	50	20	45	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	22	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	41	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	42	P
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	32	P

GRAND TOTAL = 807/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

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S8058605   SACHIN SUKHLECHA   SANGEETA   , 71045592F   , S8058605 ,   PICT   , S8058605

01. DISCRETE STRUCTURES	PP	100	40	74	P C	11. ENG MATHS III	PP	100	40	69	P
02. COMPUTER ORGANIZATION	PP	100	40	43	P C	12. COMPUTER GRAPHICS	PP	100	40	41	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	63	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	41	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	69	P C	14. DATA STRUCTURES AND FILES	PP	100	40	82	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	46	P C	15. DATA COMMUNICATION	PP	100	40	53	P
06. DIGITAL LABORATORY	TW	50	20	36	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	20	P
07. DIGITAL LABORATORY	PR	50	20	41	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	39	P
08. PROGRAMMING LABORATORY	TW	50	20	37	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	15	P
09. PROGRAMMING LABORATORY	PR	50	20	41	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	38	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	32	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	31	P
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	42	P

GRAND TOTAL = 953/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

. . . . UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(INFORMATION TECHNOLOGY) . . . .

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  NOTE: FIRST LINE : SEAT NO.,  NAME OF THE CANDIDATE,  MOTHER, PERMANENT REG. NO.,  PREVIOUS SEAT NO.,  COLLEGE,  SEAT NO.

        OTHER LINES: HEAD OF PASSING,  MAX. MARKS,  MIN. PASS MARKS,  MARKS OBTAINED,  P/F:PASS/FAIL,  C:PREVIOUS CARRY OVER

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  S8058606   SALVE AJINKYA PRABHAKAR            KAVITA            , 71045594B   , S8058606 ,  PICT     , S8058606


  01. DISCRETE STRUCTURES           PP   100   40   55  P C     11. ENG MATHS III                  PP   100   40   40  P
  02. COMPUTER ORGANIZATION         PP   100   40   58  P C     12. COMPUTER GRAPHICS              PP   100   40   58  P
  03. DIGITAL ELECTRONICS & LOGIC DESIGPP  100  40   60  P C     13. PROCESSOR ARCHITECTURE & INTER. PP   100   40   47  P
  04. FUNDAMENTAL OF DATA STRUCTURES PP   100   40   75  P C     14. DATA STRUCTURES AND FILES      PP   100   40   70  P
  05. HUMANITIES AND SOCIAL SCIENCES PP   100   40   52  P C     15. DATA COMMUNICATION             PP   100   40   62  P
  06. DIGITAL LABORATORY            TW    50   20   33  P C     16. PROCESSOR INTERFACING LABORATORY TW   25   10   15  P
  07. DIGITAL LABORATORY            PR    50   20   36  P C     17. PROCESSOR INTERFACING LABORATORY OR   50   20   05  F
  08. PROGRAMMING LABORATORY        TW    50   20   36  P C     18. DATA STRUCTURES AND FILES LAB   TW    25   10   12  P
  09. PROGRAMMING LABORATORY        PR    50   20   48  P C     19. DATA STRUCTURES AND FILES LAB   PR    50   20   36  P
  10. COMMUNICATION AND LANGUAGE LAB. TW  50   20   32  P C     20. OBJECT ORIENTED PROGRAMMING LAB TW    50   20   36  P
                                                               21. OBJECT ORIENTED PROGRAMMING LAB PR    50   20   47  P

GRAND TOTAL =  913/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :


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  S8058607   SANJAY SINGH                      PREMA             , 71053988G   , S8058607 ,  PICT     , S8058607


  01. DISCRETE STRUCTURES           PP   100   40   66  P C     11. ENG MATHS III                  PP   100   40   64  P
  02. COMPUTER ORGANIZATION         PP   100   40   65  P C     12. COMPUTER GRAPHICS              PP   100   40   57  P
  03. DIGITAL ELECTRONICS & LOGIC DESIGPP  100  40   67  P C     13. PROCESSOR ARCHITECTURE & INTER. PP   100   40   44  P
  04. FUNDAMENTAL OF DATA STRUCTURES PP   100   40   63  P C     14. DATA STRUCTURES AND FILES      PP   100   40   75  P
  05. HUMANITIES AND SOCIAL SCIENCES PP   100   40   53  P C     15. DATA COMMUNICATION             PP   100   40   61  P
  06. DIGITAL LABORATORY            TW    50   20   31  P C     16. PROCESSOR INTERFACING LABORATORY TW   25   10   20  P
  07. DIGITAL LABORATORY            PR    50   20   38  P C     17. PROCESSOR INTERFACING LABORATORY OR   50   20   39  P
  08. PROGRAMMING LABORATORY        TW    50   20   39  P C     18. DATA STRUCTURES AND FILES LAB   TW    25   10   16  P
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09. PROGRAMMING LABORATORY          PR   50   20   48  P C    19. DATA STRUCTURES AND FILES LAB    PR   50   20   41  P

10. COMMUNICATION AND LANGUAGE LAB. TW   50   20   38  P C    20. OBJECT ORIENTED PROGRAMMING LAB  TW   50   20   38  P

                                                             21. OBJECT ORIENTED PROGRAMMING LAB  PR   50   20   32  P
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GRAND TOTAL = 995/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

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S8058608   SARTHAK MAJITHIA                   POOJA              , 71045598E    , S8058608 ,  PICT    , S8058608

01. DISCRETE STRUCTURES              PP  100   40   63  P C    11. ENG MATHS III                    PP  100   40   26  F

02. COMPUTER ORGANIZATION            PP  100   40   52  P C    12. COMPUTER GRAPHICS                PP  100   40   57  P

03. DIGITAL ELECTRONICS & LOGIC DESIGPP  100   40   62  P C    13. PROCESSOR ARCHITECTURE & INTER.  PP  100   40   AA  F

04. FUNDAMENTAL OF DATA STRUCTURES   PP  100   40   63  P C    14. DATA STRUCTURES AND FILES        PP  100   40   28  F

05. HUMANITIES AND SOCIAL SCIENCES   PP  100   40   48  P C    15. DATA COMMUNICATION               PP  100   40   48  P

06. DIGITAL LABORATORY               TW   50   20   20  P C    16. PROCESSOR INTERFACING LABORATORY TW   25   10   10  P

07. DIGITAL LABORATORY               PR   50   20   30  P C    17. PROCESSOR INTERFACING LABORATORY OR   50   20   AA  F

08. PROGRAMMING LABORATORY           TW   50   20   20  P C    18. DATA STRUCTURES AND FILES LAB    TW   25   10   15  P

09. PROGRAMMING LABORATORY           PR   50   20   48  P C    19. DATA STRUCTURES AND FILES LAB    PR   50   20   43  P

10. COMMUNICATION AND LANGUAGE LAB.  TW   50   20   30  P C    20. OBJECT ORIENTED PROGRAMMING LAB  TW   50   20   20  P

                                                             21. OBJECT ORIENTED PROGRAMMING LAB  PR   50   20   39  P
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GRAND TOTAL = 722/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

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 NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.

        OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S8058609   SARWAR PRIYANKA ASHOK   MEERA   , 71045599C   , S8058609 , PICT   , S8058609

cols05

01. DISCRETE STRUCTURES	PP	100	40	59	P C	11. ENG MATHS III	PP	100	40	20	F
02. COMPUTER ORGANIZATION	PP	100	40	44	P C	12. COMPUTER GRAPHICS	PP	100	40	50	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	52	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	40	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	49	P C	14. DATA STRUCTURES AND FILES	PP	100	40	55	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	55	P C	15. DATA COMMUNICATION	PP	100	40	46	P
06. DIGITAL LABORATORY	TW	50	20	31	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	19	P
07. DIGITAL LABORATORY	PR	50	20	22	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	12	F
08. PROGRAMMING LABORATORY	TW	50	20	38	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	14	P
09. PROGRAMMING LABORATORY	PR	50	20	30	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	10	F
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	31	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	38	P
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	20#	P

GRAND TOTAL = 735+03/1500, RESULT: FAILS A.T.K.T. #[O.163+O.1][O.163]   RESULT RESERVED FOR BKLG

ORDN. 1 MARKS : (21)2,

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S8058610   SATPUTE ABHINANDAN ASHOK   SAVITA   , 71134977M   , S8058610 , PICT   , S8058610

01. DISCRETE STRUCTURES	PP	100	40	53	P C	11. ENG MATHS III	PP	100	40	57	P
02. COMPUTER ORGANIZATION	PP	100	40	77	P C	12. COMPUTER GRAPHICS	PP	100	40	68	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	63	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	84	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	56	P C	14. DATA STRUCTURES AND FILES	PP	100	40	69	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	59	P C	15. DATA COMMUNICATION	PP	100	40	58	P
06. DIGITAL LABORATORY	TW	50	20	34	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	19	P
07. DIGITAL LABORATORY	PR	50	20	29	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	42	P
08. PROGRAMMING LABORATORY	TW	50	20	36	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	21	P
09. PROGRAMMING LABORATORY	PR	50	20	44	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	44	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	32	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	41	P
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	32	P

GRAND TOTAL = 1018/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

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S8058611   SATPUTE SWAPNIL BHASKAR   SUREKHA   , 71045600L   , S8058611 , PICT   , S8058611

01. DISCRETE STRUCTURES	PP	100	40	63	P C		11. ENG MATHS III	PP	100	40	40	P	
02. COMPUTER ORGANIZATION	PP	100	40	41	P C		12. COMPUTER GRAPHICS	PP	100	40	45	P	
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	41	P C		13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	64	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	66	P C		14. DATA STRUCTURES AND FILES	PP	100	40	55	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	40	P C		15. DATA COMMUNICATION	PP	100	40	47	P
06. DIGITAL LABORATORY	TW	50	20	34	P C		16. PROCESSOR INTERFACING LABORATORY	TW	25	10	13	P
07. DIGITAL LABORATORY	PR	50	20	29	P C		17. PROCESSOR INTERFACING LABORATORY	OR	50	20	40	P
08. PROGRAMMING LABORATORY	TW	50	20	37	P C		18. DATA STRUCTURES AND FILES LAB	TW	25	10	16	P
09. PROGRAMMING LABORATORY	PR	50	20	41	P C		19. DATA STRUCTURES AND FILES LAB	PR	50	20	40	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	35	P C		20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	30	P
							21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	36	P

GRAND TOTAL = 853/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

.... UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(INFORMATION TECHNOLOGY) ....

   DATE : 18 AUG. 2011   CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.   PAGE NO. 37 (459)
....
 NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.

       OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S8058612   SETHI SUPREET SATISHKUMAR   PREETI   , 71045605M   , S8058612 , PICT   , S8058612

01. DISCRETE STRUCTURES	PP	100	40	50	P C		11. ENG MATHS III	PP	100	40	40	P	
02. COMPUTER ORGANIZATION	PP	100	40	49	P C		12. COMPUTER GRAPHICS	PP	100	40	40	P	
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	53	P C		13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	32	F
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	47	P C		14. DATA STRUCTURES AND FILES	PP	100	40	31	F

05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	40	P		15. DATA COMMUNICATION	PP	100	40	61 P
06. DIGITAL LABORATORY	TW	50	20	27	P C		16. PROCESSOR INTERFACING LABORATORY	TW	25	10	13 P
07. DIGITAL LABORATORY	PR	50	20	25	P		17. PROCESSOR INTERFACING LABORATORY	OR	50	20	32 P
08. PROGRAMMING LABORATORY	TW	50	20	30	P C		18. DATA STRUCTURES AND FILES LAB	TW	25	10	13 P
09. PROGRAMMING LABORATORY	PR	50	20	40	P C		19. DATA STRUCTURES AND FILES LAB	PR	50	20	30 P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	26	P C		20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	36 P
							21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	42 P

GRAND TOTAL = 757/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

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S8058613   SHAH KUNAL ARVIND   PRAMILA   , 71045607H   , S8058613 ,   PICT   , S8058613

01. DISCRETE STRUCTURES	PP	100	40	65	P C		11. ENG MATHS III	PP	100	40	50 P
02. COMPUTER ORGANIZATION	PP	100	40	51	P C		12. COMPUTER GRAPHICS	PP	100	40	40 P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	61	P C		13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	48 P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	60	P C		14. DATA STRUCTURES AND FILES	PP	100	40	63 P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	40	P C		15. DATA COMMUNICATION	PP	100	40	62 P
06. DIGITAL LABORATORY	TW	50	20	39	P C		16. PROCESSOR INTERFACING LABORATORY	TW	25	10	23 P
07. DIGITAL LABORATORY	PR	50	20	26	P C		17. PROCESSOR INTERFACING LABORATORY	OR	50	20	36 P
08. PROGRAMMING LABORATORY	TW	50	20	41	P C		18. DATA STRUCTURES AND FILES LAB	TW	25	10	21 P
09. PROGRAMMING LABORATORY	PR	50	20	45	P C		19. DATA STRUCTURES AND FILES LAB	PR	50	20	44 P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	38	P C		20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	44 P
							21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	33 P

GRAND TOTAL = 930/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

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S8058614   SHAIKH SHEEBANRAZA ZAHEER   NASEEM   , 71134978K   , S8058614 ,   PICT   , S8058614

01. DISCRETE STRUCTURES	PP	100	40	41	P C		11. ENG MATHS III	PP	100	40	40 P

02. COMPUTER ORGANIZATION	PP	100	40	60	P C	12. COMPUTER GRAPHICS	PP	100	40	45	P	
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	63	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	67	P	
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	58	P C	14. DATA STRUCTURES AND FILES	PP	100	40	72	P	
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	54	P C	15. DATA COMMUNICATION	PP	100	40	64	P	
06. DIGITAL LABORATORY	TW	50	20	36	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	18	P	
07. DIGITAL LABORATORY	PR	50	20	24	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	32	P	
08. PROGRAMMING LABORATORY	TW	50	20	41	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	18	P	
09. PROGRAMMING LABORATORY	PR	50	20	38	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	40	P	
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	41	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	40	P	
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	37	P	

GRAND TOTAL = 929/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

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UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(INFORMATION TECHNOLOGY)

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.

        OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S8058615   SHEIKH LINA JAMIL AHMED   NISHAT   , 71134979H   , S8058615 , PICT   , S8058615

01. DISCRETE STRUCTURES	PP	100	40	44	P C	11. ENG MATHS III	PP	100	40	61	P	
02. COMPUTER ORGANIZATION	PP	100	40	64	P C	12. COMPUTER GRAPHICS	PP	100	40	61	P	
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	60	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	70	P	
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	85	P C	14. DATA STRUCTURES AND FILES	PP	100	40	71	P	
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	55	P C	15. DATA COMMUNICATION	PP	100	40	74	P	
06. DIGITAL LABORATORY	TW	50	20	38	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	19	P	
07. DIGITAL LABORATORY	PR	50	20	26	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	40	P	
08. PROGRAMMING LABORATORY	TW	50	20	43	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	22	P	
09. PROGRAMMING LABORATORY	PR	50	20	38	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	37	P	

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10. COMMUNICATION AND LANGUAGE LAB.  TW   50   20   40   P C        20. OBJECT ORIENTED PROGRAMMING LAB  TW   50   20   45   P
                                                                    21. OBJECT ORIENTED PROGRAMMING LAB  PR   50   20   30   P
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GRAND TOTAL = 1023/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

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S8058616    SHETTY SHEFALI RAVINDRA            VANDANA            , 71045612D    , S8058616 ,   PICT     , S8058616

01. DISCRETE STRUCTURES           PP   100   40   61   P C        11. ENG MATHS III                    PP   100   40   59   P
02. COMPUTER ORGANIZATION         PP   100   40   62   P C        12. COMPUTER GRAPHICS                PP   100   40   32*  P
03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100   40   59   P C        13. PROCESSOR ARCHITECTURE & INTER.  PP   100   40   42   P
04. FUNDAMENTAL OF DATA STRUCTURES PP  100   40   56   P C        14. DATA STRUCTURES AND FILES        PP   100   40   64   P
05. HUMANITIES AND SOCIAL SCIENCES PP  100   40   46   P C        15. DATA COMMUNICATION               PP   100   40   51   P
06. DIGITAL LABORATORY            TW    50   20   35   P C        16. PROCESSOR INTERFACING LABORATORY TW    25   10   22   P
07. DIGITAL LABORATORY            PR    50   20   36   P C        17. PROCESSOR INTERFACING LABORATORY OR    50   20   41   P
08. PROGRAMMING LABORATORY        TW    50   20   41   P C        18. DATA STRUCTURES AND FILES LAB    TW    25   10   22   P
09. PROGRAMMING LABORATORY        PR    50   20   42   P C        19. DATA STRUCTURES AND FILES LAB    PR    50   20   43   P
10. COMMUNICATION AND LANGUAGE LAB. TW  50   20   40   P C        20. OBJECT ORIENTED PROGRAMMING LAB  TW    50   20   46   P
                                                                 21. OBJECT ORIENTED PROGRAMMING LAB  PR    50   20   30   P
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GRAND TOTAL = 930/1500, RESULT: FIRST CLASS   * [O.4]

ORDN. 1 MARKS :

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S8058617    SHEWALE ANULA TUSHAR               NEETA             , 71045613B    , S8058617 ,   PICT     , S8058617

01. DISCRETE STRUCTURES           PP   100   40   49   P C        11. ENG MATHS III                    PP   100   40   57   P
02. COMPUTER ORGANIZATION         PP   100   40   50   P C        12. COMPUTER GRAPHICS                PP   100   40   23   F
03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100   40   40   P C        13. PROCESSOR ARCHITECTURE & INTER.  PP   100   40   40   P
04. FUNDAMENTAL OF DATA STRUCTURES PP  100   40   42   P C        14. DATA STRUCTURES AND FILES        PP   100   40   55   P
05. HUMANITIES AND SOCIAL SCIENCES PP  100   40   40   P C        15. DATA COMMUNICATION               PP   100   40   40   P
06. DIGITAL LABORATORY            TW    50   20   27   P C        16. PROCESSOR INTERFACING LABORATORY TW    25   10   16   P
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07. DIGITAL LABORATORY	PR	50	20	30	P C		17. PROCESSOR INTERFACING LABORATORY	OR	50	20	04	F
08. PROGRAMMING LABORATORY	TW	50	20	29	P C		18. DATA STRUCTURES AND FILES LAB	TW	25	10	14	P
09. PROGRAMMING LABORATORY	PR	50	20	38	P C		19. DATA STRUCTURES AND FILES LAB	PR	50	20	09	F
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	31	P C		20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	37	P
							21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	25	P

GRAND TOTAL = 696/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

. . . . UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(INFORMATION TECHNOLOGY) . . . .

   DATE : 18 AUG. 2011   CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.   PAGE NO. 39 (461)

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   NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.

        OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S8058618   SHINDE AKASH ARUN   SHOBHA   , 71134980M   , S8058618 , PICT   , S8058618

01. DISCRETE STRUCTURES	PP	100	40	40	P C		11. ENG MATHS III	PP	100	40	40	P
02. COMPUTER ORGANIZATION	PP	100	40	69	P C		12. COMPUTER GRAPHICS	PP	100	40	49	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	49	P C		13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	54	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	70	P C		14. DATA STRUCTURES AND FILES	PP	100	40	70	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	52	P C		15. DATA COMMUNICATION	PP	100	40	54	P
06. DIGITAL LABORATORY	TW	50	20	39	P C		16. PROCESSOR INTERFACING LABORATORY	TW	25	10	21	P
07. DIGITAL LABORATORY	PR	50	20	23	P C		17. PROCESSOR INTERFACING LABORATORY	OR	50	20	39	P
08. PROGRAMMING LABORATORY	TW	50	20	41	P C		18. DATA STRUCTURES AND FILES LAB	TW	25	10	21	P
09. PROGRAMMING LABORATORY	PR	50	20	37	P C		19. DATA STRUCTURES AND FILES LAB	PR	50	20	43	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	30	P C		20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	47	P
							21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	34	P

GRAND TOTAL = 922/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

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S8058619   SHINGANE ANKUSH DINANATH   VEENA   , 71134981K   , S8058619 ,   PICT   , S8058619

01. DISCRETE STRUCTURES	PP	100	40	52	P C		11. ENG MATHS III	PP	100	40	52	P
02. COMPUTER ORGANIZATION	PP	100	40	69	P C		12. COMPUTER GRAPHICS	PP	100	40	50	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	53	P C		13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	68	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	61	P C		14. DATA STRUCTURES AND FILES	PP	100	40	63	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	49	P C		15. DATA COMMUNICATION	PP	100	40	69	P
06. DIGITAL LABORATORY	TW	50	20	37	P C		16. PROCESSOR INTERFACING LABORATORY	TW	25	10	18	P
07. DIGITAL LABORATORY	PR	50	20	36	P C		17. PROCESSOR INTERFACING LABORATORY	OR	50	20	32	P
08. PROGRAMMING LABORATORY	TW	50	20	44	P C		18. DATA STRUCTURES AND FILES LAB	TW	25	10	19	P
09. PROGRAMMING LABORATORY	PR	50	20	44	P C		19. DATA STRUCTURES AND FILES LAB	PR	50	20	24	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	40	P C		20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	37	P
							21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	40	P

GRAND TOTAL = 957/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

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S8058620   SHIRUDE NEHA RAJIV   SMITA   , 71059492F   , S8058620 ,   PICT   , S8058620

01. DISCRETE STRUCTURES	PP	100	40	61	P C		11. ENG MATHS III	PP	100	40	72	P
02. COMPUTER ORGANIZATION	PP	100	40	61	P C		12. COMPUTER GRAPHICS	PP	100	40	48	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	49	P C		13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	49	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	54	P C		14. DATA STRUCTURES AND FILES	PP	100	40	64	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	51	P C		15. DATA COMMUNICATION	PP	100	40	60	P
06. DIGITAL LABORATORY	TW	50	20	29	P C		16. PROCESSOR INTERFACING LABORATORY	TW	25	10	19	P
07. DIGITAL LABORATORY	PR	50	20	33	P C		17. PROCESSOR INTERFACING LABORATORY	OR	50	20	05	F
08. PROGRAMMING LABORATORY	TW	50	20	41	P C		18. DATA STRUCTURES AND FILES LAB	TW	25	10	15	P
09. PROGRAMMING LABORATORY	PR	50	20	42	P C		19. DATA STRUCTURES AND FILES LAB	PR	50	20	30	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	39	P C		20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	44	P
							21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	32	P

GRAND TOTAL = 898/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

. . . . UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(INFORMATION TECHNOLOGY) . . . .

    DATE : 18 AUG. 2011   CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.   PAGE NO. 40 (462)

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  NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.

        OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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  S8058621   SHUBHI YEDE   ANJU   , 71045620E   , S8058621 ,   PICT   , S8058621

  01. DISCRETE STRUCTURES   PP   100   40   68   P C   11. ENG MATHS III   PP   100   40   65   P
  02. COMPUTER ORGANIZATION   PP   100   40   64   P C   12. COMPUTER GRAPHICS   PP   100   40   49   P
  03. DIGITAL ELECTRONICS & LOGIC DESIGPP   100   40   58   P C   13. PROCESSOR ARCHITECTURE & INTER. PP   100   40   60   P
  04. FUNDAMENTAL OF DATA STRUCTURES   PP   100   40   72   P C   14. DATA STRUCTURES AND FILES   PP   100   40   59   P
  05. HUMANITIES AND SOCIAL SCIENCES   PP   100   40   53   P C   15. DATA COMMUNICATION   PP   100   40   62   P
  06. DIGITAL LABORATORY   TW   50   20   32   P C   16. PROCESSOR INTERFACING LABORATORY TW   25   10   19   P
  07. DIGITAL LABORATORY   PR   50   20   41   P C   17. PROCESSOR INTERFACING LABORATORY OR   50   20   45   P
  08. PROGRAMMING LABORATORY   TW   50   20   38   P C   18. DATA STRUCTURES AND FILES LAB   TW   25   10   17   P
  09. PROGRAMMING LABORATORY   PR   50   20   42   P C   19. DATA STRUCTURES AND FILES LAB   PR   50   20   42   P
  10. COMMUNICATION AND LANGUAGE LAB. TW   50   20   38   P C   20. OBJECT ORIENTED PROGRAMMING LAB TW   50   20   40   P
                                                                 21. OBJECT ORIENTED PROGRAMMING LAB PR   50   20   36   P

GRAND TOTAL = 1000/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

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  S8058623   SINGH PRABHAKAR KRISHNAKUMAR   GAYTRIDEVI   , 71134982H   , S8058623 ,   PICT   , S8058623

  01. DISCRETE STRUCTURES   PP   100   40   53   P C   11. ENG MATHS III   PP   100   40   40   P
  02. COMPUTER ORGANIZATION   PP   100   40   52   P C   12. COMPUTER GRAPHICS   PP   100   40   58   P

03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	50	P C		13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	73	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	64	P C		14. DATA STRUCTURES AND FILES	PP	100	40	67	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	54	P C		15. DATA COMMUNICATION	PP	100	40	62	P
06. DIGITAL LABORATORY	TW	50	20	39	P C		16. PROCESSOR INTERFACING LABORATORY	TW	25	10	20	P
07. DIGITAL LABORATORY	PR	50	20	33	P C		17. PROCESSOR INTERFACING LABORATORY	OR	50	20	28	P
08. PROGRAMMING LABORATORY	TW	50	20	41	P C		18. DATA STRUCTURES AND FILES LAB	TW	25	10	22	P
09. PROGRAMMING LABORATORY	PR	50	20	39	P C		19. DATA STRUCTURES AND FILES LAB	PR	50	20	33	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	38	P C		20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	44	P
							21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	25	P

GRAND TOTAL = 935/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

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S8058624   SOMANI NEHA NANDKISHOR   ARATI   , 71045628L   , S8058624 ,   PICT   , S8058624

01. DISCRETE STRUCTURES	PP	100	40	52	P C		11. ENG MATHS III	PP	100	40	56	P
02. COMPUTER ORGANIZATION	PP	100	40	51	P C		12. COMPUTER GRAPHICS	PP	100	40	49	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	52	P C		13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	15	F
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	40	P C		14. DATA STRUCTURES AND FILES	PP	100	40	67	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	51	P C		15. DATA COMMUNICATION	PP	100	40	69	P
06. DIGITAL LABORATORY	TW	50	20	26	P C		16. PROCESSOR INTERFACING LABORATORY	TW	25	10	17	P
07. DIGITAL LABORATORY	PR	50	20	37	P C		17. PROCESSOR INTERFACING LABORATORY	OR	50	20	04	F
08. PROGRAMMING LABORATORY	TW	50	20	28	P C		18. DATA STRUCTURES AND FILES LAB	TW	25	10	15	P
09. PROGRAMMING LABORATORY	PR	50	20	20	P C		19. DATA STRUCTURES AND FILES LAB	PR	50	20	37	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	36	P C		20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	41	P
							21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	35	P

GRAND TOTAL = 798/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S8058625   SOMWANSHI HARSHAL SURESH   NUTAN   , 71045629J   , S8058625 , PICT   , S8058625

01. DISCRETE STRUCTURES	PP	100	40	40	P C		11. ENG MATHS III		PP	100	40	47	P
02. COMPUTER ORGANIZATION	PP	100	40	43	P C		12. COMPUTER GRAPHICS		PP	100	40	45	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	69	P C		13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	40	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	66	P		14. DATA STRUCTURES AND FILES		PP	100	40	57	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	44	P C		15. DATA COMMUNICATION		PP	100	40	42	P
06. DIGITAL LABORATORY	TW	50	20	31	P C		16. PROCESSOR INTERFACING LABORATORY	TW	25	10	17	P
07. DIGITAL LABORATORY	PR	50	20	37	P C		17. PROCESSOR INTERFACING LABORATORY	OR	50	20	05	F
08. PROGRAMMING LABORATORY	TW	50	20	37	P C		18. DATA STRUCTURES AND FILES LAB	TW	25	10	15	P
09. PROGRAMMING LABORATORY	PR	50	20	44	P C		19. DATA STRUCTURES AND FILES LAB	PR	50	20	33	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	31	P C		20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	35	P
							21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	40	P

GRAND TOTAL = 818+05/1500, RESULT: FAILS A.T.K.T.[O.163]

ORDN. 1 MARKS :

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S8058626   SONAWANE JAYANT SHAM   RAJESHRI   , 71045630B   , S8058626 , PICT   , S8058626

01. DISCRETE STRUCTURES	PP	100	40	61	P C		11. ENG MATHS III		PP	100	40	64	P
02. COMPUTER ORGANIZATION	PP	100	40	56	P C		12. COMPUTER GRAPHICS		PP	100	40	40	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	46	P C		13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	71	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	58	P C		14. DATA STRUCTURES AND FILES		PP	100	40	65	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	51	P C		15. DATA COMMUNICATION		PP	100	40	48	P
06. DIGITAL LABORATORY	TW	50	20	32	P C		16. PROCESSOR INTERFACING LABORATORY	TW	25	10	19	P
07. DIGITAL LABORATORY	PR	50	20	35	P C		17. PROCESSOR INTERFACING LABORATORY	OR	50	20	39	P

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08. PROGRAMMING LABORATORY	TW	50	20	37	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	15	P
09. PROGRAMMING LABORATORY	PR	50	20	44	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	42	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	39	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	38	P
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	32	P

GRAND TOTAL = 932+05/1500, RESULT: FIRST CLASS[O.163]

ORDN. 1 MARKS :

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S8058627   SONY PRERNA ASHOK   BHAGWATI   , 71134983F   , S8058627 ,   PICT   , S8058627

01. DISCRETE STRUCTURES	PP	100	40	40	P C	11. ENG MATHS III	PP	100	40	40	P
02. COMPUTER ORGANIZATION	PP	100	40	66	P C	12. COMPUTER GRAPHICS	PP	100	40	49	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	55	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	55	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	61	P C	14. DATA STRUCTURES AND FILES	PP	100	40	62	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	46	P C	15. DATA COMMUNICATION	PP	100	40	61	P
06. DIGITAL LABORATORY	TW	50	20	41	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	20	P
07. DIGITAL LABORATORY	PR	50	20	40	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	40	P
08. PROGRAMMING LABORATORY	TW	50	20	44	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	19	P
09. PROGRAMMING LABORATORY	PR	50	20	37	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	45	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	39	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	39	P
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	42	P

GRAND TOTAL = 941/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

. . . . UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(INFORMATION TECHNOLOGY) . . . .

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S8058628   SURADKAR SWAPNIL RAJENDRA   SANGITA   , 71134984D   , S8058628 ,   PICT   , S8058628

01. DISCRETE STRUCTURES	PP	100	40	28	F		11. ENG MATHS III	PP	100	40	40	P
02. COMPUTER ORGANIZATION	PP	100	40	57	P C		12. COMPUTER GRAPHICS	PP	100	40	43	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	53	P C		13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	48	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	53	P C		14. DATA STRUCTURES AND FILES	PP	100	40	52	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	55	P C		15. DATA COMMUNICATION	PP	100	40	60	P
06. DIGITAL LABORATORY	TW	50	20	34	P C		16. PROCESSOR INTERFACING LABORATORY	TW	25	10	16	P
07. DIGITAL LABORATORY	PR	50	20	30	P C		17. PROCESSOR INTERFACING LABORATORY	OR	50	20	21	P
08. PROGRAMMING LABORATORY	TW	50	20	38	P C		18. DATA STRUCTURES AND FILES LAB	TW	25	10	16	P
09. PROGRAMMING LABORATORY	PR	50	20	35	P C		19. DATA STRUCTURES AND FILES LAB	PR	50	20	39	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	35	P C		20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	41	P
							21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	28	P

GRAND TOTAL = 822/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

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S8058629   TAORI SANIKA SANJAY   RUPALI   , 71045640K   , S8058629 ,   PICT   , S8058629

01. DISCRETE STRUCTURES	PP	100	40	61	P C		11. ENG MATHS III	PP	100	40	67	P
02. COMPUTER ORGANIZATION	PP	100	40	53	P C		12. COMPUTER GRAPHICS	PP	100	40	53	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	49	P C		13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	48	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	72	P C		14. DATA STRUCTURES AND FILES	PP	100	40	61	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	50	P C		15. DATA COMMUNICATION	PP	100	40	56	P
06. DIGITAL LABORATORY	TW	50	20	34	P C		16. PROCESSOR INTERFACING LABORATORY	TW	25	10	20	P
07. DIGITAL LABORATORY	PR	50	20	30	P C		17. PROCESSOR INTERFACING LABORATORY	OR	50	20	35	P
08. PROGRAMMING LABORATORY	TW	50	20	42	P C		18. DATA STRUCTURES AND FILES LAB	TW	25	10	18	P
09. PROGRAMMING LABORATORY	PR	50	20	40	P C		19. DATA STRUCTURES AND FILES LAB	PR	50	20	20	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	39	P C		20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	45	P
							21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	38	P

GRAND TOTAL = 931/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

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  S8058630   TATHE VISHAL BHAGWAT   KALPANA   , 71134985B   , S8058630 ,   PICT   , S8058630

01. DISCRETE STRUCTURES	PP	100	40	66	P C		11. ENG MATHS III	PP	100	40	61	P
02. COMPUTER ORGANIZATION	PP	100	40	75	P C		12. COMPUTER GRAPHICS	PP	100	40	56	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	57	P C		13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	55	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	58	P C		14. DATA STRUCTURES AND FILES	PP	100	40	73	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	57	P C		15. DATA COMMUNICATION	PP	100	40	66	P
06. DIGITAL LABORATORY	TW	50	20	35	P C		16. PROCESSOR INTERFACING LABORATORY	TW	25	10	22	P
07. DIGITAL LABORATORY	PR	50	20	36	P C		17. PROCESSOR INTERFACING LABORATORY	OR	50	20	21	P
08. PROGRAMMING LABORATORY	TW	50	20	39	P C		18. DATA STRUCTURES AND FILES LAB	TW	25	10	21	P
09. PROGRAMMING LABORATORY	PR	50	20	38	P C		19. DATA STRUCTURES AND FILES LAB	PR	50	20	43	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	40	P C		20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	48	P
							21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	38	P

GRAND TOTAL = 1005/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

. . . . UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(INFORMATION TECHNOLOGY) . . . .

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  NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.

        OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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  S8058631   THOMBRE PRIYANKA MILIND   JAYMALA   , 71045649C   , S8058631 ,   PICT   , S8058631

01. DISCRETE STRUCTURES	PP	100	40	58	P C		11. ENG MATHS III	PP	100	40	14	F
02. COMPUTER ORGANIZATION	PP	100	40	27	F		12. COMPUTER GRAPHICS	PP	100	40	53	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	50	P C		13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	25	F

04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	55	P C		14. DATA STRUCTURES AND FILES	PP	100	40	60	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	47	P C		15. DATA COMMUNICATION	PP	100	40	40	P
06. DIGITAL LABORATORY	TW	50	20	36	P C		16. PROCESSOR INTERFACING LABORATORY	TW	25	10	20	P
07. DIGITAL LABORATORY	PR	50	20	20	P C		17. PROCESSOR INTERFACING LABORATORY	OR	50	20	37	P
08. PROGRAMMING LABORATORY	TW	50	20	37	P C		18. DATA STRUCTURES AND FILES LAB	TW	25	10	17	P
09. PROGRAMMING LABORATORY	PR	50	20	42	P C		19. DATA STRUCTURES AND FILES LAB	PR	50	20	28	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	36	P C		20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	42	P
							21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	30	P

GRAND TOTAL = 774/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

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S8058632   TIWARI ROHIT VINOD   SHAKUNTALA   , 71045652C   , S8058632 ,   PICT   , S8058632

01. DISCRETE STRUCTURES	PP	100	40	47	P C		11. ENG MATHS III	PP	100	40	22	F
02. COMPUTER ORGANIZATION	PP	100	40	40	P C		12. COMPUTER GRAPHICS	PP	100	40	40	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	43	P C		13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	40	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	46	P C		14. DATA STRUCTURES AND FILES	PP	100	40	47	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	46	P C		15. DATA COMMUNICATION	PP	100	40	53	P
06. DIGITAL LABORATORY	TW	50	20	20	P C		16. PROCESSOR INTERFACING LABORATORY	TW	25	10	13	P
07. DIGITAL LABORATORY	PR	50	20	35	P		17. PROCESSOR INTERFACING LABORATORY	OR	50	20	30	P
08. PROGRAMMING LABORATORY	TW	50	20	28	P C		18. DATA STRUCTURES AND FILES LAB	TW	25	10	11	P
09. PROGRAMMING LABORATORY	PR	50	20	32	P C		19. DATA STRUCTURES AND FILES LAB	PR	50	20	10	F
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	27	P C		20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	30	P
							21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	16	F

GRAND TOTAL = 676/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

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S8058633   UKARANDE SNEHAL ANIL   JAYASHRI   , 71134986L   , S8058633 ,   PICT   , S8058633

01. DISCRETE STRUCTURES	PP	100	40	49	P C		11. ENG MATHS III	PP	100	40	40	P
02. COMPUTER ORGANIZATION	PP	100	40	47	P C		12. COMPUTER GRAPHICS	PP	100	40	62	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	65	P C		13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	60	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	66	P C		14. DATA STRUCTURES AND FILES	PP	100	40	70	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	57	P C		15. DATA COMMUNICATION	PP	100	40	66	P
06. DIGITAL LABORATORY	TW	50	20	39	P C		16. PROCESSOR INTERFACING LABORATORY	TW	25	10	23	P
07. DIGITAL LABORATORY	PR	50	20	35	P C		17. PROCESSOR INTERFACING LABORATORY	OR	50	20	25	P
08. PROGRAMMING LABORATORY	TW	50	20	42	P C		18. DATA STRUCTURES AND FILES LAB	TW	25	10	21	P
09. PROGRAMMING LABORATORY	PR	50	20	44	P C		19. DATA STRUCTURES AND FILES LAB	PR	50	20	38	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	37	P C		20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	48	P
							21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	25	P

GRAND TOTAL = 959/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

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UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(INFORMATION TECHNOLOGY)

DATE : 18 AUG. 2011   CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.   PAGE NO. 44 (466)

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.

       OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S8058634   UPPOD BALAJI SAMBHAJI   SHOBHA   , 71045653M   , S8058634 , PICT   , S8058634

01. DISCRETE STRUCTURES	PP	100	40	40	P C		11. ENG MATHS III	PP	100	40	25	F
02. COMPUTER ORGANIZATION	PP	100	40	51	P C		12. COMPUTER GRAPHICS	PP	100	40	40	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	40	P		13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	25	F
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	40	P C		14. DATA STRUCTURES AND FILES	PP	100	40	43	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	45	P C		15. DATA COMMUNICATION	PP	100	40	47	P
06. DIGITAL LABORATORY	TW	50	20	35	P C		16. PROCESSOR INTERFACING LABORATORY	TW	25	10	13	P
07. DIGITAL LABORATORY	PR	50	20	23	P C		17. PROCESSOR INTERFACING LABORATORY	OR	50	20	28	P
08. PROGRAMMING LABORATORY	TW	50	20	34	P C		18. DATA STRUCTURES AND FILES LAB	TW	25	10	13	P

09. PROGRAMMING LABORATORY	PR	50	20	20	P		19. DATA STRUCTURES AND FILES LAB	PR	50	20	12 F
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	38	P C		20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	37 P
							21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	17 F

GRAND TOTAL = 666/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

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S8058635   VADASADAWALA SHABNAM WAHID BHAI   FARIDA   , 71045655H   , S8058635 ,   PICT   , S8058635

01. DISCRETE STRUCTURES	PP	100	40	49	P C		11. ENG MATHS III	PP	100	40	54 P
02. COMPUTER ORGANIZATION	PP	100	40	57	P C		12. COMPUTER GRAPHICS	PP	100	40	46 P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	57	P C		13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	49 P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	52	P C		14. DATA STRUCTURES AND FILES	PP	100	40	52 P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	52	P C		15. DATA COMMUNICATION	PP	100	40	53 P
06. DIGITAL LABORATORY	TW	50	20	37	P C		16. PROCESSOR INTERFACING LABORATORY	TW	25	10	21 P
07. DIGITAL LABORATORY	PR	50	20	38	P C		17. PROCESSOR INTERFACING LABORATORY	OR	50	20	35 P
08. PROGRAMMING LABORATORY	TW	50	20	41	P C		18. DATA STRUCTURES AND FILES LAB	TW	25	10	19 P
09. PROGRAMMING LABORATORY	PR	50	20	40	P C		19. DATA STRUCTURES AND FILES LAB	PR	50	20	26 P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	44	P C		20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	43 P
							21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	30 P

GRAND TOTAL = 895+05/1500, RESULT: FIRST CLASS [O.2]

ORDN. 1 MARKS :

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S8058636   VARSHA SINHA   SEEMA SINHA   , 71058776H   , S8058636 ,   PICT   , S8058636

01. DISCRETE STRUCTURES	PP	100	40	46	P C		11. ENG MATHS III	PP	100	40	68 P
02. COMPUTER ORGANIZATION	PP	100	40	62	P C		12. COMPUTER GRAPHICS	PP	100	40	63 P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	70	P C		13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	48 P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	74	P C		14. DATA STRUCTURES AND FILES	PP	100	40	64 P

05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	60	P C	15. DATA COMMUNICATION	PP	100	40	75	P
06. DIGITAL LABORATORY	TW	50	20	41	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	22	P
07. DIGITAL LABORATORY	PR	50	20	38	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	40	P
08. PROGRAMMING LABORATORY	TW	50	20	45	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	20	P
09. PROGRAMMING LABORATORY	PR	50	20	39	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	38	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	42	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	48	P
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	34	P

GRAND TOTAL = 1037/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

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DATE : 18 AUG. 2011   CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.   PAGE NO. 45 (467)
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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.
      OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S8058637   VISHWAS JAIN   GEETA   , 71045658B   , S8058637 ,   PICT   , S8058637

01. DISCRETE STRUCTURES	PP	100	40	60	P C	11. ENG MATHS III	PP	100	40	75	P
02. COMPUTER ORGANIZATION	PP	100	40	58	P C	12. COMPUTER GRAPHICS	PP	100	40	49	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	69	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	55	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	70	P C	14. DATA STRUCTURES AND FILES	PP	100	40	66	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	53	P C	15. DATA COMMUNICATION	PP	100	40	58	P
06. DIGITAL LABORATORY	TW	50	20	41	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	20	P
07. DIGITAL LABORATORY	PR	50	20	36	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	42	P
08. PROGRAMMING LABORATORY	TW	50	20	44	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	17	P
09. PROGRAMMING LABORATORY	PR	50	20	44	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	28	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	44	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	42	P
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	34	P

GRAND TOTAL = 1005/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

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S8058638   VIVEK GIRIDHAR KANNAKE   PUSHPA   , 71045659L   , S8058638 ,   PICT   , S8058638

01. DISCRETE STRUCTURES	PP	100	40	40	P C	11. ENG MATHS III	PP	100	40	23	F
02. COMPUTER ORGANIZATION	PP	100	40	43	P C	12. COMPUTER GRAPHICS	PP	100	40	43	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	40	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	40	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	40	P	14. DATA STRUCTURES AND FILES	PP	100	40	63	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	48	P C	15. DATA COMMUNICATION	PP	100	40	47	P
06. DIGITAL LABORATORY	TW	50	20	20	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	15	P
07. DIGITAL LABORATORY	PR	50	20	21	P	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	28	P
08. PROGRAMMING LABORATORY	TW	50	20	35	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	14	P
09. PROGRAMMING LABORATORY	PR	50	20	23	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	22	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	35	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	38	P
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	38	P

GRAND TOTAL =   716/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

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S8058639   VIVERT JAIN   NIDHI   , 71045660D   , S8058639 ,   PICT   , S8058639

01. DISCRETE STRUCTURES	PP	100	40	64	P C	11. ENG MATHS III	PP	100	40	53	P
02. COMPUTER ORGANIZATION	PP	100	40	43	P C	12. COMPUTER GRAPHICS	PP	100	40	42	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	46	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	48	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	52	P C	14. DATA STRUCTURES AND FILES	PP	100	40	62	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	47	P C	15. DATA COMMUNICATION	PP	100	40	57	P
06. DIGITAL LABORATORY	TW	50	20	42	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	20	P
07. DIGITAL LABORATORY	PR	50	20	37	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	33	P
08. PROGRAMMING LABORATORY	TW	50	20	42	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	18	P
09. PROGRAMMING LABORATORY	PR	50	20	32	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	14	F

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                                                          cols05
10. COMMUNICATION AND LANGUAGE LAB. TW   50   20   42  P C      20. OBJECT ORIENTED PROGRAMMING LAB  TW   50   20   44  P
                                                               21. OBJECT ORIENTED PROGRAMMING LAB  PR   50   20   36  P

GRAND TOTAL =  874/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :


. . . . . . . . . . . . . . . UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(INFORMATION TECHNOLOGY) . . . . . . . . . . . . . . . . . . . . . . . . .
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  NOTE: FIRST LINE : SEAT NO.,  NAME OF THE CANDIDATE,  MOTHER,  PERMANENT REG. NO.,  PREVIOUS SEAT NOS.,  COLLEGE,   SEAT NO.
        OTHER LINES: HEAD OF PASSING,  MAX. MARKS,  MIN. PASS MARKS,  MARKS OBTAINED,  P/F:PASS/FAIL,  C:PREVIOUS CARRY OVER

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  S8058640   WAGHMODE SACHIN DADASO            SHOBHA           , 71045663J    , S8058640 ,  PICT     , S8058640

01. DISCRETE STRUCTURES          PP   100   40   48  P C      11. ENG MATHS III                    PP   100   40   46  P
02. COMPUTER ORGANIZATION        PP   100   40   44  P C      12. COMPUTER GRAPHICS                PP   100   40   47  P
03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100  40   51  P C     13. PROCESSOR ARCHITECTURE & INTER. PP   100   40   40  P
04. FUNDAMENTAL OF DATA STRUCTURES PP 100   40   43  P C      14. DATA STRUCTURES AND FILES        PP   100   40   58  P
05. HUMANITIES AND SOCIAL SCIENCES PP 100   40   43  P C      15. DATA COMMUNICATION               PP   100   40   58  P
06. DIGITAL LABORATORY           TW    50   20   28  P C      16. PROCESSOR INTERFACING LABORATORY TW   25   10   17  P
07. DIGITAL LABORATORY           PR    50   20   27  P C      17. PROCESSOR INTERFACING LABORATORY OR   50   20   25  P
08. PROGRAMMING LABORATORY       TW    50   20   34  P C      18. DATA STRUCTURES AND FILES LAB    TW   25   10   15  P
09. PROGRAMMING LABORATORY       PR    50   20   35  P C      19. DATA STRUCTURES AND FILES LAB    PR   50   20   33  P
10. COMMUNICATION AND LANGUAGE LAB. TW 50   20   32  P C      20. OBJECT ORIENTED PROGRAMMING LAB  TW   50   20   38  P
                                                             21. OBJECT ORIENTED PROGRAMMING LAB  PR   50   20   35  P

GRAND TOTAL =  797/1500, RESULT: SECOND CLASS

ORDN. 1 MARKS :


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  S8058641   WAYKOLE VRUSHALI PRAVIN           ARUNA            , 71045666C    , S8058641 ,  PICT     , S8058641
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01. DISCRETE STRUCTURES	PP	100	40	63	P C	11. ENG MATHS III	PP	100	40	57	P
02. COMPUTER ORGANIZATION	PP	100	40	50	P C	12. COMPUTER GRAPHICS	PP	100	40	48	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	56	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	46	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	64	P C	14. DATA STRUCTURES AND FILES	PP	100	40	64	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	45	P C	15. DATA COMMUNICATION	PP	100	40	46	P
06. DIGITAL LABORATORY	TW	50	20	40	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	19	P
07. DIGITAL LABORATORY	PR	50	20	39	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	38	P
08. PROGRAMMING LABORATORY	TW	50	20	40	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	20	P
09. PROGRAMMING LABORATORY	PR	50	20	44	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	46	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	40	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	44	P
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	40	P

GRAND TOTAL = 949/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

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S8058642   YADAV SNEHA HANAMANT   NANDA   , 71134987J   , S8058642 ,   PICT   , S8058642

01. DISCRETE STRUCTURES	PP	100	40	45	P C	11. ENG MATHS III	PP	100	40	59	P
02. COMPUTER ORGANIZATION	PP	100	40	49	P C	12. COMPUTER GRAPHICS	PP	100	40	44	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	65	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	60	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	65	P C	14. DATA STRUCTURES AND FILES	PP	100	40	77	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	50	P C	15. DATA COMMUNICATION	PP	100	40	59	P
06. DIGITAL LABORATORY	TW	50	20	39	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	24	P
07. DIGITAL LABORATORY	PR	50	20	29	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	39	P
08. PROGRAMMING LABORATORY	TW	50	20	43	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	22	P
09. PROGRAMMING LABORATORY	PR	50	20	40	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	46	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	38	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	47	P
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	32	P

GRAND TOTAL = 972/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S8058643   ZAJRIYA AASHAY MAHESH   CHHAYA   , 71045670M   , S8058643 , PICT   , S8058643

01. DISCRETE STRUCTURES	PP	100	40	65	P C	11. ENG MATHS III	PP	100	40	49	P
02. COMPUTER ORGANIZATION	PP	100	40	40	P C	12. COMPUTER GRAPHICS	PP	100	40	46	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	72	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	49	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	52	P C	14. DATA STRUCTURES AND FILES	PP	100	40	69	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	53	P C	15. DATA COMMUNICATION	PP	100	40	50	P
06. DIGITAL LABORATORY	TW	50	20	40	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	21	P
07. DIGITAL LABORATORY	PR	50	20	36	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	38	P
08. PROGRAMMING LABORATORY	TW	50	20	39	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	18	P
09. PROGRAMMING LABORATORY	PR	50	20	32	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	43	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	32	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	38	P
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	40	P

GRAND TOTAL = 922/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

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S8058644   ABNAVE NRUPEN PRAKASH   HEMLATA   , 70925316J   , S8058644 , PICT   , S8058644

01. DISCRETE STRUCTURES	PP	100	40	40	P C	11. ENG MATHS III	PP	100	40	40	P C
02. COMPUTER ORGANIZATION	PP	100	40	40	P C	12. COMPUTER GRAPHICS	PP	100	40	40	P C
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	46	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	40	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	52	P C	14. DATA STRUCTURES AND FILES	PP	100	40	48	P C
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	40	P C	15. DATA COMMUNICATION	PP	100	40	40	P C

06. DIGITAL LABORATORY	TW	50	20	23	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	10	P C
07. DIGITAL LABORATORY	PR	50	20	22	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	20	P C
08. PROGRAMMING LABORATORY	TW	50	20	24	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	10	P C
09. PROGRAMMING LABORATORY	PR	50	20	32	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	33	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	25	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	20	P C
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	20	P C

GRAND TOTAL = 665/1500, RESULT: PASS CLASS

ORDN. 1 MARKS :

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S8058647   ANIKET PALLEWAD   NEELAWATI   , 70925330D   , S8058647 ,   PICT   , S8058647

01. DISCRETE STRUCTURES	PP	100	40	41	P C	11. ENG MATHS III	PP	100	40	40	P
02. COMPUTER ORGANIZATION	PP	100	40	51	P C	12. COMPUTER GRAPHICS	PP	100	40	47	P C
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	40	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	40	P C
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	43	P C	14. DATA STRUCTURES AND FILES	PP	100	40	46	P C
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	51	P C	15. DATA COMMUNICATION	PP	100	40	40	P C
06. DIGITAL LABORATORY	TW	50	20	32	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	10	P C
07. DIGITAL LABORATORY	PR	50	20	23	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	22	P C
08. PROGRAMMING LABORATORY	TW	50	20	35	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	13	P C
09. PROGRAMMING LABORATORY	PR	50	20	30	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	20	P C
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	20	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	25	P C
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	22	P C

GRAND TOTAL = 691/1500, RESULT: PASS CLASS

ORDN. 1 MARKS :

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                          UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(INFORMATION TECHNOLOGY)
    DATE : 18 AUG. 2011   CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.   PAGE NO. 48 (470)
. . . .

 NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S8058650   AVASARE ARUN RAKHAMAJI   PADMINI   , 70925345B   , S8058650 , PICT   , S8058650

01. DISCRETE STRUCTURES	PP	100	40	40	P C	11. ENG MATHS III	PP	100	40	40	P
02. COMPUTER ORGANIZATION	PP	100	40	50	P C	12. COMPUTER GRAPHICS	PP	100	40	42	P C
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	45	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	21	F
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	43	P C	14. DATA STRUCTURES AND FILES	PP	100	40	45	P C
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	40	P C	15. DATA COMMUNICATION	PP	100	40	40	P C
06. DIGITAL LABORATORY	TW	50	20	27	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	14	P C
07. DIGITAL LABORATORY	PR	50	20	31	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	35	P C
08. PROGRAMMING LABORATORY	TW	50	20	40	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	19	P C
09. PROGRAMMING LABORATORY	PR	50	20	33	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	30	P C
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	38	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	38	P C
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	32	P C

GRAND TOTAL = 743/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

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S8058652   BACHHAV HARSHAL RAGHUNATH   VIMAL   , 70925349E   , S8058652 , PICT   , S8058652

01. DISCRETE STRUCTURES	PP	100	40	AA	F	11. ENG MATHS III	PP	100	40	AA	F
02. COMPUTER ORGANIZATION	PP	100	40	14	F	12. COMPUTER GRAPHICS	PP	100	40	AA	F
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	AA	F	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	AA	F
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	08	F	14. DATA STRUCTURES AND FILES	PP	100	40	AA	F
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	40	P C	15. DATA COMMUNICATION	PP	100	40	AA	F
06. DIGITAL LABORATORY	TW	50	20	25	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	10	P C
07. DIGITAL LABORATORY	PR	50	20	AA	F	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	AA	F
08. PROGRAMMING LABORATORY	TW	50	20	32	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	10	P C
09. PROGRAMMING LABORATORY	PR	50	20	AA	F	19. DATA STRUCTURES AND FILES LAB	PR	50	20	AA	F
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	25	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	20	P C

21. OBJECT ORIENTED PROGRAMMING LAB PR   50   20   AA   F

GRAND TOTAL = 184/1500, RESULT: FAILS

ORDN. 1 MARKS :

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 S8058653   BERDE SIDDESH VISHWANATH   VAISHALI   , 71073822G   , S8058653 ,   PICT   , S8058653

 01. DISCRETE STRUCTURES   PP   100   40   40 P C   11. ENG MATHS III   PP   100   40   AA F
 02. COMPUTER ORGANIZATION   PP   100   40   51 P C   12. COMPUTER GRAPHICS   PP   100   40   56 P C
 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 40   59 P C   13. PROCESSOR ARCHITECTURE & INTER. PP 100   40   40 P C
 04. FUNDAMENTAL OF DATA STRUCTURES PP 100   40   50 P C   14. DATA STRUCTURES AND FILES   PP   100   40   55 P C
 05. HUMANITIES AND SOCIAL SCIENCES PP 100   40   51 P C   15. DATA COMMUNICATION   PP   100   40   52 P C
 06. DIGITAL LABORATORY   TW   50   20   34 P C   16. PROCESSOR INTERFACING LABORATORY TW 25   10   18 P C
 07. DIGITAL LABORATORY   PR   50   20   30 P C   17. PROCESSOR INTERFACING LABORATORY OR 50   20   31 P C
 08. PROGRAMMING LABORATORY   TW   50   20   39 P C   18. DATA STRUCTURES AND FILES LAB   TW   25   10   15 P C
 09. PROGRAMMING LABORATORY   PR   50   20   30 P C   19. DATA STRUCTURES AND FILES LAB   PR   50   20   36 P C
 10. COMMUNICATION AND LANGUAGE LAB. TW 50   20   38 P C   20. OBJECT ORIENTED PROGRAMMING LAB TW   50   20   36 P C
                                                               21. OBJECT ORIENTED PROGRAMMING LAB PR   50   20   32 P C

GRAND TOTAL = 793/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

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            UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(INFORMATION TECHNOLOGY)
   DATE : 18 AUG. 2011   CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.   PAGE NO. 49 (471)
. . . .
 NOTE: FIRST LINE : SEAT NO.,   NAME OF THE CANDIDATE,   MOTHER, PERMANENT REG. NO.,   PREVIOUS SEAT NO.,   COLLEGE,   SEAT NO.
       OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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 S8058654   BHALERAO MANGESH GANESH   NANDA   , 70925361D   , S8058654 ,   PICT   , S8058654

 01. DISCRETE STRUCTURES   PP   100   40   52 P C   11. ENG MATHS III   PP   100   40   44 P C

02. COMPUTER ORGANIZATION	PP	100	40	40	P C	12. COMPUTER GRAPHICS	PP	100	40	51	P C
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	45	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	05	F
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	40	P C	14. DATA STRUCTURES AND FILES	PP	100	40	63	P C
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	44	P C	15. DATA COMMUNICATION	PP	100	40	40	P C
06. DIGITAL LABORATORY	TW	50	20	33	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	14	P C
07. DIGITAL LABORATORY	PR	50	20	22	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	22	P C
08. PROGRAMMING LABORATORY	TW	50	20	32	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	13	P C
09. PROGRAMMING LABORATORY	PR	50	20	30	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	26	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	36	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	25	P C
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	32	P C

GRAND TOTAL = 709/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

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S8058655   BHARADIYA MAYURI SATISHCHANDRA   ANITA   , 71073843K   , S8058655 ,   PICT   , S8058655

01. DISCRETE STRUCTURES	PP	100	40	45	P C	11. ENG MATHS III	PP	100	40	40	P
02. COMPUTER ORGANIZATION	PP	100	40	43	P C	12. COMPUTER GRAPHICS	PP	100	40	43	P C
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	58	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	49	P C
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	54	P C	14. DATA STRUCTURES AND FILES	PP	100	40	68	P C
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	53	P C	15. DATA COMMUNICATION	PP	100	40	44	P C
06. DIGITAL LABORATORY	TW	50	20	38	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	18	P C
07. DIGITAL LABORATORY	PR	50	20	37	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	36	P C
08. PROGRAMMING LABORATORY	TW	50	20	35	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	18	P C
09. PROGRAMMING LABORATORY	PR	50	20	25	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	39	P C
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	35	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	38	P C
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	36	P C

GRAND TOTAL = 852/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

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S8058657   DEORE NIKHIL BAPUSAHEB   MANDAKINI   , 70801397L   , S8058657 ,   PICT   , S8058657

01. DISCRETE STRUCTURES	PP	100	40	53	P C		11. ENG MATHS III	PP	100	40	40	P
02. COMPUTER ORGANIZATION	PP	100	40	41	P C		12. COMPUTER GRAPHICS	PP	100	40	49	P C
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	40	P C		13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	40	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	40	P C		14. DATA STRUCTURES AND FILES	PP	100	40	58	P C
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	40	P C		15. DATA COMMUNICATION	PP	100	40	48	P
06. DIGITAL LABORATORY	TW	50	20	25	P C		16. PROCESSOR INTERFACING LABORATORY	TW	25	10	10	P C
07. DIGITAL LABORATORY	PR	50	20	21	P C		17. PROCESSOR INTERFACING LABORATORY	OR	50	20	04	F
08. PROGRAMMING LABORATORY	TW	50	20	33	P C		18. DATA STRUCTURES AND FILES LAB	TW	25	10	10	P C
09. PROGRAMMING LABORATORY	PR	50	20	28	P C		19. DATA STRUCTURES AND FILES LAB	PR	50	20	08	F
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	32	P C		20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	20	P C
							21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	25	P

GRAND TOTAL = 665/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

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                    UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(INFORMATION TECHNOLOGY)
     DATE : 18 AUG. 2011   CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.   PAGE NO. 50 (472)
. . . .
  NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.
        OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S8058658   DESHPANDE AJINKYA AJIT   ANAGHA   , 71073825M   , S8058658 ,   PICT   , S8058658

01. DISCRETE STRUCTURES	PP	100	40	40	P C		11. ENG MATHS III	PP	100	40	16	F
02. COMPUTER ORGANIZATION	PP	100	40	52	P C		12. COMPUTER GRAPHICS	PP	100	40	46	P C
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	49	P C		13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	55	P C
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	43	P C		14. DATA STRUCTURES AND FILES	PP	100	40	54	P C
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	53	P C		15. DATA COMMUNICATION	PP	100	40	44	P C
06. DIGITAL LABORATORY	TW	50	20	35	P C		16. PROCESSOR INTERFACING LABORATORY	TW	25	10	18	P C

07. DIGITAL LABORATORY	PR	50	20	30	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	42	P C
08. PROGRAMMING LABORATORY	TW	50	20	37	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	20	P C
09. PROGRAMMING LABORATORY	PR	50	20	32	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	45	P C
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	35	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	41	P C
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	40	P C

GRAND TOTAL = 827/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

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S8058659   GATHE KAPILCHAND NAMDEORAO   SUNANDA   , 70925424F   , S8058659 ,   PICT   , S8058659

01. DISCRETE STRUCTURES	PP	100	40	40	P C	11. ENG MATHS III	PP	100	40	42	P
02. COMPUTER ORGANIZATION	PP	100	40	45	P C	12. COMPUTER GRAPHICS	PP	100	40	45	P C
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	41	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	40	P C
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	47	P C	14. DATA STRUCTURES AND FILES	PP	100	40	47	P C
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	44	P C	15. DATA COMMUNICATION	PP	100	40	42	P C
06. DIGITAL LABORATORY	TW	50	20	35	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	11	P C
07. DIGITAL LABORATORY	PR	50	20	20	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	24	P C
08. PROGRAMMING LABORATORY	TW	50	20	37	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	13	P C
09. PROGRAMMING LABORATORY	PR	50	20	26	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	40	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	42	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	37	P C
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	35	P C

GRAND TOTAL = 753/1500, RESULT: SECOND CLASS

ORDN. 1 MARKS :

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S8058662   JADHAV DEEPANJAN MADHAV   ARUNA   , 70925445J   , S8058662 ,   PICT   , S8058662

01. DISCRETE STRUCTURES	PP	100	40	40	P C	11. ENG MATHS III	PP	100	40	20	F
02. COMPUTER ORGANIZATION	PP	100	40	55	P C	12. COMPUTER GRAPHICS	PP	100	40	53	P C
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	51	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	43	P C

04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	46	P C	14. DATA STRUCTURES AND FILES	PP	100	40	61	P C
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	48	P C	15. DATA COMMUNICATION	PP	100	40	47	P C
06. DIGITAL LABORATORY	TW	50	20	34	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	11	P C
07. DIGITAL LABORATORY	PR	50	20	28	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	21	P C
08. PROGRAMMING LABORATORY	TW	50	20	41	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	12	P C
09. PROGRAMMING LABORATORY	PR	50	20	20	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	AA	F
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	41	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	34	P C
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	AA	F

GRAND TOTAL = 706/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

. . . . UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(INFORMATION TECHNOLOGY) . . . .

. . . .

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NOS., COLLEGE,   SEAT NO.

        OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S8058663   JADHAV PRASHANT DAYANAND   MALAN   , 70503890E   , S8058663 ,   PICT   , S8058663

01. DISCRETE STRUCTURES	PP	100	40	AA	F	11. ENG MATHS III	PP	100	40	AA	F
02. COMPUTER ORGANIZATION	PP	100	40	AA	F	12. COMPUTER GRAPHICS	PP	100	40	AA	F
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	AA	F	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	AA	F
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	AA	F	14. DATA STRUCTURES AND FILES	PP	100	40	40	P C
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	AA	F	15. DATA COMMUNICATION	PP	100	40	40	P C
06. DIGITAL LABORATORY	TW	50	20	AA	F	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	10	P C
07. DIGITAL LABORATORY	PR	50	20	AA	F	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	AA	F
08. PROGRAMMING LABORATORY	TW	50	20	AA	F	18. DATA STRUCTURES AND FILES LAB	TW	25	10	10	P C
09. PROGRAMMING LABORATORY	PR	50	20	AA	F	19. DATA STRUCTURES AND FILES LAB	PR	50	20	AA	F
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	AA	F	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	20	P C
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	AA	F

GRAND TOTAL = 120/1500, RESULT: FAILS   RESULT RESERVED FOR OTHR

ORDN. 1 MARKS :

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S8058665   KAKADE SHASHIKANT AVINASH   NANDA   , 70925460B   , S8058665 ,   PICT   , S8058665

01. DISCRETE STRUCTURES	PP	100	40	44	P C		11. ENG MATHS III		PP	100	40	46	P C
02. COMPUTER ORGANIZATION	PP	100	40	52	P C		12. COMPUTER GRAPHICS		PP	100	40	40	P C
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	53	P C		13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	54	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	45	P C		14. DATA STRUCTURES AND FILES	PP	100	40	50	P C
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	40	P C		15. DATA COMMUNICATION	PP	100	40	40	P C
06. DIGITAL LABORATORY	TW	50	20	42	P C		16. PROCESSOR INTERFACING LABORATORY	TW	25	10	15	P C
07. DIGITAL LABORATORY	PR	50	20	38	P C		17. PROCESSOR INTERFACING LABORATORY	OR	50	20	31	P C
08. PROGRAMMING LABORATORY	TW	50	20	38	P C		18. DATA STRUCTURES AND FILES LAB	TW	25	10	10	P C
09. PROGRAMMING LABORATORY	PR	50	20	30	P C		19. DATA STRUCTURES AND FILES LAB	PR	50	20	37	P C
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	42	P C		20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	32	P C
							21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	38	P C

GRAND TOTAL = 817+08/1500, RESULT: HIGHER SECOND CLASS   [O.2]

ORDN. 1 MARKS :

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S8058666   KHANDAGALE SURAJ PANDURANG   SUNITA   , 70925476J   , S8058666 ,   PICT   , S8058666

01. DISCRETE STRUCTURES	PP	100	40	40	P C		11. ENG MATHS III		PP	100	40	56	P
02. COMPUTER ORGANIZATION	PP	100	40	51	P C		12. COMPUTER GRAPHICS		PP	100	40	61	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	64	P C		13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	40	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	51	P C		14. DATA STRUCTURES AND FILES	PP	100	40	59	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	48	P C		15. DATA COMMUNICATION	PP	100	40	45	P
06. DIGITAL LABORATORY	TW	50	20	36	P C		16. PROCESSOR INTERFACING LABORATORY	TW	25	10	15	P
07. DIGITAL LABORATORY	PR	50	20	30	P C		17. PROCESSOR INTERFACING LABORATORY	OR	50	20	40	P
08. PROGRAMMING LABORATORY	TW	50	20	41	P C		18. DATA STRUCTURES AND FILES LAB	TW	25	10	13	P

09. PROGRAMMING LABORATORY	PR	50	20	30	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	42	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	40	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	33	P
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	28	P

GRAND TOTAL = 863/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

. . . . .UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(INFORMATION TECHNOLOGY). . . . .

DATE : 18 AUG. 2011   CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.   PAGE NO. 52 (474)

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NOS., COLLEGE,   SEAT NO.

        OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S8058667   KOTAWAL SACHIN DILIP   SHASHIKALA   , 71073833B   , S8058667 , PICT   , S8058667

01. DISCRETE STRUCTURES	PP	100	40	40	P C	11. ENG MATHS III	PP	100	40	22	F
02. COMPUTER ORGANIZATION	PP	100	40	45	P C	12. COMPUTER GRAPHICS	PP	100	40	42	P C
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	52	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	40	P C
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	42	P C	14. DATA STRUCTURES AND FILES	PP	100	40	47	P C
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	50	P C	15. DATA COMMUNICATION	PP	100	40	48	P C
06. DIGITAL LABORATORY	TW	50	20	35	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	19	P C
07. DIGITAL LABORATORY	PR	50	20	38	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	37	P C
08. PROGRAMMING LABORATORY	TW	50	20	39	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	22	P C
09. PROGRAMMING LABORATORY	PR	50	20	22	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	41	P C
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	36	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	41	P C
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	36	P C

GRAND TOTAL = 794/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

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S8058669   MAGAR ASHWINI DEELIP   SHALINI   , 71073835J   , S8058669 ,   PICT   , S8058669

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01. DISCRETE STRUCTURES          PP  100  40  48  P C    11. ENG MATHS III                    PP  100  40  40  P
02. COMPUTER ORGANIZATION        PP  100  40  40  P C    12. COMPUTER GRAPHICS                PP  100  40  51  P C
03. DIGITAL ELECTRONICS & LOGIC DESIGPP  100  40  60  P C    13. PROCESSOR ARCHITECTURE & INTER.  PP  100  40  47  P C
04. FUNDAMENTAL OF DATA STRUCTURES PP  100  40  51  P C    14. DATA STRUCTURES AND FILES        PP  100  40  54  P C
05. HUMANITIES AND SOCIAL SCIENCES PP  100  40  53  P C    15. DATA COMMUNICATION               PP  100  40  44  P C
06. DIGITAL LABORATORY           TW   50  20  36  P C    16. PROCESSOR INTERFACING LABORATORY TW   25  10  16  P C
07. DIGITAL LABORATORY           PR   50  20  25  P C    17. PROCESSOR INTERFACING LABORATORY OR   50  20  24  P C
08. PROGRAMMING LABORATORY       TW   50  20  36  P C    18. DATA STRUCTURES AND FILES LAB    TW   25  10  19  P C
09. PROGRAMMING LABORATORY       PR   50  20  20  P C    19. DATA STRUCTURES AND FILES LAB    PR   50  20  41  P C
10. COMMUNICATION AND LANGUAGE LAB. TW   50  20  32  P C    20. OBJECT ORIENTED PROGRAMMING LAB  TW   50  20  42  P C
                                                         21. OBJECT ORIENTED PROGRAMMING LAB  PR   50  20  23  P C
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GRAND TOTAL = 802/1500, RESULT: SECOND CLASS

ORDN. 1 MARKS :

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S8058670   MAHAJAN SACHINRAVINDRA   SARALA   , 70701521K   , S8058670 ,   PICT   , S8058670

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01. DISCRETE STRUCTURES          PP  100  40  AA  F      11. ENG MATHS III                    PP  100  40  AA  F
02. COMPUTER ORGANIZATION        PP  100  40  AA  F      12. COMPUTER GRAPHICS                PP  100  40  48  P C
03. DIGITAL ELECTRONICS & LOGIC DESIGPP  100  40  AA  F      13. PROCESSOR ARCHITECTURE & INTER.  PP  100  40  40  P C
04. FUNDAMENTAL OF DATA STRUCTURES PP  100  40  AA  F      14. DATA STRUCTURES AND FILES        PP  100  40  45  P C
05. HUMANITIES AND SOCIAL SCIENCES PP  100  40  AA  F      15. DATA COMMUNICATION               PP  100  40  57  P C
06. DIGITAL LABORATORY           TW   50  20  AA  F      16. PROCESSOR INTERFACING LABORATORY TW   25  10  11  P C
07. DIGITAL LABORATORY           PR   50  20  AA  F      17. PROCESSOR INTERFACING LABORATORY OR   50  20  20  P C
08. PROGRAMMING LABORATORY       TW   50  20  AA  F      18. DATA STRUCTURES AND FILES LAB    TW   25  10  12  P C
09. PROGRAMMING LABORATORY       PR   50  20  AA  F      19. DATA STRUCTURES AND FILES LAB    PR   50  20  25  P
10. COMMUNICATION AND LANGUAGE LAB. TW   50  20  AA  F      20. OBJECT ORIENTED PROGRAMMING LAB  TW   50  20  24  P C
                                                         21. OBJECT ORIENTED PROGRAMMING LAB  PR   50  20  26  P
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GRAND TOTAL = 308/1500, RESULT: FAILS   RESULT RESERVED FOR OTHR

. . . . UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(INFORMATION TECHNOLOGY) . . . .

    DATE : 18 AUG. 2011   CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.   PAGE NO. 53 (475)

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 NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.

       OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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 S8058671   MAYANK BHATNAGAR   SHEETAL   , 70925502M   , S8058671 , PICT   , S8058671

 01. DISCRETE STRUCTURES   PP 100 40 63 P C   11. ENG MATHS III   PP 100 40 24 F
 02. COMPUTER ORGANIZATION   PP 100 40 41 P C   12. COMPUTER GRAPHICS   PP 100 40 42 P C
 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 40 67 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 40 P C
 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 41 P C   14. DATA STRUCTURES AND FILES   PP 100 40 47 P C
 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 43 P C   15. DATA COMMUNICATION   PP 100 40 42 P C
 06. DIGITAL LABORATORY   TW   50 20 42 P C   16. PROCESSOR INTERFACING LABORATORY TW   25 10 21 P C
 07. DIGITAL LABORATORY   PR   50 20 40 P C   17. PROCESSOR INTERFACING LABORATORY OR   50 20 37 P C
 08. PROGRAMMING LABORATORY   TW   50 20 41 P C   18. DATA STRUCTURES AND FILES LAB   TW   25 10 22 P C
 09. PROGRAMMING LABORATORY   PR   50 20 38 P C   19. DATA STRUCTURES AND FILES LAB   PR   50 20 35 P C
 10. COMMUNICATION AND LANGUAGE LAB. TW   50 20 40 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW   50 20 46 P C
                                                           21. OBJECT ORIENTED PROGRAMMING LAB PR   50 20 40 P C

 GRAND TOTAL = 852/1500, RESULT: FAILS A.T.K.T.

 ORDN. 1 MARKS :

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 S8058672   MONISH PATEL   NEETA   , 70925508L   , S8058672 , PICT   , S8058672

 01. DISCRETE STRUCTURES   PP 100 40 40 P C   11. ENG MATHS III   PP 100 40 40 P C
 02. COMPUTER ORGANIZATION   PP 100 40 40 P C   12. COMPUTER GRAPHICS   PP 100 40 40 P C
 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 40 23 F   13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 40 P
 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 49 P C   14. DATA STRUCTURES AND FILES   PP 100 40 40 P C

05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	42	P C	15. DATA COMMUNICATION	PP	100	40	40	P C
06. DIGITAL LABORATORY	TW	50	20	32	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	10	P C
07. DIGITAL LABORATORY	PR	50	20	20	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	25	P
08. PROGRAMMING LABORATORY	TW	50	20	38	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	10	P C
09. PROGRAMMING LABORATORY	PR	50	20	30	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	20	P C
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	35	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	20	P C
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	17	F

GRAND TOTAL = 651/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

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S8058673   MUNOT PAYAL KAMLESHKUMAR   MEENAKSHI   , 70925517K   , S8058673 ,   PICT   , S8058673

01. DISCRETE STRUCTURES	PP	100	40	51	P C	11. ENG MATHS III	PP	100	40	27	F
02. COMPUTER ORGANIZATION	PP	100	40	40	P C	12. COMPUTER GRAPHICS	PP	100	40	12	F
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	50	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	14	F
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	45	P C	14. DATA STRUCTURES AND FILES	PP	100	40	40	P C
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	40	P C	15. DATA COMMUNICATION	PP	100	40	15	F
06. DIGITAL LABORATORY	TW	50	20	35	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	11	P C
07. DIGITAL LABORATORY	PR	50	20	20	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	AA	F
08. PROGRAMMING LABORATORY	TW	50	20	33	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	15	P C
09. PROGRAMMING LABORATORY	PR	50	20	20	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	27	P C
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	40	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	41	P C
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	AA	F

GRAND TOTAL = 576/1500, RESULT: FAILS   RESULT RESERVED FOR BKLG

ORDN. 1 MARKS :

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                    UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(INFORMATION TECHNOLOGY)
     DATE : 18 AUG. 2011   CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.   PAGE NO. 54 (476)

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

         OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S8058674   N Y GARIN   YI MALIVATTEY   , 70925519F   , S8058674 ,   PICT   , S8058674

01. DISCRETE STRUCTURES	PP	100	40	45	P C	11. ENG MATHS III	PP	100	40	40	P
02. COMPUTER ORGANIZATION	PP	100	40	40	P C	12. COMPUTER GRAPHICS	PP	100	40	42	P C
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	41	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	27	F
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	40	P C	14. DATA STRUCTURES AND FILES	PP	100	40	40	P C
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	40	P C	15. DATA COMMUNICATION	PP	100	40	40	P C
06. DIGITAL LABORATORY	TW	50	20	33	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	16	P C
07. DIGITAL LABORATORY	PR	50	20	30	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	35	P C
08. PROGRAMMING LABORATORY	TW	50	20	35	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	18	P C
09. PROGRAMMING LABORATORY	PR	50	20	30	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	40	P C
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	38	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	44	P C
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	29	P

GRAND TOTAL = 743/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

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S8058675   NAVANDE NEHAL MALLIKARJUN   PRABHA   , 70925525L   , S8058675 ,   PICT   , S8058675

01. DISCRETE STRUCTURES	PP	100	40	49	P C	11. ENG MATHS III	PP	100	40	40	P
02. COMPUTER ORGANIZATION	PP	100	40	40	P C	12. COMPUTER GRAPHICS	PP	100	40	43	P C
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	59	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	40	P C
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	54	P C	14. DATA STRUCTURES AND FILES	PP	100	40	52	P C
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	48	P C	15. DATA COMMUNICATION	PP	100	40	58	P C
06. DIGITAL LABORATORY	TW	50	20	38	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	17	P C
07. DIGITAL LABORATORY	PR	50	20	25	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	35	P C
08. PROGRAMMING LABORATORY	TW	50	20	32	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	18	P C
09. PROGRAMMING LABORATORY	PR	50	20	34	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	25	P C

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10. COMMUNICATION AND LANGUAGE LAB.  TW   50   20   41  P C     20. OBJECT ORIENTED PROGRAMMING LAB  TW   50   20   40  P C
                                                                21. OBJECT ORIENTED PROGRAMMING LAB  PR   50   20   20  P C
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GRAND TOTAL = 808/1500, RESULT: SECOND CLASS

ORDN. 1 MARKS :

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S8058684   SHIVADE PRATHAMESH SHRIKANT   NILIMA   , 70925608G   , S8058684 ,   PICT   , S8058684

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01. DISCRETE STRUCTURES          PP   100   40   44  P C     11. ENG MATHS III                   PP   100   40   25  F
02. COMPUTER ORGANIZATION        PP   100   40   41  P C     12. COMPUTER GRAPHICS               PP   100   40   40  P C
03. DIGITAL ELECTRONICS & LOGIC DESIGPP  100  40   43  P C   13. PROCESSOR ARCHITECTURE & INTER.  PP   100   40   45  P C
04. FUNDAMENTAL OF DATA STRUCTURES PP 100   40   57  P C     14. DATA STRUCTURES AND FILES       PP   100   40   49  P C
05. HUMANITIES AND SOCIAL SCIENCES PP 100   40   43  P C     15. DATA COMMUNICATION              PP   100   40   40  P C
06. DIGITAL LABORATORY           TW    50   20   34  P C     16. PROCESSOR INTERFACING LABORATORY TW    25   10   14  P C
07. DIGITAL LABORATORY           PR    50   20   28  P C     17. PROCESSOR INTERFACING LABORATORY OR    50   20   21  P C
08. PROGRAMMING LABORATORY       TW    50   20   36  P C     18. DATA STRUCTURES AND FILES LAB    TW    25   10   16  P C
09. PROGRAMMING LABORATORY       PR    50   20   33  P C     19. DATA STRUCTURES AND FILES LAB    PR    50   20   33  P C
10. COMMUNICATION AND LANGUAGE LAB. TW 50   20   38  P C     20. OBJECT ORIENTED PROGRAMMING LAB  TW    50   20   28  P C
                                                            21. OBJECT ORIENTED PROGRAMMING LAB  PR    50   20   33  P C
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GRAND TOTAL = 741/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

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                    UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(INFORMATION TECHNOLOGY)
    DATE : 18 AUG. 2011   CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.   PAGE NO. 55 (477)
. . . .

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.

      OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S8058685   SHRIKANTH JAIKUMAR   BHUVNA   , 70801622H   , S8058685 ,   PICT   , S8058685

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01. DISCRETE STRUCTURES              PP  100  40  40  P C    11. ENG MATHS III                  PP  100  40  66  P
02. COMPUTER ORGANIZATION            PP  100  40  40  P C    12. COMPUTER GRAPHICS              PP  100  40  28  F
03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100  40  53  P C    13. PROCESSOR ARCHITECTURE & INTER. PP  100  40  47  P
04. FUNDAMENTAL OF DATA STRUCTURES   PP  100  40  70  P      14. DATA STRUCTURES AND FILES      PP  100  40  59  P
05. HUMANITIES AND SOCIAL SCIENCES   PP  100  40  40  P C    15. DATA COMMUNICATION             PP  100  40  54  P
06. DIGITAL LABORATORY               TW   50  20  20  P C    16. PROCESSOR INTERFACING LABORATORY TW  25  10  21  P
07. DIGITAL LABORATORY               PR   50  20  32  P C    17. PROCESSOR INTERFACING LABORATORY OR  50  20  35  P
08. PROGRAMMING LABORATORY           TW   50  20  20  P C    18. DATA STRUCTURES AND FILES LAB   TW   25  10  21  P
09. PROGRAMMING LABORATORY           PR   50  20  29  P C    19. DATA STRUCTURES AND FILES LAB   PR   50  20  44  P
10. COMMUNICATION AND LANGUAGE LAB.  TW   50  20  20  P C    20. OBJECT ORIENTED PROGRAMMING LAB TW   50  20  39  P
                                                             21. OBJECT ORIENTED PROGRAMMING LAB PR   50  20  33  P
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GRAND TOTAL = 811/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

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S8058686   SUYASH PANDEY   SHAILA   , 70925625G   , S8058686 ,   PICT   , S8058686

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01. DISCRETE STRUCTURES              PP  100  40  58  P C    11. ENG MATHS III                  PP  100  40  40  P C
02. COMPUTER ORGANIZATION            PP  100  40  48  P C    12. COMPUTER GRAPHICS              PP  100  40  40  P C
03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100  40  46  P C    13. PROCESSOR ARCHITECTURE & INTER. PP  100  40  AA  F
04. FUNDAMENTAL OF DATA STRUCTURES   PP  100  40  50  P C    14. DATA STRUCTURES AND FILES      PP  100  40  40  P C
05. HUMANITIES AND SOCIAL SCIENCES   PP  100  40  40  P C    15. DATA COMMUNICATION             PP  100  40  40  P C
06. DIGITAL LABORATORY               TW   50  20  21  P C    16. PROCESSOR INTERFACING LABORATORY TW  25  10  10  P C
07. DIGITAL LABORATORY               PR   50  20  27  P C    17. PROCESSOR INTERFACING LABORATORY OR  50  20  AA  F
08. PROGRAMMING LABORATORY           TW   50  20  23  P C    18. DATA STRUCTURES AND FILES LAB   TW   25  10  10  P C
09. PROGRAMMING LABORATORY           PR   50  20  30  P C    19. DATA STRUCTURES AND FILES LAB   PR   50  20  AA  F
10. COMMUNICATION AND LANGUAGE LAB.  TW   50  20  25  P C    20. OBJECT ORIENTED PROGRAMMING LAB TW   50  20  20  P C
                                                             21. OBJECT ORIENTED PROGRAMMING LAB PR   50  20  32  P
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GRAND TOTAL = 600/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

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S8058687   THAKUR BHAVANA PRAKASH   ARADHANA   , 70925633H   , S8058687 , PICT   , S8058687

01. DISCRETE STRUCTURES	PP	100	40	40	P C		11. ENG MATHS III		PP	100	40	54	P C
02. COMPUTER ORGANIZATION	PP	100	40	51	P C		12. COMPUTER GRAPHICS		PP	100	40	48	P C
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	40	P C		13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	AA	F
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	40	P C		14. DATA STRUCTURES AND FILES		PP	100	40	51	P C
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	44	P C		15. DATA COMMUNICATION		PP	100	40	40	P C
06. DIGITAL LABORATORY	TW	50	20	39	P C		16. PROCESSOR INTERFACING LABORATORY	TW	25	10	19	P C
07. DIGITAL LABORATORY	PR	50	20	32	P C		17. PROCESSOR INTERFACING LABORATORY	OR	50	20	32	P C
08. PROGRAMMING LABORATORY	TW	50	20	34	P C		18. DATA STRUCTURES AND FILES LAB	TW	25	10	17	P C
09. PROGRAMMING LABORATORY	PR	50	20	23	P C		19. DATA STRUCTURES AND FILES LAB	PR	50	20	35	P C
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	33	P C		20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	41	P C
							21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	28	P C

GRAND TOTAL = 741/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

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            UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(INFORMATION TECHNOLOGY)
    DATE : 18 AUG. 2011   CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.   PAGE NO. 56 (478)
. . . .

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.

        OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S8058691   VIVEK KUMAR   SUMAN   , 70925651F   , S8058691 , PICT   , S8058691

01. DISCRETE STRUCTURES	PP	100	40	55	P C		11. ENG MATHS III		PP	100	40	41	P
02. COMPUTER ORGANIZATION	PP	100	40	41	P C		12. COMPUTER GRAPHICS		PP	100	40	41	P C
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	54	P C		13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	40	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	48	P C		14. DATA STRUCTURES AND FILES		PP	100	40	40	P C
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	53	P C		15. DATA COMMUNICATION		PP	100	40	41	P C

06. DIGITAL LABORATORY	TW	50	20	35	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	10	P C
07. DIGITAL LABORATORY	PR	50	20	28	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	24	P C
08. PROGRAMMING LABORATORY	TW	50	20	35	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	12	P C
09. PROGRAMMING LABORATORY	PR	50	20	30	P	19. DATA STRUCTURES AND FILES LAB	PR	50	20	25	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	37	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	25	P C
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	22	P

GRAND TOTAL = 737/1500, RESULT: PASS CLASS

ORDN. 1 MARKS :

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S8058692   WAGH ABHISHEK KARBHARI   KRANTI   , 70925654L   , S8058692 ,   PICT   , S8058692

01. DISCRETE STRUCTURES	PP	100	40	46	P C	11. ENG MATHS III	PP	100	40	23	F
02. COMPUTER ORGANIZATION	PP	100	40	40	P C	12. COMPUTER GRAPHICS	PP	100	40	40	P C
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	40	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	09	F
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	40	P C	14. DATA STRUCTURES AND FILES	PP	100	40	43	P C
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	40	P C	15. DATA COMMUNICATION	PP	100	40	30	F
06. DIGITAL LABORATORY	TW	50	20	25	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	10	P C
07. DIGITAL LABORATORY	PR	50	20	30	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	20	P C
08. PROGRAMMING LABORATORY	TW	50	20	25	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	10	P C
09. PROGRAMMING LABORATORY	PR	50	20	28	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	20	P C
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	21	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	20	P C
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	22	P C

GRAND TOTAL = 582/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

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