DATE: 31 JAN. 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 01 (370) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71100709J , , PICT , T80053001 T80053001 AAKASH SHINDE MAMTA 01. CONTROL SYSTEMS 100 40 51 P 02. DIGITAL COMMUNICATION 100 40 44 P 03. DIGITAL COMMUNICATION 50 20 07 F PR 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 55 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 37 P 06. MICROCONTROLLERS & APPLICATION PP 100 40 43 P 50 20 10 F 07. MICROCONTROLLERS & APPLICATION PR 08. DIGITAL SIGNAL PROCESSING 100 40 40 P 09. DIGITAL SIGNAL PROCESSING 50 20 24 P OR 50 20 25 P 10. ELECTRONIC DESIGN PRACTICE OR FIRST TERM TOTAL = 336/750. ORDN. 1 MARKS: T80053002 AASHIQ HANEEF AHMED RASHEEDA , 71106382G , , PICT , T80053002 01. CONTROL SYSTEMS 100 40 81 P 02. DIGITAL COMMUNICATION 100 40 67 P PP 03. DIGITAL COMMUNICATION 20 37 P PR 50 40 66 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 50 20 43 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 100 40 56 P 06. MICROCONTROLLERS & APPLICATION PP 50 20 40 P 07. MICROCONTROLLERS & APPLICATION PR 08. DIGITAL SIGNAL PROCESSING 100 40 72 P 09. DIGITAL SIGNAL PROCESSING 50 20 42 P OR 50 20 26 P 10. ELECTRONIC DESIGN PRACTICE OR FIRST TERM TOTAL = 530/750. ORDN. 1 MARKS: T80053003 ABHISHEK KUMAR KIRAN DEVI , 71100714E , т80053003 , PICT 01. CONTROL SYSTEMS 100 40 62 P PP 100 40 65 P 02. DIGITAL COMMUNICATION 03. DIGITAL COMMUNICATION 50 20 28 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 71 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 32 P 06. MICROCONTROLLERS & APPLICATION PP 100 40 59 P 07. MICROCONTROLLERS & APPLICATION PR 50 20 30 P 100 40 59 P 08. DIGITAL SIGNAL PROCESSING 09. DIGITAL SIGNAL PROCESSING 50 20 35 P OR 10. ELECTRONIC DESIGN PRACTICE OR 50 20 24 P FIRST TERM TOTAL = 465/750. ORDN. 1 MARKS:

DATE: 31 JAN. 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 02 (371) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER T80053004 ABHISHEK SHARMA KANTA , 71100715C , , PICT , T80053004 01. CONTROL SYSTEMS 100 40 61 P 02. DIGITAL COMMUNICATION 100 40 66 P 03. DIGITAL COMMUNICATION 50 20 30 P PR 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 64 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 38 P 06. MICROCONTROLLERS & APPLICATION PP 100 40 61 P 07. MICROCONTROLLERS & APPLICATION PR 50 20 38 P 08. DIGITAL SIGNAL PROCESSING 100 40 55 P 09. DIGITAL SIGNAL PROCESSING 50 20 36 P OR 50 20 25 P 10. ELECTRONIC DESIGN PRACTICE OR FIRST TERM TOTAL = 474/750. ORDN. 1 MARKS: T80053005 ABIZAR HUSSAIN NASEEM , 71100716M , , PICT , T80053005 01. CONTROL SYSTEMS 100 40 55 P 02. DIGITAL COMMUNICATION 100 40 43 P 03. DIGITAL COMMUNICATION 20 30 P PR 50 100 40 59 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 50 20 38 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 100 40 54 P 06. MICROCONTROLLERS & APPLICATION PP 50 20 30 P 07. MICROCONTROLLERS & APPLICATION PR 08. DIGITAL SIGNAL PROCESSING 100 40 53 P 09. DIGITAL SIGNAL PROCESSING 50 20 32 P OR 50 20 26 P 10. ELECTRONIC DESIGN PRACTICE OR FIRST TERM TOTAL = 420/750. ORDN. 1 MARKS: T80053006 ADITYA KALIA NEELAM , 71100718н , т80053006 , PICT 100 40 52 P 01. CONTROL SYSTEMS PP 100 40 40 P 02. DIGITAL COMMUNICATION 03. DIGITAL COMMUNICATION 50 20 20 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 44 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 34 P 06. MICROCONTROLLERS & APPLICATION PP 100 40 53 P 07. MICROCONTROLLERS & APPLICATION PR 50 20 25 P 100 40 32 F 08. DIGITAL SIGNAL PROCESSING 09. DIGITAL SIGNAL PROCESSING 50 20 11 F OR 10. ELECTRONIC DESIGN PRACTICE OR 50 20 22 P FIRST TERM TOTAL = 333/750. ORDN. 1 MARKS:

DATE: 31 JAN. 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 03 (372) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71100721H , , PICT , T80053007 T80053007 AGRAWAL PRANAT ADITYA ASHA PP 100 40 47 P 01. CONTROL SYSTEMS 02. DIGITAL COMMUNICATION 100 40 40 P 03. DIGITAL COMMUNICATION 50 20 08 F PR 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 47 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 36 P 06. MICROCONTROLLERS & APPLICATION PP 100 40 47 P 07. MICROCONTROLLERS & APPLICATION PR 50 20 21 P 08. DIGITAL SIGNAL PROCESSING 100 40 42 P 09. DIGITAL SIGNAL PROCESSING 50 20 11 F OR 50 20 24 P 10. ELECTRONIC DESIGN PRACTICE OR FIRST TERM TOTAL = 323/750. ORDN. 1 MARKS: T80053008 AIKAL SANKET KABIRNATH SANGEETHA , 71100723D , , PICT , т80053008 01. CONTROL SYSTEMS 100 40 46 P 02. DIGITAL COMMUNICATION 100 40 56 P 03. DIGITAL COMMUNICATION 20 27 P PR 50 40 50 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 50 20 41 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 100 40 43 P 06. MICROCONTROLLERS & APPLICATION PP 50 20 30 P 07. MICROCONTROLLERS & APPLICATION PR 08. DIGITAL SIGNAL PROCESSING 100 40 53 P 09. DIGITAL SIGNAL PROCESSING 50 20 31 P OR 50 20 26 P 10. ELECTRONIC DESIGN PRACTICE OR FIRST TERM TOTAL = 403/750. ORDN. 1 MARKS: T80053009 ALI ASGER MODI **ZEENAT** , 71100724B , т80053009 , PICT 01. CONTROL SYSTEMS 100 40 40 P PP 100 40 40 P 02. DIGITAL COMMUNICATION 03. DIGITAL COMMUNICATION 50 20 30 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 48 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 34 P 06. MICROCONTROLLERS & APPLICATION PP 100 40 56 P 07. MICROCONTROLLERS & APPLICATION PR 50 20 27 P 100 40 32 F 08. DIGITAL SIGNAL PROCESSING 09. DIGITAL SIGNAL PROCESSING 50 20 23 P OR 10. ELECTRONIC DESIGN PRACTICE OR 50 20 22 P FIRST TERM TOTAL = 352/750. ORDN. 1 MARKS:

DATE: 31 JAN. 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 04 (373) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER T80053010 AMBURE GANESH VITHALRAO ANITA , 71100726J , , PICT , T80053010 01. CONTROL SYSTEMS 100 40 61 P PP 02. DIGITAL COMMUNICATION 100 40 53 P 03. DIGITAL COMMUNICATION 50 20 09 F PR 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 61 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 32 P 06. MICROCONTROLLERS & APPLICATION PP 100 40 47 P 07. MICROCONTROLLERS & APPLICATION PR 50 20 29 P 08. DIGITAL SIGNAL PROCESSING 100 40 51 P 09. DIGITAL SIGNAL PROCESSING 50 20 30 P OR 10. ELECTRONIC DESIGN PRACTICE 50 20 23 P OR FIRST TERM TOTAL = 396/750. ORDN. 1 MARKS: , т80053011 T80053011 ANUPAM BHATTACHARJEE MOUSUMI 01. CONTROL SYSTEMS 100 40 69 P 02. DIGITAL COMMUNICATION 100 40 63 P PP 03. DIGITAL COMMUNICATION 20 33 P PR 50 40 68 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 50 20 27 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 100 40 61 P 06. MICROCONTROLLERS & APPLICATION PP 50 20 28 P 07. MICROCONTROLLERS & APPLICATION 08. DIGITAL SIGNAL PROCESSING 100 40 54 P 09. DIGITAL SIGNAL PROCESSING 50 20 44 P OR 50 20 37 P 10. ELECTRONIC DESIGN PRACTICE OR FIRST TERM TOTAL = 484/750. ORDN. 1 MARKS: T80053012 BAGALE PRASHANT MUTYAPPA MAHADEVI , 71236306J , T80053012 , PICT 01. CONTROL SYSTEMS PP 100 40 52 P PP 100 40 45 P 02. DIGITAL COMMUNICATION 03. DIGITAL COMMUNICATION 50 20 06 F 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 40 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 42 P 06. MICROCONTROLLERS & APPLICATION PP 100 40 62 P 07. MICROCONTROLLERS & APPLICATION PR 50 20 36 P 100 40 51 P 08. DIGITAL SIGNAL PROCESSING 09. DIGITAL SIGNAL PROCESSING 50 20 38 P OR 10. ELECTRONIC DESIGN PRACTICE OR 50 20 26 P FIRST TERM TOTAL = 398/750. ORDN. 1 MARKS:

DATE: 31 JAN. 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 05 (374) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71100737D , , PICT , T80053013 T80053013 BAGALE PRITESH NAMDEV JIJABAI 01. CONTROL SYSTEMS 100 40 61 P PP 02. DIGITAL COMMUNICATION 100 40 AA F 03. DIGITAL COMMUNICATION 50 20 32 P PR 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 59 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 43 P 100 40 29 F 06. MICROCONTROLLERS & APPLICATION PP 07. MICROCONTROLLERS & APPLICATION PR 50 20 32 P 08. DIGITAL SIGNAL PROCESSING 100 40 53 P 50 20 41 P 09. DIGITAL SIGNAL PROCESSING OR 10. ELECTRONIC DESIGN PRACTICE 50 20 21 P OR FIRST TERM TOTAL = 371/750. ORDN. 1 MARKS: T80053014 BAGUL KAUSTUBH HITENDRA JAYSHRI , 71100738B , , PICT , T80053014 01. CONTROL SYSTEMS 100 40 60 P 02. DIGITAL COMMUNICATION 100 40 51 P 03. DIGITAL COMMUNICATION 20 42 P PR 50 40 63 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 50 20 42 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 100 40 58 P 06. MICROCONTROLLERS & APPLICATION PP 50 20 29 P 07. MICROCONTROLLERS & APPLICATION PR 08. DIGITAL SIGNAL PROCESSING 100 40 44 P 50 20 36 P 09. DIGITAL SIGNAL PROCESSING OR 10. ELECTRONIC DESIGN PRACTICE OR 50 20 34 P FIRST TERM TOTAL = 459/750. ORDN. 1 MARKS: T80053015 BHOSALE ABHISHEK BHAUSAHEB MIRA , 71100750M , т80053015 , PICT 01. CONTROL SYSTEMS 100 40 66 P 100 40 40 P 02. DIGITAL COMMUNICATION PP 03. DIGITAL COMMUNICATION 50 20 37 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 53 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 36 P 06. MICROCONTROLLERS & APPLICATION PP 100 40 54 P 07. MICROCONTROLLERS & APPLICATION PR 50 20 38 P 100 40 40 P 08. DIGITAL SIGNAL PROCESSING 09. DIGITAL SIGNAL PROCESSING 50 20 24 P OR 10. ELECTRONIC DESIGN PRACTICE OR 50 20 37 P FIRST TERM TOTAL = 425/750. ORDN. 1 MARKS:

DATE: 31 JAN. 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 06 (375) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71100752H , , PICT , T80053016 T80053016 BHUTADA GOPAL MADHUSUDAN BASANTI 01. CONTROL SYSTEMS 100 40 67 P PP 02. DIGITAL COMMUNICATION 100 40 61 P 03. DIGITAL COMMUNICATION 50 20 39 P PR 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 72 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 44 P 100 40 58 P 06. MICROCONTROLLERS & APPLICATION PP 50 20 42 P 07. MICROCONTROLLERS & APPLICATION PR 08. DIGITAL SIGNAL PROCESSING 100 40 60 P 50 20 36 P 09. DIGITAL SIGNAL PROCESSING OR 50 20 38 P 10. ELECTRONIC DESIGN PRACTICE OR FIRST TERM TOTAL = 517/750. ORDN. 1 MARKS: T80053017 BUDHWANT UMA SHYAMRAO NANDINI , т80053017 01. CONTROL SYSTEMS 100 40 56 P 02. DIGITAL COMMUNICATION 100 40 45 P 03. DIGITAL COMMUNICATION 20 32 P PR 50 40 44 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 50 20 40 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 100 40 55 P 06. MICROCONTROLLERS & APPLICATION PP 50 20 32 P 07. MICROCONTROLLERS & APPLICATION PR 08. DIGITAL SIGNAL PROCESSING 100 40 47 P 50 20 28 P 09. DIGITAL SIGNAL PROCESSING OR 10. ELECTRONIC DESIGN PRACTICE OR 50 20 32 P FIRST TERM TOTAL = 411/750. ORDN. 1 MARKS: T80053018 BUTALA KARAN SANDESH NANDINI , 71100759E , T80053018 , PICT 01. CONTROL SYSTEMS 100 40 65 P 100 40 63 P 02. DIGITAL COMMUNICATION PP 03. DIGITAL COMMUNICATION 50 20 36 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 57 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 36 P 06. MICROCONTROLLERS & APPLICATION PP 100 40 60 P 07. MICROCONTROLLERS & APPLICATION PR 50 20 38 P 100 40 54 P 08. DIGITAL SIGNAL PROCESSING 09. DIGITAL SIGNAL PROCESSING 50 20 34 P OR 10. ELECTRONIC DESIGN PRACTICE OR 50 20 39 P FIRST TERM TOTAL = 482/750. ORDN. 1 MARKS:

DATE: 31 JAN. 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 07 (376) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71236307G , , PICT , T80053019 T80053019 CHANDAK RASIKA RAMGOPAL ALAKNANDA 01. CONTROL SYSTEMS 100 40 46 P PP 02. DIGITAL COMMUNICATION 100 40 63 P 03. DIGITAL COMMUNICATION 50 20 36 P PR 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 58 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 45 P 06. MICROCONTROLLERS & APPLICATION PP 100 40 62 P 50 20 30 P 07. MICROCONTROLLERS & APPLICATION PR 08. DIGITAL SIGNAL PROCESSING 100 40 70 P 09. DIGITAL SIGNAL PROCESSING 50 20 35 P OR 50 20 23 P 10. ELECTRONIC DESIGN PRACTICE OR FIRST TERM TOTAL = 468/750. ORDN. 1 MARKS: T80053020 CHANDAN PANCHLORIA SUMITRADEVI , т80053020 01. CONTROL SYSTEMS 100 40 45 P 02. DIGITAL COMMUNICATION 100 40 43 P PP 03. DIGITAL COMMUNICATION 20 32 P PR 50 40 58 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 50 20 40 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 100 40 40 P 06. MICROCONTROLLERS & APPLICATION PP 50 20 33 P 07. MICROCONTROLLERS & APPLICATION PR 08. DIGITAL SIGNAL PROCESSING 100 40 44 P 09. DIGITAL SIGNAL PROCESSING 50 20 21 P OR 10. ELECTRONIC DESIGN PRACTICE OR 50 20 28 P FIRST TERM TOTAL = 384/750. ORDN. 1 MARKS: T80053021 CHAVARE PRANAV NITIN NITISHA , 71100765к , т80053021 , PICT 01. CONTROL SYSTEMS 100 40 61 P 100 40 58 P 02. DIGITAL COMMUNICATION PP 03. DIGITAL COMMUNICATION 50 20 42 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 60 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 45 P 06. MICROCONTROLLERS & APPLICATION PP 100 40 54 P 07. MICROCONTROLLERS & APPLICATION PR 50 20 41 P 100 40 66 P 08. DIGITAL SIGNAL PROCESSING 09. DIGITAL SIGNAL PROCESSING 50 20 45 P OR 10. ELECTRONIC DESIGN PRACTICE OR 50 20 35 P FIRST TERM TOTAL = 507/750. ORDN. 1 MARKS:

DATE: 31 JAN. 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 08 (377) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER T80053022 CHAVHAN YOGESH LAXMAN , 71100766н , , , рІСТ , Т80053022 ASHA 01. CONTROL SYSTEMS 100 40 81 P 02. DIGITAL COMMUNICATION 100 40 65 P 03. DIGITAL COMMUNICATION 50 20 36 P PR 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 75 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 41 P 06. MICROCONTROLLERS & APPLICATION PP 100 40 65 P 07. MICROCONTROLLERS & APPLICATION PR 50 20 35 P 08. DIGITAL SIGNAL PROCESSING 100 40 56 P 09. DIGITAL SIGNAL PROCESSING 50 20 42 P OR 10. ELECTRONIC DESIGN PRACTICE 50 20 27 P OR FIRST TERM TOTAL = 523/750. ORDN. 1 MARKS: T80053023 CHHATRE JAI VILAS HEMA , 71100767F , , PICT , T80053023 01. CONTROL SYSTEMS 100 40 65 P 02. DIGITAL COMMUNICATION 100 40 40 P 03. DIGITAL COMMUNICATION 20 43 P PR 50 100 40 66 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 50 20 44 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 40 54 P 06. MICROCONTROLLERS & APPLICATION PP 100 50 20 42 P 07. MICROCONTROLLERS & APPLICATION PR 08. DIGITAL SIGNAL PROCESSING 100 40 49 P 09. DIGITAL SIGNAL PROCESSING 50 20 43 P OR 10. ELECTRONIC DESIGN PRACTICE OR 50 20 35 P FIRST TERM TOTAL = 481/750. ORDN. 1 MARKS: T80053024 CHIDRAWAR DNYANESHWARI GIRISH , 71100768D , т80053024 GEETA , PICT 100 40 77 P 01. CONTROL SYSTEMS 100 40 60 P 02. DIGITAL COMMUNICATION PΡ 03. DIGITAL COMMUNICATION 50 20 38 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 70 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 36 P 06. MICROCONTROLLERS & APPLICATION PP 100 40 66 P 07. MICROCONTROLLERS & APPLICATION PR 50 20 38 P 100 40 55 P 08. DIGITAL SIGNAL PROCESSING 09. DIGITAL SIGNAL PROCESSING 50 20 43 P OR 10. ELECTRONIC DESIGN PRACTICE OR 50 20 24 P FIRST TERM TOTAL = 507/750. ORDN. 1 MARKS:

DATE: 31 JAN. 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 09 (378) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71100769B , , PICT , T80053025 T80053025 CHITALE JANHAVI GOVIND VARSHA 01. CONTROL SYSTEMS 100 40 56 P PP 02. DIGITAL COMMUNICATION 100 40 48 P 03. DIGITAL COMMUNICATION 50 20 41 P PR 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 55 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 42 P 100 40 57 P 06. MICROCONTROLLERS & APPLICATION PP 07. MICROCONTROLLERS & APPLICATION PR 50 20 38 P 08. DIGITAL SIGNAL PROCESSING 100 40 47 P 50 20 42 P 09. DIGITAL SIGNAL PROCESSING OR 50 20 25 P 10. ELECTRONIC DESIGN PRACTICE OR FIRST TERM TOTAL = 451/750. ORDN. 1 MARKS: T80053026 DESHMUKH AMRUTA VILAS ANJALI , 71100779K , , PICT , т80053026 01. CONTROL SYSTEMS 100 40 71 P 02. DIGITAL COMMUNICATION 100 40 55 P 03. DIGITAL COMMUNICATION 20 44 P PR 50 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 71 P 50 20 41 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 100 40 61 P 06. MICROCONTROLLERS & APPLICATION PP 50 20 36 P 07. MICROCONTROLLERS & APPLICATION 08. DIGITAL SIGNAL PROCESSING 100 40 58 P 50 20 42 P 09. DIGITAL SIGNAL PROCESSING OR 50 20 28 P 10. ELECTRONIC DESIGN PRACTICE OR FIRST TERM TOTAL = 507/750. ORDN. 1 MARKS: T80053028 DESHPANDE ADITI VIDHYADHAR , 71100781M , T80053028 ALKA , PICT 01. CONTROL SYSTEMS 100 40 58 P 100 40 42 P 02. DIGITAL COMMUNICATION PP 03. DIGITAL COMMUNICATION 50 20 27 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 46 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 38 P 06. MICROCONTROLLERS & APPLICATION PP 100 40 51 P 07. MICROCONTROLLERS & APPLICATION PR 50 20 30 P 100 40 51 P 08. DIGITAL SIGNAL PROCESSING 09. DIGITAL SIGNAL PROCESSING 50 20 26 P OR 10. ELECTRONIC DESIGN PRACTICE OR 50 20 34 P FIRST TERM TOTAL = 403/750. ORDN. 1 MARKS:

DATE: 31 JAN. 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 10 (379) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER T80053029 DHANDAR RUCHITA PANDURANG , 71236308E , , PICT , T80053029 VIJAYA 01. CONTROL SYSTEMS 100 40 64 P PP 02. DIGITAL COMMUNICATION 100 40 65 P 03. DIGITAL COMMUNICATION 50 20 38 P PR 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 62 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 46 P 06. MICROCONTROLLERS & APPLICATION PP 100 40 70 P 50 20 34 P 07. MICROCONTROLLERS & APPLICATION PR 08. DIGITAL SIGNAL PROCESSING 100 40 57 P 09. DIGITAL SIGNAL PROCESSING 50 20 31 P OR 10. ELECTRONIC DESIGN PRACTICE 50 20 31 P OR FIRST TERM TOTAL = 498/750. ORDN. 1 MARKS: T80053030 DHANE AKSHAY MUKUNDRAO SHARADA , 71236309C , , PICT , т80053030 01. CONTROL SYSTEMS 100 40 64 P 02. DIGITAL COMMUNICATION 100 40 60 P 03. DIGITAL COMMUNICATION 20 32 P PR 50 40 66 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 50 20 42 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 100 40 59 P 06. MICROCONTROLLERS & APPLICATION PP 50 20 38 P 07. MICROCONTROLLERS & APPLICATION PR 08. DIGITAL SIGNAL PROCESSING 100 40 62 P 09. DIGITAL SIGNAL PROCESSING 50 20 34 P OR 50 20 28 P 10. ELECTRONIC DESIGN PRACTICE OR FIRST TERM TOTAL = 485/750. ORDN. 1 MARKS: T80053031 DHOLE NILAKSHI AJAY CHHAYA , 71100786в , т80053031 , PICT 100 40 57 P 01. CONTROL SYSTEMS 100 40 48 P 02. DIGITAL COMMUNICATION PP 03. DIGITAL COMMUNICATION 50 20 37 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 54 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 41 P 06. MICROCONTROLLERS & APPLICATION PP 100 40 49 P 07. MICROCONTROLLERS & APPLICATION PR 50 20 25 P 100 40 50 P 08. DIGITAL SIGNAL PROCESSING 09. DIGITAL SIGNAL PROCESSING 50 20 39 P OR 10. ELECTRONIC DESIGN PRACTICE OR 50 20 27 P FIRST TERM TOTAL = 427/750. ORDN. 1 MARKS:

DATE: 31 JAN. 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 11 (380) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71236311E , , PICT , T80053032 T80053032 DONGARE MANOJ JAYSING SHOBHA 01. CONTROL SYSTEMS 100 40 40 P PP 02. DIGITAL COMMUNICATION 100 40 43 P 03. DIGITAL COMMUNICATION 50 20 27 P PR 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 43 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 43 P 100 40 52 P 06. MICROCONTROLLERS & APPLICATION PP 07. MICROCONTROLLERS & APPLICATION PR 50 20 44 P 08. DIGITAL SIGNAL PROCESSING 100 40 41 P 09. DIGITAL SIGNAL PROCESSING 50 20 20 P OR 50 20 31 P 10. ELECTRONIC DESIGN PRACTICE OR FIRST TERM TOTAL = 384/750. ORDN. 1 MARKS: T80053033 GAIKWAD PRATIKSHA JAGANNATH ANITA , 71100794C , , PICT , T80053033 01. CONTROL SYSTEMS 100 40 56 P 02. DIGITAL COMMUNICATION 100 40 52 P 03. DIGITAL COMMUNICATION 20 42 P PR 50 40 62 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 50 20 42 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 100 40 52 P 06. MICROCONTROLLERS & APPLICATION PP 50 20 42 P 07. MICROCONTROLLERS & APPLICATION PR 08. DIGITAL SIGNAL PROCESSING 100 40 50 P 50 20 39 P 09. DIGITAL SIGNAL PROCESSING OR 50 20 30 P 10. ELECTRONIC DESIGN PRACTICE OR FIRST TERM TOTAL = 467/750. ORDN. 1 MARKS: T80053034 GAVADE SACHIN SHIVAJI **SNEHAL** , 71100798F , T80053034 , PICT 01. CONTROL SYSTEMS 100 40 49 P 100 40 40 P 02. DIGITAL COMMUNICATION PP 03. DIGITAL COMMUNICATION 50 20 30 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 50 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 39 P 06. MICROCONTROLLERS & APPLICATION PP 100 40 48 P 07. MICROCONTROLLERS & APPLICATION PR 50 20 27 P 100 40 32 F 08. DIGITAL SIGNAL PROCESSING 09. DIGITAL SIGNAL PROCESSING 50 20 34 P OR 10. ELECTRONIC DESIGN PRACTICE OR 50 20 27 P FIRST TERM TOTAL = 376/750. ORDN. 1 MARKS:

UNIVERSITY O	F PUNE	T.E	.(200	8 PA	T.)(ELECTRO	NICS & T	ELEC.) EXAMINA	TION NOV. 20)12	
DATE : 31 JAN. 2013	CENT	TRE : I	PUNE	INST	ITUTE OF CO	MPUTER TI	ECHNOLOGY, PUN	IE.	PAGE NO.	12 (381)
NOTE: FIRST LINE : SEAT NO., NAME (
OTHER LINES: HEAD OF PASSING,										
T80053035 GHARDE SAGAR BHIMRAO					HARDA		, 71236313M			
01. CONTROL SYSTEMS	PP	100	40		P		, 7123031314	,	, 1101	, 100033033
02. DIGITAL COMMUNICATION		100	40		Р					
03. DIGITAL COMMUNICATION	PR	50	20		Р					
04. NETWORK SYNTHESIS & FILTER DESIG	GNPP	100	40	41	Р					
05. NETWORK SYNTHESIS & FILTER DESIG	GNTW	50	20	39	Р					
06. MICROCONTROLLERS & APPLICATION	PP	100	40	57	Р					
07. MICROCONTROLLERS & APPLICATION	PR	50	20	35	Р					
08. DIGITAL SIGNAL PROCESSING	PP	100	40	40	Р					
09. DIGITAL SIGNAL PROCESSING	OR	50	20	22	Р					
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	29	Р					
FIRST TERM TOTAL = 379/750.										
ORDN. 1 MARKS :										
T80053036 GIRI POOJA RAVINDRA				L	ATA		, 71100805в	,	, PICT	, т80053036
01. CONTROL SYSTEMS	PP	100	40		Р					
02. DIGITAL COMMUNICATION	PP	100	40		Р					
03. DIGITAL COMMUNICATION	PR	50	20		Р					
04. NETWORK SYNTHESIS & FILTER DESIG		100	40	67						
05. NETWORK SYNTHESIS & FILTER DESIG	GNTW	50	20	46						
06. MICROCONTROLLERS & APPLICATION	PP	100	40	65						
07. MICROCONTROLLERS & APPLICATION	PR	50	20	38						
08. DIGITAL SIGNAL PROCESSING	PP	100	40	58						
09. DIGITAL SIGNAL PROCESSING	OR	50	20	40						
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	30	Р					
FIRST TERM TOTAL = 500/750.										
ORDN. 1 MARKS :										
T80053037 GOKHALE SIDDHARTH ADESH		• •			 UJATA		, 71100808G	,	, PICT	, т80053037
01. CONTROL SYSTEMS	PP	100	40		P		,	,	, , , , , ,	, 100055057
02. DIGITAL COMMUNICATION	PP	100	40	50						
03. DIGITAL COMMUNICATION	PR	50	20	43						
04. NETWORK SYNTHESIS & FILTER DESIGNATION		100	40	66						
05. NETWORK SYNTHESIS & FILTER DESIGNATION		50	20	46						
06. MICROCONTROLLERS & APPLICATION	PP	100	40	66						
07. MICROCONTROLLERS & APPLICATION	PR	50	20	44						
08. DIGITAL SIGNAL PROCESSING	PP	100	40	56						
09. DIGITAL SIGNAL PROCESSING	OR	50	20	45	Р					
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	39						
FIRST TERM TOTAL = 524/750.										
ORDN. 1 MARKS :										

DATE: 31 JAN. 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 13 (382) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER T80053038 HARSHIKA THUSU , 71100817F , , PICT , T80053038 ANITA 01. CONTROL SYSTEMS 100 40 74 P 02. DIGITAL COMMUNICATION 100 40 51 P 03. DIGITAL COMMUNICATION 50 20 37 P PR 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 55 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 46 P 06. MICROCONTROLLERS & APPLICATION PP 100 40 69 P 50 20 42 P 07. MICROCONTROLLERS & APPLICATION PR 08. DIGITAL SIGNAL PROCESSING 100 40 69 P 09. DIGITAL SIGNAL PROCESSING 50 20 48 P OR 50 20 38 P 10. ELECTRONIC DESIGN PRACTICE OR FIRST TERM TOTAL = 529/750. ORDN. 1 MARKS: T80053039 HIMANSHU JAIN RACHANA , 71100821D , , PICT , т80053039 01. CONTROL SYSTEMS 100 40 53 P 02. DIGITAL COMMUNICATION 100 40 44 P 03. DIGITAL COMMUNICATION 20 04 F PR 50 100 40 42 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 50 20 25 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 100 40 28 F 06. MICROCONTROLLERS & APPLICATION PP 50 20 10 F 07. MICROCONTROLLERS & APPLICATION PR 08. DIGITAL SIGNAL PROCESSING 100 40 40 P 09. DIGITAL SIGNAL PROCESSING 50 20 11 F OR 10. ELECTRONIC DESIGN PRACTICE OR 50 20 15 F FIRST TERM TOTAL = 272/750. ORDN. 1 MARKS: T80053040 ISHTE PRAKASH BHAGVAN KAMAL , 70925442D , т80053040 , PICT 100 40 40 P 01. CONTROL SYSTEMS 100 40 40 P 02. DIGITAL COMMUNICATION PP 03. DIGITAL COMMUNICATION 50 20 20 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 52 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 33 P 06. MICROCONTROLLERS & APPLICATION PP 100 40 40 P 07. MICROCONTROLLERS & APPLICATION PR 50 20 11 F 100 40 40 P 08. DIGITAL SIGNAL PROCESSING 09. DIGITAL SIGNAL PROCESSING 50 20 10 F OR 10. ELECTRONIC DESIGN PRACTICE OR 50 20 22 P FIRST TERM TOTAL = 308/750. ORDN. 1 MARKS:

UNIVERSITY O	F PUNE	E,T.E	.(200	8 PA	T.)(ELECTRO	NICS & T	ELEC.) EXAMINAT	TION NOV. 20	12	
DATE : 31 JAN. 2013	CENT	ΓRE :	PUNE	INST	TITUTE OF CO	MPUTER T	ECHNOLOGY, PUNE	Ξ.	PAGE NO.	14 (383)
NOTE: FIRST LINE : SEAT NO., NAME										
OTHER LINES: HEAD OF PASSING,										
T80053041 JAGTAP MANJIRI ARVIND					HUBHANGI		, 71100825G		, PICT	, т80053041
01. CONTROL SYSTEMS	PP	100	40		P		, /11000230	,	, 120.	, 100033012
02. DIGITAL COMMUNICATION		100	40	AA	. F					
03. DIGITAL COMMUNICATION	PR	50	20		. Р					
04. NETWORK SYNTHESIS & FILTER DESI	GNPP	100	40		' F					
05. NETWORK SYNTHESIS & FILTER DESI	GNTW	50	20	36	Р					
06. MICROCONTROLLERS & APPLICATION	PP	100	40	27	F					
07. MICROCONTROLLERS & APPLICATION	PR	50	20	09) F					
08. DIGITAL SIGNAL PROCESSING	PP	100	40	43	Р					
09. DIGITAL SIGNAL PROCESSING	OR	50	20	12	F					
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	22	P					
FIRST TERM TOTAL = $240/750$.										
ORDN. 1 MARKS :										
T80053042 JAIN SAJAL ABHAYKUMAR				S	JUNITA		, 71100827C	,	, PICT	, т80053042
01. CONTROL SYSTEMS	PP	100	40	45	Р					
02. DIGITAL COMMUNICATION	PP	100	40	42	Р					
03. DIGITAL COMMUNICATION	PR	50	20	13	F					
04. NETWORK SYNTHESIS & FILTER DESI	GNPP	100	40	40) P					
05. NETWORK SYNTHESIS & FILTER DESI	GNTW	50	20	36	Б Р					
06. MICROCONTROLLERS & APPLICATION	PP	100	40	55	Р					
07. MICROCONTROLLERS & APPLICATION	PR	50	20	22	P					
08. DIGITAL SIGNAL PROCESSING	PP	100	40	40) P					
09. DIGITAL SIGNAL PROCESSING	OR	50	20	30) P					
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	26	Р					
FIRST TERM TOTAL = $349/750$.										
ORDN. 1 MARKS :										
T80053043 JEDGULE UMASHANKAR RADH			40		NITA		, 71100831M	,	, PICT	, т80053043
01. CONTROL SYSTEMS	PP	100	40		P					
02. DIGITAL COMMUNICATION	PP	100	40	48						
03. DIGITAL COMMUNICATION	PR	50 100	20		. P					
04. NETWORK SYNTHESIS & FILTER DESI		100	40	67						
05. NETWORK SYNTHESIS & FILTER DESI		50 100	20	39						
06. MICROCONTROLLERS & APPLICATION	PP	100	40 20		P					
07. MICROCONTROLLERS & APPLICATION	PR	50 100	20 40	40 45						
08. DIGITAL SIGNAL PROCESSING	PP OP	100	40 20	45						
09. DIGITAL SIGNAL PROCESSING	OR OR	50 50	20	35 24						
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	24	. Р					
FIRST TERM TOTAL = 449/750. ORDN. 1 MARKS :										
UNDIN. I WARKS .										

DATE: 31 JAN. 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 15 (384) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71100835D , , PICT , T80053044 T80053044 JOSHI APOORV VIJAY ANAGHA 01. CONTROL SYSTEMS 100 40 74 P 02. DIGITAL COMMUNICATION 100 40 45 P 03. DIGITAL COMMUNICATION 50 20 22 P PR 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 52 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 45 P 06. MICROCONTROLLERS & APPLICATION PP 100 40 59 P 07. MICROCONTROLLERS & APPLICATION PR 50 20 38 P 08. DIGITAL SIGNAL PROCESSING 100 40 43 P 50 20 39 P 09. DIGITAL SIGNAL PROCESSING OR 50 20 25 P 10. ELECTRONIC DESIGN PRACTICE OR FIRST TERM TOTAL = 442/750. ORDN. 1 MARKS: T80053045 JOSHI CHAITRALI SANJAY VARSHA , 71100836B , , PICT , T80053045 01. CONTROL SYSTEMS 100 40 75 P 02. DIGITAL COMMUNICATION 100 40 50 P 03. DIGITAL COMMUNICATION 20 37 P PR 50 40 68 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 50 20 46 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 100 40 57 P 06. MICROCONTROLLERS & APPLICATION PP 50 20 45 P 07. MICROCONTROLLERS & APPLICATION PR 08. DIGITAL SIGNAL PROCESSING 100 40 69 P 09. DIGITAL SIGNAL PROCESSING 50 20 46 P OR 10. ELECTRONIC DESIGN PRACTICE OR 50 20 36 P FIRST TERM TOTAL = 529/750. ORDN. 1 MARKS: T80053046 KADAM SHRIKANT DEVIDASRAO SANJIVANI , 71100840L , т80053046 , PICT 01. CONTROL SYSTEMS PP 100 40 52 P 100 40 43 P 02. DIGITAL COMMUNICATION PP 03. DIGITAL COMMUNICATION 50 20 09 F 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 53 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 36 P 06. MICROCONTROLLERS & APPLICATION PP 100 40 51 P 07. MICROCONTROLLERS & APPLICATION PR 50 20 35 P 100 40 48 P 08. DIGITAL SIGNAL PROCESSING 09. DIGITAL SIGNAL PROCESSING 50 20 36 P OR 10. ELECTRONIC DESIGN PRACTICE OR 50 20 35 P FIRST TERM TOTAL = 398/750. ORDN. 1 MARKS:

DATE: 31 JAN. 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 16 (385) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER NANDA , 71100844C , , PICT , T80053047 T80053047 KAMBLE AMRUTA GORAKH 01. CONTROL SYSTEMS PP 100 40 74 P 02. DIGITAL COMMUNICATION 100 40 55 P 03. DIGITAL COMMUNICATION 50 20 38 P PR 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 71 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 47 P 100 40 55 P 06. MICROCONTROLLERS & APPLICATION PP 07. MICROCONTROLLERS & APPLICATION PR 50 20 40 P 08. DIGITAL SIGNAL PROCESSING 100 40 46 P 50 20 41 P 09. DIGITAL SIGNAL PROCESSING OR 50 20 26 P 10. ELECTRONIC DESIGN PRACTICE OR FIRST TERM TOTAL = 493/750. ORDN. 1 MARKS: T80053048 KAMBLE DASHARATH SATAPPA KAMAL , 71236314K , , PICT , т80053048 01. CONTROL SYSTEMS 100 40 50 P 02. DIGITAL COMMUNICATION 100 40 40 P 03. DIGITAL COMMUNICATION 20 28 P PR 50 40 31 F 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 50 20 42 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 100 40 62 P 06. MICROCONTROLLERS & APPLICATION PP 50 20 25 P 07. MICROCONTROLLERS & APPLICATION 08. DIGITAL SIGNAL PROCESSING 100 40 45 P 50 20 25 P 09. DIGITAL SIGNAL PROCESSING OR 50 20 25 P 10. ELECTRONIC DESIGN PRACTICE OR FIRST TERM TOTAL = 373/750. ORDN. 1 MARKS: T80053049 KAMBLE PRATIK RAVINDRA , 71100845M , т80053049 SUMAN , PICT 01. CONTROL SYSTEMS 100 40 54 P 100 40 45 P 02. DIGITAL COMMUNICATION PP 03. DIGITAL COMMUNICATION 50 20 30 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 58 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 26 P 06. MICROCONTROLLERS & APPLICATION PP 100 40 48 P 07. MICROCONTROLLERS & APPLICATION PR 50 20 27 P 100 40 44 P 08. DIGITAL SIGNAL PROCESSING 09. DIGITAL SIGNAL PROCESSING 50 20 17 F OR 10. ELECTRONIC DESIGN PRACTICE OR 50 20 22 P FIRST TERM TOTAL = 371/750. ORDN. 1 MARKS:

DATE: 31 JAN. 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 17 (386) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71236315H , , PICT , T80053050 T80053050 KAMTHE SNEHAL SUNIL ALKA PP 100 40 67 P 01. CONTROL SYSTEMS 02. DIGITAL COMMUNICATION 100 40 57 P 03. DIGITAL COMMUNICATION 50 20 33 P PR 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 58 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 47 P 06. MICROCONTROLLERS & APPLICATION PP 100 40 75 P 50 20 45 P 07. MICROCONTROLLERS & APPLICATION PR 08. DIGITAL SIGNAL PROCESSING 100 40 69 P 09. DIGITAL SIGNAL PROCESSING 50 20 25 P OR 50 20 23 P 10. ELECTRONIC DESIGN PRACTICE OR FIRST TERM TOTAL = 499/750. ORDN. 1 MARKS: , т80053051 T80053051 KANSARA KRUTIKA SANJAY JIGISHA 01. CONTROL SYSTEMS 100 40 64 P 02. DIGITAL COMMUNICATION 100 40 52 P 03. DIGITAL COMMUNICATION 20 45 P PR 50 100 40 56 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 50 20 48 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 100 40 71 P 06. MICROCONTROLLERS & APPLICATION PP 50 20 45 P 07. MICROCONTROLLERS & APPLICATION PR 08. DIGITAL SIGNAL PROCESSING 100 40 48 P 09. DIGITAL SIGNAL PROCESSING 50 20 44 P OR 10. ELECTRONIC DESIGN PRACTICE OR 50 20 29 P FIRST TERM TOTAL = 502/750. ORDN. 1 MARKS: T80053052 KANUNGO KAPIL RAMESH KRISHNA , 71100849D , T80053052 , PICT 01. CONTROL SYSTEMS 100 40 68 P 100 40 61 P 02. DIGITAL COMMUNICATION PP 03. DIGITAL COMMUNICATION 50 20 30 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 47 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 34 P 06. MICROCONTROLLERS & APPLICATION PP 100 40 60 P 07. MICROCONTROLLERS & APPLICATION PR 50 20 30 P 100 40 57 P 08. DIGITAL SIGNAL PROCESSING 09. DIGITAL SIGNAL PROCESSING 50 20 40 P OR 10. ELECTRONIC DESIGN PRACTICE OR 50 20 33 P FIRST TERM TOTAL = 460/750. ORDN. 1 MARKS:

DATE: 31 JAN. 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 18 (387) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER KALPANA , 71236316F , , PICT , T80053053 T80053053 KASHID SONALI BHARAT 01. CONTROL SYSTEMS PP 100 40 68 P 02. DIGITAL COMMUNICATION 100 40 49 P 03. DIGITAL COMMUNICATION 50 20 36 P PR 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 53 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 42 P 100 40 66 P 06. MICROCONTROLLERS & APPLICATION PP 07. MICROCONTROLLERS & APPLICATION PR 50 20 35 P 08. DIGITAL SIGNAL PROCESSING 100 40 53 P 50 20 29 P 09. DIGITAL SIGNAL PROCESSING OR 50 20 25 P 10. ELECTRONIC DESIGN PRACTICE OR FIRST TERM TOTAL = 456/750. ORDN. 1 MARKS: T80053054 KESARKAR ONKAR PANDURANG ANITA , 71236317D , , PICT , т80053054 01. CONTROL SYSTEMS 100 40 63 P 02. DIGITAL COMMUNICATION 100 40 52 P 03. DIGITAL COMMUNICATION 20 38 P PR 50 40 52 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 50 20 43 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 100 40 64 P 06. MICROCONTROLLERS & APPLICATION PP 50 20 43 P 07. MICROCONTROLLERS & APPLICATION 08. DIGITAL SIGNAL PROCESSING 100 40 56 P 50 20 40 P 09. DIGITAL SIGNAL PROCESSING OR 50 20 34 P 10. ELECTRONIC DESIGN PRACTICE OR FIRST TERM TOTAL = 485/750. ORDN. 1 MARKS: T80053055 KHADATKAR NIRAJ MANOHAR , 71100855J , т80053055 TARA , PICT 01. CONTROL SYSTEMS 100 40 68 P 100 40 52 P 02. DIGITAL COMMUNICATION PP 03. DIGITAL COMMUNICATION 50 20 33 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 57 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 40 P 06. MICROCONTROLLERS & APPLICATION PP 100 40 64 P 07. MICROCONTROLLERS & APPLICATION PR 50 20 32 P 100 40 60 P 08. DIGITAL SIGNAL PROCESSING 09. DIGITAL SIGNAL PROCESSING 50 20 33 P OR 10. ELECTRONIC DESIGN PRACTICE OR 50 20 23 P FIRST TERM TOTAL = 462/750. ORDN. 1 MARKS:

DATE: 31 JAN. 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 19 (388) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71100859M , , PICT , T80053056 T80053056 KHAMBE DIGVIJAY RAJARAM MANJUSHA 01. CONTROL SYSTEMS 100 40 45 P PP 02. DIGITAL COMMUNICATION 100 40 53 P 03. DIGITAL COMMUNICATION 50 20 38 P PR 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 59 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 36 P 06. MICROCONTROLLERS & APPLICATION PP 100 40 70 P 50 20 32 P 07. MICROCONTROLLERS & APPLICATION PR 08. DIGITAL SIGNAL PROCESSING 100 40 53 P 50 20 45 P 09. DIGITAL SIGNAL PROCESSING OR 10. ELECTRONIC DESIGN PRACTICE 50 20 27 P OR FIRST TERM TOTAL = 458/750. ORDN. 1 MARKS: T80053057 KOCHAR ANKITA VINOD SUREKHA , 71100865F , , PICT , т80053057 01. CONTROL SYSTEMS 100 40 50 P 02. DIGITAL COMMUNICATION 100 40 48 P PP 03. DIGITAL COMMUNICATION 20 36 P PR 50 40 50 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 50 20 37 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 100 40 53 P 06. MICROCONTROLLERS & APPLICATION PP 50 20 25 P 07. MICROCONTROLLERS & APPLICATION PR 08. DIGITAL SIGNAL PROCESSING 100 40 44 P 50 20 17 F 09. DIGITAL SIGNAL PROCESSING OR 10. ELECTRONIC DESIGN PRACTICE OR 50 20 21 P FIRST TERM TOTAL = 381/750. ORDN. 1 MARKS: T80053058 KRISHNAN SANTOSH KUMAR RADHIKA , 71100867в , T80053058 , PICT 01. CONTROL SYSTEMS 100 40 63 P 100 40 52 P 02. DIGITAL COMMUNICATION PP 03. DIGITAL COMMUNICATION 50 20 41 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 55 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 43 P 06. MICROCONTROLLERS & APPLICATION PP 100 40 59 P 07. MICROCONTROLLERS & APPLICATION PR 50 20 35 P 100 40 52 P 08. DIGITAL SIGNAL PROCESSING 09. DIGITAL SIGNAL PROCESSING 50 20 48 P OR 10. ELECTRONIC DESIGN PRACTICE OR 50 20 28 P FIRST TERM TOTAL = 476/750. ORDN. 1 MARKS:

DATE: 31 JAN. 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 20 (389) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71100871L , , PICT , T80053059 T80053059 KULKARNI KRANTI MADHAV MADHURI 01. CONTROL SYSTEMS 100 40 55 P PP 02. DIGITAL COMMUNICATION 100 40 47 P 03. DIGITAL COMMUNICATION 50 20 40 P PR 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 53 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 44 P 06. MICROCONTROLLERS & APPLICATION PP 100 40 65 P 50 20 35 P 07. MICROCONTROLLERS & APPLICATION PR 08. DIGITAL SIGNAL PROCESSING 100 40 60 P 50 20 39 P 09. DIGITAL SIGNAL PROCESSING OR 50 20 27 P 10. ELECTRONIC DESIGN PRACTICE OR FIRST TERM TOTAL = 465/750. ORDN. 1 MARKS: T80053060 KUNAL AGARWAL POONAM DEVI , 71100875C , , PICT , т80053060 01. CONTROL SYSTEMS 100 40 56 P 02. DIGITAL COMMUNICATION 100 40 54 P 03. DIGITAL COMMUNICATION 20 42 P PR 50 100 40 45 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 50 20 38 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 100 40 54 P 06. MICROCONTROLLERS & APPLICATION PP 50 20 40 P 07. MICROCONTROLLERS & APPLICATION PR 08. DIGITAL SIGNAL PROCESSING 100 40 51 P 09. DIGITAL SIGNAL PROCESSING 50 20 32 P OR 50 20 35 P 10. ELECTRONIC DESIGN PRACTICE OR FIRST TERM TOTAL = 447/750. ORDN. 1 MARKS: T80053061 KURHADE RUSHIKESH SAMBHAJI SANGITA , 71100876M , т80053061 , PICT 01. CONTROL SYSTEMS 100 40 68 P 100 40 53 P 02. DIGITAL COMMUNICATION PP 03. DIGITAL COMMUNICATION 50 20 40 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 49 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 47 P 06. MICROCONTROLLERS & APPLICATION PP 100 40 65 P 07. MICROCONTROLLERS & APPLICATION PR 50 20 46 P 100 40 56 P 08. DIGITAL SIGNAL PROCESSING 09. DIGITAL SIGNAL PROCESSING 50 20 42 P OR 10. ELECTRONIC DESIGN PRACTICE OR 50 20 34 P FIRST TERM TOTAL = 500/750. ORDN. 1 MARKS:

DATE: 31 JAN. 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 21 (390) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71100877к , , , , рІСТ , Т80053062 T80053062 KUSH VARMA PARVEEN 01. CONTROL SYSTEMS 100 40 70 P 02. DIGITAL COMMUNICATION 100 40 50 P 03. DIGITAL COMMUNICATION 50 20 30 P PR 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 57 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 44 P 06. MICROCONTROLLERS & APPLICATION PP 100 40 69 P 50 20 45 P 07. MICROCONTROLLERS & APPLICATION PR 08. DIGITAL SIGNAL PROCESSING 100 40 51 P 50 20 47 P 09. DIGITAL SIGNAL PROCESSING OR 10. ELECTRONIC DESIGN PRACTICE 50 20 28 P OR FIRST TERM TOTAL = 491/750. ORDN. 1 MARKS: T80053063 M SUBRAMANIAN M KOHILA , 71100885L , , PICT , т80053063 01. CONTROL SYSTEMS 100 40 40 P 02. DIGITAL COMMUNICATION 100 40 28 F 03. DIGITAL COMMUNICATION 20 36 P PR 50 100 40 45 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 50 20 33 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 100 40 51 P 06. MICROCONTROLLERS & APPLICATION PP 50 20 08 F 07. MICROCONTROLLERS & APPLICATION PR 08. DIGITAL SIGNAL PROCESSING 100 40 55 P 09. DIGITAL SIGNAL PROCESSING 50 20 33 P OR 50 20 35 P 10. ELECTRONIC DESIGN PRACTICE OR FIRST TERM TOTAL = 364/750. ORDN. 1 MARKS: T80053064 MAHAJAN PRATEEK PARAG PREETI , 71100888E , т80053064 , PICT 01. CONTROL SYSTEMS PP 100 40 51 P 100 40 50 P 02. DIGITAL COMMUNICATION PP 03. DIGITAL COMMUNICATION 50 20 42 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 58 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 46 P 06. MICROCONTROLLERS & APPLICATION PP 100 40 62 P 07. MICROCONTROLLERS & APPLICATION PR 50 20 40 P 100 40 63 P 08. DIGITAL SIGNAL PROCESSING 09. DIGITAL SIGNAL PROCESSING 50 20 40 P OR 10. ELECTRONIC DESIGN PRACTICE OR 50 20 39 P FIRST TERM TOTAL = 491/750. ORDN. 1 MARKS:

DATE: 31 JAN. 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 22 (391) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71236318B , , PICT , T80053065 T80053065 MAHANGARE BHAGYASHRI BABASAHEB SUREKHA 01. CONTROL SYSTEMS 40 44 P 100 02. DIGITAL COMMUNICATION 100 40 44 P 03. DIGITAL COMMUNICATION 50 20 34 P PR 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 40 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 41 P 100 40 55 P 06. MICROCONTROLLERS & APPLICATION PP 07. MICROCONTROLLERS & APPLICATION PR 50 20 30 P 08. DIGITAL SIGNAL PROCESSING 100 40 40 P 50 20 36 P 09. DIGITAL SIGNAL PROCESSING OR 50 20 27 P 10. ELECTRONIC DESIGN PRACTICE OR FIRST TERM TOTAL = 391/750. ORDN. 1 MARKS: T80053066 MALI RAKESH SAKHARAM REKHA , 71100889C , , PICT , т80053066 53 P 01. CONTROL SYSTEMS 100 40 02. DIGITAL COMMUNICATION 100 40 54 P 03. DIGITAL COMMUNICATION 20 37 P PR 50 40 51 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 50 20 39 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 100 40 65 P 06. MICROCONTROLLERS & APPLICATION PP 50 20 29 P 07. MICROCONTROLLERS & APPLICATION PR 08. DIGITAL SIGNAL PROCESSING 100 40 60 P 50 20 25 P 09. DIGITAL SIGNAL PROCESSING OR 10. ELECTRONIC DESIGN PRACTICE OR 50 20 38 P FIRST TERM TOTAL = 451/750. ORDN. 1 MARKS: T80053067 METHIKAR PRIYANKA JITENDRA SUSHMA , 71100895н , PICT , т80053067 100 40 75 P 01. CONTROL SYSTEMS 100 40 54 P 02. DIGITAL COMMUNICATION PP 03. DIGITAL COMMUNICATION 50 20 45 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 60 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 43 P 06. MICROCONTROLLERS & APPLICATION PP 100 40 63 P 07. MICROCONTROLLERS & APPLICATION PR 50 20 42 P 100 40 51 P 08. DIGITAL SIGNAL PROCESSING 09. DIGITAL SIGNAL PROCESSING 50 20 35 P OR 10. ELECTRONIC DESIGN PRACTICE OR 50 20 39 P FIRST TERM TOTAL = 507/750. ORDN. 1 MARKS:

DATE: 31 JAN. 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 23 (392) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71236319L , , PICT , T80053068 T80053068 MOHITE SHEETAL CHANDRAKANT SAVITA 01. CONTROL SYSTEMS 100 40 64 P 02. DIGITAL COMMUNICATION 100 40 46 P 03. DIGITAL COMMUNICATION 50 20 43 P PR 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 62 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 40 P 100 40 55 P 06. MICROCONTROLLERS & APPLICATION PP 07. MICROCONTROLLERS & APPLICATION PR 50 20 36 P 08. DIGITAL SIGNAL PROCESSING 100 40 58 P 09. DIGITAL SIGNAL PROCESSING 50 20 43 P OR 50 20 41 P 10. ELECTRONIC DESIGN PRACTICE OR FIRST TERM TOTAL = 488/750. ORDN. 1 MARKS: T80053069 MORE VIKAS CHANDRABHAN SUMAN , 71045527F , , PICT , т80053069 01. CONTROL SYSTEMS 100 40 50 P 02. DIGITAL COMMUNICATION 100 40 32 F 03. DIGITAL COMMUNICATION 20 06 F PR 50 40 40 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 50 20 24 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 100 40 40 P 06. MICROCONTROLLERS & APPLICATION PP 50 20 25 P 07. MICROCONTROLLERS & APPLICATION PR 08. DIGITAL SIGNAL PROCESSING 100 40 40 P 50 20 10 F 09. DIGITAL SIGNAL PROCESSING OR 50 20 28 P 10. ELECTRONIC DESIGN PRACTICE OR FIRST TERM TOTAL = 295/750. ORDN. 1 MARKS: T80053070 MUSALE VIVEK KISANRAO INDUBAI , 71236320D , т80053070 , PICT 01. CONTROL SYSTEMS 100 40 44 P 100 40 40 P 02. DIGITAL COMMUNICATION PP 03. DIGITAL COMMUNICATION 50 20 07 F 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 45 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 38 P 06. MICROCONTROLLERS & APPLICATION PP 100 40 53 P 07. MICROCONTROLLERS & APPLICATION PR 50 20 27 P 100 40 27 F 08. DIGITAL SIGNAL PROCESSING 09. DIGITAL SIGNAL PROCESSING 50 20 30 P OR 10. ELECTRONIC DESIGN PRACTICE OR 50 20 27 P FIRST TERM TOTAL = 338/750. ORDN. 1 MARKS:

DATE: 31 JAN. 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 24 (393) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71236321B , , PICT , T80053071 T80053071 NADEKAR SANJIV CHANDRAKANT ALKA 01. CONTROL SYSTEMS 100 40 60 P 02. DIGITAL COMMUNICATION 100 40 44 P 03. DIGITAL COMMUNICATION 50 20 22 P PR 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 50 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 40 P 100 40 44 P 06. MICROCONTROLLERS & APPLICATION PP 07. MICROCONTROLLERS & APPLICATION PR 50 20 34 P 08. DIGITAL SIGNAL PROCESSING 100 40 55 P 50 20 35 P 09. DIGITAL SIGNAL PROCESSING OR 50 20 27 P 10. ELECTRONIC DESIGN PRACTICE OR FIRST TERM TOTAL = 411/750. ORDN. 1 MARKS: , т80053072 T80053072 NAIK DIGJA ANKUSH VASANTI , 71100906G , , PICT 01. CONTROL SYSTEMS 100 40 68 P 02. DIGITAL COMMUNICATION 100 40 51 P 03. DIGITAL COMMUNICATION 20 42 P PR 50 40 71 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 50 20 47 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 100 40 63 P 06. MICROCONTROLLERS & APPLICATION PP 50 20 35 P 07. MICROCONTROLLERS & APPLICATION PR 08. DIGITAL SIGNAL PROCESSING 100 40 47 P 09. DIGITAL SIGNAL PROCESSING 50 20 36 P OR 50 20 28 P 10. ELECTRONIC DESIGN PRACTICE OR FIRST TERM TOTAL = 488/750. ORDN. 1 MARKS: T80053073 NAKHATE ANIKET BHAGWANRAO , 71100909M , т80053073 ANITA , PICT 01. CONTROL SYSTEMS PP 100 40 43 P 100 40 40 P 02. DIGITAL COMMUNICATION PP 03. DIGITAL COMMUNICATION 50 20 27 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 51 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 44 P 06. MICROCONTROLLERS & APPLICATION PP 100 40 40 P 07. MICROCONTROLLERS & APPLICATION PR 50 20 40 P 100 40 40 P 08. DIGITAL SIGNAL PROCESSING 09. DIGITAL SIGNAL PROCESSING 50 20 40 P OR 10. ELECTRONIC DESIGN PRACTICE OR 50 20 32 P FIRST TERM TOTAL = 397/750. ORDN. 1 MARKS:

DATE: 31 JAN. 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 25 (394) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER T80053074 NARKAR PRAJAKTA SUHAS SUPRIYA , 71100914H , , PICT , T80053074 01. CONTROL SYSTEMS PP 100 40 72 P 02. DIGITAL COMMUNICATION 100 40 50 P 03. DIGITAL COMMUNICATION 50 20 44 P PR 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 65 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 47 P 06. MICROCONTROLLERS & APPLICATION PP 100 40 64 P 50 20 42 P 07. MICROCONTROLLERS & APPLICATION PR 08. DIGITAL SIGNAL PROCESSING 100 40 68 P 09. DIGITAL SIGNAL PROCESSING 50 20 40 P OR 50 20 38 P 10. ELECTRONIC DESIGN PRACTICE OR FIRST TERM TOTAL = 530/750. ORDN. 1 MARKS: , т80053075 T80053075 NERLEKAR SHWETA PRAKASH SUGANDHA , 71045546B , , PICT 01. CONTROL SYSTEMS 100 40 56 P 02. DIGITAL COMMUNICATION 100 40 40 P 03. DIGITAL COMMUNICATION 20 36 P PR 50 100 40 49 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 50 20 40 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 100 40 47 P 06. MICROCONTROLLERS & APPLICATION PP 50 20 39 P 07. MICROCONTROLLERS & APPLICATION PR 08. DIGITAL SIGNAL PROCESSING 100 40 40 P 09. DIGITAL SIGNAL PROCESSING 50 20 41 P OR 10. ELECTRONIC DESIGN PRACTICE OR 50 20 37 P FIRST TERM TOTAL = 425/750. ORDN. 1 MARKS: T80053076 NIKAM JEEVAN SHANKAR SANGITA , 71045547L , т80053076 , PICT 01. CONTROL SYSTEMS 100 40 50 P PP 100 40 40 P 02. DIGITAL COMMUNICATION 03. DIGITAL COMMUNICATION 50 20 08 F 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 30 F 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 37 P 06. MICROCONTROLLERS & APPLICATION PP 100 40 49 P 07. MICROCONTROLLERS & APPLICATION PR 50 20 11 F 100 40 46 P 08. DIGITAL SIGNAL PROCESSING 09. DIGITAL SIGNAL PROCESSING 50 20 27 P OR 10. ELECTRONIC DESIGN PRACTICE OR 50 20 39 P FIRST TERM TOTAL = 337/750. ORDN. 1 MARKS:

DATE: 31 JAN. 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 26 (395) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71100917B , , PICT , T80053077 T80053077 NIKITA AVINASH ANGRE CHITRA 01. CONTROL SYSTEMS PP 100 40 43 P 02. DIGITAL COMMUNICATION 100 40 40 P 03. DIGITAL COMMUNICATION 50 20 35 P PR 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 47 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 38 P 06. MICROCONTROLLERS & APPLICATION PP 100 40 40 P 07. MICROCONTROLLERS & APPLICATION PR 50 20 38 P 08. DIGITAL SIGNAL PROCESSING 100 40 42 P 09. DIGITAL SIGNAL PROCESSING 50 20 25 P OR 50 20 29 P 10. ELECTRONIC DESIGN PRACTICE OR FIRST TERM TOTAL = 377/750. ORDN. 1 MARKS: T80053078 PADGILWAR GAURAV VISHWAMBHAR SAVITA , 71100920B , , PICT , т80053078 01. CONTROL SYSTEMS 100 40 59 P 02. DIGITAL COMMUNICATION 100 40 52 P 03. DIGITAL COMMUNICATION 20 28 P PR 50 100 40 60 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 50 20 40 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 100 40 62 P 06. MICROCONTROLLERS & APPLICATION PP 50 20 42 P 07. MICROCONTROLLERS & APPLICATION PR 08. DIGITAL SIGNAL PROCESSING 100 40 45 P 09. DIGITAL SIGNAL PROCESSING 50 20 38 P OR 10. ELECTRONIC DESIGN PRACTICE OR 50 20 33 P FIRST TERM TOTAL = 459/750. ORDN. 1 MARKS: T80053079 PADHYE ABHINAV ANANT ANURADHA , 71236322L , т80053079 , PICT 01. CONTROL SYSTEMS 100 40 76 P 100 40 54 P 02. DIGITAL COMMUNICATION PP 03. DIGITAL COMMUNICATION 50 20 34 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 53 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 46 P 06. MICROCONTROLLERS & APPLICATION PP 100 40 61 P 07. MICROCONTROLLERS & APPLICATION PR 50 20 42 P 100 40 60 P 08. DIGITAL SIGNAL PROCESSING 09. DIGITAL SIGNAL PROCESSING 50 20 41 P OR 10. ELECTRONIC DESIGN PRACTICE OR 50 20 36 P FIRST TERM TOTAL = 503/750. ORDN. 1 MARKS:

DATE: 31 JAN. 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 27 (396) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71100921L , , PICT , T80053080 T80053080 PADOLE NEHA SUNIL NIRMALA 01. CONTROL SYSTEMS PP 100 40 74 P 02. DIGITAL COMMUNICATION 100 40 59 P 03. DIGITAL COMMUNICATION 50 20 35 P PR 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 62 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 37 P 06. MICROCONTROLLERS & APPLICATION PP 100 40 72 P 50 20 42 P 07. MICROCONTROLLERS & APPLICATION PR 08. DIGITAL SIGNAL PROCESSING 100 40 59 P 09. DIGITAL SIGNAL PROCESSING 50 20 38 P OR 50 20 40 P 10. ELECTRONIC DESIGN PRACTICE OR FIRST TERM TOTAL = 518/750. ORDN. 1 MARKS: T80053081 PAGARIYA BHUSHAN RAMESHCHAND MANJUSHRI , 71100923G , , PICT , T80053081 01. CONTROL SYSTEMS 100 40 73 P 02. DIGITAL COMMUNICATION 100 40 44 P 03. DIGITAL COMMUNICATION 20 43 P PR 50 100 40 63 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 50 20 39 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 100 40 66 P 06. MICROCONTROLLERS & APPLICATION PP 50 20 43 P 07. MICROCONTROLLERS & APPLICATION PR 08. DIGITAL SIGNAL PROCESSING 100 40 53 P 09. DIGITAL SIGNAL PROCESSING 50 20 43 P OR 50 20 39 P 10. ELECTRONIC DESIGN PRACTICE OR FIRST TERM TOTAL = 506/750. ORDN. 1 MARKS: T80053082 PANDEY JUHI SANJAY SUNITA , 71100927K , т80053082 , PICT 100 40 70 P 01. CONTROL SYSTEMS PP 100 40 56 P 02. DIGITAL COMMUNICATION 03. DIGITAL COMMUNICATION 50 20 46 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 74 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 49 P 06. MICROCONTROLLERS & APPLICATION PP 100 40 66 P 07. MICROCONTROLLERS & APPLICATION PR 50 20 45 P 100 40 76 P 08. DIGITAL SIGNAL PROCESSING 09. DIGITAL SIGNAL PROCESSING 50 20 45 P OR 10. ELECTRONIC DESIGN PRACTICE OR 50 20 41 P FIRST TERM TOTAL = 568/750. ORDN. 1 MARKS:

UNIVERSITY OF	PUNE	,T.E.(2008	PAT.)	(ELECTRON	NICS & T	ELEC.) EX	XAMINATIO	ON NOV. 201	L2	
DATE : 31 JAN. 2013	CENT	RE : PU	NE IN	ISTITU	TE OF COM	MPUTER T	ECHNOLOGY	Y, PUNE.		PAGE N	NO. 28 (397)
NOTE: FIRST LINE : SEAT NO., NAME O OTHER LINES: HEAD OF PASSING,	F THE	CANDID MARKS,	ATE, MIN	MOTH I. PAS	ER, PERMA S MARKS,	ANENT RE	G. NO., OBTAINED,	PREVIOUS	S SEAT NO., ASS/FAIL,	C:PREVIOUS	SEAT NO. CARRY OVER
T80053083 PANDILWAR RAMESHWAR KISH				 SUJA			, 711009			, PICT	, т80053083
01. CONTROL SYSTEMS	PP	100	40	61 P			,		,	,	,
02. DIGITAL COMMUNICATION	PP			40 P							
03. DIGITAL COMMUNICATION	PR	50	20	07 F	:						
04. NETWORK SYNTHESIS & FILTER DESIG	NPP	100	40	51 P	•						
05. NETWORK SYNTHESIS & FILTER DESIG	NTW	50	20	21 P	•						
06. MICROCONTROLLERS & APPLICATION	PP	100	40	41 P	•						
07. MICROCONTROLLERS & APPLICATION	PR	50	20	12 F	•						
08. DIGITAL SIGNAL PROCESSING	PP	100	40	43 P	•						
09. DIGITAL SIGNAL PROCESSING	OR	50	20	41 P	•						
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	22 P	•						
FIRST TERM TOTAL = 339/750.											
ORDN. 1 MARKS :											
T80053084 PARDESHI SAGAR RAJKUMAR				USHA			, 711009	931н	,	, PICT	, т80053084
01. CONTROL SYSTEMS	PP	100	40	65 P	•						
02. DIGITAL COMMUNICATION	PP	100	40	48 P	•						
03. DIGITAL COMMUNICATION	PR	50	20	44 P	•						
04. NETWORK SYNTHESIS & FILTER DESIG	NPP	100	40	57 P	•						
05. NETWORK SYNTHESIS & FILTER DESIG	NTW	50	20	43 P	•						
06. MICROCONTROLLERS & APPLICATION	PP	100	40	63 P	•						
07. MICROCONTROLLERS & APPLICATION	PR	50	20	40 P	•						
08. DIGITAL SIGNAL PROCESSING	PP			61 P							
09. DIGITAL SIGNAL PROCESSING	OR			39 P							
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	36 P	•						
FIRST TERM TOTAL = 496/750.											
ORDN. 1 MARKS :											
								226-			
T80053085 PATHAK SWANAND SUDHIR		100	40	SONA			, 711009	936J	,	, PICT	, т80053085
01. CONTROL SYSTEMS				63 P							
02. DIGITAL COMMUNICATION	PP			50 P							
03. DIGITAL COMMUNICATION	PR			43 P							
04. NETWORK SYNTHESIS & FILTER DESIG				55 P							
05. NETWORK SYNTHESIS & FILTER DESIG				42 P							
06. MICROCONTROLLERS & APPLICATION	PP			58 P							
07. MICROCONTROLLERS & APPLICATION	PR			37 P							
08. DIGITAL SIGNAL PROCESSING	PP OB			40 P							
09. DIGITAL SIGNAL PROCESSING	OR OR			40 P							
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	36 P	•						
FIRST TERM TOTAL = 464/750.											
ORDN. 1 MARKS :											

DATE: 31 JAN. 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 29 (398) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER LATA , 71100937G , , PICT , T80053086 T80053086 PATIL AMOL HIRAMAN 01. CONTROL SYSTEMS 100 40 58 P 02. DIGITAL COMMUNICATION 100 40 24 F 03. DIGITAL COMMUNICATION 50 20 27 P PR 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 45 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 41 P 06. MICROCONTROLLERS & APPLICATION PP 100 40 54 P 07. MICROCONTROLLERS & APPLICATION PR 50 20 32 P 08. DIGITAL SIGNAL PROCESSING 100 40 45 P 50 20 35 P 09. DIGITAL SIGNAL PROCESSING OR 50 20 29 P 10. ELECTRONIC DESIGN PRACTICE OR FIRST TERM TOTAL = 390/750. ORDN. 1 MARKS: T80053087 PATIL ASHISH ISHWAR KUSUM , 71100939C , , PICT , т80053087 01. CONTROL SYSTEMS 100 40 71 P 02. DIGITAL COMMUNICATION 100 40 53 P 03. DIGITAL COMMUNICATION 20 34 P PR 50 40 55 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 50 20 41 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 100 40 56 P 06. MICROCONTROLLERS & APPLICATION PP 50 20 32 P 07. MICROCONTROLLERS & APPLICATION PR 08. DIGITAL SIGNAL PROCESSING 100 40 54 P 50 20 37 P 09. DIGITAL SIGNAL PROCESSING OR 50 20 34 P 10. ELECTRONIC DESIGN PRACTICE OR FIRST TERM TOTAL = 467/750. ORDN. 1 MARKS: T80053088 PATIL PRADNYA RAJENDRA **SNEHAL** , 71236323J , T80053088 , PICT 01. CONTROL SYSTEMS 100 40 62 P 100 40 47 P 02. DIGITAL COMMUNICATION PP 03. DIGITAL COMMUNICATION 50 20 38 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 58 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 40 P 06. MICROCONTROLLERS & APPLICATION PP 100 40 67 P 07. MICROCONTROLLERS & APPLICATION PR 50 20 36 P 100 40 53 P 08. DIGITAL SIGNAL PROCESSING 09. DIGITAL SIGNAL PROCESSING 50 20 38 P OR 10. ELECTRONIC DESIGN PRACTICE OR 50 20 34 P FIRST TERM TOTAL = 473/750. ORDN. 1 MARKS:

DATE: 31 JAN. 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 30 (399) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71100942C , , PICT , T80053089 T80053089 PATIL PUJA VIJAY MIRA 01. CONTROL SYSTEMS PP 100 40 58 P 02. DIGITAL COMMUNICATION PP 100 40 52 P 03. DIGITAL COMMUNICATION 50 20 34 P PR 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 48 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 37 P 06. MICROCONTROLLERS & APPLICATION PP 100 40 53 P 07. MICROCONTROLLERS & APPLICATION PR 50 20 36 P 08. DIGITAL SIGNAL PROCESSING 100 40 45 P 09. DIGITAL SIGNAL PROCESSING 50 20 25 P OR 50 20 38 P 10. ELECTRONIC DESIGN PRACTICE OR FIRST TERM TOTAL = 426/750. ORDN. 1 MARKS: T80053090 PATIL SHIVRATNA BALASAHEB SHEELA , т80053090 01. CONTROL SYSTEMS 100 40 63 P 02. DIGITAL COMMUNICATION 100 40 41 P 03. DIGITAL COMMUNICATION 20 37 P PR 50 40 55 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 50 20 35 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 100 40 56 P 06. MICROCONTROLLERS & APPLICATION PP 50 20 39 P 07. MICROCONTROLLERS & APPLICATION PR 08. DIGITAL SIGNAL PROCESSING 100 40 48 P 09. DIGITAL SIGNAL PROCESSING 50 20 20 P OR 10. ELECTRONIC DESIGN PRACTICE OR 50 20 37 P FIRST TERM TOTAL = 431/750. ORDN. 1 MARKS: T80053091 PAVAN TENKALE SHIVKANTA , 71100946F , т80053091 , PICT 01. CONTROL SYSTEMS 100 40 56 P PP 100 40 44 P 02. DIGITAL COMMUNICATION 03. DIGITAL COMMUNICATION 50 20 AA F 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 51 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 25 P 06. MICROCONTROLLERS & APPLICATION PP 100 40 40 P 07. MICROCONTROLLERS & APPLICATION PR 50 20 02 F 100 40 32 F 08. DIGITAL SIGNAL PROCESSING 09. DIGITAL SIGNAL PROCESSING 50 20 AA F OR 10. ELECTRONIC DESIGN PRACTICE OR 50 20 16 F FIRST TERM TOTAL = 266/750. ORDN. 1 MARKS:

DATE: 31 JAN. 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 31 (400) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER T80053092 PAWAR AJAY BALNATH , 71100948B , , PICT , T80053092 SUNITA 01. CONTROL SYSTEMS 100 40 48 P 02. DIGITAL COMMUNICATION 100 40 50 P 03. DIGITAL COMMUNICATION 50 20 40 P PR 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 46 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 45 P 100 40 56 P 06. MICROCONTROLLERS & APPLICATION PP 07. MICROCONTROLLERS & APPLICATION PR 50 20 38 P 08. DIGITAL SIGNAL PROCESSING 100 40 49 P 09. DIGITAL SIGNAL PROCESSING 50 20 41 P OR 10. ELECTRONIC DESIGN PRACTICE 50 20 35 P OR FIRST TERM TOTAL = 448/750. ORDN. 1 MARKS: T80053093 PAWAR ASHWINI SUDHAKAR VANDANA , 71236325E , , PICT , т80053093 01. CONTROL SYSTEMS 100 40 58 P 02. DIGITAL COMMUNICATION 100 40 48 P 03. DIGITAL COMMUNICATION 20 39 P PR 50 40 64 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 50 20 43 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 100 40 64 P 06. MICROCONTROLLERS & APPLICATION PP 50 20 28 P 07. MICROCONTROLLERS & APPLICATION 08. DIGITAL SIGNAL PROCESSING 100 40 56 P 50 20 30 P 09. DIGITAL SIGNAL PROCESSING OR 10. ELECTRONIC DESIGN PRACTICE OR 50 20 32 P FIRST TERM TOTAL = 462/750. ORDN. 1 MARKS: T80053094 PHAPALE MONIKA DILIP , 71100955E , т80053094 ALKA , PICT 01. CONTROL SYSTEMS 100 40 44 P PP 100 40 40 P 02. DIGITAL COMMUNICATION 03. DIGITAL COMMUNICATION 50 20 32 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 40 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 43 P 06. MICROCONTROLLERS & APPLICATION PP 100 40 60 P 07. MICROCONTROLLERS & APPLICATION PR 50 20 26 P 100 40 40 P 08. DIGITAL SIGNAL PROCESSING 09. DIGITAL SIGNAL PROCESSING 50 20 28 P OR 10. ELECTRONIC DESIGN PRACTICE OR 50 20 30 P FIRST TERM TOTAL = 383/750. ORDN. 1 MARKS:

DATE: 31 JAN. 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 32 (401) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER T80053095 PRAGATI VASANT SHAH , 71100958к , , , рІСТ , Т80053095 VARSHA 01. CONTROL SYSTEMS PP 100 40 55 P 02. DIGITAL COMMUNICATION 100 40 55 P 03. DIGITAL COMMUNICATION 50 20 35 P PR 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 49 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 43 P 06. MICROCONTROLLERS & APPLICATION PP 100 40 61 P 50 20 32 P 07. MICROCONTROLLERS & APPLICATION PR 08. DIGITAL SIGNAL PROCESSING 100 40 58 P 09. DIGITAL SIGNAL PROCESSING 50 20 40 P OR 10. ELECTRONIC DESIGN PRACTICE 50 20 38 P OR FIRST TERM TOTAL = 466/750. ORDN. 1 MARKS: T80053096 PRAMOD KUMAR VERMA SUMAN DEVI , т80053096 01. CONTROL SYSTEMS 100 40 69 P 02. DIGITAL COMMUNICATION 100 40 51 P PP 03. DIGITAL COMMUNICATION 20 40 P PR 50 100 40 54 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 50 20 40 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 100 40 65 P 06. MICROCONTROLLERS & APPLICATION PP 50 20 41 P 07. MICROCONTROLLERS & APPLICATION PR 08. DIGITAL SIGNAL PROCESSING 100 40 51 P 09. DIGITAL SIGNAL PROCESSING 50 20 41 P OR 10. ELECTRONIC DESIGN PRACTICE OR 50 20 39 P FIRST TERM TOTAL = 491/750. ORDN. 1 MARKS: T80053097 PRAYAG SHREYAS NAGNATH JAYASHRI , 71100960M , т80053097 , PICT 01. CONTROL SYSTEMS 100 40 59 P 100 40 42 P 02. DIGITAL COMMUNICATION PP 03. DIGITAL COMMUNICATION 50 20 38 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 55 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 34 P 06. MICROCONTROLLERS & APPLICATION PP 100 40 50 P 07. MICROCONTROLLERS & APPLICATION PR 50 20 31 P 100 40 40 P 08. DIGITAL SIGNAL PROCESSING 09. DIGITAL SIGNAL PROCESSING 50 20 25 P OR 10. ELECTRONIC DESIGN PRACTICE OR 50 20 31 P FIRST TERM TOTAL = 405/750. ORDN. 1 MARKS:

DATE: 31 JAN. 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 33 (402) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71100961K , , PICT , T80053098 T80053098 PURANDARE ADWAIT PRASHANT MONIKA 01. CONTROL SYSTEMS 100 40 73 P PP 02. DIGITAL COMMUNICATION 100 40 59 P 03. DIGITAL COMMUNICATION 50 20 45 P PR 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 64 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 47 P 100 40 70 P 06. MICROCONTROLLERS & APPLICATION PP 50 20 40 P 07. MICROCONTROLLERS & APPLICATION PR 08. DIGITAL SIGNAL PROCESSING 100 40 70 P 50 20 45 P 09. DIGITAL SIGNAL PROCESSING OR 10. ELECTRONIC DESIGN PRACTICE 50 20 37 P OR FIRST TERM TOTAL = 550/750. ORDN. 1 MARKS: T80053099 RATHOD AKASH NURSING NALINI , 71100970J , , PICT , т80053099 01. CONTROL SYSTEMS 100 40 54 P 02. DIGITAL COMMUNICATION 100 40 40 P 03. DIGITAL COMMUNICATION 20 32 P PR 50 40 40 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 50 20 42 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 40 51 P 06. MICROCONTROLLERS & APPLICATION PP 100 50 20 30 P 07. MICROCONTROLLERS & APPLICATION PR 08. DIGITAL SIGNAL PROCESSING 100 40 40 P 50 20 30 P 09. DIGITAL SIGNAL PROCESSING OR 50 20 34 P 10. ELECTRONIC DESIGN PRACTICE OR FIRST TERM TOTAL = 393/750. ORDN. 1 MARKS: T80053100 RATHOD VIJAY HIRAMAN KANTA , 71236326C , PICT , T80053100 100 40 60 P 01. CONTROL SYSTEMS 100 40 57 P 02. DIGITAL COMMUNICATION PP 03. DIGITAL COMMUNICATION 50 20 28 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 52 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 39 P 06. MICROCONTROLLERS & APPLICATION PP 100 40 61 P 07. MICROCONTROLLERS & APPLICATION PR 50 20 32 P 100 40 42 P 08. DIGITAL SIGNAL PROCESSING 09. DIGITAL SIGNAL PROCESSING 50 20 34 P OR 10. ELECTRONIC DESIGN PRACTICE OR 50 20 36 P FIRST TERM TOTAL = 441/750. ORDN. 1 MARKS:

DATE: 31 JAN. 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 34 (403) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71100972E , , PICT , T80053101 T80053101 RAUT CHAITANYA SADASHIV CHHAYA 01. CONTROL SYSTEMS 100 40 53 P PP 02. DIGITAL COMMUNICATION 100 40 47 P 03. DIGITAL COMMUNICATION 50 20 06 F PR 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 56 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 32 P 06. MICROCONTROLLERS & APPLICATION PP 100 40 59 P 07. MICROCONTROLLERS & APPLICATION PR 50 20 33 P 08. DIGITAL SIGNAL PROCESSING 100 40 40 P 09. DIGITAL SIGNAL PROCESSING 50 20 47 P OR 50 20 32 P 10. ELECTRONIC DESIGN PRACTICE OR FIRST TERM TOTAL = 405/750. ORDN. 1 MARKS: , т80053102 T80053102 ROKADE VINITA SATISHKUMAR VIBHAVARI , 71100977F , , PICT 01. CONTROL SYSTEMS 100 40 41 P 02. DIGITAL COMMUNICATION 100 40 40 P 03. DIGITAL COMMUNICATION 20 30 P PR 50 100 40 40 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 50 20 33 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 100 40 41 P 06. MICROCONTROLLERS & APPLICATION PP 50 20 26 P 07. MICROCONTROLLERS & APPLICATION 08. DIGITAL SIGNAL PROCESSING 100 40 48 P 09. DIGITAL SIGNAL PROCESSING 50 20 08 F OR 10. ELECTRONIC DESIGN PRACTICE OR 50 20 29 P FIRST TERM TOTAL = 336/750. ORDN. 1 MARKS: T80053103 RUDRAWAR SOURABH ASHWIN SHOBHA , 71100979в , T80053103 , PICT 01. CONTROL SYSTEMS 100 40 56 P 100 40 52 P 02. DIGITAL COMMUNICATION PP 03. DIGITAL COMMUNICATION 50 20 33 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 58 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 35 P 06. MICROCONTROLLERS & APPLICATION PP 100 40 43 P 07. MICROCONTROLLERS & APPLICATION PR 50 20 28 P 100 40 49 P 08. DIGITAL SIGNAL PROCESSING 09. DIGITAL SIGNAL PROCESSING 50 20 30 P OR 10. ELECTRONIC DESIGN PRACTICE OR 50 20 32 P FIRST TERM TOTAL = 416/750. ORDN. 1 MARKS:

DATE: 31 JAN. 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 35 (404) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71100984J , , PICT , T80053104 T80053104 SALUNKE SAYALI RAJENDRA SANGEETA 01. CONTROL SYSTEMS 100 40 60 P PP 02. DIGITAL COMMUNICATION 100 40 51 P 03. DIGITAL COMMUNICATION 50 20 40 P PR 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 60 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 43 P 100 40 64 P 06. MICROCONTROLLERS & APPLICATION PP 07. MICROCONTROLLERS & APPLICATION PR 50 20 35 P 08. DIGITAL SIGNAL PROCESSING 100 40 64 P 50 20 38 P 09. DIGITAL SIGNAL PROCESSING OR 50 20 23 P 10. ELECTRONIC DESIGN PRACTICE OR FIRST TERM TOTAL = 478/750. ORDN. 1 MARKS: T80053105 SALUNKHE MITALI KISHOR BHARATI , 71236327M , , PICT , T80053105 01. CONTROL SYSTEMS 100 40 63 P 02. DIGITAL COMMUNICATION 100 40 40 P 03. DIGITAL COMMUNICATION 20 26 P PR 50 40 55 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 50 20 40 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 100 40 63 P 06. MICROCONTROLLERS & APPLICATION PP 50 20 40 P 07. MICROCONTROLLERS & APPLICATION PR 08. DIGITAL SIGNAL PROCESSING 100 40 46 P 50 20 34 P 09. DIGITAL SIGNAL PROCESSING OR 10. ELECTRONIC DESIGN PRACTICE OR 50 20 22 P FIRST TERM TOTAL = 429/750. ORDN. 1 MARKS: T80053106 SARODE BHUSHAN CHANDRAKANT , 71236328к , т80053106 ALKA , PICT 01. CONTROL SYSTEMS PP 100 40 63 P 100 40 40 P 02. DIGITAL COMMUNICATION PP 03. DIGITAL COMMUNICATION 50 20 36 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 49 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 44 P 06. MICROCONTROLLERS & APPLICATION PP 100 40 51 P 07. MICROCONTROLLERS & APPLICATION PR 50 20 41 P 100 40 56 P 08. DIGITAL SIGNAL PROCESSING 09. DIGITAL SIGNAL PROCESSING 50 20 35 P OR 10. ELECTRONIC DESIGN PRACTICE OR 50 20 23 P FIRST TERM TOTAL = 438/750. ORDN. 1 MARKS:

DATE: 31 JAN. 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 36 (405) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER T80053107 SARWAR ABHIJIT ASHOK , 71100988M , , PICT , T80053107 MEERA 01. CONTROL SYSTEMS PP 100 40 51 P 02. DIGITAL COMMUNICATION 100 40 40 P 03. DIGITAL COMMUNICATION 50 20 30 P PR 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 40 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 36 P 06. MICROCONTROLLERS & APPLICATION PP 100 40 42 P 07. MICROCONTROLLERS & APPLICATION PR 50 20 30 P 08. DIGITAL SIGNAL PROCESSING 100 40 42 P 50 20 36 P 09. DIGITAL SIGNAL PROCESSING OR 50 20 28 P 10. ELECTRONIC DESIGN PRACTICE OR FIRST TERM TOTAL = 375/750. ORDN. 1 MARKS: T80053108 SHAIKH MAAZ FAYYAZ SHAHEEN , 71100998J , , PICT , T80053108 01. CONTROL SYSTEMS 100 40 73 P 02. DIGITAL COMMUNICATION 100 40 53 P PP 03. DIGITAL COMMUNICATION 20 35 P PR 50 40 62 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 50 20 42 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 100 40 58 P 06. MICROCONTROLLERS & APPLICATION PP 50 20 35 P 07. MICROCONTROLLERS & APPLICATION PR 08. DIGITAL SIGNAL PROCESSING 100 40 44 P 50 20 37 P 09. DIGITAL SIGNAL PROCESSING OR 50 20 29 P 10. ELECTRONIC DESIGN PRACTICE OR FIRST TERM TOTAL = 468/750. ORDN. 1 MARKS: T80053109 SHELKE ISHWAR ANKUSH , 71101001D , т80053109 MUKTA , PICT 01. CONTROL SYSTEMS 100 40 60 P 100 40 49 P 02. DIGITAL COMMUNICATION PP 03. DIGITAL COMMUNICATION 50 20 40 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 62 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 36 P 06. MICROCONTROLLERS & APPLICATION PP 100 40 57 P 07. MICROCONTROLLERS & APPLICATION PR 50 20 35 P 100 40 55 P 08. DIGITAL SIGNAL PROCESSING 09. DIGITAL SIGNAL PROCESSING 50 20 36 P OR 10. ELECTRONIC DESIGN PRACTICE OR 50 20 37 P FIRST TERM TOTAL = 467/750. ORDN. 1 MARKS:

DATE: 31 JAN. 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 37 (406) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER SAVITA , 71101004J , , PICT , T80053110 T80053110 SHIKHA SUMAN SINHA 01. CONTROL SYSTEMS 100 40 55 P 02. DIGITAL COMMUNICATION 100 40 48 P 03. DIGITAL COMMUNICATION 50 20 36 P PR 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 60 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 38 P 100 40 59 P 06. MICROCONTROLLERS & APPLICATION PP 07. MICROCONTROLLERS & APPLICATION PR 50 20 24 P 08. DIGITAL SIGNAL PROCESSING 100 40 40 P 50 20 34 P 09. DIGITAL SIGNAL PROCESSING OR 10. ELECTRONIC DESIGN PRACTICE 50 20 13 F OR FIRST TERM TOTAL = 407/750. ORDN. 1 MARKS: T80053111 SHITRE SOURABH DNYANESHWAR SNEHA , 71101011M , , PICT , т80053111 01. CONTROL SYSTEMS 100 40 47 P 02. DIGITAL COMMUNICATION 100 40 40 P 03. DIGITAL COMMUNICATION 20 42 P PR 50 40 46 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 50 20 43 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 100 40 48 P 06. MICROCONTROLLERS & APPLICATION PP 50 20 40 P 07. MICROCONTROLLERS & APPLICATION 08. DIGITAL SIGNAL PROCESSING 100 40 46 P 09. DIGITAL SIGNAL PROCESSING 50 20 35 P OR 10. ELECTRONIC DESIGN PRACTICE OR 50 20 38 P FIRST TERM TOTAL = 425/750. ORDN. 1 MARKS: T80053112 SHRADDHA AGRAWAL REKHA , 71101013н , т80053112 , PICT 100 40 76 P 01. CONTROL SYSTEMS PP 100 40 40 P 02. DIGITAL COMMUNICATION 03. DIGITAL COMMUNICATION 50 20 36 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 51 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 41 P 06. MICROCONTROLLERS & APPLICATION PP 100 40 58 P 07. MICROCONTROLLERS & APPLICATION PR 50 20 27 P 100 40 43 P 08. DIGITAL SIGNAL PROCESSING 09. DIGITAL SIGNAL PROCESSING 50 20 36 P OR 10. ELECTRONIC DESIGN PRACTICE OR 50 20 23 P FIRST TERM TOTAL = 431/750. ORDN. 1 MARKS:

UNIVERSITY OF DATE: 31 JAN. 2013		-	-				TELEC.)			NOV. 20		PAGE NO). 38 (407)
NOTE: FIRST LINE : SEAT NO., NAME O OTHER LINES: HEAD OF PASSING,				-	-						•	•	
T80053113 SIDDAMAL GANESH UDDANAPP			• •		 REMA	 		 01014F				PICT	, т80053113
01. CONTROL SYSTEMS	PP	100	40	48			•		·		•		,
02. DIGITAL COMMUNICATION	PP	100	40	43	Р								
03. DIGITAL COMMUNICATION	PR	50	20	38	Р								
04. NETWORK SYNTHESIS & FILTER DESIG	NPP	100	40	57	Р								
05. NETWORK SYNTHESIS & FILTER DESIG	NTW	50	20	40	Р								
06. MICROCONTROLLERS & APPLICATION	PP	100	40	41	Р								
07. MICROCONTROLLERS & APPLICATION	PR	50	20	30	Р								
08. DIGITAL SIGNAL PROCESSING	PP	100	40	49	Р								
09. DIGITAL SIGNAL PROCESSING	OR	50	20	37	Р								
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	34	Р								
FIRST TERM TOTAL = $417/750$.													
ORDN. 1 MARKS :													
TOOF2114 CIDDUANT KUUDANA						 	7110					DICT	T900F2114
T80053114 SIDDHANT KHURANA	DD	100	40		ROJ		, /110)1016в	,		,	PICT	, т80053114
01. CONTROL SYSTEMS	PP	100	40	40									
02. DIGITAL COMMUNICATION	PP	100	40	13									
03. DIGITAL COMMUNICATION	PR	50	20	20									
04. NETWORK SYNTHESIS & FILTER DESIG		100	40	42									
05. NETWORK SYNTHESIS & FILTER DESIG		50	20	31									
06. MICROCONTROLLERS & APPLICATION	PP 	100	40	32									
07. MICROCONTROLLERS & APPLICATION	PR 	50	20	13									
08. DIGITAL SIGNAL PROCESSING	PP	100	40	24									
	OR	50	20	24									
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	32	Р								
FIRST TERM TOTAL = 271/750.													
ORDN. 1 MARKS :													
T80053115 SONAWANE ANUJA GIRIDHAR			• •	 St	 JNITA	 	. 7110	01019G				PICT	, т80053115
01. CONTROL SYSTEMS	PP	100	40	47			,		,		,		,
02. DIGITAL COMMUNICATION	PP	100	40	28									
03. DIGITAL COMMUNICATION	PR	50	20	25									
04. NETWORK SYNTHESIS & FILTER DESIG		100	40	47									
05. NETWORK SYNTHESIS & FILTER DESIG		50	20	41									
06. MICROCONTROLLERS & APPLICATION	PP	100	40	55									
07. MICROCONTROLLERS & APPLICATION	PR	50	20	29									
08. DIGITAL SIGNAL PROCESSING	PP	100	40	40									
09. DIGITAL SIGNAL PROCESSING	OR	50	20	26									
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	23									
FIRST TERM TOTAL = 361/750.	٥.,	33			•								
ORDN. 1 MARKS :													
5.15.11 ± 10.0110 1													

DATE: 31 JAN. 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 39 (408) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER T80053116 SONOO KUMAR , 71101020L , , PICT , T80053116 GAURADEVI 01. CONTROL SYSTEMS 100 40 64 P 02. DIGITAL COMMUNICATION 100 40 42 P 03. DIGITAL COMMUNICATION 50 20 10 F PR 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 63 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 29 P 100 40 48 P 06. MICROCONTROLLERS & APPLICATION PP 07. MICROCONTROLLERS & APPLICATION PR 50 20 09 F 08. DIGITAL SIGNAL PROCESSING 100 40 47 P 09. DIGITAL SIGNAL PROCESSING 50 20 35 P OR 50 20 28 P 10. ELECTRONIC DESIGN PRACTICE OR FIRST TERM TOTAL = 375/750. ORDN. 1 MARKS: T80053117 SONTAKKE BASVESHWAR RAOSAHEB SEETABAI , т80053117 01. CONTROL SYSTEMS 100 40 65 P 02. DIGITAL COMMUNICATION 100 40 40 P 03. DIGITAL COMMUNICATION 20 30 P PR 50 40 50 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 50 20 35 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 100 40 56 P 06. MICROCONTROLLERS & APPLICATION PP 50 20 31 P 07. MICROCONTROLLERS & APPLICATION 08. DIGITAL SIGNAL PROCESSING 100 40 40 P 50 20 35 P 09. DIGITAL SIGNAL PROCESSING OR 10. ELECTRONIC DESIGN PRACTICE OR 50 20 31 P FIRST TERM TOTAL = 413/750. ORDN. 1 MARKS: T80053118 SUMIT SINGH USHA , 71101023E , т80053118 , PICT 100 40 91 P 01. CONTROL SYSTEMS PP 100 40 60 P 02. DIGITAL COMMUNICATION 03. DIGITAL COMMUNICATION 50 20 44 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 68 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 43 P 06. MICROCONTROLLERS & APPLICATION PP 100 40 68 P 07. MICROCONTROLLERS & APPLICATION PR 50 20 33 P 100 40 53 P 08. DIGITAL SIGNAL PROCESSING 09. DIGITAL SIGNAL PROCESSING 50 20 39 P OR 10. ELECTRONIC DESIGN PRACTICE OR 50 20 32 P FIRST TERM TOTAL = 531/750. ORDN. 1 MARKS:

DATE: 31 JAN. 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 40 (409) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71101025M , , PICT , T80053119 T80053119 TANK HIMANSHU SHARAD GEETA 01. CONTROL SYSTEMS PP 100 40 50 P 02. DIGITAL COMMUNICATION 100 40 40 P 03. DIGITAL COMMUNICATION 50 20 35 P PR 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 53 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 32 P 06. MICROCONTROLLERS & APPLICATION PP 100 40 41 P 07. MICROCONTROLLERS & APPLICATION PR 50 20 28 P 08. DIGITAL SIGNAL PROCESSING 100 40 40 P 09. DIGITAL SIGNAL PROCESSING 50 20 35 P OR 50 20 28 P 10. ELECTRONIC DESIGN PRACTICE OR FIRST TERM TOTAL = 382/750. ORDN. 1 MARKS: , т80053120 T80053120 TANNA VATSAL AJAY VAISHALI , 71236329н , , ріст 01. CONTROL SYSTEMS 100 40 62 P 02. DIGITAL COMMUNICATION 100 40 28 F PP 03. DIGITAL COMMUNICATION 20 12 F PR 50 40 43 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 50 20 34 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 100 40 43 P 06. MICROCONTROLLERS & APPLICATION PP 50 20 23 P 07. MICROCONTROLLERS & APPLICATION PR 08. DIGITAL SIGNAL PROCESSING 100 40 40 P 09. DIGITAL SIGNAL PROCESSING 50 20 30 P OR 10. ELECTRONIC DESIGN PRACTICE OR 50 20 29 P FIRST TERM TOTAL = 344/750. ORDN. 1 MARKS: T80053121 TANYA SINGHAL SUSHMA , 71101026к , т80053121 , PICT 01. CONTROL SYSTEMS 100 40 58 P PP 100 40 40 P 02. DIGITAL COMMUNICATION 03. DIGITAL COMMUNICATION 50 20 34 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 40 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 37 P 06. MICROCONTROLLERS & APPLICATION PP 100 40 51 P 07. MICROCONTROLLERS & APPLICATION PR 50 20 30 P 100 40 51 P 08. DIGITAL SIGNAL PROCESSING 09. DIGITAL SIGNAL PROCESSING 50 20 34 P OR 10. ELECTRONIC DESIGN PRACTICE OR 50 20 33 P FIRST TERM TOTAL = 408/750. ORDN. 1 MARKS:

DATE: 31 JAN. 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 41 (410) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71236330M , , PICT , T80053122 T80053122 TAWATE PRIYA GIRISH SNEHAPRABHA 01. CONTROL SYSTEMS PP 100 40 65 P 02. DIGITAL COMMUNICATION 100 40 59 P 03. DIGITAL COMMUNICATION 50 20 38 P PR 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 67 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 44 P 100 40 71 P 06. MICROCONTROLLERS & APPLICATION PP 50 20 45 P 07. MICROCONTROLLERS & APPLICATION PR 08. DIGITAL SIGNAL PROCESSING 100 40 60 P 09. DIGITAL SIGNAL PROCESSING 50 20 42 P OR 50 20 39 P 10. ELECTRONIC DESIGN PRACTICE OR FIRST TERM TOTAL = 530/750. ORDN. 1 MARKS: , т80053123 T80053123 TILAK AMEY CHANDRASHEKHAR APARNA , 71101036G , , PICT 01. CONTROL SYSTEMS 100 40 65 P 02. DIGITAL COMMUNICATION 100 40 41 P 03. DIGITAL COMMUNICATION 20 45 P PR 50 100 40 58 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 50 20 45 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 100 40 55 P 06. MICROCONTROLLERS & APPLICATION PP 50 20 41 P 07. MICROCONTROLLERS & APPLICATION PR 08. DIGITAL SIGNAL PROCESSING 100 40 43 P 09. DIGITAL SIGNAL PROCESSING 50 20 39 P OR 50 20 25 P 10. ELECTRONIC DESIGN PRACTICE OR FIRST TERM TOTAL = 457/750. ORDN. 1 MARKS: T80053124 TIWARI BHAVYA YOGESH , 71101037E , т80053124 NEHA , PICT 01. CONTROL SYSTEMS 100 40 62 P PP 100 40 40 P 02. DIGITAL COMMUNICATION 03. DIGITAL COMMUNICATION 50 20 36 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 53 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 34 P 06. MICROCONTROLLERS & APPLICATION PP 100 40 40 P 07. MICROCONTROLLERS & APPLICATION PR 50 20 26 P 100 40 42 P 08. DIGITAL SIGNAL PROCESSING 09. DIGITAL SIGNAL PROCESSING 50 20 38 P OR 10. ELECTRONIC DESIGN PRACTICE OR 50 20 21 P FIRST TERM TOTAL = 392/750. ORDN. 1 MARKS:

UNIVERSITY O	F PUNE	T.E	.(200	8 PAT	.)(ELE	ECTRONICS &	& TELEC.) EXA	MINATION NOV	'. 2012	
DATE : 31 JAN. 2013	CENT	ΓRE :	PUNE	INSTI	TUTE 0	OF COMPUTER	R TECHNOLOGY,	PUNE.	PAGE	NO. 42 (411)
NOTE: FIRST LINE : SEAT NO., NAME (SEAT NO
OTHER LINES: HEAD OF PASSING,										
T80053125 TIWARI VIPUL TARUN					DMA		, 7110103		, PIC	
01. CONTROL SYSTEMS	PP	100	40	61			,	,	,	,
02. DIGITAL COMMUNICATION	PP	100	40	29	F					
03. DIGITAL COMMUNICATION	PR	50	20	07	F					
04. NETWORK SYNTHESIS & FILTER DESIG	GNPP	100	40	40	Р					
05. NETWORK SYNTHESIS & FILTER DESIG	GNTW	50	20	31	Р					
06. MICROCONTROLLERS & APPLICATION	PP	100	40	55	Р					
07. MICROCONTROLLERS & APPLICATION	PR	50	20	03	F					
08. DIGITAL SIGNAL PROCESSING	PP	100	40	29	F					
09. DIGITAL SIGNAL PROCESSING	OR	50	20	20	Р					
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	12	F					
FIRST TERM TOTAL = $287/750$.										
ORDN. 1 MARKS :										
T80053126 VAIBHAV RAMDAS BORUDE				MΑ	NDAKIN	NI	, 7110104	3к ,	, PIC	т , т80053126
01. CONTROL SYSTEMS	PP	100	40	78	Р					
02. DIGITAL COMMUNICATION	PP	100	40	42	Р					
03. DIGITAL COMMUNICATION	PR	50	20	42	Р					
04. NETWORK SYNTHESIS & FILTER DESIGNATION	GNPP	100	40	69	Р					
05. NETWORK SYNTHESIS & FILTER DESIG	GNTW	50	20	37	Р					
06. MICROCONTROLLERS & APPLICATION	PP	100	40	64	Р					
07. MICROCONTROLLERS & APPLICATION	PR	50	20	34	Р					
08. DIGITAL SIGNAL PROCESSING	PP	100	40	58	Р					
09. DIGITAL SIGNAL PROCESSING	OR	50	20	47	Р					
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	37	Р					
FIRST TERM TOTAL = $508/750$.										
ORDN. 1 MARKS :										
T80053127 VAIDYA SANDIP MILIND					NGEETA	4	, 7110104	4н ,	, PIC	т , т80053127
01. CONTROL SYSTEMS	PP	100	40	65						
02. DIGITAL COMMUNICATION	PP	100	40	42						
03. DIGITAL COMMUNICATION	PR	50	20	35						
04. NETWORK SYNTHESIS & FILTER DESIG	GNPP	100	40	56						
05. NETWORK SYNTHESIS & FILTER DESIG	GNTW	50	20	42						
06. MICROCONTROLLERS & APPLICATION	PP	100	40	53	Р					
07. MICROCONTROLLERS & APPLICATION	PR	50	20	41						
08. DIGITAL SIGNAL PROCESSING	PP	100	40	40						
09. DIGITAL SIGNAL PROCESSING	OR	50	20	35						
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	37	Р					
FIRST TERM TOTAL = 446/750.										
ORDN. 1 MARKS :										

DATE: 31 JAN. 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 43 (412) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER T80053128 VARADE RUSHIKESH DILIP , 71101045F , , PICT , T80053128 CHHAYA 01. CONTROL SYSTEMS 100 40 46 P PP 02. DIGITAL COMMUNICATION 100 40 AA F 03. DIGITAL COMMUNICATION 50 20 23 P PR 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 40 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 27 P 06. MICROCONTROLLERS & APPLICATION PP 100 40 40 P 07. MICROCONTROLLERS & APPLICATION PR 50 20 05 F 08. DIGITAL SIGNAL PROCESSING 100 40 26 F 09. DIGITAL SIGNAL PROCESSING 50 20 40 P OR 50 20 29 P 10. ELECTRONIC DESIGN PRACTICE OR FIRST TERM TOTAL = 276/750. ORDN. 1 MARKS: , т80053129 T80053129 VASAGADEKAR SWATI PRADEEP SUPRIYA 01. CONTROL SYSTEMS 100 40 75 P 02. DIGITAL COMMUNICATION 100 40 60 P 03. DIGITAL COMMUNICATION 20 36 P PR 50 40 74 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 50 20 43 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 100 40 58 P 06. MICROCONTROLLERS & APPLICATION PP 50 20 41 P 07. MICROCONTROLLERS & APPLICATION PR 08. DIGITAL SIGNAL PROCESSING 100 40 51 P 09. DIGITAL SIGNAL PROCESSING 50 20 39 P OR 10. ELECTRONIC DESIGN PRACTICE OR 50 20 30 P FIRST TERM TOTAL = 507/750. ORDN. 1 MARKS: T80053130 VIDHATE TRUPTI SOPAN **VARSHA** , 71101048L , т80053130 , PICT 01. CONTROL SYSTEMS 100 40 52 P 40 27 F 02. DIGITAL COMMUNICATION 100 PP 03. DIGITAL COMMUNICATION 50 20 36 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 40 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 38 P 06. MICROCONTROLLERS & APPLICATION PP 100 40 41 P 07. MICROCONTROLLERS & APPLICATION PR 50 20 27 P 100 40 45 P 08. DIGITAL SIGNAL PROCESSING 09. DIGITAL SIGNAL PROCESSING 50 20 30 P OR 10. ELECTRONIC DESIGN PRACTICE OR 50 20 27 P FIRST TERM TOTAL = 363/750. ORDN. 1 MARKS:

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DATE : SI SAN: 2013	CLIVI		ONE	111311		01 (011)	in recinologi, rone:	' ' '	de No.			± 1)
NOTE: FIRST LINE : SEAT NO., NAME												
·				-	-		RKS OBTAINED, P/F:PASS/FAIL, C		•			
T80053135 ABDULE MONALI MUKUND					ANDHYA		, 71045354L , T8053001				T8005	
01. CONTROL SYSTEMS	PP	100	40		РС		. SIGNAL CODING & ESTIMATION THE	-	100	40		РС
02. DIGITAL COMMUNICATION	PP	100	40	49	РС	12	. SIGNAL CODING & ESTIMATION THE	ORYPR	50	20	37	Р
03. DIGITAL COMMUNICATION	PR	50	20	28	РС	13	SYSTEM PROGRA.& OPERATING SYS.	PP	100	40	57	РС
04. NETWORK SYNTHESIS & FILTER DESI	GNPP	100	40	53	РС	14	SYSTEM PROGRA.& OPERATING SYS.	TW	50	20	39	РС
05. NETWORK SYNTHESIS & FILTER DESI	GNTW	50	20	41	РС	15	COMPUTER ORGANIZATION & ARCHIT	EC PP	100	40	54	РС
06. MICROCONTROLLERS & APPLICATION	PP	100	40	47	РС	16	. INDUSTRIAL MANAGEMENT	PP	100	40	51	РС
07. MICROCONTROLLERS & APPLICATION	PR	50	20	32	РС	17	. WAVE THEORY & ANTENNA	PP	100	40	47	РС
08. DIGITAL SIGNAL PROCESSING	PP	100	40	42	РС	18	. WAVE THEORY & ANTENNA	PR	50	20	25	РС
09. DIGITAL SIGNAL PROCESSING	OR	50	20	30	РС	19	MINI PROJECT & SEMINAR	OR	50	20	38	РС
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	25	РС	20	. TEST & MEASUREMENT TECHNIQUES	OR	50	20	33	РС
GRAND TOTAL = 825/1500, RESULT: HIGH	ER SEC	COND CI	LASS									
ORDN. 1 MARKS :												
T80053136 ABHISHEK ANAND				IN	NDRANI		, 71045356G , т8053003	, Р	ICT	,	T8005	3136
01. CONTROL SYSTEMS	PP	100	40	63	РС	11	. SIGNAL CODING & ESTIMATION THE	ORYPP	100	40	55	РС
02. DIGITAL COMMUNICATION	PP	100	40	40	РС	12	SIGNAL CODING & ESTIMATION THE	ORYPR	50	20	28	РС
03. DIGITAL COMMUNICATION	PR	50	20	33	РС	13	SYSTEM PROGRA.& OPERATING SYS.	PP	100	40	43	РС
04. NETWORK SYNTHESIS & FILTER DESI	GNPP	100	40	51	РС	14	SYSTEM PROGRA.& OPERATING SYS.	TW	50	20	39	РС
05. NETWORK SYNTHESIS & FILTER DESI	GNTW	50	20	36	РС	15	COMPUTER ORGANIZATION & ARCHIT	EC PP	100	40	59	РС
06. MICROCONTROLLERS & APPLICATION	PP	100	40	40	РС	16	. INDUSTRIAL MANAGEMENT	PP	100	40	44	РС
07. MICROCONTROLLERS & APPLICATION	PR	50	20	25	РС	17	. WAVE THEORY & ANTENNA	PP	100	40	40	РС
08. DIGITAL SIGNAL PROCESSING	PP	100	40	52	РС	18	. WAVE THEORY & ANTENNA	PR	50	20	38	Р
09. DIGITAL SIGNAL PROCESSING	OR	50	20	37	РС	19	MINI PROJECT & SEMINAR	OR	50	20	38	РС
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	25	РС	20	TEST & MEASUREMENT TECHNIQUES	OR	50	20	35	РС
GRAND TOTAL = $821+04/1500$, RESULT: H	IGHER	SECON	D CLA	SS [[0.2]							
ORDN. 1 MARKS :												
T80053137 BHAGAT ABHILASHA RAMDAS				MΑ	ADHURI		, 71045386」 , т8053013	, P	ICT	,	T8005	3137
01. CONTROL SYSTEMS	PP	100	40	AA	F	11	. SIGNAL CODING & ESTIMATION THE	ORYPP	100	40	51	РС
02. DIGITAL COMMUNICATION	PP	100	40	50	PС	12	. SIGNAL CODING & ESTIMATION THE	ORYPR	50	20	28	РС
03. DIGITAL COMMUNICATION	PR	50	20	20	РС	13	SYSTEM PROGRA.& OPERATING SYS.	PP	100	40	58	РС
04. NETWORK SYNTHESIS & FILTER DESI	GNPP	100	40	48	РС	14	SYSTEM PROGRA.& OPERATING SYS.	TW	50	20	41	РС
05. NETWORK SYNTHESIS & FILTER DESI	GNTW	50	20	39	РС	15	COMPUTER ORGANIZATION & ARCHIT	EC PP	100	40	51	РС
06. MICROCONTROLLERS & APPLICATION	PP	100	40	41	РС	16	. INDUSTRIAL MANAGEMENT	PP	100	40	61	РС
07. MICROCONTROLLERS & APPLICATION	PR	50	20	30	РС	17	. WAVE THEORY & ANTENNA	PP	100	40	49	РС
08. DIGITAL SIGNAL PROCESSING	PP	100	40	56	РС	18	. WAVE THEORY & ANTENNA	PR	50	20	30	РС
09. DIGITAL SIGNAL PROCESSING	OR	50	20	32	РС	19	. MINI PROJECT & SEMINAR	OR	50	20	32	Р
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	32	РС	20	. TEST & MEASUREMENT TECHNIQUES	OR	50	20	38	РС
GRAND TOTAL = 787/1500, RESULT: FAIL	S A.T.	K.T.										
ORDN. 1 MARKS :												

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , т80053138 REENA , 70801366L , T8053114 , PICT T80053138 BHATIA DHAWAL RAMESHCHAND 11. SIGNAL CODING & ESTIMATION THEORYPP 100 40 32 F 01. CONTROL SYSTEMS PP 100 40 49 P C 02. DIGITAL COMMUNICATION 100 40 07 F 12. SIGNAL CODING & ESTIMATION THEORYPR 50 20 22 P C 03. DIGITAL COMMUNICATION 50 20 40 P C 13. SYSTEM PROGRA. & OPERATING SYS. PP 100 40 49 PC PR 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 40 P C 14. SYSTEM PROGRA. & OPERATING SYS. TW 50 20 38 P C 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 39 P C 15. COMPUTER ORGANIZATION & ARCHITEC PP 100 40 40 P C 100 40 40 P C 16. INDUSTRIAL MANAGEMENT 100 40 44 P C 06. MICROCONTROLLERS & APPLICATION PP PP 100 40 AA F 07. MICROCONTROLLERS & APPLICATION PR 50 20 35 P C PP 17. WAVE THEORY & ANTENNA 08. DIGITAL SIGNAL PROCESSING PP 100 40 44 P C 50 20 38 P C 18. WAVE THEORY & ANTENNA PR 50 20 22 P C 50 20 39 P C 09. DIGITAL SIGNAL PROCESSING OR 19. MINI PROJECT & SEMINAR OR OR 50 20 25 P C 20. TEST & MEASUREMENT TECHNIQUES OR 50 20 39 P C 10. ELECTRONIC DESIGN PRACTICE GRAND TOTAL = 682/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS: T80053139 BHOIR PRASAD SHIVAJI USHA , 70925368M , T8053115 , PICT , т80053139 01. CONTROL SYSTEMS 100 40 40 P C 11. SIGNAL CODING & ESTIMATION THEORYPP 100 40 40 P C PP 100 40 40 P C 02. DIGITAL COMMUNICATION 12. SIGNAL CODING & ESTIMATION THEORYPR 50 20 30 P C 03. DIGITAL COMMUNICATION 20 23 P C 13. SYSTEM PROGRA. & OPERATING SYS. PP 100 40 PC PR 50 40 P C 40 50 20 34 P C 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 14. SYSTEM PROGRA. & OPERATING SYS. TW 50 20 38 P C 100 40 21 F 05. NETWORK SYNTHESIS & FILTER DESIGNTW 15. COMPUTER ORGANIZATION & ARCHITEC PP 100 40 43 P C 100 40 40 P C 06. MICROCONTROLLERS & APPLICATION PP 16. INDUSTRIAL MANAGEMENT 50 20 20 P C PP 100 40 14 F 07. MICROCONTROLLERS & APPLICATION PR 17. WAVE THEORY & ANTENNA 100 40 40 P C 50 20 35 P C 08. DIGITAL SIGNAL PROCESSING PP 18. WAVE THEORY & ANTENNA PR 50 20 26 P C 50 20 32 P C 09. DIGITAL SIGNAL PROCESSING OR 19. MINI PROJECT & SEMINAR OR 20. TEST & MEASUREMENT TECHNIQUES OR 50 20 28 P C 10. ELECTRONIC DESIGN PRACTICE OR 50 20 21 P C GRAND TOTAL = 645/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS: T80053140 BHOSALE PRASHANT DNYANADEO RANJANA , 71045393M , T8053016 , PICT , T80053140 01. CONTROL SYSTEMS PP 100 40 59 P C 11. SIGNAL CODING & ESTIMATION THEORYPP 100 40 66 P C 02. DIGITAL COMMUNICATION 100 40 45 P C 12. SIGNAL CODING & ESTIMATION THEORYPR 50 20 30 P C PΡ 03. DIGITAL COMMUNICATION 50 20 32 P C 13. SYSTEM PROGRA. & OPERATING SYS. PP 100 40 57 PC PR 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 61 P C 14. SYSTEM PROGRA. & OPERATING SYS. TW 50 20 37 P C 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 32 P C 15. COMPUTER ORGANIZATION & ARCHITEC PP 100 40 65 P C 06. MICROCONTROLLERS & APPLICATION PP 100 40 46 P C 16. INDUSTRIAL MANAGEMENT PP 100 40 60 P C 07. MICROCONTROLLERS & APPLICATION PR 50 20 36 P C PP 100 40 55 P C 17. WAVE THEORY & ANTENNA PP 100 40 60 P C 50 20 35 P 08. DIGITAL SIGNAL PROCESSING 18. WAVE THEORY & ANTENNA 09. DIGITAL SIGNAL PROCESSING 50 20 22 P C 19. MINI PROJECT & SEMINAR 50 20 22 P C OR OR 10. ELECTRONIC DESIGN PRACTICE OR 50 20 26 P C 20. TEST & MEASUREMENT TECHNIQUES OR 50 20 30 P C GRAND TOTAL = 876/1500, RESULT: HIGHER SECOND CLASS ORDN. 1 MARKS:

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 NOTI					-	-			REG. NO., PREVIOUS SEAT NO., O		•	 SEAT RRY C	_	
									71045427/					
	053141 GADHE GANESH VITTHALRAC) PP	100	40		EVALBAI P C		11	, 71045427K , T8053025 , SIGNAL CODING & ESTIMATION THEOR		100	, 40	т8005 46	
	CONTROL SYSTEMS DIGITAL COMMUNICATION	PP PP	100	40	40	PC			SIGNAL CODING & ESTIMATION THEOR		100 50	20	29	P C P C
	DIGITAL COMMUNICATION DIGITAL COMMUNICATION		50	20	36	PC			SYSTEM PROGRA. & OPERATING SYS.		100	40		PC
	NETWORK SYNTHESIS & FILTER DESI	PR	100	40	47	PC			SYSTEM PROGRA.& OPERATING SYS.	PP	50	20		PC
	NETWORK SYNTHESIS & FILTER DESI		50	20	35	PC			COMPUTER ORGANIZATION & ARCHITEC	TW	100	40	55	PC
	MICROCONTROLLERS & APPLICATION		100	40	45	PC			INDUSTRIAL MANAGEMENT	PP	100	40		P C
	MICROCONTROLLERS & APPLICATION MICROCONTROLLERS & APPLICATION	PP PR	50	20	38	PC			WAVE THEORY & ANTENNA	PP PP	100	40	43	P
	DIGITAL SIGNAL PROCESSING	PP	100	40	57	PC			WAVE THEORY & ANTENNA	PR	50	20		P C
	DIGITAL SIGNAL PROCESSING DIGITAL SIGNAL PROCESSING		50	20	35	PC		_	MINI PROJECT & SEMINAR		50 50	20	34	PC
		OR	50 50	20	33					OR	50 50	20		PC
	ELECTRONIC DESIGN PRACTICE TOTAL = 820+05/1500, RESULT: H	OR					2	20.	TEST & MEASUREMENT TECHNIQUES	OR	30	20	32	PC
	1 MARKS :	ITGHEK	SECUNL	CLA	.55 [_0.2]								
						··· [LIMA		•	, 71045435L , T8053026		· · · · ICT		т8005	21/2
	CONTROL SYSTEMS	PP	100	40		P C	1	11	SIGNAL CODING & ESTIMATION THEOR		100	, 40		P C
	DIGITAL COMMUNICATION	PP	100	40	43				SIGNAL CODING & ESTIMATION THEOR		50	20	34	P
	DIGITAL COMMUNICATION DIGITAL COMMUNICATION	PR	50	20	31				SYSTEM PROGRA. & OPERATING SYS.		100	40		P C
	NETWORK SYNTHESIS & FILTER DESI		100	40	56	PC			SYSTEM PROGRA.& OPERATING SYS.	PP	50	20	40	PC
	NETWORK SYNTHESIS & FILTER DESI		50	20	31				COMPUTER ORGANIZATION & ARCHITEC	TW	100	40	48	PC
	MICROCONTROLLERS & APPLICATION		100	40	31 47	PC			INDUSTRIAL MANAGEMENT		100	40	40 57	PC
	MICROCONTROLLERS & APPLICATION MICROCONTROLLERS & APPLICATION	PP		-	30			-	WAVE THEORY & ANTENNA	PP			-	
		PR	50 100	20						PP	100	40	40 23	P
	DIGITAL SIGNAL PROCESSING DIGITAL SIGNAL PROCESSING	PP	100 50	40 20	57 30	P C			WAVE THEORY & ANTENNA MINI PROJECT & SEMINAR	PR OB	50 50	20 20	30	P C P C
		OR	50 50		27	P C				OR				_
	ELECTRONIC DESIGN PRACTICE TOTAL = 806/1500, RESULT: SECO	OR		20	21	РС	2	20.	TEST & MEASUREMENT TECHNIQUES	OR	50	20	30	РС
		IND CL	455											
	1 MARKS :													
						 MI ATA		•	, 71045437G , T8053028				т8005	
	053143 GHONE PRASAD SHARAD	DD	100	40		MLATA P C	1	11	SIGNAL CODING & ESTIMATION THEOR			, 40		P C
	CONTROL SYSTEMS	PP	100	40 40		PC			SIGNAL CODING & ESTIMATION THEOR		100	20	31	
	DIGITAL COMMUNICATION	PP									50 100	_		P C
	DIGITAL COMMUNICATION	PR	50 100	20	29				SYSTEM PROGRA & OPERATING SYS.	PP	100	40		PC
	NETWORK SYNTHESIS & FILTER DESI		100	40		P C			SYSTEM PROGRA. OPERATING SYS.	TW	50 100	20		
	NETWORK SYNTHESIS & FILTER DESI		50 100	20		P C			COMPUTER ORGANIZATION & ARCHITEC		100	40		P C
	MICROCONTROLLERS & APPLICATION	PP	100	40	40				INDUSTRIAL MANAGEMENT	PP	100	40		PC
	MICROCONTROLLERS & APPLICATION	PR	50	20	35				WAVE THEORY & ANTENNA	PP	100	40		P C
	DIGITAL SIGNAL PROCESSING	PP	100	40	52				WAVE THEORY & ANTENNA	PR	50	20		P C
	DIGITAL SIGNAL PROCESSING	OR	50	20		PC			MINI PROJECT & SEMINAR	OR	50	20		PC
	ELECTRONIC DESIGN PRACTICE	OR	50	20	23	PС	2	۷.	TEST & MEASUREMENT TECHNIQUES	OR	50	20	28	РС
	TOTAL = 837/1500, RESULT: HIGH	IER SE	COND CL	_ASS										
ORDN.	1 MARKS :													
								•						

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , т80053144 ROHINI , 71045438E , T8053029 , PICT T80053144 GUJAR AKASH SANJAY 01. CONTROL SYSTEMS PP 100 40 45 P C 11. SIGNAL CODING & ESTIMATION THEORYPP 100 40 64 P C 02. DIGITAL COMMUNICATION PP 100 40 41 P C 12. SIGNAL CODING & ESTIMATION THEORYPR 50 20 03. DIGITAL COMMUNICATION 50 20 27 P C 13. SYSTEM PROGRA.& OPERATING SYS. PP 100 40 53 P PR 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 50 P C 14. SYSTEM PROGRA. & OPERATING SYS. TW 50 20 39 P C 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 32 P C 15. COMPUTER ORGANIZATION & ARCHITEC PP 100 40 54 P C 100 40 48 P C 16. INDUSTRIAL MANAGEMENT 100 40 41 P C 06. MICROCONTROLLERS & APPLICATION PP PP 100 40 40 P C 07. MICROCONTROLLERS & APPLICATION PR 50 20 36 P C PP 17. WAVE THEORY & ANTENNA 08. DIGITAL SIGNAL PROCESSING PP 100 40 52 P C 50 20 30 P C 18. WAVE THEORY & ANTENNA PR 50 20 20 P C 50 20 38 P C 09. DIGITAL SIGNAL PROCESSING OR 19. MINI PROJECT & SEMINAR OR 10. ELECTRONIC DESIGN PRACTICE OR 50 20 30 P C 20. TEST & MEASUREMENT TECHNIQUES OR 50 20 31 P C GRAND TOTAL = 805/1500, RESULT: SECOND CLASS ORDN. 1 MARKS: T80053145 INGLE CHETAN VASUDEO SHALINI , 71129930н , Т8053035 , РІСТ , T80053145 01. CONTROL SYSTEMS 100 40 64 P C 11. SIGNAL CODING & ESTIMATION THEORYPP 100 40 48 P C 100 40 02. DIGITAL COMMUNICATION PP 48 P C 12. SIGNAL CODING & ESTIMATION THEORYPR 50 20 28 P C 20 37 P C 13. SYSTEM PROGRA. & OPERATING SYS. PP 100 40 51 P C 03. DIGITAL COMMUNICATION PR 50 40 53 P C 50 20 40 P C 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 14. SYSTEM PROGRA. & OPERATING SYS. TW 50 20 42 P C 100 40 69 P C 05. NETWORK SYNTHESIS & FILTER DESIGNTW 15. COMPUTER ORGANIZATION & ARCHITEC PP 100 40 51 P C 06. MICROCONTROLLERS & APPLICATION PP 100 40 56 P C 16. INDUSTRIAL MANAGEMENT 50 20 34 P C PP 100 40 40 P C 07. MICROCONTROLLERS & APPLICATION PR 17. WAVE THEORY & ANTENNA 100 40 52 P C 50 20 36 P 08. DIGITAL SIGNAL PROCESSING PP 18. WAVE THEORY & ANTENNA PR 50 20 37 P C 50 20 41 P C 09. DIGITAL SIGNAL PROCESSING OR 19. MINI PROJECT & SEMINAR OR 50 20 24 P C 20. TEST & MEASUREMENT TECHNIQUES OR 50 20 35 P C 10. ELECTRONIC DESIGN PRACTICE OR GRAND TOTAL = 886/1500, RESULT: HIGHER SECOND CLASS ORDN. 1 MARKS: , т80053146 T80053146 JADHAV OMKAR FULCHAND JYOTSNA , 70701463J , T8053037 , PICT PP 100 40 45 P C 01. CONTROL SYSTEMS 11. SIGNAL CODING & ESTIMATION THEORYPP 100 40 42 P C 02. DIGITAL COMMUNICATION 100 40 41 P C 12. SIGNAL CODING & ESTIMATION THEORYPR 50 20 AA F PP 03. DIGITAL COMMUNICATION 50 20 29 P C 13. SYSTEM PROGRA. & OPERATING SYS. PP 100 40 P C PR 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 53 P C 14. SYSTEM PROGRA.& OPERATING SYS. TW 50 20 32 P C 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 27 P C 15. COMPUTER ORGANIZATION & ARCHITEC PP 100 40 49 P 06. MICROCONTROLLERS & APPLICATION PP 100 40 48 P C 16. INDUSTRIAL MANAGEMENT PP 100 40 41 P C 07. MICROCONTROLLERS & APPLICATION PR 50 20 22 P C PP 100 40 48 P C 17. WAVE THEORY & ANTENNA 100 40 40 P C 50 20 15 F 08. DIGITAL SIGNAL PROCESSING 18. WAVE THEORY & ANTENNA PP PR 09. DIGITAL SIGNAL PROCESSING 50 20 36 P C 19. MINI PROJECT & SEMINAR 50 20 30 P C OR OR 10. ELECTRONIC DESIGN PRACTICE OR 50 20 25 P C 20. TEST & MEASUREMENT TECHNIQUES OR 50 20 AA F GRAND TOTAL = 663/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71045495D , T8053047 , PICT , т80053147 T80053147 KULKARNI HARSHAD RAVINDRA SADHANA 01. CONTROL SYSTEMS PP 100 40 47 P C 11. SIGNAL CODING & ESTIMATION THEORYPP 100 40 69 P C 02. DIGITAL COMMUNICATION 100 40 47 P C 12. SIGNAL CODING & ESTIMATION THEORYPR 50 20 03. DIGITAL COMMUNICATION 50 20 34 P C 13. SYSTEM PROGRA.& OPERATING SYS. PP 100 40 PR 66 P C 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 54 P C 14. SYSTEM PROGRA. & OPERATING SYS. TW 50 20 39 P C 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 33 P C 15. COMPUTER ORGANIZATION & ARCHITEC PP 100 40 61 P C 100 40 59 P C 16. INDUSTRIAL MANAGEMENT 100 40 58 P C 06. MICROCONTROLLERS & APPLICATION PP PP 100 40 52 P C 07. MICROCONTROLLERS & APPLICATION PR 50 20 30 P C PP 17. WAVE THEORY & ANTENNA 08. DIGITAL SIGNAL PROCESSING PP 100 40 70 P C 50 20 37 P C 18. WAVE THEORY & ANTENNA PR 50 20 35 P C 50 20 46 P C 09. DIGITAL SIGNAL PROCESSING OR 19. MINI PROJECT & SEMINAR OR OR 50 20 32 P C 10. ELECTRONIC DESIGN PRACTICE 20. TEST & MEASUREMENT TECHNIQUES OR 50 20 40 P C GRAND TOTAL = 949/1500, RESULT: FIRST CLASS ORDN. 1 MARKS: T80053148 KULKARNI SAURABH CHANDRAKANT SWARALI , 71045497L , T8053048 , PICT , т80053148 01. CONTROL SYSTEMS 100 40 57 P C 11. SIGNAL CODING & ESTIMATION THEORYPP 100 40 65 P C 40 48 P C 02. DIGITAL COMMUNICATION PP 100 12. SIGNAL CODING & ESTIMATION THEORYPR 50 20 28 P C 20 30 P C 13. SYSTEM PROGRA. & OPERATING SYS. PP 100 40 71 P C 03. DIGITAL COMMUNICATION PR 50 40 50 20 44 P C 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 65 P C 14. SYSTEM PROGRA. & OPERATING SYS. TW 50 20 39 P C 100 40 62 P C 05. NETWORK SYNTHESIS & FILTER DESIGNTW 15. COMPUTER ORGANIZATION & ARCHITEC PP 100 40 56 P C 06. MICROCONTROLLERS & APPLICATION PP 100 40 56 P C 16. INDUSTRIAL MANAGEMENT 50 20 32 P C PP 100 40 61 P C 07. MICROCONTROLLERS & APPLICATION PR 17. WAVE THEORY & ANTENNA 100 40 65 P C 50 20 33 P C 08. DIGITAL SIGNAL PROCESSING PP 18. WAVE THEORY & ANTENNA PR 50 20 35 P C 50 20 40 P C 09. DIGITAL SIGNAL PROCESSING OR 19. MINI PROJECT & SEMINAR OR 50 20 20 P C 20. TEST & MEASUREMENT TECHNIQUES OR 50 20 35 P 10. ELECTRONIC DESIGN PRACTICE OR GRAND TOTAL = 942/1500, RESULT: FIRST CLASS ORDN. 1 MARKS: T80053149 MAHESHWARI SNEHAL RAVINDRA AARTI , 71045512H , T8053057 , PICT , T80053149 01. CONTROL SYSTEMS PP 100 40 40 P C 11. SIGNAL CODING & ESTIMATION THEORYPP 100 40 46 P C 02. DIGITAL COMMUNICATION 100 40 48 P C 12. SIGNAL CODING & ESTIMATION THEORYPR 50 20 26 P PΡ 03. DIGITAL COMMUNICATION 50 20 38 P C 13. SYSTEM PROGRA.& OPERATING SYS. PP 100 40 40 P C PR 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 45 P C 14. SYSTEM PROGRA. & OPERATING SYS. TW 50 20 39 P C 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 38 P C 15. COMPUTER ORGANIZATION & ARCHITEC PP 100 40 40 P C 06. MICROCONTROLLERS & APPLICATION PP 100 40 49 P C 16. INDUSTRIAL MANAGEMENT PP 100 40 30# P 07. MICROCONTROLLERS & APPLICATION PR 50 20 34 P C PP 100 40 40 P C 17. WAVE THEORY & ANTENNA PP 100 40 41 P C 50 08. DIGITAL SIGNAL PROCESSING 18. WAVE THEORY & ANTENNA 20 33 P C PR 09. DIGITAL SIGNAL PROCESSING 50 20 32 P C 19. MINI PROJECT & SEMINAR 50 20 39 P C OR OR 10. ELECTRONIC DESIGN PRACTICE OR 50 20 28 P C 20. TEST & MEASUREMENT TECHNIQUES OR 50 20 26 P C GRAND TOTAL = 752/1500, RESULT: SECOND CLASS # [0.4] ORDN. 1 MARKS:

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										REG. NO., PREVIOUS SEAT NO., O			 SEAT		• •
		·				-	-			(S OBTAINED, P/F:PASS/FAIL, C:		•			
т80	053150	MANE GAURANGI LALASAHEB				KΑ	LPANA			, 71045518G , т8053060	, P	ICT	,	т8005	3150
01.	CONTROL	SYSTEMS	PP	100	40	40	РС	1	11.	SIGNAL CODING & ESTIMATION THEOR	RYPP	100	40	52	РС
02.	DIGITAL	COMMUNICATION	PP	100	40	54	РС	1	12.	SIGNAL CODING & ESTIMATION THEOR	RYPR	50	20	37	Р
03.	DIGITAL	COMMUNICATION	PR	50	20	30	РС	1	13.	SYSTEM PROGRA.& OPERATING SYS.	PP	100	40	44	РС
04.	NETWORK	SYNTHESIS & FILTER DESI	GNPP	100	40	44	РС	1	14.	SYSTEM PROGRA.& OPERATING SYS.	TW	50	20	41	РС
05.	NETWORK	SYNTHESIS & FILTER DESI	GNTW	50	20	42	РС	1	15.	COMPUTER ORGANIZATION & ARCHITEC	C PP	100	40	43	РС
06.	MICROCO	NTROLLERS & APPLICATION	PP	100	40	57	РС	1	16.	INDUSTRIAL MANAGEMENT	PP	100	40	47	РС
07.	MICROCO	NTROLLERS & APPLICATION	PR	50	20	39	РС	-	17.	WAVE THEORY & ANTENNA	PP	100	40	41	РС
08.	DIGITAL	SIGNAL PROCESSING	PP	100	40	52	РС	-	18.	WAVE THEORY & ANTENNA	PR	50	20	29	РС
09.	DIGITAL	SIGNAL PROCESSING	OR	50	20	32	РС	-	19.	MINI PROJECT & SEMINAR	OR	50	20	38	РС
10.	ELECTRO	NIC DESIGN PRACTICE	OR	50	20	30	РС	Ź	20.	TEST & MEASUREMENT TECHNIQUES	OR	50	20	35	РС
GRAND	TOTAL =	827/1500, RESULT: HIGH	ER SEC	OND CL	ASS										
ORDN.	1 MARKS	:													
Т80	053151	MORE NISHANT PRAVIN				SH	IARADA			, 70925509」 , т8053065	, P	ICT	,	T8005	3151
01.	CONTROL	SYSTEMS	PP	100	40	56	Р		11.	SIGNAL CODING & ESTIMATION THEOR	RYPP	100	40	48	РС
02.	DIGITAL	COMMUNICATION	PP	100	40	29	F		12.	SIGNAL CODING & ESTIMATION THEOR	RYPR	50	20	30	РС
03.	DIGITAL	COMMUNICATION	PR	50	20	30	РС		13.	SYSTEM PROGRA.& OPERATING SYS.	PP	100	40	49	РС
04.	NETWORK	SYNTHESIS & FILTER DESI	GNPP	100	40	43	РС		14.	SYSTEM PROGRA.& OPERATING SYS.	TW	50	20	39	РС
05.	NETWORK	SYNTHESIS & FILTER DESI	GNTW	50	20	37	РС		15.	COMPUTER ORGANIZATION & ARCHITEC	C PP	100	40	32	F
06.	MICROCO	NTROLLERS & APPLICATION	PP	100	40	43	РС		16.	INDUSTRIAL MANAGEMENT	PP	100	40	45	Р
07.	MICROCO	NTROLLERS & APPLICATION	PR	50	20	37	РС	1	17.	WAVE THEORY & ANTENNA	PP	100	40	40	РС
08.	DIGITAL	SIGNAL PROCESSING	PP	100	40	50	РС	1	18.	WAVE THEORY & ANTENNA	PR	50	20	30	РС
09.	DIGITAL	SIGNAL PROCESSING	OR	50	20	20	РС	_	19.	MINI PROJECT & SEMINAR	OR	50	20	22	Р
10.	ELECTRO	NIC DESIGN PRACTICE	OR	50	20	33	РС	2	20.	TEST & MEASUREMENT TECHNIQUES	OR	50	20	28	РС
GRAND	TOTAL =	741/1500, RESULT: FAIL	S A.T.	K.T.											
ORDN.	1 MARKS	:													
Т80	053152	MRIDUL NAGAR				MΔ	UCNA			, 71045528D , T8053066	, P	ICT	,	T8005	3152
01.	CONTROL	SYSTEMS	PP	100	40	40	PС	1	11.	SIGNAL CODING & ESTIMATION THEOR	RYPP	100	40	57	РС
02.	DIGITAL	COMMUNICATION	PP	100	40	51	PС	1	12.	SIGNAL CODING & ESTIMATION THEOR	RYPR	50	20	35	Р
03.	DIGITAL	COMMUNICATION	PR	50	20	38	РС	1	13.	SYSTEM PROGRA.& OPERATING SYS.	PP	100	40	66	РС
04.	NETWORK	SYNTHESIS & FILTER DESI	GNPP	100	40	54	PС	1	14.	SYSTEM PROGRA.& OPERATING SYS.	TW	50	20	34	РС
05.	NETWORK	SYNTHESIS & FILTER DESI	GNTW	50	20	32	PС	1	15.	COMPUTER ORGANIZATION & ARCHITEC	C PP	100	40	63	РС
06.	MICROCO	NTROLLERS & APPLICATION	PP	100	40	63	PС	1	16.	INDUSTRIAL MANAGEMENT	PP	100	40	64	РС
07.	MICROCO	NTROLLERS & APPLICATION	PR	50	20	30	РС	1	17.	WAVE THEORY & ANTENNA	PP	100	40	56	РС
08.	DIGITAL	SIGNAL PROCESSING	PP	100	40	59	P C	1	18.	WAVE THEORY & ANTENNA	PR	50	20	32	Р
09.	DIGITAL	SIGNAL PROCESSING	OR	50	20	36	P C	1	19.	MINI PROJECT & SEMINAR	OR	50	20	38	P C
10.	ELECTRO	NIC DESIGN PRACTICE	OR	50	20	28	P C	Ź	20.	TEST & MEASUREMENT TECHNIQUES	OR	50	20	30	P C
GRAND	TOTAL =	906/1500, RESULT: FIRS	T CLAS	S											
ORDN.	1 MARKS	:													

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , т80053153 NANDA , 71129934L , T8053072 , PICT T80053153 NEVASE PRAJAKTA MARUTI 01. CONTROL SYSTEMS PP 100 40 61 P C 11. SIGNAL CODING & ESTIMATION THEORYPP 100 40 67 PC 02. DIGITAL COMMUNICATION PP 100 40 53 P C 12. SIGNAL CODING & ESTIMATION THEORYPR 50 20 32 P C 03. DIGITAL COMMUNICATION 50 20 26 P C 13. SYSTEM PROGRA. & OPERATING SYS. PP 100 40 68 P C PR 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 40 P C 14. SYSTEM PROGRA. & OPERATING SYS. TW 50 20 40 P C 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 41 P C 15. COMPUTER ORGANIZATION & ARCHITEC PP 100 40 67 P C 100 40 50 P C 16. INDUSTRIAL MANAGEMENT 100 40 66 P C 06. MICROCONTROLLERS & APPLICATION PP PP 50 20 32 P C 100 40 64 P C 07. MICROCONTROLLERS & APPLICATION PR PP 17. WAVE THEORY & ANTENNA 08. DIGITAL SIGNAL PROCESSING PP 100 40 60 P C 50 20 33 P C 18. WAVE THEORY & ANTENNA PR 50 20 30 P C 50 20 41 P C 09. DIGITAL SIGNAL PROCESSING OR 19. MINI PROJECT & SEMINAR OR OR 50 20 35 P C 10. ELECTRONIC DESIGN PRACTICE 20. TEST & MEASUREMENT TECHNIQUES OR 50 20 30 P GRAND TOTAL = 936/1500, RESULT: FIRST CLASS ORDN. 1 MARKS: T80053154 NITESH KR SINGH RITA , 70801532J , T8053125 , PICT , т80053154 01. CONTROL SYSTEMS 100 40 50 P C 11. SIGNAL CODING & ESTIMATION THEORYPP 100 40 32 F 100 40 50 20 22 P 02. DIGITAL COMMUNICATION PP AA F 12. SIGNAL CODING & ESTIMATION THEORYPR 20 23 P C 13. SYSTEM PROGRA. OPERATING SYS. PP 100 40 AA F 03. DIGITAL COMMUNICATION PR 50 40 44 P C 50 20 26 P C 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 14. SYSTEM PROGRA. & OPERATING SYS. TW 50 20 22 P C 100 40 46 P C 05. NETWORK SYNTHESIS & FILTER DESIGNTW 15. COMPUTER ORGANIZATION & ARCHITEC PP 100 40 44 P C 100 40 49 P C 06. MICROCONTROLLERS & APPLICATION PP 16. INDUSTRIAL MANAGEMENT PP 50 20 28 P C 100 40 AA F 07. MICROCONTROLLERS & APPLICATION PR 17. WAVE THEORY & ANTENNA 100 40 40 P C 50 20 25 P C 08. DIGITAL SIGNAL PROCESSING PP 18. WAVE THEORY & ANTENNA PR 50 20 26 P C 50 20 20 P 09. DIGITAL SIGNAL PROCESSING OR 19. MINI PROJECT & SEMINAR OR 50 20 28 P C 20. TEST & MEASUREMENT TECHNIQUES OR 50 20 30 P C 10. ELECTRONIC DESIGN PRACTICE GRAND TOTAL = 555/1500, RESULT: FAILS ORDN. 1 MARKS: T80053155 PAWAR PRADIPKUMAR RAOSAHEB , т80053155 SANGITA , 71045572M , T8053081 , PICT 01. CONTROL SYSTEMS PP 100 40 59 P C 11. SIGNAL CODING & ESTIMATION THEORYPP 100 40 57 P C 02. DIGITAL COMMUNICATION 100 40 58 P C 12. SIGNAL CODING & ESTIMATION THEORYPR 50 20 36 P C PΡ 03. DIGITAL COMMUNICATION 50 20 32 P C 13. SYSTEM PROGRA. & OPERATING SYS. PP 100 40 59 P C PR 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 65 P C 14. SYSTEM PROGRA. & OPERATING SYS. TW 50 20 41 P C 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 36 P C 15. COMPUTER ORGANIZATION & ARCHITEC PP 100 40 58 P 06. MICROCONTROLLERS & APPLICATION PP 100 40 40 P C 16. INDUSTRIAL MANAGEMENT PP 100 40 58 P C 07. MICROCONTROLLERS & APPLICATION PR 50 20 28 P C PP 100 40 57 P C 17. WAVE THEORY & ANTENNA 100 40 55 P C 50 20 35 P C 08. DIGITAL SIGNAL PROCESSING 18. WAVE THEORY & ANTENNA PR 09. DIGITAL SIGNAL PROCESSING 50 20 38 P C 19. MINI PROJECT & SEMINAR 50 20 35 P C OR OR OR 50 20 33 P C 10. ELECTRONIC DESIGN PRACTICE 20. TEST & MEASUREMENT TECHNIQUES OR 50 20 40 P C GRAND TOTAL = 920/1500, RESULT: FIRST CLASS ORDN. 1 MARKS:

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DAIE : 31 JAN. 2013							R TECHNOLOGY, PUNE.				-	-
NOTE: FIRST LINE : SEAT NO., NAME										SEAT		
OTHER LINES: HEAD OF PASSING,	MAX	. MARK	S, M	IN. P	ASS MARKS	, MAR	KS OBTAINED, P/F:PASS/FAIL, C:	PREVI	OUS CAF	RRY O	VER	
T80053156 PAWAR SURAJ SUBHASH					DYA		, 71045573к , т8053082	-		-	т8005	
01. CONTROL SYSTEMS	PP		40		PC		SIGNAL CODING & ESTIMATION THEO			40		РС
02. DIGITAL COMMUNICATION	PP	100	40		PC		SIGNAL CODING & ESTIMATION THEO	RYPR	50	20	35	РС
03. DIGITAL COMMUNICATION	PR	50	20	20	Р	13.	SYSTEM PROGRA.& OPERATING SYS.	PP	100	40	40	РС
04. NETWORK SYNTHESIS & FILTER DESI	GNPP	100	40	43	PC	14.	SYSTEM PROGRA.& OPERATING SYS.	TW	50	20	36	РС
05. NETWORK SYNTHESIS & FILTER DESI	GNTW	50	20	31	PC	15.	COMPUTER ORGANIZATION & ARCHITE	C PP	100	40	40	РС
06. MICROCONTROLLERS & APPLICATION	PP	100	40	40	Р	16.	INDUSTRIAL MANAGEMENT	PP	100	40	45	РС
07. MICROCONTROLLERS & APPLICATION	PR	50	20	28	PC	17.	WAVE THEORY & ANTENNA	PP	100	40	43	Р
08. DIGITAL SIGNAL PROCESSING	PP	100	40	46	PC	18.	WAVE THEORY & ANTENNA	PR	50	20	34	Р
09. DIGITAL SIGNAL PROCESSING	OR	50	20	20	PC	19.	MINI PROJECT & SEMINAR	OR	50	20	41	РС
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	27	P C	20.	TEST & MEASUREMENT TECHNIQUES	OR	50	20	33	Р
GRAND TOTAL = 732/1500, RESULT: PASS	CLASS	S										
ORDN. 1 MARKS :												
T80053157 PAWAR VISHAL ALIAS DIGV	/IJAY E	BALASA	HEB	VI	JAYANTI		, 71070203г , т8053083	, P	ICT	,	т8005	3157
01. CONTROL SYSTEMS	PP	100	40	55	РC	11.	SIGNAL CODING & ESTIMATION THEO	RYPP	100	40	AA	F
02. DIGITAL COMMUNICATION	PP	100	40	AA	F	12.	SIGNAL CODING & ESTIMATION THEO	RYPR	50	20	AA	F
03. DIGITAL COMMUNICATION	PR	50	20	44	РС	13.	SYSTEM PROGRA.& OPERATING SYS.	PP	100	40	AA	F
04. NETWORK SYNTHESIS & FILTER DESI	GNPP	100	40	AA	F	14.	SYSTEM PROGRA.& OPERATING SYS.	TW	50	20	AA	F
05. NETWORK SYNTHESIS & FILTER DESI	GNTW	50	20	27	РС	15.	COMPUTER ORGANIZATION & ARCHITE	C PP	100	40	AA	F
06. MICROCONTROLLERS & APPLICATION	PP	100	40	40	РC		INDUSTRIAL MANAGEMENT	PP	100	40	AA	F
07. MICROCONTROLLERS & APPLICATION	PR	50	20	30	РC	17.	WAVE THEORY & ANTENNA	PP	100	40	AA	F
08. DIGITAL SIGNAL PROCESSING	PP	100	40	AA			WAVE THEORY & ANTENNA	PR	50	20	AA	
09. DIGITAL SIGNAL PROCESSING	OR	50	20		Р С		MINI PROJECT & SEMINAR	OR	50	20	AA	
	OR	50			P C		TEST & MEASUREMENT TECHNIQUES	_	50	20	AA	
GRAND TOTAL = 237/1500, RESULT: FAIL	• • • • • • • • • • • • • • • • • • • •	30	20	2.1	1 C	20.	TEST & MEASONEMENT TECHNIQUES		ULT RES			-
ORDN. 1 MARKS:	.5							KLS	OLI KL	JLIVL	D TOK	DICEC
T80053158 PRASHANT POPAT JAGTAP					KTA		, 70801568K , T8053128				т8005	
01. CONTROL SYSTEMS	PP	100	40		P C	11	SIGNAL CODING & ESTIMATION THEO	•		, 40		P C
	PP	100			P C		SIGNAL CODING & ESTIMATION THEO		50			
02. DIGITAL COMMUNICATION			40							20		P C
03. DIGITAL COMMUNICATION	PR	50 100	20		P C	_	SYSTEM PROGRA. OPERATING SYS.	PP		40		P C
04. NETWORK SYNTHESIS & FILTER DESI		100	40		P C		SYSTEM PROGRA. OPERATING SYS.	TW	50	20		P C
05. NETWORK SYNTHESIS & FILTER DESI		50	20		P C		COMPUTER ORGANIZATION & ARCHITE		100	40		P C
06. MICROCONTROLLERS & APPLICATION	PP 	100	40		P C	_	INDUSTRIAL MANAGEMENT	PP 	100	40		P C
07. MICROCONTROLLERS & APPLICATION	PR 	50	20		P C		WAVE THEORY & ANTENNA	PP 	100	40		P C
08. DIGITAL SIGNAL PROCESSING	PP	100	40	17			WAVE THEORY & ANTENNA	PR	50	20		РС
09. DIGITAL SIGNAL PROCESSING	OR	50	20		PC		MINI PROJECT & SEMINAR	OR	50	20	20	
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	21	PC	20.	TEST & MEASUREMENT TECHNIQUES	OR	50	20	30	РС
GRAND TOTAL = $642/1500$, RESULT: FAIL	S A.T.	.K.T.										
ORDN. 1 MARKS :												

DATE: 31 JAN. 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 53 (422)

NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71129939м , T8053093 , PICT , т80053159 T80053159 SHENDKAR SUSHMA HANUMANT REKHA 47 P C 01. CONTROL SYSTEMS 100 40 11. SIGNAL CODING & ESTIMATION THEORYPP 100 40 40 P C PP 02. DIGITAL COMMUNICATION 100 40 49 P C 12. SIGNAL CODING & ESTIMATION THEORYPR 32 P C 50 20 03. DIGITAL COMMUNICATION 50 20 24 P C 13. SYSTEM PROGRA. & OPERATING SYS. PP 100 40 44 P C PR 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 40 P C 14. SYSTEM PROGRA. & OPERATING SYS. TW 50 20 42 P C 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 45 P C 15. COMPUTER ORGANIZATION & ARCHITEC PP 100 40 43 P C 100 40 43 P C 16. INDUSTRIAL MANAGEMENT 100 40 42 P C 06. MICROCONTROLLERS & APPLICATION PP PР 100 40 29 F 07. MICROCONTROLLERS & APPLICATION PR 50 20 30 P C PP 17. WAVE THEORY & ANTENNA 08. DIGITAL SIGNAL PROCESSING 100 40 57 P C 50 20 22 P C 18. WAVE THEORY & ANTENNA PR 50 20 30 P C 50 20 45 P C 09. DIGITAL SIGNAL PROCESSING OR 19. MINI PROJECT & SEMINAR OR 50 20 27 P C 10. ELECTRONIC DESIGN PRACTICE 20. TEST & MEASUREMENT TECHNIQUES OR 50 20 31 P C OR GRAND TOTAL = 762/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS: T80053160 SONU SINGH USHA , 71045633G , T8053097 , PICT , т80053160 01. CONTROL SYSTEMS 100 40 29 F 02. DIGITAL COMMUNICATION 100 40 21 F PP 03. DIGITAL COMMUNICATION 20 07 F PR 50 40 40 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 50 20 31 P C 05. NETWORK SYNTHESIS & FILTER DESIGNTW 100 40 40 P 06. MICROCONTROLLERS & APPLICATION PP 50 20 11 F 07. MICROCONTROLLERS & APPLICATION PR 100 40 31 F 08. DIGITAL SIGNAL PROCESSING 50 20 35 P 09. DIGITAL SIGNAL PROCESSING OR 10. ELECTRONIC DESIGN PRACTICE OR 50 20 14 F FIRST TERM TOTAL = 259/750. ORDN. 1 MARKS: , T8053098 , PICT T80053161 SUBAGHYA MAHAJAN , 71045635C , T80053161 VIJAY 01. CONTROL SYSTEMS 100 40 45 P C 11. SIGNAL CODING & ESTIMATION THEORYPP 100 40 40 P C PP 100 40 41 P 02. DIGITAL COMMUNICATION 12. SIGNAL CODING & ESTIMATION THEORYPR 50 20 34 P 03. DIGITAL COMMUNICATION 50 20 30 P C 13. SYSTEM PROGRA.& OPERATING SYS. PP 100 40 46 P C 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 40 P C 14. SYSTEM PROGRA. OPERATING SYS. TW 50 20 38 P C 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 26 P C 15. COMPUTER ORGANIZATION & ARCHITEC PP 100 40 44 P C 06. MICROCONTROLLERS & APPLICATION PP 100 40 55 P C 16. INDUSTRIAL MANAGEMENT 100 40 40 P C PP 07. MICROCONTROLLERS & APPLICATION PR 50 20 32 P C 100 40 42 P C 17. WAVE THEORY & ANTENNA PP 50 20 21 P C 08. DIGITAL SIGNAL PROCESSING 100 40 49 P C 18. WAVE THEORY & ANTENNA 09. DIGITAL SIGNAL PROCESSING 50 20 33 P C 19. MINI PROJECT & SEMINAR 50 20 42 P C OR OR 50 20 22 P C 10. ELECTRONIC DESIGN PRACTICE OR 20. TEST & MEASUREMENT TECHNIQUES OR 50 20 35 P C GRAND TOTAL = 755/1500, RESULT: SECOND CLASS ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

Т80	053162	THORAT TANVI TATYASAHEB				SU	LOCHANA	, 71045650G , T8053102 , PICT , T80053	3162
01.	CONTROL	SYSTEMS	PP	100	40	40	P C	11. SIGNAL CODING & ESTIMATION THEORYPP 100 40 56	РС
02.	DIGITAL	COMMUNICATION	PP	100	40	45	P C	12. SIGNAL CODING & ESTIMATION THEORYPR 50 20 22	РС
03.	DIGITAL	COMMUNICATION	PR	50	20	39	P C	13. SYSTEM PROGRA.& OPERATING SYS. PP 100 40 52	РС
04.	NETWORK	SYNTHESIS & FILTER DESI	GNPP	100	40	45	P C	14. SYSTEM PROGRA.& OPERATING SYS. TW 50 20 43	P C
05.	NETWORK	SYNTHESIS & FILTER DESI	GNTW	50	20	37	P C	15. COMPUTER ORGANIZATION & ARCHITEC PP 100 40 44	РС
06.	MICROCO	NTROLLERS & APPLICATION	PP	100	40	40	P C	16. INDUSTRIAL MANAGEMENT PP 100 40 56	РС
07.	MICROCO	NTROLLERS & APPLICATION	PR	50	20	23	P C	17. WAVE THEORY & ANTENNA PP 100 40 40	РС
08.	DIGITAL	SIGNAL PROCESSING	PP	100	40	45	P C	18. WAVE THEORY & ANTENNA PR 50 20 28	Р
09.	DIGITAL	SIGNAL PROCESSING	OR	50	20	38	P C	19. MINI PROJECT & SEMINAR OR 50 20 40	РС
10.	ELECTRO	NIC DESIGN PRACTICE	OR	50	20	32	P C	20. TEST & MEASUREMENT TECHNIQUES OR 50 20 35	P C
GRAND	TOTAL =	800/1500 RESULT: SECO	ND CLA	55					

GRAND TOTAL = 800/1500, RESULT: SECOND CLASS

ORDN. 1 MARKS:

T80053163 WATHORE SANKET UTTAMRAO KAVITA . 71045665E . T8053106 . PICT . T80053163

T80053163 WATHORE SANKET UTTAMRAO				KAV	/ITA	, 71045665E , т8053106 ,	PICT	, ٦	8005	3163
01. CONTROL SYSTEMS	PP	100	40	40	РС	11. SIGNAL CODING & ESTIMATION THEORYPP	100	40	41	P C
02. DIGITAL COMMUNICATION	PP	100	40	32#	Р	12. SIGNAL CODING & ESTIMATION THEORYPR	50	20	34	P C
03. DIGITAL COMMUNICATION	PR	50	20	29	РС	13. SYSTEM PROGRA. & OPERATING SYS. PP	100	40	52	P C
04. NETWORK SYNTHESIS & FILTER DESIGNATION	GNPP	100	40	47	РС	14. SYSTEM PROGRA.& OPERATING SYS. TW	50	20	40	P C
05. NETWORK SYNTHESIS & FILTER DESIG	GNTW	50	20	32	РС	15. COMPUTER ORGANIZATION & ARCHITEC PP	100	40	40	P C
06. MICROCONTROLLERS & APPLICATION	PP	100	40	41	РС	16. INDUSTRIAL MANAGEMENT PP	100	40	47	P C
07. MICROCONTROLLERS & APPLICATION	PR	50	20	26	РС	17. WAVE THEORY & ANTENNA PP	100	40	40	P C
08. DIGITAL SIGNAL PROCESSING	PP	100	40	43	РС	18. WAVE THEORY & ANTENNA PR	50	20	37	P C
09. DIGITAL SIGNAL PROCESSING	OR	50	20	26	P C	19. MINI PROJECT & SEMINAR OR	50	20	22	Р

20. TEST & MEASUREMENT TECHNIQUES OR 50 20 33 P C

GRAND TOTAL = 727/1500, RESULT: PASS CLASS # [0.4]

10. ELECTRONIC DESIGN PRACTICE

ORDN. 1 MARKS :

OR 50 20 25 P C

UNIVERSITY OF PUNE ,T.E.(2008 PAT.)(COMPUTER) EXAMINATION NOV. 2012 DATE: 31 JAN. 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 01 (424) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71100710B , , PICT , T80054201 T80054201 AAMIR AHMED ABOOBAKAR SADIQUE YASMEEN 01. DATABASE MANAGEMENT SYSTEMS 100 40 29 F PP 02. DATA COMMUNICATION 100 40 61 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 48 P 04. DIGITAL SIGNAL PROCESSING 100 40 42 P 05. THEORY OF COMPUTATION 100 40 47 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 20 34 P 50 20 37 P 07. RDBMS & VISUAL PROGRAMMING LAB. PR 08. SIGNAL PROCESSING LABORATORY 25 10 18 P TW 09. SIGNAL PROCESSING LABORATORY 50 20 21 P OR 25 10 19 P 10. HARDWARE LABORATORY TW 11. HARDWARE LABORATORY 50 20 10 F PR FIRST TERM TOTAL = 366/750. ORDN. 1 MARKS: T80054202 ABNAVE OMKAR RAJENDRA , 71045358C , , PICT , T80054202 SHOBHA 01. DATABASE MANAGEMENT SYSTEMS 100 40 51 P 100 40 02. DATA COMMUNICATION AA F 100 40 AA F 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 AA F 04. DIGITAL SIGNAL PROCESSING 05. THEORY OF COMPUTATION 100 40 AA F 50 20 21 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 AA F 25 10 10 P 08. SIGNAL PROCESSING LABORATORY TW 09. SIGNAL PROCESSING LABORATORY OR 50 20 09 F 25 10 15 P 10. HARDWARE LABORATORY TW 11. HARDWARE LABORATORY 50 20 AA F PR FIRST TERM TOTAL = 106/750. ORDN. 1 MARKS: , 71100728E T80054203 AMRUTKAR SNEHAL RAJENDRA JYOTI , PICT , T80054203 01. DATABASE MANAGEMENT SYSTEMS 100 40 52 P 02. DATA COMMUNICATION PP 100 40 74 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 64 P 04. DIGITAL SIGNAL PROCESSING 100 40 65 P 05. THEORY OF COMPUTATION 100 40 51 P 50 20 43 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 44 P 08. SIGNAL PROCESSING LABORATORY 25 10 22 P TW 50 20 30 P 09. SIGNAL PROCESSING LABORATORY 10. HARDWARE LABORATORY 25 10 23 P TW 11. HARDWARE LABORATORY 50 20 38 P PR FIRST TERM TOTAL = 506/750. ORDN. 1 MARKS:

UNIVERSITY OF PUNE ,T.E.(2008 PAT.)(COMPUTER) EXAMINATION NOV. 2012

DATE: 31 JAN. 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 02 (425) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER MADHURI , 71100735H , , PICT , T80054204 T80054204 AVHAD PRIYA SANTOSH 01. DATABASE MANAGEMENT SYSTEMS 100 40 60 P 02. DATA COMMUNICATION 100 40 67 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 78 P 04. DIGITAL SIGNAL PROCESSING 100 40 76 P 05. THEORY OF COMPUTATION 100 40 56 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 20 34 P 50 20 39 P 07. RDBMS & VISUAL PROGRAMMING LAB. PR 08. SIGNAL PROCESSING LABORATORY 25 10 16 P TW 09. SIGNAL PROCESSING LABORATORY 50 20 37 P OR 25 10 18 P 10. HARDWARE LABORATORY TW 11. HARDWARE LABORATORY 50 20 32 P PR FIRST TERM TOTAL = 513/750. ORDN. 1 MARKS: T80054205 BALI SONIYA NARAYAN , 71100740D , , PICT , T80054205 SHOBHA 01. DATABASE MANAGEMENT SYSTEMS 100 40 42 P 100 40 62 P 02. DATA COMMUNICATION PP 100 40 54 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 40 P 04. DIGITAL SIGNAL PROCESSING 05. THEORY OF COMPUTATION 100 40 40 P 50 20 36 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 20 43 P 07. RDBMS & VISUAL PROGRAMMING LAB. PR 25 10 18 P 08. SIGNAL PROCESSING LABORATORY TW 09. SIGNAL PROCESSING LABORATORY OR 50 20 07 F 25 10 21 P 10. HARDWARE LABORATORY TW 11. HARDWARE LABORATORY 50 20 34 P PR FIRST TERM TOTAL = 397/750. ORDN. 1 MARKS: , 71100741B T80054206 BALIWANT SOURABH SANJAY , PICT , т80054206 KALPANA 01. DATABASE MANAGEMENT SYSTEMS 100 40 43 P PP 02. DATA COMMUNICATION PP 100 40 61 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 45 P 04. DIGITAL SIGNAL PROCESSING 100 40 44 P 05. THEORY OF COMPUTATION 100 40 44 P 50 20 33 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 00 F 08. SIGNAL PROCESSING LABORATORY 25 10 16 P TW 50 20 22 P 09. SIGNAL PROCESSING LABORATORY 10. HARDWARE LABORATORY 25 10 18 P TW 11. HARDWARE LABORATORY 50 20 30 P PR FIRST TERM TOTAL = 356/750. ORDN. 1 MARKS:

UNIVERSITY OF PUNE ,T.E.(2008 PAT.)(COMPUTER) EXAMINATION NOV. 2012 DATE: 31 JAN. 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 03 (426) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71239286G , , PICT , T80054207 T80054207 BAPAT RUTUGANDHA SUNIL ANAGHA 01. DATABASE MANAGEMENT SYSTEMS 100 40 55 P PP 02. DATA COMMUNICATION 100 40 62 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 65 P 04. DIGITAL SIGNAL PROCESSING 100 40 43 P 05. THEORY OF COMPUTATION 100 40 57 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 20 37 P 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 30 P 08. SIGNAL PROCESSING LABORATORY 25 10 18 P TW 09. SIGNAL PROCESSING LABORATORY 50 20 22 P OR 25 10 21 P 10. HARDWARE LABORATORY TW 11. HARDWARE LABORATORY 50 20 26 P PR FIRST TERM TOTAL = 436/750. ORDN. 1 MARKS: T80054208 BAPAT SWARNADEE SANDEEP , 71100742L , , PICT , т80054208 RIJUTA 01. DATABASE MANAGEMENT SYSTEMS 100 40 58 P 100 40 73 P 02. DATA COMMUNICATION 100 40 70 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 61 P 04. DIGITAL SIGNAL PROCESSING 05. THEORY OF COMPUTATION 100 40 58 P 50 20 38 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 20 38 P 07. RDBMS & VISUAL PROGRAMMING LAB. PR 25 10 22 P 08. SIGNAL PROCESSING LABORATORY TW 09. SIGNAL PROCESSING LABORATORY 50 20 39 P ΩR 25 10 22 P 10. HARDWARE LABORATORY TW 11. HARDWARE LABORATORY 50 20 40 P PR FIRST TERM TOTAL = 519/750. ORDN. 1 MARKS: , 71239287E T80054209 BARDE KAJAL NARAYAN , PICT , т80054209 SULOCHANA 01. DATABASE MANAGEMENT SYSTEMS 100 40 57 P 02. DATA COMMUNICATION PP 100 40 62 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 72 P 04. DIGITAL SIGNAL PROCESSING 100 40 58 P 05. THEORY OF COMPUTATION 100 40 51 P 50 20 42 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 45 P 08. SIGNAL PROCESSING LABORATORY 25 10 22 P TW 50 20 38 P 09. SIGNAL PROCESSING LABORATORY 10. HARDWARE LABORATORY 25 10 22 P TW 11. HARDWARE LABORATORY 50 20 42 P PR FIRST TERM TOTAL = 511/750.

UNIVERSITY OF PUNE ,T.E.(2008 PAT.)(COMPUTER) EXAMINATION NOV. 2012 DATE: 31 JAN. 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 04 (427) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71239288C , , PICT , T80054210 T80054210 BARDE KOMAL NARAYAN SULOCHANA 01. DATABASE MANAGEMENT SYSTEMS 100 40 56 P 02. DATA COMMUNICATION 100 40 60 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 76 P 04. DIGITAL SIGNAL PROCESSING 100 40 62 P 05. THEORY OF COMPUTATION 100 40 42 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 20 42 P 50 20 43 P 07. RDBMS & VISUAL PROGRAMMING LAB. PR 08. SIGNAL PROCESSING LABORATORY 25 10 21 P TW 09. SIGNAL PROCESSING LABORATORY 50 20 40 P OR 25 10 22 P 10. HARDWARE LABORATORY TW 11. HARDWARE LABORATORY 50 20 38 P PR FIRST TERM TOTAL = 502/750. ORDN. 1 MARKS: T80054211 BAWALE DEVEN ABHAY SANDHYA , 71100745E , , PICT , т80054211 01. DATABASE MANAGEMENT SYSTEMS 100 40 40 P 02. DATA COMMUNICATION 100 40 53 P PP 100 40 53 P 03. MICROPROCESSORS & MICROCONTROLLERPP 04. DIGITAL SIGNAL PROCESSING 100 40 40 P 05. THEORY OF COMPUTATION 100 40 23 F 50 20 40 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 20 44 P 07. RDBMS & VISUAL PROGRAMMING LAB. PR 25 10 21 P 08. SIGNAL PROCESSING LABORATORY TW 09. SIGNAL PROCESSING LABORATORY OR 50 20 42 P 25 10 23 P 10. HARDWARE LABORATORY TW 11. HARDWARE LABORATORY 50 20 36 P PR FIRST TERM TOTAL = 415/750. ORDN. 1 MARKS: , 71100746C , PICT T80054212 BELOTE AMOL PRABHAKAR , т80054212 GANGUBAI 01. DATABASE MANAGEMENT SYSTEMS PP 100 40 48 P 02. DATA COMMUNICATION PP 100 40 67 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 57 P 04. DIGITAL SIGNAL PROCESSING 100 40 45 P 05. THEORY OF COMPUTATION 100 40 41 P 50 20 31 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 32 P 08. SIGNAL PROCESSING LABORATORY 25 10 18 P TW 50 20 25 P 09. SIGNAL PROCESSING LABORATORY 10. HARDWARE LABORATORY 25 10 16 P TW 11. HARDWARE LABORATORY 50 20 24 P PR FIRST TERM TOTAL = 404/750.

UNIVERSITY OF PUNE ,T.E.(2008 PAT.)(COMPUTER) EXAMINATION NOV. 2012 DATE: 31 JAN. 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 05 (428) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71239289M , , PICT , T80054213 T80054213 BHADKE ASHUTOSH CHANDRAKANT SUNANDA 01. DATABASE MANAGEMENT SYSTEMS 100 40 51 P 02. DATA COMMUNICATION 100 40 79 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 75 P 04. DIGITAL SIGNAL PROCESSING 100 40 66 P 05. THEORY OF COMPUTATION 100 40 55 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 20 33 P 50 20 37 P 07. RDBMS & VISUAL PROGRAMMING LAB. PR 08. SIGNAL PROCESSING LABORATORY 25 10 17 P TW 09. SIGNAL PROCESSING LABORATORY 50 20 37 P OR 25 10 19 P 10. HARDWARE LABORATORY TW 11. HARDWARE LABORATORY 50 20 22 P PR FIRST TERM TOTAL = 491/750. ORDN. 1 MARKS: T80054214 BHANDARI RUHI BABU , 71239290E , , PICT , T80054214 SMITA 01. DATABASE MANAGEMENT SYSTEMS PP 100 40 51 P 100 40 75 P 02. DATA COMMUNICATION PP 100 40 70 P 03. MICROPROCESSORS & MICROCONTROLLERPP 04. DIGITAL SIGNAL PROCESSING 100 40 40 P 05. THEORY OF COMPUTATION 100 40 50 P 50 20 40 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 20 38 P 07. RDBMS & VISUAL PROGRAMMING LAB. PR 25 10 19 P 08. SIGNAL PROCESSING LABORATORY TW 09. SIGNAL PROCESSING LABORATORY OR 50 20 08 F 25 10 21 P 10. HARDWARE LABORATORY TW 11. HARDWARE LABORATORY 50 20 40 P PR FIRST TERM TOTAL = 452/750. ORDN. 1 MARKS: , 71239291C T80054215 BHANDARI YASHASHRI VASANT RANJANA , PICT , т80054215 01. DATABASE MANAGEMENT SYSTEMS 100 40 55 P 02. DATA COMMUNICATION PP 100 40 71 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 69 P 04. DIGITAL SIGNAL PROCESSING 100 40 42 P 05. THEORY OF COMPUTATION 100 40 45 P 50 20 38 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 39 P 08. SIGNAL PROCESSING LABORATORY 25 10 16 P TW 50 20 40 P 09. SIGNAL PROCESSING LABORATORY 10. HARDWARE LABORATORY 25 10 18 P TW 11. HARDWARE LABORATORY 50 20 35 P PR FIRST TERM TOTAL = 468/750.

ORDN. 1 MARKS :

UNIVERSITY OF PUNE ,T.E.(2008 PAT.)(COMPUTER) EXAMINATION NOV. 2012 DATE: 31 JAN. 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 06 (429) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER KOKILA , 70701380B , , PICT , T80054216 T80054216 BHOMBE AMOL SONAJI 01. DATABASE MANAGEMENT SYSTEMS 100 40 AA F 02. DATA COMMUNICATION 100 40 55 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 52 P 04. DIGITAL SIGNAL PROCESSING 100 40 29 F 05. THEORY OF COMPUTATION 100 40 42 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 20 21 P 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 AA F 08. SIGNAL PROCESSING LABORATORY 25 10 13 P TW 09. SIGNAL PROCESSING LABORATORY 50 20 21 P OR 25 10 13 P 10. HARDWARE LABORATORY TW 11. HARDWARE LABORATORY 50 20 05 F PR FIRST TERM TOTAL = 251/750. ORDN. 1 MARKS: T80054217 BHURKE SHWETA MILIND , 71100751K , , PICT , т80054217 MADHURA PP 100 40 50 P 01. DATABASE MANAGEMENT SYSTEMS 02. DATA COMMUNICATION 100 40 66 P 100 40 62 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 40 P 04. DIGITAL SIGNAL PROCESSING 05. THEORY OF COMPUTATION 100 40 46 P 50 20 42 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 20 42 P 07. RDBMS & VISUAL PROGRAMMING LAB. PR 25 10 22 P 08. SIGNAL PROCESSING LABORATORY TW 09. SIGNAL PROCESSING LABORATORY OR 50 20 27 P 25 10 23 P 10. HARDWARE LABORATORY TW 11. HARDWARE LABORATORY 50 20 38 P PR FIRST TERM TOTAL = 458/750. ORDN. 1 MARKS: , 71100762E T80054218 CHAUDHAR VISHAL SHESHRAO , PICT , т80054218 SAVITA 01. DATABASE MANAGEMENT SYSTEMS 100 40 52 P PP 02. DATA COMMUNICATION PP 100 40 72 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 55 P 04. DIGITAL SIGNAL PROCESSING 100 40 42 P 05. THEORY OF COMPUTATION 100 40 32 F 50 20 24 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 34 P 08. SIGNAL PROCESSING LABORATORY 25 10 16 P TW 50 20 30 P 09. SIGNAL PROCESSING LABORATORY 10. HARDWARE LABORATORY 25 10 18 P TW 11. HARDWARE LABORATORY 50 20 24 P PR FIRST TERM TOTAL = 399/750. ORDN. 1 MARKS:

UNIVERSITY OF PUNE ,T.E.(2008 PAT.)(COMPUTER) EXAMINATION NOV. 2012 DATE: 31 JAN. 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 07 (430) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71100763C , , PICT , T80054219 T80054219 CHAUDHARI POOJA DHANRAJ DNYNESHWARI 01. DATABASE MANAGEMENT SYSTEMS PP 100 40 53 P 02. DATA COMMUNICATION 100 40 75 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 63 P 04. DIGITAL SIGNAL PROCESSING 100 40 51 P 05. THEORY OF COMPUTATION 100 40 54 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 20 38 P 50 20 37 P 07. RDBMS & VISUAL PROGRAMMING LAB. PR 08. SIGNAL PROCESSING LABORATORY 25 10 21 P TW 09. SIGNAL PROCESSING LABORATORY 50 20 33 P OR 25 10 21 P 10. HARDWARE LABORATORY TW 11. HARDWARE LABORATORY 50 20 20 P PR FIRST TERM TOTAL = 466/750. ORDN. 1 MARKS: T80054220 CHAVAN KARISHMA SANJAY SHARADA , 71239292M , , PICT , т80054220 01. DATABASE MANAGEMENT SYSTEMS PP 100 40 60 P 100 40 72 P 02. DATA COMMUNICATION 100 40 72 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 60 P 04. DIGITAL SIGNAL PROCESSING 05. THEORY OF COMPUTATION 100 40 46 P 50 20 36 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 20 38 P 07. RDBMS & VISUAL PROGRAMMING LAB. PR 25 10 19 P 08. SIGNAL PROCESSING LABORATORY TW 09. SIGNAL PROCESSING LABORATORY OR 50 20 39 P 25 10 20 P 10. HARDWARE LABORATORY TW 11. HARDWARE LABORATORY 50 20 36 P PR FIRST TERM TOTAL = 498/750. ORDN. 1 MARKS: , 71239293к , PICT T80054221 CHIYA IFFA IMTIAZ **FAMIDA** , т80054221 01. DATABASE MANAGEMENT SYSTEMS 100 40 48 P 02. DATA COMMUNICATION PP 100 40 53 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 43 P 04. DIGITAL SIGNAL PROCESSING 100 40 45 P 05. THEORY OF COMPUTATION 100 40 40 P 50 20 35 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 40 P 08. SIGNAL PROCESSING LABORATORY 25 10 18 P TW 50 20 39 P 09. SIGNAL PROCESSING LABORATORY 10. HARDWARE LABORATORY 25 10 19 P TW 11. HARDWARE LABORATORY 50 20 32 P PR FIRST TERM TOTAL = 412/750.

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ORDN. 1 MARKS:

DATE: 31 JAN. 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 09 (432)

NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER LATA , 71100774J , , PICT , T80054225 T80054225 DAMSE SAYALI LAXMAN 100 40 40 P 01. DATABASE MANAGEMENT SYSTEMS 02. DATA COMMUNICATION 100 40 40 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 43 P 04. DIGITAL SIGNAL PROCESSING 100 40 20 F 05. THEORY OF COMPUTATION 100 40 40 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 20 31 P 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 15 F 08. SIGNAL PROCESSING LABORATORY 25 10 18 P TW 09. SIGNAL PROCESSING LABORATORY 50 20 28 P OR 25 10 17 P 10. HARDWARE LABORATORY TW 11. HARDWARE LABORATORY 50 20 08 F PR FIRST TERM TOTAL = 300/750. ORDN. 1 MARKS: T80054226 DEBOJEET CHATTERJEE , 71100775G , , PICT , т80054226 SHIKHA 01. DATABASE MANAGEMENT SYSTEMS 100 40 61 P 02. DATA COMMUNICATION 100 40 74 P 100 40 65 P 03. MICROPROCESSORS & MICROCONTROLLERPP 04. DIGITAL SIGNAL PROCESSING 100 40 52 P 05. THEORY OF COMPUTATION 100 40 50 P 50 20 36 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 20 42 P 07. RDBMS & VISUAL PROGRAMMING LAB. PR 25 10 18 P 08. SIGNAL PROCESSING LABORATORY TW 09. SIGNAL PROCESSING LABORATORY OR 50 20 37 P 25 10 20 P 10. HARDWARE LABORATORY TW 11. HARDWARE LABORATORY 50 20 43 P PR FIRST TERM TOTAL = 498/750. ORDN. 1 MARKS: , 71100776E T80054227 DEEPANSHU JOSHI , PICT , т80054227 LALITA 01. DATABASE MANAGEMENT SYSTEMS 100 40 45 P 02. DATA COMMUNICATION PP 100 40 40 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 40 P 04. DIGITAL SIGNAL PROCESSING 100 40 24 F 05. THEORY OF COMPUTATION 100 40 30 F 50 20 27 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 29 P 08. SIGNAL PROCESSING LABORATORY 25 10 12 P TW 50 20 26 P 09. SIGNAL PROCESSING LABORATORY 10. HARDWARE LABORATORY 25 10 13 P TW 11. HARDWARE LABORATORY 50 20 10 F PR FIRST TERM TOTAL = 296/750. ORDN. 1 MARKS:

UNIVERSITY OF PUNE ,T.E.(2008 PAT.)(COMPUTER) EXAMINATION NOV. 2012 DATE: 31 JAN. 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 10 (433) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER MEENAKSHI , 71239294H , , PICT , T80054228 T80054228 DESALE VRUSHALI SANJEEV 01. DATABASE MANAGEMENT SYSTEMS 100 40 48 P PP 02. DATA COMMUNICATION 100 40 73 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 82 P 04. DIGITAL SIGNAL PROCESSING 100 40 66 P 05. THEORY OF COMPUTATION 100 40 55 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 20 38 P 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 36 P 08. SIGNAL PROCESSING LABORATORY 25 10 22 P TW 09. SIGNAL PROCESSING LABORATORY 50 20 33 P OR 25 10 19 P 10. HARDWARE LABORATORY TW 11. HARDWARE LABORATORY 50 20 40 P PR FIRST TERM TOTAL = 512/750. ORDN. 1 MARKS: T80054229 DESHMUKH AKSHAY ANIL JYOTI , 71100778M , , PICT , т80054229 01. DATABASE MANAGEMENT SYSTEMS PP 100 40 57 P 100 40 64 P 02. DATA COMMUNICATION 100 40 62 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 56 P 04. DIGITAL SIGNAL PROCESSING 05. THEORY OF COMPUTATION 100 40 40 P 50 20 35 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 20 38 P 07. RDBMS & VISUAL PROGRAMMING LAB. PR 25 10 17 P 08. SIGNAL PROCESSING LABORATORY TW 09. SIGNAL PROCESSING LABORATORY OR 50 20 37 P 25 10 18 P 10. HARDWARE LABORATORY TW 11. HARDWARE LABORATORY 50 20 24 P PR FIRST TERM TOTAL = 448/750. ORDN. 1 MARKS: , 71100782K T80054230 DESHPANDE ANUJ ABHAY , PICT , т80054230 KALPANA 01. DATABASE MANAGEMENT SYSTEMS PP 100 40 50 P 02. DATA COMMUNICATION PP 100 40 69 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 66 P 04. DIGITAL SIGNAL PROCESSING 100 40 46 P 05. THEORY OF COMPUTATION 100 40 50 P 50 20 34 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 38 P 08. SIGNAL PROCESSING LABORATORY 25 10 16 P TW 50 20 34 P 09. SIGNAL PROCESSING LABORATORY 10. HARDWARE LABORATORY 25 10 16 P

FIRST TERM TOTAL = 449/750. ORDN. 1 MARKS:

50 20 30 P

TW

PR

11. HARDWARE LABORATORY

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UNIVERSITY OF PUNE ,T.E.(2008 PAT.)(COMPUTER) EXAMINATION NOV. 2012 DATE: 31 JAN. 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 12 (435) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71100789G , , PICT , T80054234 T80054234 DOIPHODE NISHIGANDHA SANJAY MADHUBALA 01. DATABASE MANAGEMENT SYSTEMS 100 40 49 P 02. DATA COMMUNICATION 100 40 44 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 45 P 04. DIGITAL SIGNAL PROCESSING 100 40 41 P 05. THEORY OF COMPUTATION 100 40 32 F 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 20 36 P 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 40 P 08. SIGNAL PROCESSING LABORATORY 25 10 17 P TW 09. SIGNAL PROCESSING LABORATORY 50 20 32 P OR 25 10 19 P 10. HARDWARE LABORATORY TW 11. HARDWARE LABORATORY 50 20 30 P PR FIRST TERM TOTAL = 385/750. ORDN. 1 MARKS: T80054235 DOSHI ARCHIT RAVINDRA , 71100790L , , PICT , т80054235 ARCHANA 01. DATABASE MANAGEMENT SYSTEMS PP 100 40 45 P 02. DATA COMMUNICATION 100 40 60 P 100 40 56 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 47 P 04. DIGITAL SIGNAL PROCESSING 05. THEORY OF COMPUTATION 100 40 40 P 50 20 34 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 20 38 P 07. RDBMS & VISUAL PROGRAMMING LAB. PR 25 10 17 P 08. SIGNAL PROCESSING LABORATORY TW 09. SIGNAL PROCESSING LABORATORY OR 50 20 26 P 25 10 20 P 10. HARDWARE LABORATORY TW 11. HARDWARE LABORATORY 50 20 28 P PR FIRST TERM TOTAL = 411/750. ORDN. 1 MARKS: , 71045424E T80054236 DURUGKAR SURAJ ANIL , PICT , т80054236 NIRMALA 01. DATABASE MANAGEMENT SYSTEMS 100 40 AA F 02. DATA COMMUNICATION PP 100 40 AA F 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 AA F 04. DIGITAL SIGNAL PROCESSING 100 40 AA F 05. THEORY OF COMPUTATION 100 40 AA F 50 20 22 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 AA F 08. SIGNAL PROCESSING LABORATORY 25 10 12 P TW 50 20 21 P 09. SIGNAL PROCESSING LABORATORY 10. HARDWARE LABORATORY 25 10 12 P TW 11. HARDWARE LABORATORY 50 20 05 F PR FIRST TERM TOTAL = 72/750. ORDN. 1 MARKS:

UNIVERSITY OF PUNE ,T.E.(2008 PAT.)(COMPUTER) EXAMINATION NOV. 2012 DATE: 31 JAN. 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 13 (436) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71100791J , , , PICT , T80054237 T80054237 EKHANDE ATUL ANIL TRISHALA 01. DATABASE MANAGEMENT SYSTEMS PP 100 40 57 P 02. DATA COMMUNICATION 100 40 49 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 40 P 04. DIGITAL SIGNAL PROCESSING 100 40 40 P 05. THEORY OF COMPUTATION 100 40 40 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 20 34 P 50 20 36 P 07. RDBMS & VISUAL PROGRAMMING LAB. PR 08. SIGNAL PROCESSING LABORATORY 25 10 16 P TW 09. SIGNAL PROCESSING LABORATORY 50 20 30 P OR 25 10 19 P 10. HARDWARE LABORATORY TW 11. HARDWARE LABORATORY 50 20 36 P PR FIRST TERM TOTAL = 397/750. ORDN. 1 MARKS: T80054238 GABHALE VARSHA LIMBAJI , 71045426M , , PICT , т80054238 JANABAI 01. DATABASE MANAGEMENT SYSTEMS 100 40 42 P 100 40 32 F 02. DATA COMMUNICATION 100 40 40 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 41 P 04. DIGITAL SIGNAL PROCESSING 05. THEORY OF COMPUTATION 100 40 28 F 50 20 33 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 20 22 P 07. RDBMS & VISUAL PROGRAMMING LAB. PR 25 10 18 P 08. SIGNAL PROCESSING LABORATORY TW 09. SIGNAL PROCESSING LABORATORY OR 50 20 32 P 25 10 20 P 10. HARDWARE LABORATORY TW 11. HARDWARE LABORATORY 50 20 10 F PR FIRST TERM TOTAL = 318/750. ORDN. 1 MARKS: , 71100795м T80054239 GALGALI VAISHNAVI VITHAL , PICT , т80054239 VAISHALI 01. DATABASE MANAGEMENT SYSTEMS 100 40 70 P PP 02. DATA COMMUNICATION PP 100 40 82 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 66 P 04. DIGITAL SIGNAL PROCESSING 100 40 57 P 05. THEORY OF COMPUTATION 100 40 55 P 50 20 37 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 29 P 08. SIGNAL PROCESSING LABORATORY 25 10 19 P TW 50 20 34 P 09. SIGNAL PROCESSING LABORATORY 10. HARDWARE LABORATORY 25 10 22 P TW 50 20 40 P 11. HARDWARE LABORATORY PR FIRST TERM TOTAL = 511/750. ORDN. 1 MARKS:

UNIVERSITY OF PUNE ,T.E.(2008 PAT.)(COMPUTER) EXAMINATION NOV. 2012 DATE: 31 JAN. 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 14 (437) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71100797H , , , РІСТ , Т80054240 T80054240 GANVIR AJAY SUDHAKAR SUREKHA 100 40 67 P 01. DATABASE MANAGEMENT SYSTEMS 02. DATA COMMUNICATION 100 40 57 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 56 P 04. DIGITAL SIGNAL PROCESSING 100 40 59 P 05. THEORY OF COMPUTATION 100 40 56 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 20 28 P 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 31 P 08. SIGNAL PROCESSING LABORATORY 25 10 11 P TW 09. SIGNAL PROCESSING LABORATORY 50 20 22 P OR 25 10 12 P 10. HARDWARE LABORATORY TW 11. HARDWARE LABORATORY 50 20 20 P PR FIRST TERM TOTAL = 419/750. ORDN. 1 MARKS: , 71100799D , , PICT , т80054241 T80054241 GAVALI TANUJA KASHINATH PARVATA 01. DATABASE MANAGEMENT SYSTEMS 100 40 52 P 100 40 44 P 02. DATA COMMUNICATION 100 40 47 P 03. MICROPROCESSORS & MICROCONTROLLERPP 04. DIGITAL SIGNAL PROCESSING 100 40 40 P 05. THEORY OF COMPUTATION 100 40 40 P 50 20 32 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 20 25 P 07. RDBMS & VISUAL PROGRAMMING LAB. PR 25 10 16 P 08. SIGNAL PROCESSING LABORATORY TW 09. SIGNAL PROCESSING LABORATORY OR 50 20 21 P 25 10 15 P 10. HARDWARE LABORATORY TW 11. HARDWARE LABORATORY 50 20 22 P PR FIRST TERM TOTAL = 354/750. ORDN. 1 MARKS: , 71100801K T80054242 GEET KAUR GIRVAR SINGH SUKHMANI PAVANJEET , PICT , т80054242 01. DATABASE MANAGEMENT SYSTEMS 100 40 58 P 02. DATA COMMUNICATION PP 100 40 56 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 41 P 04. DIGITAL SIGNAL PROCESSING 100 40 43 P 05. THEORY OF COMPUTATION 100 40 40 P 50 20 32 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 35 P 08. SIGNAL PROCESSING LABORATORY 25 10 16 P TW 50 20 23 P 09. SIGNAL PROCESSING LABORATORY 10. HARDWARE LABORATORY 25 10 15 P TW 50 20 08 F 11. HARDWARE LABORATORY PR FIRST TERM TOTAL = 367/750. ORDN. 1 MARKS:

UNIVERSITY OF PUNE ,T.E.(2008 PAT.)(COMPUTER) EXAMINATION NOV. 2012 DATE: 31 JAN. 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 15 (438) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71100803F , , PICT , T80054243 T80054243 GHULE RUTUJA POPAT SANGEETA 01. DATABASE MANAGEMENT SYSTEMS 100 40 61 P 02. DATA COMMUNICATION 100 40 61 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 45 P 04. DIGITAL SIGNAL PROCESSING 100 40 47 P 05. THEORY OF COMPUTATION 100 40 51 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 20 33 P 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 36 P 08. SIGNAL PROCESSING LABORATORY 25 10 17 P TW 09. SIGNAL PROCESSING LABORATORY 50 20 30 P OR 25 10 20 P 10. HARDWARE LABORATORY TW 11. HARDWARE LABORATORY 50 20 36 P PR FIRST TERM TOTAL = 437/750. ORDN. 1 MARKS: T80054244 GINA RIGZIN CHETEN , 71100804D , , PICT , т80054244 ARCHANA 01. DATABASE MANAGEMENT SYSTEMS 100 40 59 P 100 40 76 P 02. DATA COMMUNICATION PP 100 40 65 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 63 P 04. DIGITAL SIGNAL PROCESSING 05. THEORY OF COMPUTATION 100 40 50 P 50 20 43 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 20 45 P 07. RDBMS & VISUAL PROGRAMMING LAB. PR 25 10 23 P 08. SIGNAL PROCESSING LABORATORY TW 50 20 40 P 09. SIGNAL PROCESSING LABORATORY OR 25 10 21 P 10. HARDWARE LABORATORY TW 11. HARDWARE LABORATORY 50 20 42 P PR FIRST TERM TOTAL = 527/750. ORDN. 1 MARKS: , 71100809E T80054245 GOLE MADHURI JAYANT SANJIVANI , PICT , т80054245 01. DATABASE MANAGEMENT SYSTEMS 100 40 68 P 02. DATA COMMUNICATION PP 100 40 78 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 56 P 04. DIGITAL SIGNAL PROCESSING 100 40 56 P 05. THEORY OF COMPUTATION 100 40 49 P 50 20 31 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 40 P 08. SIGNAL PROCESSING LABORATORY 25 10 15 P TW 50 20 31 P 09. SIGNAL PROCESSING LABORATORY 10. HARDWARE LABORATORY 25 10 18 P TW 11. HARDWARE LABORATORY 50 20 28 P PR FIRST TERM TOTAL = 470/750. ORDN. 1 MARKS:

UNIVERSITY OF PUNE ,T.E.(2008 PAT.)(COMPUTER) EXAMINATION NOV. 2012 DATE: 31 JAN. 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 16 (439) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71239296D , , PICT , T80054246 T80054246 GUJAR MANDAR SHRINIVAS LEELA 100 40 57 P 01. DATABASE MANAGEMENT SYSTEMS PP 02. DATA COMMUNICATION 100 40 67 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 71 P 04. DIGITAL SIGNAL PROCESSING 100 40 43 P 05. THEORY OF COMPUTATION 100 40 49 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 20 43 P 50 20 44 P 07. RDBMS & VISUAL PROGRAMMING LAB. PR 08. SIGNAL PROCESSING LABORATORY 25 10 22 P TW 09. SIGNAL PROCESSING LABORATORY 50 20 36 P OR 25 10 22 P 10. HARDWARE LABORATORY TW 11. HARDWARE LABORATORY 50 20 41 P PR FIRST TERM TOTAL = 495/750. ORDN. 1 MARKS: T80054247 GUPTA TARUN SHITALPRASAD USHA , 71100813C , , PICT , т80054247 01. DATABASE MANAGEMENT SYSTEMS 100 40 67 P 100 40 80 P 02. DATA COMMUNICATION 100 40 65 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 59 P 04. DIGITAL SIGNAL PROCESSING 05. THEORY OF COMPUTATION 100 40 46 P 50 20 34 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 20 43 P 07. RDBMS & VISUAL PROGRAMMING LAB. PR 25 10 18 P 08. SIGNAL PROCESSING LABORATORY TW 09. SIGNAL PROCESSING LABORATORY OR 50 20 43 P 25 10 21 P 10. HARDWARE LABORATORY TW 11. HARDWARE LABORATORY 50 20 42 P PR FIRST TERM TOTAL = 518/750. ORDN. 1 MARKS: , 71100814M T80054248 GURJAR ADITYA SHRIKANT , PICT , т80054248 BHARATI 01. DATABASE MANAGEMENT SYSTEMS 100 40 56 P PP 02. DATA COMMUNICATION PP 100 40 81 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 64 P 04. DIGITAL SIGNAL PROCESSING 100 40 66 P 05. THEORY OF COMPUTATION 100 40 49 P 50 20 35 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 27 P 08. SIGNAL PROCESSING LABORATORY 25 10 22 P TW 50 20 43 P 09. SIGNAL PROCESSING LABORATORY 10. HARDWARE LABORATORY 25 10 20 P TW 50 20 42 P 11. HARDWARE LABORATORY PR FIRST TERM TOTAL = 505/750. ORDN. 1 MARKS:

UNIVERSITY OF PUNE ,T.E.(2008 PAT.)(COMPUTER) EXAMINATION NOV. 2012 DATE: 31 JAN. 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 17 (440) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71100818D , , PICT , T80054249 T80054249 HARSHITA GANGRADE NEETA 01. DATABASE MANAGEMENT SYSTEMS 100 40 79 P 02. DATA COMMUNICATION 100 40 84 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 71 P 04. DIGITAL SIGNAL PROCESSING 100 40 69 P 05. THEORY OF COMPUTATION 100 40 55 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 20 32 P 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 43 P 08. SIGNAL PROCESSING LABORATORY 25 10 16 P TW 09. SIGNAL PROCESSING LABORATORY 50 20 40 P OR 25 10 20 P 10. HARDWARE LABORATORY TW 11. HARDWARE LABORATORY 50 20 43 P PR FIRST TERM TOTAL = 552/750. ORDN. 1 MARKS: T80054250 IBUSE AMANIYA MUZIR , 71239297B , , PICT , т80054250 YASMEEN 01. DATABASE MANAGEMENT SYSTEMS 100 40 65 P 100 40 78 P 02. DATA COMMUNICATION PP 100 40 50 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 57 P 04. DIGITAL SIGNAL PROCESSING 05. THEORY OF COMPUTATION 100 40 40 P 50 20 39 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 20 39 P 07. RDBMS & VISUAL PROGRAMMING LAB. PR 25 10 17 P 08. SIGNAL PROCESSING LABORATORY TW 09. SIGNAL PROCESSING LABORATORY OR 50 20 32 P 25 10 20 P 10. HARDWARE LABORATORY TW 11. HARDWARE LABORATORY 50 20 30 P PR FIRST TERM TOTAL = 467/750. ORDN. 1 MARKS: , 71100822B T80054251 INGOLE SHAILESH GANESH , PICT , т80054251 ARTI 01. DATABASE MANAGEMENT SYSTEMS 100 40 72 P PP 02. DATA COMMUNICATION PP 100 40 76 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 61 P 04. DIGITAL SIGNAL PROCESSING 100 40 71 P 05. THEORY OF COMPUTATION 100 40 48 P 50 20 37 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 42 P 08. SIGNAL PROCESSING LABORATORY 25 10 22 P TW 50 20 37 P 09. SIGNAL PROCESSING LABORATORY 10. HARDWARE LABORATORY 25 10 20 P TW 11. HARDWARE LABORATORY 50 20 26 P PR FIRST TERM TOTAL = 512/750. ORDN. 1 MARKS:

UNIVERSITY OF PUNE ,T.E.(2008 PAT.)(COMPUTER) EXAMINATION NOV. 2012 DATE: 31 JAN. 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 18 (441) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER LATA , 71045453J , , , PICT , T80054252 T80054252 JADHAV AMOL JETA 100 40 60 P 01. DATABASE MANAGEMENT SYSTEMS 02. DATA COMMUNICATION 100 40 60 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 46 P 04. DIGITAL SIGNAL PROCESSING 100 40 11 F 05. THEORY OF COMPUTATION 100 40 26 F 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 20 21 P 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 AA F 08. SIGNAL PROCESSING LABORATORY 25 10 10 P TW 09. SIGNAL PROCESSING LABORATORY 50 20 AA F OR 25 10 10 P 10. HARDWARE LABORATORY TW 11. HARDWARE LABORATORY 50 20 AA F PR FIRST TERM TOTAL = 244/750. ORDN. 1 MARKS: T80054253 JADHAV APURWA MANIK SANDHYA , 71100823L , , PICT , T80054253 01. DATABASE MANAGEMENT SYSTEMS PP 100 40 68 P 100 40 71 P 02. DATA COMMUNICATION PP 100 40 48 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 58 P 04. DIGITAL SIGNAL PROCESSING 05. THEORY OF COMPUTATION 100 40 55 P 50 20 35 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 20 39 P 07. RDBMS & VISUAL PROGRAMMING LAB. PR 25 10 20 P 08. SIGNAL PROCESSING LABORATORY TW 09. SIGNAL PROCESSING LABORATORY 50 20 33 P OR 25 10 20 P 10. HARDWARE LABORATORY TW 11. HARDWARE LABORATORY 50 20 40 P PR FIRST TERM TOTAL = 487/750. ORDN. 1 MARKS: , 71239298L T80054254 JADHAV ROHINI DAGADU , PICT , т80054254 RANJANA 01. DATABASE MANAGEMENT SYSTEMS 100 40 72 P 02. DATA COMMUNICATION PP 100 40 73 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 67 P 04. DIGITAL SIGNAL PROCESSING 100 40 50 P 05. THEORY OF COMPUTATION 100 40 53 P 50 20 40 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 41 P 08. SIGNAL PROCESSING LABORATORY 25 10 18 P TW 50 20 30 P 09. SIGNAL PROCESSING LABORATORY 10. HARDWARE LABORATORY 25 10 21 P TW 50 20 30 P 11. HARDWARE LABORATORY PR FIRST TERM TOTAL = 495/750. ORDN. 1 MARKS:

UNIVERSITY OF PUNE ,T.E.(2008 PAT.)(COMPUTER) EXAMINATION NOV. 2012 DATE: 31 JAN. 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 19 (442) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER ARUNA , 71239299J , , PICT , T80054255 T80054255 JANTRE NEHA ASHOK 01. DATABASE MANAGEMENT SYSTEMS 100 40 70 P 02. DATA COMMUNICATION 100 40 68 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 74 P 04. DIGITAL SIGNAL PROCESSING 100 40 46 P 05. THEORY OF COMPUTATION 100 40 43 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 20 36 P 50 20 42 P 07. RDBMS & VISUAL PROGRAMMING LAB. PR 08. SIGNAL PROCESSING LABORATORY 25 10 16 P TW 09. SIGNAL PROCESSING LABORATORY 50 20 34 P OR 25 10 20 P 10. HARDWARE LABORATORY TW 11. HARDWARE LABORATORY 50 20 34 P PR FIRST TERM TOTAL = 483/750. ORDN. 1 MARKS: T80054256 JASIM , 71100830C , , PICT , т80054256 MATMUNA 01. DATABASE MANAGEMENT SYSTEMS 100 40 62 P 100 40 68 P 02. DATA COMMUNICATION PP 100 40 62 P 03. MICROPROCESSORS & MICROCONTROLLERPP 04. DIGITAL SIGNAL PROCESSING 100 40 66 P 05. THEORY OF COMPUTATION 100 40 55 P 50 20 41 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 20 41 P 07. RDBMS & VISUAL PROGRAMMING LAB. PR 25 10 23 P 08. SIGNAL PROCESSING LABORATORY TW 09. SIGNAL PROCESSING LABORATORY OR 50 20 35 P 25 10 20 P 10. HARDWARE LABORATORY TW 11. HARDWARE LABORATORY 50 20 36 P PR FIRST TERM TOTAL = 509/750. ORDN. 1 MARKS: , 71100832K T80054257 JOGLEKAR SPHOORTI CHANDRASHEKHAR PO0JA , PICT , т80054257 01. DATABASE MANAGEMENT SYSTEMS 100 40 73 P 02. DATA COMMUNICATION PP 100 40 78 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 67 P 04. DIGITAL SIGNAL PROCESSING 100 40 72 P 05. THEORY OF COMPUTATION 100 40 61 P 50 20 33 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 42 P 08. SIGNAL PROCESSING LABORATORY 25 10 20 P TW 50 20 34 P 09. SIGNAL PROCESSING LABORATORY 10. HARDWARE LABORATORY 25 10 20 P TW 50 20 45 P 11. HARDWARE LABORATORY PR FIRST TERM TOTAL = 545/750. ORDN. 1 MARKS:

UNIVERSITY OF PUNE ,T.E.(2008 PAT.)(COMPUTER) EXAMINATION NOV. 2012

DATE: 31 JAN. 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 20 (443) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71100834F , , PICT , T80054258 T80054258 JOSHI ANUJA RAMKRUSHNA VEENA 01. DATABASE MANAGEMENT SYSTEMS 100 40 65 P 02. DATA COMMUNICATION 100 40 76 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 63 P 04. DIGITAL SIGNAL PROCESSING 100 40 56 P 05. THEORY OF COMPUTATION 100 40 62 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 20 40 P 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 42 P 08. SIGNAL PROCESSING LABORATORY 25 10 20 P TW 09. SIGNAL PROCESSING LABORATORY 50 20 40 P OR 25 10 19 P 10. HARDWARE LABORATORY TW 11. HARDWARE LABORATORY 50 20 35 P PR FIRST TERM TOTAL = 518/750. ORDN. 1 MARKS: T80054259 JOSHI ESHAN CHANDRASHEKHAR , 71100837L , , PICT , т80054259 MEDHAVINI 01. DATABASE MANAGEMENT SYSTEMS 100 40 58 P 40 73 P 02. DATA COMMUNICATION 100 100 40 55 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 61 P 04. DIGITAL SIGNAL PROCESSING 05. THEORY OF COMPUTATION 100 40 42 P 50 20 33 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 20 32 P 07. RDBMS & VISUAL PROGRAMMING LAB. PR 25 10 17 P 08. SIGNAL PROCESSING LABORATORY TW 09. SIGNAL PROCESSING LABORATORY 50 20 32 P OR 25 10 22 P 10. HARDWARE LABORATORY TW 11. HARDWARE LABORATORY 50 20 40 P PR FIRST TERM TOTAL = 465/750. ORDN. 1 MARKS: , 71100843E T80054260 KALE APARNA SUNIL MADHURI , PICT , т80054260 01. DATABASE MANAGEMENT SYSTEMS 100 40 72 P 02. DATA COMMUNICATION PP 100 40 79 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 64 P 04. DIGITAL SIGNAL PROCESSING 100 40 71 P 05. THEORY OF COMPUTATION 100 40 62 P 50 20 48 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 46 P 08. SIGNAL PROCESSING LABORATORY 25 10 24 P TW 50 20 45 P 09. SIGNAL PROCESSING LABORATORY 10. HARDWARE LABORATORY 25 10 24 P TW 11. HARDWARE LABORATORY 50 20 42 P PR FIRST TERM TOTAL = 577/750. ORDN. 1 MARKS:

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UNIVERSITY OF PUNE ,T.E.(2008 PAT.)(COMPUTER) EXAMINATION NOV. 2012 DATE: 31 JAN. 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 28 (451) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71100887G , , PICT , T80054284 T80054284 MAHAJAN ABHIJEET SHIVDAS PRABHAVATI 01. DATABASE MANAGEMENT SYSTEMS 100 40 63 P PP 02. DATA COMMUNICATION 100 40 65 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 48 P 04. DIGITAL SIGNAL PROCESSING 100 40 51 P 05. THEORY OF COMPUTATION 100 40 45 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 20 27 P 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 42 P 08. SIGNAL PROCESSING LABORATORY 25 10 15 P TW 09. SIGNAL PROCESSING LABORATORY 50 20 35 P OR 25 10 16 P 10. HARDWARE LABORATORY TW 11. HARDWARE LABORATORY 50 20 30 P PR FIRST TERM TOTAL = 437/750. ORDN. 1 MARKS: T80054285 MARATHE SONALI ASHOK , 71239301D , , PICT , T80054285 ARUNA 01. DATABASE MANAGEMENT SYSTEMS PP 100 40 71 P 100 40 71 P 02. DATA COMMUNICATION 100 40 60 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 52 P 04. DIGITAL SIGNAL PROCESSING 05. THEORY OF COMPUTATION 100 40 48 P 50 20 43 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 20 42 P 07. RDBMS & VISUAL PROGRAMMING LAB. PR 25 10 22 P 08. SIGNAL PROCESSING LABORATORY TW 50 20 38 P 09. SIGNAL PROCESSING LABORATORY OR 25 10 23 P 10. HARDWARE LABORATORY TW 11. HARDWARE LABORATORY 50 20 40 P PR FIRST TERM TOTAL = 510/750. ORDN. 1 MARKS: , 71239302в T80054286 MARUDA PRAVIN POPATLAL , PICT , т80054286 NANDA 01. DATABASE MANAGEMENT SYSTEMS 100 40 72 P PP PP 02. DATA COMMUNICATION 100 40 61 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 74 P 04. DIGITAL SIGNAL PROCESSING 100 40 46 P 05. THEORY OF COMPUTATION 100 40 50 P 50 20 35 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 36 P 08. SIGNAL PROCESSING LABORATORY 25 10 19 P TW 50 20 38 P 09. SIGNAL PROCESSING LABORATORY 10. HARDWARE LABORATORY 25 10 19 P TW 50 20 30 P 11. HARDWARE LABORATORY PR FIRST TERM TOTAL = 480/750. ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71100894K , , , РІСТ , Т80054287 T80054287 MEHTA MIHIR ARPIT FALGUNI 01. DATABASE MANAGEMENT SYSTEMS 100 40 59 P 02. DATA COMMUNICATION 100 40 64 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 51 P 04. DIGITAL SIGNAL PROCESSING 100 40 77 P 05. THEORY OF COMPUTATION 100 40 43 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 20 35 P 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 43 P 08. SIGNAL PROCESSING LABORATORY 25 10 19 P TW 09. SIGNAL PROCESSING LABORATORY 50 20 35 P OR 25 10 20 P 10. HARDWARE LABORATORY TW 11. HARDWARE LABORATORY 50 20 40 P PR FIRST TERM TOTAL = 486/750. ORDN. 1 MARKS: T80054288 MISTRY HASIT NIPUN , 71100896F , , PICT , т80054288 SONAL 01. DATABASE MANAGEMENT SYSTEMS 100 40 67 P 100 40 65 P 02. DATA COMMUNICATION PP 100 40 48 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 40 P 04. DIGITAL SIGNAL PROCESSING 05. THEORY OF COMPUTATION 100 40 44 P 50 20 33 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 20 40 P 07. RDBMS & VISUAL PROGRAMMING LAB. PR 25 10 15 P 08. SIGNAL PROCESSING LABORATORY TW 09. SIGNAL PROCESSING LABORATORY OR 50 20 28 P 25 10 16 P 10. HARDWARE LABORATORY TW 11. HARDWARE LABORATORY 50 20 10 F PR FIRST TERM TOTAL = 406/750. ORDN. 1 MARKS: , 71045671K T80054289 MOHIT RAJVARDHAN , PICT , т80054289 MEENA KUMARI 01. DATABASE MANAGEMENT SYSTEMS 100 40 44 P 02. DATA COMMUNICATION PP 100 40 40 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 49 P 04. DIGITAL SIGNAL PROCESSING 100 40 16 F 05. THEORY OF COMPUTATION 100 40 41 P 50 20 32 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 38 P 08. SIGNAL PROCESSING LABORATORY 25 10 16 P TW 50 20 20 P 09. SIGNAL PROCESSING LABORATORY 10. HARDWARE LABORATORY 25 10 15 P TW 11. HARDWARE LABORATORY 50 20 36 P PR FIRST TERM TOTAL = 347/750. ORDN. 1 MARKS:

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UNIVERSITY OF PUNE ,T.E.(2008 PAT.)(COMPUTER) EXAMINATION NOV. 2012 DATE: 31 JAN. 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 31 (454) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER KAMAL , 71100908C , , PICT , T80054293 T80054293 NAIK SANDESH TARSING 01. DATABASE MANAGEMENT SYSTEMS 100 40 63 P 02. DATA COMMUNICATION 100 40 66 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 48 P 04. DIGITAL SIGNAL PROCESSING 100 40 48 P 05. THEORY OF COMPUTATION 100 40 10 F 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 20 24 P 50 20 25 P 07. RDBMS & VISUAL PROGRAMMING LAB. PR 08. SIGNAL PROCESSING LABORATORY 25 10 10 P TW 09. SIGNAL PROCESSING LABORATORY 50 20 32 P OR 25 10 13 P 10. HARDWARE LABORATORY TW 11. HARDWARE LABORATORY 50 20 26 P PR FIRST TERM TOTAL = 365/750. ORDN. 1 MARKS: T80054294 NARGUNDE TUSHAR VYANKATESH , 71100913K , , PICT , т80054294 ANJALI 01. DATABASE MANAGEMENT SYSTEMS 100 40 72 P 100 40 70 P 02. DATA COMMUNICATION 100 40 03. MICROPROCESSORS & MICROCONTROLLERPP 61 P 100 40 69 P 04. DIGITAL SIGNAL PROCESSING 05. THEORY OF COMPUTATION 100 40 65 P 50 20 40 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 20 41 P 07. RDBMS & VISUAL PROGRAMMING LAB. PR 25 10 21 P 08. SIGNAL PROCESSING LABORATORY TW 09. SIGNAL PROCESSING LABORATORY OR 50 20 33 P 10. HARDWARE LABORATORY 25 10 20 P TW 11. HARDWARE LABORATORY 50 20 35 P PR FIRST TERM TOTAL = 527/750. ORDN. 1 MARKS: , 71100915F T80054295 NAVEEN KUMAR GUPTA , PICT , т80054295 ANITA 01. DATABASE MANAGEMENT SYSTEMS 100 40 75 P 02. DATA COMMUNICATION PP 100 40 63 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 57 P 04. DIGITAL SIGNAL PROCESSING 100 40 62 P 05. THEORY OF COMPUTATION 100 40 56 P 50 20 25 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 38 P 08. SIGNAL PROCESSING LABORATORY 25 10 14 P TW 50 20 35 P 09. SIGNAL PROCESSING LABORATORY 10. HARDWARE LABORATORY 25 10 15 P TW 11. HARDWARE LABORATORY 50 20 30 P PR FIRST TERM TOTAL = 470/750. ORDN. 1 MARKS:

UNIVERSITY OF PUNE ,T.E.(2008 PAT.)(COMPUTER) EXAMINATION NOV. 2012 DATE: 31 JAN. 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 32 (455) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71100924E , , PICT , T80054296 T80054296 PALNITKAR AMOGH NARENDRA JAYASHREE 01. DATABASE MANAGEMENT SYSTEMS 100 40 65 P PP 02. DATA COMMUNICATION 100 40 69 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 54 P 04. DIGITAL SIGNAL PROCESSING 100 40 54 P 05. THEORY OF COMPUTATION 100 40 49 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 20 33 P 50 20 42 P 07. RDBMS & VISUAL PROGRAMMING LAB. PR 08. SIGNAL PROCESSING LABORATORY 25 10 15 P TW 09. SIGNAL PROCESSING LABORATORY 50 20 41 P OR 25 10 16 P 10. HARDWARE LABORATORY TW 11. HARDWARE LABORATORY 50 20 38 P PR FIRST TERM TOTAL = 476/750. ORDN. 1 MARKS: T80054297 PANDE SWAPNIL SURESH , 71100926M , , PICT , т80054297 SADHANA 01. DATABASE MANAGEMENT SYSTEMS PP 100 40 64 P 100 40 69 P 02. DATA COMMUNICATION 100 40 56 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 62 P 04. DIGITAL SIGNAL PROCESSING 05. THEORY OF COMPUTATION 100 40 48 P 50 20 38 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 20 28 P 07. RDBMS & VISUAL PROGRAMMING LAB. PR 25 10 16 P 08. SIGNAL PROCESSING LABORATORY TW 09. SIGNAL PROCESSING LABORATORY OR 50 20 32 P 25 10 19 P 10. HARDWARE LABORATORY TW 11. HARDWARE LABORATORY 50 20 28 P PR FIRST TERM TOTAL = 460/750. ORDN. 1 MARKS: , 71100929F T80054298 PANHALKAR SHREYAS SHRIKANT NAYANA , PICT , т80054298 01. DATABASE MANAGEMENT SYSTEMS 100 40 60 P 02. DATA COMMUNICATION PP 100 40 64 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 56 P 04. DIGITAL SIGNAL PROCESSING 100 40 59 P 05. THEORY OF COMPUTATION 100 40 53 P 50 20 06. RDBMS & VISUAL PROGRAMMING LAB. TW 40 P 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 41 P 08. SIGNAL PROCESSING LABORATORY 25 10 19 P TW 50 20 12 F 09. SIGNAL PROCESSING LABORATORY 10. HARDWARE LABORATORY 25 10 20 P TW 11. HARDWARE LABORATORY 50 20 38 P PR FIRST TERM TOTAL = 462/750. ORDN. 1 MARKS:

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DATE: 31 JAN. 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 34 (457) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71100940G , , PICT , T80054302 T80054302 PATIL MANDAR RAMRAO MUKTA 01. DATABASE MANAGEMENT SYSTEMS 100 40 43 P 02. DATA COMMUNICATION 100 40 46 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 44 P 04. DIGITAL SIGNAL PROCESSING 100 40 40 P 05. THEORY OF COMPUTATION 100 40 31 F 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 20 30 P 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 36 P 08. SIGNAL PROCESSING LABORATORY 25 10 18 P TW 09. SIGNAL PROCESSING LABORATORY 50 20 25 P OR 25 10 18 P 10. HARDWARE LABORATORY TW 11. HARDWARE LABORATORY 50 20 33 P PR FIRST TERM TOTAL = 364/750. ORDN. 1 MARKS: T80054303 PATIL YOJANA VILAS , 71100945H , , PICT , T80054303 SARALA 01. DATABASE MANAGEMENT SYSTEMS 100 40 70 P 100 40 72 P 02. DATA COMMUNICATION PP 100 40 65 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 63 P 04. DIGITAL SIGNAL PROCESSING 05. THEORY OF COMPUTATION 100 40 54 P 50 20 37 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 20 32 P 07. RDBMS & VISUAL PROGRAMMING LAB. PR 25 10 19 P 08. SIGNAL PROCESSING LABORATORY TW 50 20 34 P 09. SIGNAL PROCESSING LABORATORY OR 25 10 22 P 10. HARDWARE LABORATORY TW 11. HARDWARE LABORATORY 50 20 35 P PR FIRST TERM TOTAL = 503/750. ORDN. 1 MARKS: , 71100947D T80054304 PAWAR ABHIJEET SADANAND , PICT , т80054304 RENUKA 01. DATABASE MANAGEMENT SYSTEMS 100 40 64 P PP 02. DATA COMMUNICATION PP 100 40 63 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 56 P 04. DIGITAL SIGNAL PROCESSING 100 40 61 P 05. THEORY OF COMPUTATION 100 40 49 P 50 20 37 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 38 P 08. SIGNAL PROCESSING LABORATORY 25 10 20 P TW 50 20 41 P 09. SIGNAL PROCESSING LABORATORY 10. HARDWARE LABORATORY 25 10 22 P TW 11. HARDWARE LABORATORY 50 20 36 P PR FIRST TERM TOTAL = 487/750. ORDN. 1 MARKS:

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ORDN. 1 MARKS :

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01. DATABASE MANAGEMENT SYSTEMS02. DATA COMMUNICATION	PP PP	100	40	63			,	,	PICT	, т80054308
	DD		10	63	Р					
	FF	100	40	76	Р					
03. MICROPROCESSORS & MICROCONTROLL	ERPP	100	40	64	Р					
04. DIGITAL SIGNAL PROCESSING	PP	100	40	77	Р					
05. THEORY OF COMPUTATION	PP	100	40	62	Р					
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	39	Р					
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	40	Р					
08. SIGNAL PROCESSING LABORATORY	TW	25	10	18	Р					
09. SIGNAL PROCESSING LABORATORY	OR	50	20	36	Р					
10. HARDWARE LABORATORY	TW	25	10	18	Р					
11. HARDWARE LABORATORY	PR	50	20	26	Р					
FIRST TERM TOTAL = 519/750.										
ORDN. 1 MARKS :										

, PICT , т80054309

•							
	T80054309 RAMTIRTH IRA NITIN				AN	JALI	, 71100965в ,
	01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	55	Р	
	02. DATA COMMUNICATION	PP	100	40	60	Р	
	03. MICROPROCESSORS & MICROCONTROLLE	RPP	100	40	64	Р	
	04. DIGITAL SIGNAL PROCESSING	PP	100	40	45	Р	
	05. THEORY OF COMPUTATION	PP	100	40	53	Р	
	06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	34	Р	
	07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	34	Р	
	08. SIGNAL PROCESSING LABORATORY	TW	25	10	18	Р	

09. SIGNAL PROCESSING LABORATORY OR 50 20 32 P 10. HARDWARE LABORATORY TW 25 10 18 P

11. HARDWARE LABORATORY PR 50 20 30 P

FIRST TERM TOTAL = 443/750.

ORDN. 1 MARKS:

T80054310 RANGNEKAR SARVESH DATTA	TRAYA			MA	NJIRI	,	71100967	' J	,	3	,	PICT	, т80054310
01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	57	Р								
02. DATA COMMUNICATION	PP	100	40	81	Р								
03. MICROPROCESSORS & MICROCONTROLLI	ERPP	100	40	55	Р								
04. DIGITAL SIGNAL PROCESSING	PP	100	40	72	Р								
05. THEORY OF COMPUTATION	PP	100	40	70	Р								
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	42	Р								
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	38	Р								
08. SIGNAL PROCESSING LABORATORY	TW	25	10	16	Р								
09. SIGNAL PROCESSING LABORATORY	OR	50	20	38	Р								

10. HARDWARE LABORATORY 25 10 21 P

50 20 29 P 11. HARDWARE LABORATORY PR

FIRST TERM TOTAL = 519/750.

ORDN. 1 MARKS:

UNIVERSITY OF PUNE ,T.E.(2008 PAT.)(COMPUTER) EXAMINATION NOV. 2012 DATE: 31 JAN. 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 37 (460) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER JYOTI , 71100969E , , PICT , T80054311 T80054311 RATHI ROSHANI DIPAK 01. DATABASE MANAGEMENT SYSTEMS 100 40 40 P 02. DATA COMMUNICATION 100 40 55 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 50 P 04. DIGITAL SIGNAL PROCESSING 100 40 45 P 05. THEORY OF COMPUTATION 100 40 67 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 20 29 P 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 38 P 08. SIGNAL PROCESSING LABORATORY 25 10 18 P TW 09. SIGNAL PROCESSING LABORATORY 50 20 28 P OR 25 10 19 P 10. HARDWARE LABORATORY TW 11. HARDWARE LABORATORY 50 20 28 P PR FIRST TERM TOTAL = 417/750. ORDN. 1 MARKS: T80054312 RAUT VAIBHAV SHARAD RUKMINI , 71100973C , , PICT , т80054312 01. DATABASE MANAGEMENT SYSTEMS 100 40 54 P 100 40 66 P 02. DATA COMMUNICATION PP 100 40 54 P 03. MICROPROCESSORS & MICROCONTROLLERPP 04. DIGITAL SIGNAL PROCESSING 100 40 73 P 05. THEORY OF COMPUTATION 100 40 65 P 50 20 23 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 20 39 P 07. RDBMS & VISUAL PROGRAMMING LAB. PR 25 10 10 P 08. SIGNAL PROCESSING LABORATORY TW 09. SIGNAL PROCESSING LABORATORY OR 50 20 35 P 25 10 13 P 10. HARDWARE LABORATORY TW 11. HARDWARE LABORATORY 50 20 33 P PR FIRST TERM TOTAL = 465/750. ORDN. 1 MARKS: , 71100974м T80054313 RENUKE JAGDISH SHIVAJIRAO , PICT , т80054313 VANAMALA 01. DATABASE MANAGEMENT SYSTEMS 100 40 40 P 02. DATA COMMUNICATION PP 100 40 48 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 50 P 04. DIGITAL SIGNAL PROCESSING 100 40 43 P 05. THEORY OF COMPUTATION 100 40 50 P 50 20 33 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 22 P 08. SIGNAL PROCESSING LABORATORY 25 10 16 P TW 50 20 20 P 09. SIGNAL PROCESSING LABORATORY 10. HARDWARE LABORATORY 25 10 19 P TW 50 20 06 F 11. HARDWARE LABORATORY PR FIRST TERM TOTAL = 347/750. ORDN. 1 MARKS:

DATE: 31 JAN. 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 38 (461)

NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71239305G , , PICT , T80054314 T80054314 RUPNER GEETA MADHAV GOKULA 01. DATABASE MANAGEMENT SYSTEMS 100 40 60 P 02. DATA COMMUNICATION 100 40 67 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 65 P 04. DIGITAL SIGNAL PROCESSING 100 40 46 P 05. THEORY OF COMPUTATION 100 40 59 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 20 40 P 50 20 08 F 07. RDBMS & VISUAL PROGRAMMING LAB. PR 08. SIGNAL PROCESSING LABORATORY 25 10 23 P TW 09. SIGNAL PROCESSING LABORATORY 50 20 29 P OR 25 10 23 P 10. HARDWARE LABORATORY TW 11. HARDWARE LABORATORY 50 20 35 P PR FIRST TERM TOTAL = 455/750. ORDN. 1 MARKS: T80054315 SALECHA DIVYA SANJAY , 71100983L , , PICT , T80054315 NIRMALA 01. DATABASE MANAGEMENT SYSTEMS PP 100 40 31 F 100 40 53 P 02. DATA COMMUNICATION 100 40 43 P 03. MICROPROCESSORS & MICROCONTROLLERPP 04. DIGITAL SIGNAL PROCESSING 100 40 55 P 05. THEORY OF COMPUTATION 100 40 62 P 50 20 32 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 20 37 P 07. RDBMS & VISUAL PROGRAMMING LAB. PR 25 10 18 P 08. SIGNAL PROCESSING LABORATORY TW 09. SIGNAL PROCESSING LABORATORY OR 50 20 36 P 25 10 18 P 10. HARDWARE LABORATORY TW 11. HARDWARE LABORATORY 50 20 32 P PR FIRST TERM TOTAL = 417/750. ORDN. 1 MARKS: , 71100985G T80054316 SAMRIDDH HADA MAHESH **RUBY** , PICT , т80054316 01. DATABASE MANAGEMENT SYSTEMS 100 40 40 P 02. DATA COMMUNICATION PP 100 40 52 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 41 P 04. DIGITAL SIGNAL PROCESSING 100 40 42 P 05. THEORY OF COMPUTATION 100 40 40 P 50 20 30 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 04 F 08. SIGNAL PROCESSING LABORATORY 25 10 16 P TW 50 20 20 P 09. SIGNAL PROCESSING LABORATORY 10. HARDWARE LABORATORY 25 10 16 P TW 11. HARDWARE LABORATORY 50 20 25 P PR FIRST TERM TOTAL = 326/750. ORDN. 1 MARKS:

UNIVERSITY OF PUNE ,T.E.(2008 PAT.)(COMPUTER) EXAMINATION NOV. 2012 DATE: 31 JAN. 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 39 (462) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71100987C , , PICT , T80054317 T80054317 SARAF HRISHIKESH SHRIRAM VRUSHALI 01. DATABASE MANAGEMENT SYSTEMS 100 40 48 P PP 02. DATA COMMUNICATION 100 40 75 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 48 P 04. DIGITAL SIGNAL PROCESSING 100 40 70 P 05. THEORY OF COMPUTATION 100 40 69 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 20 35 P 50 20 40 P 07. RDBMS & VISUAL PROGRAMMING LAB. PR 08. SIGNAL PROCESSING LABORATORY 25 10 16 P TW 09. SIGNAL PROCESSING LABORATORY 50 20 42 P OR 25 10 16 P 10. HARDWARE LABORATORY TW 11. HARDWARE LABORATORY 50 20 35 P PR FIRST TERM TOTAL = 494/750. ORDN. 1 MARKS: , 71239306E , , PICT , т80054318 T80054318 SATHE DEEPASHREE CHANDRAKANT CHITRA 01. DATABASE MANAGEMENT SYSTEMS 100 40 56 P 40 66 P 02. DATA COMMUNICATION 100 100 40 61 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 69 P 04. DIGITAL SIGNAL PROCESSING 05. THEORY OF COMPUTATION 100 40 67 P 50 20 44 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 20 42 P 07. RDBMS & VISUAL PROGRAMMING LAB. PR 25 10 21 P 08. SIGNAL PROCESSING LABORATORY TW 09. SIGNAL PROCESSING LABORATORY 50 20 41 P OR 25 10 21 P 10. HARDWARE LABORATORY TW 11. HARDWARE LABORATORY 50 20 38 P PR FIRST TERM TOTAL = 526/750. ORDN. 1 MARKS: , 71100990C T80054319 SATYANARAYAN RAJAGOPALAN , PICT , т80054319 VIJAYASHANTHI 01. DATABASE MANAGEMENT SYSTEMS 100 40 40 P PP 02. DATA COMMUNICATION PP 100 40 47 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 43 P 04. DIGITAL SIGNAL PROCESSING 100 40 50 P 05. THEORY OF COMPUTATION 100 40 40 P 50 20 36 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 43 P 08. SIGNAL PROCESSING LABORATORY 25 10 16 P TW 50 20 29 P 09. SIGNAL PROCESSING LABORATORY 10. HARDWARE LABORATORY 25 10 20 P TW 50 20 30 P 11. HARDWARE LABORATORY PR FIRST TERM TOTAL = 394/750. ORDN. 1 MARKS:

UNIVERSITY OF PUNE ,T.E.(2008 PAT.)(COMPUTER) EXAMINATION NOV. 2012 DATE: 31 JAN. 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 40 (463) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71100991M , , PICT , T80054320 T80054320 SAVAJI ADITYA JAYANT SHRUTI 100 40 47 P 01. DATABASE MANAGEMENT SYSTEMS 02. DATA COMMUNICATION 100 40 72 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 44 P 04. DIGITAL SIGNAL PROCESSING 100 40 54 P 05. THEORY OF COMPUTATION 100 40 40 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 20 35 P 50 20 39 P 07. RDBMS & VISUAL PROGRAMMING LAB. PR 08. SIGNAL PROCESSING LABORATORY 25 10 20 P TW 09. SIGNAL PROCESSING LABORATORY 50 20 38 P OR 25 10 18 P 10. HARDWARE LABORATORY TW 11. HARDWARE LABORATORY 50 20 26 P PR FIRST TERM TOTAL = 433/750. ORDN. 1 MARKS: T80054321 SAWANT TEJASWINI NANASAHEB , 71100992K , , PICT , T80054321 SUNITA 01. DATABASE MANAGEMENT SYSTEMS 100 40 48 P 40 58 P 02. DATA COMMUNICATION 100 100 40 47 P 03. MICROPROCESSORS & MICROCONTROLLERPP 04. DIGITAL SIGNAL PROCESSING 100 40 29 F 05. THEORY OF COMPUTATION 100 40 47 P 50 20 32 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 20 30 P 07. RDBMS & VISUAL PROGRAMMING LAB. PR 25 10 16 P 08. SIGNAL PROCESSING LABORATORY TW 09. SIGNAL PROCESSING LABORATORY OR 50 20 34 P 10. HARDWARE LABORATORY 25 10 20 P TW 11. HARDWARE LABORATORY 50 20 07 F PR FIRST TERM TOTAL = 368/750. ORDN. 1 MARKS: , 71100993н T80054322 SAYALI ANUP LUNKAD **SMITA** , PICT , т80054322 01. DATABASE MANAGEMENT SYSTEMS 100 40 40 P 02. DATA COMMUNICATION PP 100 40 68 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 55 P 04. DIGITAL SIGNAL PROCESSING 100 40 54 P 05. THEORY OF COMPUTATION 100 40 54 P 50 20 31 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 32 P 08. SIGNAL PROCESSING LABORATORY 25 10 15 P TW 50 20 32 P 09. SIGNAL PROCESSING LABORATORY 10. HARDWARE LABORATORY 25 10 16 P TW 11. HARDWARE LABORATORY 50 20 10 F PR FIRST TERM TOTAL = 407/750.

UNIVERSITY OF PUNE ,T.E.(2008 PAT.)(COMPUTER) EXAMINATION NOV. 2012 DATE: 31 JAN. 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 41 (464) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER ARCHANA , 71100994F , , PICT , T80054323 T80054323 SAYKAR SAURABH VIJAY 01. DATABASE MANAGEMENT SYSTEMS 100 40 49 P 02. DATA COMMUNICATION 100 40 65 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 54 P 04. DIGITAL SIGNAL PROCESSING 100 40 56 P 05. THEORY OF COMPUTATION 100 40 51 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 20 35 P 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 40 P 08. SIGNAL PROCESSING LABORATORY 25 10 19 P TW 09. SIGNAL PROCESSING LABORATORY 50 20 34 P OR 25 10 21 P 10. HARDWARE LABORATORY TW 11. HARDWARE LABORATORY 50 20 11 F PR FIRST TERM TOTAL = 435/750. ORDN. 1 MARKS: T80054324 SHAH ROUNAK BHARAT , 71100995D , , PICT , т80054324 SHILPA 01. DATABASE MANAGEMENT SYSTEMS 100 40 60 P 100 40 80 P 02. DATA COMMUNICATION PP 100 40 69 P 03. MICROPROCESSORS & MICROCONTROLLERPP 40 75 P 100 04. DIGITAL SIGNAL PROCESSING 05. THEORY OF COMPUTATION 100 40 66 P 50 20 46 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 20 42 P 07. RDBMS & VISUAL PROGRAMMING LAB. PR 25 10 24 P 08. SIGNAL PROCESSING LABORATORY TW 09. SIGNAL PROCESSING LABORATORY OR 50 20 45 P 25 10 24 P 10. HARDWARE LABORATORY TW 11. HARDWARE LABORATORY 50 20 44 P PR FIRST TERM TOTAL = 575/750. ORDN. 1 MARKS: , 71100996в T80054325 SHAH SAMKIT UPESHKUMAR , PICT , т80054325 JAGRUTI 01. DATABASE MANAGEMENT SYSTEMS 100 40 49 P PP 02. DATA COMMUNICATION PP 100 40 60 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 56 P 04. DIGITAL SIGNAL PROCESSING 100 40 70 P 05. THEORY OF COMPUTATION 100 40 65 P 50 20 43 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 42 P 08. SIGNAL PROCESSING LABORATORY 25 10 23 P TW 50 20 43 P 09. SIGNAL PROCESSING LABORATORY 10. HARDWARE LABORATORY 25 10 23 P TW 11. HARDWARE LABORATORY 50 20 38 P PR FIRST TERM TOTAL = 512/750. ORDN. 1 MARKS:

UNIVERSITY OF PUNE ,T.E.(2008 PAT.)(COMPUTER) EXAMINATION NOV. 2012 DATE: 31 JAN. 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 42 (465)

NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71101002B , , PICT , T80054326 T80054326 SHENOY ROHIT VEDVYAS VEENA 01. DATABASE MANAGEMENT SYSTEMS 100 40 40 P 02. DATA COMMUNICATION 100 40 61 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 40 P 04. DIGITAL SIGNAL PROCESSING 100 40 50 P 05. THEORY OF COMPUTATION 100 40 52 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 20 37 P 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 40 P 08. SIGNAL PROCESSING LABORATORY 25 10 18 P TW 09. SIGNAL PROCESSING LABORATORY 50 20 30 P OR 25 10 22 P 10. HARDWARE LABORATORY TW 50 20 44 P 11. HARDWARE LABORATORY PR FIRST TERM TOTAL = 434/750. ORDN. 1 MARKS: T80054327 SHINDE SHWETA SANJAY , 71101007C , , PICT , т80054327 SNEHA 01. DATABASE MANAGEMENT SYSTEMS PP 100 40 57 P 100 40 76 P 02. DATA COMMUNICATION 100 40 60 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 68 P 04. DIGITAL SIGNAL PROCESSING 05. THEORY OF COMPUTATION 100 40 70 P 50 20 38 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 20 42 P 07. RDBMS & VISUAL PROGRAMMING LAB. PR 25 10 20 P 08. SIGNAL PROCESSING LABORATORY TW 09. SIGNAL PROCESSING LABORATORY OR 50 20 25 P 25 10 23 P 10. HARDWARE LABORATORY TW 11. HARDWARE LABORATORY 50 20 39 P PR FIRST TERM TOTAL = 518/750. ORDN. 1 MARKS: , 71101008M T80054328 SHINDE VIRENDRA BIJWANT , PICT , т80054328 SEEMA 01. DATABASE MANAGEMENT SYSTEMS 100 40 57 P PP 02. DATA COMMUNICATION PP 100 40 71 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 51 P 04. DIGITAL SIGNAL PROCESSING 100 40 64 P 05. THEORY OF COMPUTATION 100 40 59 P 50 20 43 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 40 P 08. SIGNAL PROCESSING LABORATORY 25 10 20 P TW 50 20 39 P 09. SIGNAL PROCESSING LABORATORY 10. HARDWARE LABORATORY 25 10 22 P TW 11. HARDWARE LABORATORY 50 20 33 P PR FIRST TERM TOTAL = 499/750. ORDN. 1 MARKS:

UNIVERSITY OF PUNE ,T.E.(2008 PAT.)(COMPUTER) EXAMINATION NOV. 2012 DATE: 31 JAN. 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 43 (466) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER T80054329 SHIVANGI BOHRA , 71101012K , , PICT , T80054329 MONA 01. DATABASE MANAGEMENT SYSTEMS 100 40 52 P 02. DATA COMMUNICATION 100 40 61 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 55 P 04. DIGITAL SIGNAL PROCESSING 100 40 57 P 05. THEORY OF COMPUTATION 100 40 56 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 20 34 P 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 37 P 08. SIGNAL PROCESSING LABORATORY 25 10 18 P TW 09. SIGNAL PROCESSING LABORATORY 50 20 35 P OR 25 10 20 P 10. HARDWARE LABORATORY TW 11. HARDWARE LABORATORY 50 20 12 F PR FIRST TERM TOTAL = 437/750. ORDN. 1 MARKS: T80054330 SIDDHARTH BATRA , 71045624H , , PICT , т80054330 SANGEETA 01. DATABASE MANAGEMENT SYSTEMS 100 40 40 P 100 40 AA F 02. DATA COMMUNICATION PP 100 40 46 P 03. MICROPROCESSORS & MICROCONTROLLERPP 04. DIGITAL SIGNAL PROCESSING 100 40 40 P 05. THEORY OF COMPUTATION 100 40 40 P 50 20 21 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 20 22 P 07. RDBMS & VISUAL PROGRAMMING LAB. PR 25 10 13 P 08. SIGNAL PROCESSING LABORATORY TW 09. SIGNAL PROCESSING LABORATORY OR 50 20 28 P 25 10 15 P 10. HARDWARE LABORATORY TW 11. HARDWARE LABORATORY 50 20 24 P PR FIRST TERM TOTAL = 289/750. ORDN. 1 MARKS: , 71101018J , PICT T80054331 SOLANKI GAURAV NANAJI SHOBHA , т80054331 01. DATABASE MANAGEMENT SYSTEMS PP 100 40 40 P 02. DATA COMMUNICATION PP 100 40 51 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 49 P 04. DIGITAL SIGNAL PROCESSING 100 40 48 P 05. THEORY OF COMPUTATION 100 40 31 F 50 20 30 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 22 P 08. SIGNAL PROCESSING LABORATORY 25 10 15 P TW 50 20 35 P 09. SIGNAL PROCESSING LABORATORY 10. HARDWARE LABORATORY 25 10 15 P TW 50 20 20 P 11. HARDWARE LABORATORY PR FIRST TERM TOTAL = 356/750. ORDN. 1 MARKS:

UNIVERSITY OF PUNE ,T.E.(2008 PAT.)(COMPUTER) EXAMINATION NOV. 2012 DATE: 31 JAN. 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 44 (467) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71239308M , , PICT , T80054332 T80054332 TAKALKAR SIYA HEMANT VAISHALI 01. DATABASE MANAGEMENT SYSTEMS 100 40 59 P 02. DATA COMMUNICATION 100 40 73 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 62 P 04. DIGITAL SIGNAL PROCESSING 100 40 51 P 05. THEORY OF COMPUTATION 100 40 47 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 20 38 P 50 20 40 P 07. RDBMS & VISUAL PROGRAMMING LAB. PR 08. SIGNAL PROCESSING LABORATORY 25 10 20 P TW 09. SIGNAL PROCESSING LABORATORY 50 20 38 P OR 25 10 22 P 10. HARDWARE LABORATORY TW 11. HARDWARE LABORATORY 50 20 38 P PR FIRST TERM TOTAL = 488/750. ORDN. 1 MARKS: NAZEMA BEGUM , 71101027H , , PICT , т80054333 T80054333 TARANNUM BI BI RASHEED AHMED SIDDIQUI 01. DATABASE MANAGEMENT SYSTEMS 100 40 49 P 02. DATA COMMUNICATION 100 40 61 P 100 40 59 P 03. MICROPROCESSORS & MICROCONTROLLERPP 04. DIGITAL SIGNAL PROCESSING 100 40 53 P 05. THEORY OF COMPUTATION 100 40 56 P 50 20 37 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 20 41 P 07. RDBMS & VISUAL PROGRAMMING LAB. PR 25 10 19 P 08. SIGNAL PROCESSING LABORATORY TW 50 20 38 P 09. SIGNAL PROCESSING LABORATORY OR 25 10 23 P 10. HARDWARE LABORATORY TW 11. HARDWARE LABORATORY 50 20 43 P PR FIRST TERM TOTAL = 479/750. ORDN. 1 MARKS: , 71101029D T80054334 TELHARKAR ARVIND DATTATREYA , PICT , т80054334 MEENAKSHI 01. DATABASE MANAGEMENT SYSTEMS 100 40 46 P 02. DATA COMMUNICATION PP 100 40 59 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 46 P 04. DIGITAL SIGNAL PROCESSING 100 40 66 P 05. THEORY OF COMPUTATION 100 40 57 P 50 20 40 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 41 P 08. SIGNAL PROCESSING LABORATORY 25 10 21 P TW 50 20 37 P 09. SIGNAL PROCESSING LABORATORY 10. HARDWARE LABORATORY 25 10 20 P TW 11. HARDWARE LABORATORY 50 20 38 P PR FIRST TERM TOTAL = 471/750. ORDN. 1 MARKS:

UNIVERSITY OF PUNE ,T.E.(2008 PAT.)(COMPUTER) EXAMINATION NOV. 2012 DATE: 31 JAN. 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 45 (468) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71045648E , , PICT , T80054335 T80054335 THAKUR DHIRAJSING UDAYSING SAROJADEVI 01. DATABASE MANAGEMENT SYSTEMS 100 40 AA F 02. DATA COMMUNICATION 100 40 AA F 100 40 40 P 03. MICROPROCESSORS & MICROCONTROLLERPP 04. DIGITAL SIGNAL PROCESSING 100 40 AA F 05. THEORY OF COMPUTATION 100 40 AA F 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 20 20 P 50 20 27 P 07. RDBMS & VISUAL PROGRAMMING LAB. PR 08. SIGNAL PROCESSING LABORATORY 25 10 10 P TW 09. SIGNAL PROCESSING LABORATORY 50 20 22 P OR 25 10 10 P 10. HARDWARE LABORATORY TW 11. HARDWARE LABORATORY 50 20 05 F PR FIRST TERM TOTAL = 134/750. ORDN. 1 MARKS: T80054336 THIGALE ANIKET SUNIL , 71101033B , , PICT , т80054336 MADHURI 01. DATABASE MANAGEMENT SYSTEMS PP 100 40 58 P 100 40 62 P 02. DATA COMMUNICATION 100 40 59 P 03. MICROPROCESSORS & MICROCONTROLLERPP 04. DIGITAL SIGNAL PROCESSING 100 40 57 P 05. THEORY OF COMPUTATION 100 40 61 P 50 20 40 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 20 35 P 07. RDBMS & VISUAL PROGRAMMING LAB. PR 25 10 21 P 08. SIGNAL PROCESSING LABORATORY TW 09. SIGNAL PROCESSING LABORATORY 50 20 39 P ΩR 10. HARDWARE LABORATORY 25 10 23 P TW 11. HARDWARE LABORATORY 50 20 33 P PR FIRST TERM TOTAL = 488/750. ORDN. 1 MARKS: , 71101041C T80054337 UPPAL SAHIL RAJIV JYOTI , PICT , т80054337 01. DATABASE MANAGEMENT SYSTEMS 100 40 53 P 02. DATA COMMUNICATION PP 100 40 61 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 58 P 04. DIGITAL SIGNAL PROCESSING 100 40 54 P 05. THEORY OF COMPUTATION 100 40 69 P 50 20 37 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 35 P 08. SIGNAL PROCESSING LABORATORY 25 10 16 P TW 50 20 21 P 09. SIGNAL PROCESSING LABORATORY 10. HARDWARE LABORATORY 25 10 16 P TW 50 20 40 P 11. HARDWARE LABORATORY PR FIRST TERM TOTAL = 460/750.

ORDN. 1 MARKS :

UNIVERSITY OF PUNE ,T.E.(2008 PAT.)(COMPUTER) EXAMINATION NOV. 2012 DATE: 31 JAN. 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 46 (469) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71239309K , , PICT , T80054338 T80054338 VARMA NITESH PREMSING REKHA 01. DATABASE MANAGEMENT SYSTEMS 100 40 59 P 02. DATA COMMUNICATION 100 40 73 P 100 40 61 P 03. MICROPROCESSORS & MICROCONTROLLERPP 04. DIGITAL SIGNAL PROCESSING 100 40 45 P 05. THEORY OF COMPUTATION 100 40 63 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 20 33 P 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 08 F 08. SIGNAL PROCESSING LABORATORY 25 10 15 P TW 09. SIGNAL PROCESSING LABORATORY 50 20 26 P OR 25 10 16 P 10. HARDWARE LABORATORY TW 11. HARDWARE LABORATORY 50 20 07 F PR FIRST TERM TOTAL = 406/750. ORDN. 1 MARKS: T80054339 VIKRAM DATTU , 71101049J , , PICT , т80054339 SHOBHA 01. DATABASE MANAGEMENT SYSTEMS 100 40 53 P 100 40 58 P 02. DATA COMMUNICATION PP 100 40 54 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 65 P 04. DIGITAL SIGNAL PROCESSING 05. THEORY OF COMPUTATION 100 40 80 P 50 20 42 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 20 42 P 07. RDBMS & VISUAL PROGRAMMING LAB. PR 25 10 20 P 08. SIGNAL PROCESSING LABORATORY TW 09. SIGNAL PROCESSING LABORATORY OR 50 20 43 P 25 10 22 P 10. HARDWARE LABORATORY TW 11. HARDWARE LABORATORY 50 20 36 P PR FIRST TERM TOTAL = 515/750. ORDN. 1 MARKS: , 71101060к T80054340 YARDI JUHI PRAKASH , PICT , т80054340 PRIYA 01. DATABASE MANAGEMENT SYSTEMS 100 40 52 P 02. DATA COMMUNICATION PP 100 40 73 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 63 P 04. DIGITAL SIGNAL PROCESSING 100 40 71 P 05. THEORY OF COMPUTATION 100 40 74 P 50 20 45 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 43 P 08. SIGNAL PROCESSING LABORATORY 25 10 23 P TW 50 20 38 P 09. SIGNAL PROCESSING LABORATORY 10. HARDWARE LABORATORY 25 10 23 P TW 11. HARDWARE LABORATORY 50 20 42 P PR FIRST TERM TOTAL = 547/750. ORDN. 1 MARKS:

DATE: 31 JAN. 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 47 (470)

NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71101062F , , PICT , T80054341 T80054341 YEMUL SANDEEP PRAKASH BHULAXMI 01. DATABASE MANAGEMENT SYSTEMS PP 100 40 44 P 02. DATA COMMUNICATION 100 40 62 P 100 40 53 P 03. MICROPROCESSORS & MICROCONTROLLERPP 04. DIGITAL SIGNAL PROCESSING 100 40 48 P 05. THEORY OF COMPUTATION 100 40 51 P 50 20 26 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 39 P 08. SIGNAL PROCESSING LABORATORY 25 10 15 P TW 09. SIGNAL PROCESSING LABORATORY 50 20 27 P OR 25 10 15 P 10. HARDWARE LABORATORY TW 11. HARDWARE LABORATORY 50 20 20 P PR FIRST TERM TOTAL = 400/750. ORDN. 1 MARKS: , 71101064B , , PICT , т80054342 T80054342 YEREKAR KARISHMA TUKARAM RANJANA 100 40 44 P 01. DATABASE MANAGEMENT SYSTEMS 40 67 P 02. DATA COMMUNICATION 100 40 58 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 100 40 53 P 04. DIGITAL SIGNAL PROCESSING 05. THEORY OF COMPUTATION 100 40 52 P 50 20 34 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 32 P 25 10 18 P 08. SIGNAL PROCESSING LABORATORY TW 50 20 30 P 09. SIGNAL PROCESSING LABORATORY OR 10. HARDWARE LABORATORY 25 10 21 P TW 11. HARDWARE LABORATORY 50 20 30 P PR FIRST TERM TOTAL = 439/750. ORDN. 1 MARKS: , 71045370B , T8054209 , PICT , T80054343 T80054343 ANIRUDH JODHA ANJU 01. DATABASE MANAGEMENT SYSTEMS 100 40 56 P C 12. PRINCIPLES OF PROGRAMMING LANG. PP 100 40 45 P C PP 59 P C 02. DATA COMMUNICATION PP 100 40 42 P C 13. COMPUTER NETWORKS PP 100 40 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 47 P.C 14. FINANCE & MANAGEMENT INFORMA.SYS.PP 100 40 58 P C 04. DIGITAL SIGNAL PROCESSING 100 40 46 P C 15. SYSTEMS PROGRAMMING & OPERA.SYS. PP 100 40 40 P.C 05. THEORY OF COMPUTATION 100 40 55 P C PP 100 40 40 P C PP 16. SOFTWARE ENGINEERING 50 20 25 06. RDBMS & VISUAL PROGRAMMING LAB. TW 35 P C 17. SOFTWARE LABORATORY 10 16 P C TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 38 P C 18. SOFTWARE LABORATORY 50 20 24 P PR 17 P C 08. SIGNAL PROCESSING LABORATORY 25 10 16 P C 19. COMPUTER NETWORK TW 25 10 TW 50 20 22 P C 20 30 P C 09. SIGNAL PROCESSING LABORATORY 20. COMPUTER NETWORK 50 OR 10. HARDWARE LABORATORY 25 10 19 P C 21. SOFTWARE DEVELOPMENT TOOLS LAB. TW 50 20 29 P C TW 11. HARDWARE LABORATORY 50 20 36 P C 22. SEMINAR AND TECHNICAL COMMUNI. TW 50 20 33 P C PR GRAND TOTAL = 803/1500, RESULT: SECOND CLASS ORDN. 1 MARKS:

DATE: 31 JAN. 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 48 (471)

NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER RUPINDER , 70925338к , Т8054352 , РІСТ T80054344 ARASHDEEP SINGH HEIR , т80054344 PP 100 40 40 P C 12. PRINCIPLES OF PROGRAMMING LANG. PP 100 40 19 F 01. DATABASE MANAGEMENT SYSTEMS 13. COMPUTER NETWORKS PP 100 02. DATA COMMUNICATION PP 100 40 51 P C 100 40 40 P C 14. FINANCE & MANAGEMENT INFORMA.SYS.PP 100 40 47 P.C 03. MICROPROCESSORS & MICROCONTROLLERPP 04. DIGITAL SIGNAL PROCESSING 100 40 40 P C 15. SYSTEMS PROGRAMMING & OPERA.SYS. PP 100 40 18 F 05. THEORY OF COMPUTATION 100 40 31 F 100 40 28 F 16. SOFTWARE ENGINEERING 20 20 P C 25 10 10 P C 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 17. SOFTWARE LABORATORY TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 20 P C 50 20 10 F 18. SOFTWARE LABORATORY PR 25 10 10 P C 19. COMPUTER NETWORK 25 10 12 P C 08. SIGNAL PROCESSING LABORATORY TW TW 50 20 22 P C 50 20 24 P C 09. SIGNAL PROCESSING LABORATORY 20. COMPUTER NETWORK OR OR 25 10 10 P C 10. HARDWARE LABORATORY 50 20 25 P C TW 21. SOFTWARE DEVELOPMENT TOOLS LAB. TW 11. HARDWARE LABORATORY 50 20 26 P C 22. SEMINAR AND TECHNICAL COMMUNI. TW 50 20 PC PR GRAND TOTAL = 563/1500, RESULT: FAILS ORDN. 1 MARKS: MAMTA , 71045377к , Т8054210 , РІСТ T80054345 AVHAD PRATIK GULAB , т80054345 PP 100 40 52 P C 12. PRINCIPLES OF PROGRAMMING LANG. PP 100 40 48 PC 01. DATABASE MANAGEMENT SYSTEMS 02. DATA COMMUNICATION PP 100 40 P C 13. COMPUTER NETWORKS PP 100 40 40 46 P C 40 40 P C 14. FINANCE & MANAGEMENT INFORMA.SYS.PP 100 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 52 P C 04. DIGITAL SIGNAL PROCESSING 40 42 P C 100 42 P PP 100 15. SYSTEMS PROGRAMMING & OPERA.SYS. PP 40 40 40 P C 100 05. THEORY OF COMPUTATION 100 16. SOFTWARE ENGINEERING 40 48 P C 20 38 P C 25 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 17. SOFTWARE LABORATORY 10 19 P C TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 30 P C 20 25 P 18. SOFTWARE LABORATORY PR 50 25 10 17 P C 08. SIGNAL PROCESSING LABORATORY TW 19. COMPUTER NETWORK TW 25 10 19 P C 09. SIGNAL PROCESSING LABORATORY OR 50 20 23 P C 20. COMPUTER NETWORK OR 50 20 21 P C 10. HARDWARE LABORATORY 25 10 20 P C 50 20 34 P C TW 21. SOFTWARE DEVELOPMENT TOOLS LAB. TW 11. HARDWARE LABORATORY PR 50 20 25 P C 22. SEMINAR AND TECHNICAL COMMUNI. TW 50 20 37 P.C. GRAND TOTAL = 758/1500, RESULT: SECOND CLASS ORDN. 1 MARKS: , 70801350D , T8054355 , PICT T80054346 AVLEEN UPPAL INDERDEEP , т80054346 01. DATABASE MANAGEMENT SYSTEMS PP 100 40 46 P C 12. PRINCIPLES OF PROGRAMMING LANG. PP 100 40 46 P C 02. DATA COMMUNICATION PP 100 40 AA F 13. COMPUTER NETWORKS PP 100 40 52 P C 100 40 40 P C 100 40 53 P C 03. MICROPROCESSORS & MICROCONTROLLERPP 14. FINANCE & MANAGEMENT INFORMA.SYS.PP 04. DIGITAL SIGNAL PROCESSING 100 40 AA F 15. SYSTEMS PROGRAMMING & OPERA.SYS. PP 100 40 40 P.C 05. THEORY OF COMPUTATION 100 40 40 P C PP 100 40 45 P C PP 16. SOFTWARE ENGINEERING 25 13 P C 50 20 22 P C 10 06. RDBMS & VISUAL PROGRAMMING LAB. TW 17. SOFTWARE LABORATORY TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 20 50 20 22 P 50 AA F 18. SOFTWARE LABORATORY PR 19. COMPUTER NETWORK 08. SIGNAL PROCESSING LABORATORY 25 10 15 P C TW 25 10 12 P C TW 39 P 09. SIGNAL PROCESSING LABORATORY 50 20 27 P C 50 20 OR 20. COMPUTER NETWORK OR 10. HARDWARE LABORATORY 25 10 16 P C 21. SOFTWARE DEVELOPMENT TOOLS LAB. TW 50 20 27 P C TW 11. HARDWARE LABORATORY PR 50 20 23 P C 22. SEMINAR AND TECHNICAL COMMUNI. TW 50 20 38 P C GRAND TOTAL = 616/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS:

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DATE: 31 JAN. 2013	CEN	IRE :	PUNE	TN211	TUTE	OF COMPUTE	R TECHNOLOGY, PUNE.	PAG	JE NO.	49	(4	72)
NOTE: FIRST LINE : SEAT NO., NAME	OF TH	E CAND	IDATE	, MC	THER,	PERMANENT	REG. NO., PREVIOUS SEAT NO., C	OLLE	GΕ,	SEAT	NO.	
OTHER LINES: HEAD OF PASSING,	MAX	. MARK	S, M	IN. F	PASS M	IARKS, MAR	KS OBTAINED, P/F:PASS/FAIL, C:P	REVI	OUS CA	RRY O	VER	
T80054347 BHOSALE SHITAL MANOHAR				JA	NΥΑ		, 71132426D , T8054220 ,	P	ICT	,	т8005	4347
01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	45	РС	12.	PRINCIPLES OF PROGRAMMING LANG.	PP	100	40	40	РС
02. DATA COMMUNICATION	PP	100	40	40	РС	13.	COMPUTER NETWORKS	PP	100	40	47	Р
03. MICROPROCESSORS & MICROCONTROLL	ERPP	100	40	40	РС	14.	FINANCE & MANAGEMENT INFORMA.SYS	.PP	100	40	46	РС
04. DIGITAL SIGNAL PROCESSING	PP	100	40	26	F	15.	SYSTEMS PROGRAMMING & OPERA.SYS.	PP	100	40	40	Р
05. THEORY OF COMPUTATION	PP	100	40	40	РС	16.	SOFTWARE ENGINEERING	PP	100	40	40	РС
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	34	РС	17.	SOFTWARE LABORATORY	TW	25	10	15	РС
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	22	Р	18.	SOFTWARE LABORATORY	PR	50	20	25	Р
08. SIGNAL PROCESSING LABORATORY	TW	25	10	15	РС	19.	COMPUTER NETWORK	TW	25	10	18	РС
09. SIGNAL PROCESSING LABORATORY	OR	50	20	22	РС	20.	COMPUTER NETWORK	OR	50	20	21	РС
10. HARDWARE LABORATORY	TW	25	10	18	РС	21.	SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	27	РС
11. HARDWARE LABORATORY	PR	50	20	23	Р	22.	SEMINAR AND TECHNICAL COMMUNI.	TW	50	20	34	РС
GRAND TOTAL = 678/1500, RESULT: FAIL	S A.T	.K.T.						RESU	JLT RE	SERVE	D FOR	BKLG
ORDN. 1 MARKS :												
T80054349 DURGESH CHAPORKAR				SU	JMEDH <i>A</i>		, 71045423G , T8054234 ,	P	CT	,	т8005	4349
01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	58	РС	12.	PRINCIPLES OF PROGRAMMING LANG.	PP	100	40	43	PC
02. DATA COMMUNICATION	PP	100	40	46	РС	13.	COMPUTER NETWORKS	PP	100	40	59	РС
03. MICROPROCESSORS & MICROCONTROLL	ERPP	100	40	56	РС	14.	FINANCE & MANAGEMENT INFORMA.SYS	.PP	100	40	59	PC
04. DIGITAL SIGNAL PROCESSING	PP	100	40	43	РС	15.	SYSTEMS PROGRAMMING & OPERA.SYS.	PP	100	40	44	РС
05. THEORY OF COMPUTATION	PP	100	40	40	РС	16.	SOFTWARE ENGINEERING	PP	100	40	50	РС
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	28	РС	17.	SOFTWARE LABORATORY	TW	25	10	10	РС
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	30	РС	18.	SOFTWARE LABORATORY	PR	50	20	38	Р
08. SIGNAL PROCESSING LABORATORY	TW	25	10	10	РС	19.	COMPUTER NETWORK	TW	25	10	15	РС
09. SIGNAL PROCESSING LABORATORY	OR	50	20	34	РС	20.	COMPUTER NETWORK	OR	50	20	22	РС
10. HARDWARE LABORATORY	TW	25	10	10	РС	21.	SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	20	РС
11. HARDWARE LABORATORY	PR	50	20	32	РС	22.	SEMINAR AND TECHNICAL COMMUNI.	TW	50	20	38	РС
GRAND TOTAL = 785/1500, RESULT: SECO	ND CL	ASS										
ORDN. 1 MARKS :												
T80054350 GAIKWAD MAHESH EKNATH				LA	ATA		, 71045431н , т8054239 ,	P.	ICT	,	т8005	4350
01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	40	РС	12.	PRINCIPLES OF PROGRAMMING LANG.	PP	100	40	40	РС
02. DATA COMMUNICATION	PP	100	40	42	РС	13.	COMPUTER NETWORKS	PP	100	40	40	РС
03. MICROPROCESSORS & MICROCONTROLL	ERPP	100	40	42	РС	14.	FINANCE & MANAGEMENT INFORMA.SYS	.PP	100	40	46	РС
04. DIGITAL SIGNAL PROCESSING	PP	100	40	45	РС	15.	SYSTEMS PROGRAMMING & OPERA.SYS.	PP	100	40	40	РС
05. THEORY OF COMPUTATION	PP	100	40	41	РС	16.	SOFTWARE ENGINEERING	PP	100	40	44	РС
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	34	РС	17.	SOFTWARE LABORATORY	TW	25	10	10	РС
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	25	Р	18.	SOFTWARE LABORATORY	PR	50	20	10	F
08. SIGNAL PROCESSING LABORATORY	TW	25	10	14	РС			TW	25	10	15	РС
09. SIGNAL PROCESSING LABORATORY	OR	50	20	20	РС	20.	COMPUTER NETWORK	OR	50	20	35	Р
10. HARDWARE LABORATORY	TW	25	10	13	РС	21.	SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	20	РС
11. HARDWARE LABORATORY	PR	50	20	AA	F	22.	SEMINAR AND TECHNICAL COMMUNI.	TW	50	20	27	РС
GRAND TOTAL = 643/1500, RESULT: FAIL		.K.T.										
ORDN. 1 MARKS :												
			_		-	_				_		

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , т80054351 SINDHU , 70925417C , T8054363 , PICT T80054351 GAIKWAD SHIVRAJ VITTHALRAO 01. DATABASE MANAGEMENT SYSTEMS 12. PRINCIPLES OF PROGRAMMING LANG. PP 100 40 40 PC PP 100 40 43 P C 13. COMPUTER NETWORKS PP 100 02. DATA COMMUNICATION 40 47 P C 50 P C 100 100 40 45 P C 14. FINANCE & MANAGEMENT INFORMA.SYS.PP 100 40 50 P C 03. MICROPROCESSORS & MICROCONTROLLERPP 04. DIGITAL SIGNAL PROCESSING 100 40 54 P C 15. SYSTEMS PROGRAMMING & OPERA.SYS. PP 100 40 43 P C 05. THEORY OF COMPUTATION 100 40 46 P C 100 40 40 P 16. SOFTWARE ENGINEERING 20 20 P C 25 10 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 17. SOFTWARE LABORATORY TW 10 P C 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 21 P C 50 20 27 P C 18. SOFTWARE LABORATORY PR 25 10 16 P C 19. COMPUTER NETWORK 25 10 08. SIGNAL PROCESSING LABORATORY TW TW 14 P C 50 20 40 P C 50 20 09. SIGNAL PROCESSING LABORATORY 20. COMPUTER NETWORK 42 P C OR OR 25 10 11 P C 10. HARDWARE LABORATORY 50 20 27 P C TW 21. SOFTWARE DEVELOPMENT TOOLS LAB. TW 11. HARDWARE LABORATORY PR 50 20 28 P C 22. SEMINAR AND TECHNICAL COMMUNI. TW 50 20 28 P C GRAND TOTAL = 742+08/1500, RESULT: SECOND CLASS [0.2] ORDN. 1 MARKS: SWATI , 71132428L , T8054243 , PICT T80054352 GOKHALE GAURAV SUHAS , т80054352 12. PRINCIPLES OF PROGRAMMING LANG. PP 100 40 56 P C 01. DATABASE MANAGEMENT SYSTEMS PP 100 40 63 P C 02. DATA COMMUNICATION 50 P C 13. COMPUTER NETWORKS PP 100 PP 100 40 40 61 P C 40 14. FINANCE & MANAGEMENT INFORMA.SYS.PP 100 03. MICROPROCESSORS & MICROCONTROLLERPP 100 66 P C 40 66 P C 04. DIGITAL SIGNAL PROCESSING 40 45 P C 100 PP 100 15. SYSTEMS PROGRAMMING & OPERA.SYS. PP 40 56 P C 40 46 P.C 100 05. THEORY OF COMPUTATION 100 16. SOFTWARE ENGINEERING 40 84 P C 20 25 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 28 P C 17. SOFTWARE LABORATORY 10 10 P C TW 50 20 21 P C 20 07. RDBMS & VISUAL PROGRAMMING LAB. PR 18. SOFTWARE LABORATORY PR 50 AA F 25 08. SIGNAL PROCESSING LABORATORY TW 25 10 14 P C 19. COMPUTER NETWORK TW 10 13 P C 09. SIGNAL PROCESSING LABORATORY OR 50 20 AA F 20. COMPUTER NETWORK OR 50 20 30 P C 10. HARDWARE LABORATORY 25 10 13 P C 50 20 20 P C TW 21. SOFTWARE DEVELOPMENT TOOLS LAB. TW 11. HARDWARE LABORATORY PR 50 20 AA F 22. SEMINAR AND TECHNICAL COMMUNI. TW 50 20 30 P C GRAND TOTAL = 772/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS: , 71045454G , T8054246 , PICT ANITA T80054353 JADHAV PRASHANT UTTAM , T80054353 01. DATABASE MANAGEMENT SYSTEMS PP 100 40 28 F 12. PRINCIPLES OF PROGRAMMING LANG. PP 100 40 40 P C 02. DATA COMMUNICATION PP 100 40 40 P C 13. COMPUTER NETWORKS PP 100 40 40 P C 100 40 28 F 100 40 40 P 03. MICROPROCESSORS & MICROCONTROLLERPP 14. FINANCE & MANAGEMENT INFORMA.SYS.PP 04. DIGITAL SIGNAL PROCESSING 100 40 15 F 15. SYSTEMS PROGRAMMING & OPERA.SYS. PP 100 40 23 F 05. THEORY OF COMPUTATION 100 40 19 F PP 100 40 19 F PP 16. SOFTWARE ENGINEERING 25 50 20 24 P C 10 10 P C 06. RDBMS & VISUAL PROGRAMMING LAB. TW 17. SOFTWARE LABORATORY TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 20 32 P 50 20 20 P 50 18. SOFTWARE LABORATORY PR 11 P C 08. SIGNAL PROCESSING LABORATORY 25 10 11 P C 19. COMPUTER NETWORK TW 25 10 TW 28 P 09. SIGNAL PROCESSING LABORATORY 50 20 20 P C 50 20 20. COMPUTER NETWORK OR 10. HARDWARE LABORATORY 25 10 13 P C 50 20 20 P C TW 21. SOFTWARE DEVELOPMENT TOOLS LAB. TW 11. HARDWARE LABORATORY PR 50 20 22 P C 22. SEMINAR AND TECHNICAL COMMUNI. TW 50 20 25 P C GRAND TOTAL = 528/1500, RESULT: FAILS ORDN. 1 MARKS:

DATE: 31 JAN. 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 51 (474)

NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71045460M , T8054250 , PICT T80054354 JAMES DADO ANIA , т80054354 12. PRINCIPLES OF PROGRAMMING LANG. PP 100 40 54 P C 01. DATABASE MANAGEMENT SYSTEMS PP 100 40 40 P C 13. COMPUTER NETWORKS PP 100 02. DATA COMMUNICATION 100 40 48 P C 59 P C PP 100 40 47 P C 14. FINANCE & MANAGEMENT INFORMA.SYS.PP 100 40 51 P C 03. MICROPROCESSORS & MICROCONTROLLERPP 04. DIGITAL SIGNAL PROCESSING 100 40 40 P C 15. SYSTEMS PROGRAMMING & OPERA.SYS. PP 100 40 54 P C 05. THEORY OF COMPUTATION 100 40 42 P C 100 56 P C 16. SOFTWARE ENGINEERING 40 20 28 P C 25 10 17 PC 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 17. SOFTWARE LABORATORY TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 20 P C 50 20 12 F 18. SOFTWARE LABORATORY PR 25 10 11 P C 19. COMPUTER NETWORK 25 10 08. SIGNAL PROCESSING LABORATORY TW TW 14 P C 50 20 34 P C 50 20 28 P 09. SIGNAL PROCESSING LABORATORY 20. COMPUTER NETWORK OR OR 10. HARDWARE LABORATORY 25 10 13 P C 50 20 25 P C TW 21. SOFTWARE DEVELOPMENT TOOLS LAB. TW 11. HARDWARE LABORATORY PR 50 20 25 P C 22. SEMINAR AND TECHNICAL COMMUNI. TW 50 20 40 P C GRAND TOTAL = 758/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS: SUNITA , 70925488B , T8054267 , PICT T80054355 KOTE UDAY ARUN , т80054355 40 P C 12. PRINCIPLES OF PROGRAMMING LANG. PP 100 40 46 PC 01. DATABASE MANAGEMENT SYSTEMS PP 100 40 02. DATA COMMUNICATION 49 P C 13. COMPUTER NETWORKS PP 100 PP 100 40 40 48 P C 40 14. FINANCE & MANAGEMENT INFORMA.SYS.PP 100 03. MICROPROCESSORS & MICROCONTROLLERPP 100 47 P C 40 55 P C 04. DIGITAL SIGNAL PROCESSING 40 100 42 P PP 100 44 P C 15. SYSTEMS PROGRAMMING & OPERA.SYS. PP 40 40 40 P C 100 05. THEORY OF COMPUTATION 100 16. SOFTWARE ENGINEERING 40 63 P C 20 25 12 P C 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 34 P C 17. SOFTWARE LABORATORY 10 TW 50 20 22 P C 20 07. RDBMS & VISUAL PROGRAMMING LAB. PR 18. SOFTWARE LABORATORY PR 50 21 P C 25 10 11 P C 08. SIGNAL PROCESSING LABORATORY TW 25 10 15 P C 19. COMPUTER NETWORK TW 09. SIGNAL PROCESSING LABORATORY OR 50 20 20 P C 20. COMPUTER NETWORK OR 50 20 24 P 10. HARDWARE LABORATORY 25 10 17 P C 50 20 20 P C TW 21. SOFTWARE DEVELOPMENT TOOLS LAB. TW 11. HARDWARE LABORATORY 50 20 36 P C 22. SEMINAR AND TECHNICAL COMMUNI. TW 50 20 34 P C PR GRAND TOTAL = 740+10/1500, RESULT: SECOND CLASS [0.2] RESULT RESERVED FOR BKLG ORDN. 1 MARKS: , 71045496B , T8054273 , PICT JYOTI T80054356 KULKARNI MANASI MILIND , т80054356 01. DATABASE MANAGEMENT SYSTEMS PP 100 40 61 P C 12. PRINCIPLES OF PROGRAMMING LANG. PP 100 40 40 P C 02. DATA COMMUNICATION PP 100 40 45 P C 13. COMPUTER NETWORKS PP 100 40 40 P.C 100 40 44 P C 100 40 57 P C 03. MICROPROCESSORS & MICROCONTROLLERPP 14. FINANCE & MANAGEMENT INFORMA.SYS.PP 04. DIGITAL SIGNAL PROCESSING 100 40 40 P C 15. SYSTEMS PROGRAMMING & OPERA.SYS. PP 100 40 44 P.C 05. THEORY OF COMPUTATION 100 40 51 P C PP 100 40 55 P C PP 16. SOFTWARE ENGINEERING 25 50 20 30 P C 10 20 P C 06. RDBMS & VISUAL PROGRAMMING LAB. TW 17. SOFTWARE LABORATORY TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 20 34 P C 18. SOFTWARE LABORATORY 50 20 35 P C 50 PR 08. SIGNAL PROCESSING LABORATORY 25 10 17 P C 19. COMPUTER NETWORK TW 25 10 18 P C TW 50 20 35 P 09. SIGNAL PROCESSING LABORATORY 50 20 30 P C OR 20. COMPUTER NETWORK OR 10. HARDWARE LABORATORY 25 10 20 P C 21. SOFTWARE DEVELOPMENT TOOLS LAB. TW 50 20 30 P C TW 11. HARDWARE LABORATORY PR 50 20 30 P C 22. SEMINAR AND TECHNICAL COMMUNI. TW 50 20 35 P C GRAND TOTAL = 811/1500, RESULT: SECOND CLASS ORDN. 1 MARKS:

DATE: 31 JAN. 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 52 (475)

NOTE: FIRST LINE: SEAT NO., NAME O				-	-				•		
OTHER LINES: HEAD OF PASSING,			•			•	RKS OBTAINED, P/F:PASS/FAIL, C:F				
T80054357 KULKARNI PRATHAMESH DEEF					DYA		, 71132433G , т8054274 ,				т80054357
01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	66	РС	12.	PRINCIPLES OF PROGRAMMING LANG.	PP	100	40	46 P C
02. DATA COMMUNICATION	PP	100	40	48	РС	13.	. COMPUTER NETWORKS	PP	100	40	60 P C
03. MICROPROCESSORS & MICROCONTROLLE	RPP	100	40	52	РС	14.	FINANCE & MANAGEMENT INFORMA.SYS	. PP	100	40	60 P C
04. DIGITAL SIGNAL PROCESSING	PP	100	40	50	РС	15.	SYSTEMS PROGRAMMING & OPERA.SYS.	PP	100	40	67 P C
05. THEORY OF COMPUTATION	PP	100	40	59	РС	16	. SOFTWARE ENGINEERING	PP	100	40	63 P C
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	41	РС	17	SOFTWARE LABORATORY	TW	25	10	20 P C
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	42	РС	18.	SOFTWARE LABORATORY	PR	50	20	42 P C
08. SIGNAL PROCESSING LABORATORY	TW	25	10	19	РС	19	. COMPUTER NETWORK	TW	25	10	18 P C
09. SIGNAL PROCESSING LABORATORY	OR	50	20	31	РС	20	. COMPUTER NETWORK	OR	50	20	36 P
10. HARDWARE LABORATORY	TW	25	10	20	РС	21.	SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	33 P C
11. HARDWARE LABORATORY	PR	50	20		РС		SEMINAR AND TECHNICAL COMMUNI.		50	20	35 P C
GRAND TOTAL = 951/1500, RESULT: FIRST	CLAS	S									
ORDN. 1 MARKS :											
T80054358 KUNTALWAD GNYANESH GAJJA	ARAM			SA	RASWAT	ΉI	, 71045501в , т8054276 ,	PJ	ГСТ	, -	т80054358
01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	40	РС	12.	PRINCIPLES OF PROGRAMMING LANG.	PP	100	40	40 P C
02. DATA COMMUNICATION	PP	100	40	40	РС	13.	. COMPUTER NETWORKS	PP	100	40	40 P C
03. MICROPROCESSORS & MICROCONTROLLE	RPP	100	40	40	РС	14.	FINANCE & MANAGEMENT INFORMA.SYS	.PP	100	40	41 P C
04. DIGITAL SIGNAL PROCESSING	PP	100	40	44	РС	15.	SYSTEMS PROGRAMMING & OPERA.SYS.	PP	100	40	50 P C
05. THEORY OF COMPUTATION	PP	100	40	44	РС	16	SOFTWARE ENGINEERING	PP	100	40	46 P C
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	30	РС		SOFTWARE LABORATORY	TW	25	10	14 P C
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	22	РС	18.	SOFTWARE LABORATORY	PR	50	20	00 F
08. SIGNAL PROCESSING LABORATORY	TW	25	10	14	РС		. COMPUTER NETWORK	TW	25	10	13 P C
09. SIGNAL PROCESSING LABORATORY	OR	50	20	36	РС	20	. COMPUTER NETWORK	OR	50	20	20 P C
10. HARDWARE LABORATORY	TW	25	10	15	РС	21.	SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	22 P C
11. HARDWARE LABORATORY	PR	50	20	21	РС		SEMINAR AND TECHNICAL COMMUNI.	TW	50	20	36 P C
GRAND TOTAL = 668/1500, RESULT: FAILS								RESI			D FOR BKLG
ORDN. 1 MARKS :											
T80054359 LATIFI AAMASH IMTIYAZ AF	HMED			SH	AHERA		, 70925492L , т8054277 ,	PJ	ГСТ	, -	т80054359
01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	50	РС	12.	PRINCIPLES OF PROGRAMMING LANG.	PP	100	40	40 P C
02. DATA COMMUNICATION	PP	100	40	40	РС	13.	. COMPUTER NETWORKS	PP	100	40	43 P C
03. MICROPROCESSORS & MICROCONTROLLE	RPP	100	40		РС	14.	FINANCE & MANAGEMENT INFORMA.SYS	.PP	100	40	46 P C
04. DIGITAL SIGNAL PROCESSING	PP	100	40		F	15.	SYSTEMS PROGRAMMING & OPERA.SYS.	PP	100	40	41 P C
05. THEORY OF COMPUTATION	PP	100	40	48	РС	16.	. SOFTWARE ENGINEERING	PP	100	40	52 P C
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	26	РС	17.	SOFTWARE LABORATORY	TW	25	10	10 P C
07. RDBMS & VISUAL PROGRAMMING LAB.		50	20		РС		SOFTWARE LABORATORY	PR	50	20	22 P
08. SIGNAL PROCESSING LABORATORY	TW	25	10		РC		COMPUTER NETWORK	TW	25	10	11 P C
09. SIGNAL PROCESSING LABORATORY	OR	50	20		РC	_	COMPUTER NETWORK	OR	50	20	22 P
10. HARDWARE LABORATORY	TW	25	10		P C		SOFTWARE DEVELOPMENT TOOLS LAB.	_	50	20	20 P C
11. HARDWARE LABORATORY	PR	50	20		P C			TW	50	20	24 P C
GRAND TOTAL = 647/1500, RESULT: FAILS			= *		-			==	- •	= =	- -
ORDN. 1 MARKS:											
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DATE: 31 JAN. 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 53 (476)

NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71045510M , T8054279 , PICT T80054360 MAHARANWAR NAMITA ANILRAO RENUKA , т80054360 01. DATABASE MANAGEMENT SYSTEMS PP 100 40 57 P C 12. PRINCIPLES OF PROGRAMMING LANG. PP 100 40 51 P C 13. COMPUTER NETWORKS PP 100 02. DATA COMMUNICATION 40 47 P C 47 P.C 100 100 40 57 P C 14. FINANCE & MANAGEMENT INFORMA.SYS.PP 100 40 56 P C 03. MICROPROCESSORS & MICROCONTROLLERPP 04. DIGITAL SIGNAL PROCESSING 100 40 46 P C 15. SYSTEMS PROGRAMMING & OPERA.SYS. PP 100 40 69 P C 05. THEORY OF COMPUTATION 100 40 61 P C 100 62 P.C 16. SOFTWARE ENGINEERING 40 20 40 P C 25 10 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 17. SOFTWARE LABORATORY TW 18 P C 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 20 P C 50 20 31 P C 18. SOFTWARE LABORATORY PR 08. SIGNAL PROCESSING LABORATORY 25 10 19 P C 19. COMPUTER NETWORK 25 18 P C TW TW 10 50 20 22 P C 50 20 35 P 09. SIGNAL PROCESSING LABORATORY 20. COMPUTER NETWORK OR OR 10. HARDWARE LABORATORY 25 10 20 P C 50 20 32 P C TW 21. SOFTWARE DEVELOPMENT TOOLS LAB. TW PR 50 20 26 P C 11. HARDWARE LABORATORY 22. SEMINAR AND TECHNICAL COMMUNI. TW 50 20 41 P C GRAND TOTAL = 875/1500, RESULT: HIGHER SECOND CLASS ORDN. 1 MARKS: , 70925499н , т8054283 , РІСТ T80054361 MANE OM ARUN RAJANI , т80054361 40 P C 12. PRINCIPLES OF PROGRAMMING LANG. PP 100 40 32# P 01. DATABASE MANAGEMENT SYSTEMS PP 100 40 02. DATA COMMUNICATION PP 100 40 P C 13. COMPUTER NETWORKS PP 100 40 40 48 P 40 40 P C 03. MICROPROCESSORS & MICROCONTROLLERPP 100 14. FINANCE & MANAGEMENT INFORMA.SYS.PP 100 40 48 P C 04. DIGITAL SIGNAL PROCESSING 40 40 P C 100 PP 100 15. SYSTEMS PROGRAMMING & OPERA.SYS. PP 40 40 P C 40 45 P C 100 05. THEORY OF COMPUTATION 100 16. SOFTWARE ENGINEERING 40 45 P C 50 20 24 P C 25 06. RDBMS & VISUAL PROGRAMMING LAB. TW 17. SOFTWARE LABORATORY 10 10 P C TW 50 20 22 P C 50 20 07. RDBMS & VISUAL PROGRAMMING LAB. PR 18. SOFTWARE LABORATORY PR 28 P C 08. SIGNAL PROCESSING LABORATORY TW 25 10 11 P C 19. COMPUTER NETWORK TW 25 10 11 P C 50 20 21 P C 09. SIGNAL PROCESSING LABORATORY OR 20. COMPUTER NETWORK OR 50 20 26 P 10. HARDWARE LABORATORY 25 10 14 P C 50 20 20 P C TW 21. SOFTWARE DEVELOPMENT TOOLS LAB. TW 11. HARDWARE LABORATORY PR 50 20 28 P C 22. SEMINAR AND TECHNICAL COMMUNI. TW 50 20 28 P C GRAND TOTAL = 661/1500, RESULT: PASS CLASS # [0.4]ORDN. 1 MARKS: , 71045523C , T8054286 , PICT SANGEETA T80054362 MASKE SHRUTI HIRALAL , т80054362 01. DATABASE MANAGEMENT SYSTEMS PP 100 40 40 P C 12. PRINCIPLES OF PROGRAMMING LANG. PP 100 40 45 P C 02. DATA COMMUNICATION PP 100 40 44 P.C 13. COMPUTER NETWORKS PP 100 40 53 P 100 40 47 P C 100 40 42 P C 03. MICROPROCESSORS & MICROCONTROLLERPP 14. FINANCE & MANAGEMENT INFORMA.SYS.PP 04. DIGITAL SIGNAL PROCESSING 100 40 42 P C 15. SYSTEMS PROGRAMMING & OPERA.SYS. PP 100 40 49 P.C 05. THEORY OF COMPUTATION 100 40 48 P C PP 100 40 46 P.C PP 16. SOFTWARE ENGINEERING 25 50 20 40 P C 10 19 P C 06. RDBMS & VISUAL PROGRAMMING LAB. TW 17. SOFTWARE LABORATORY TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 20 35 P C 18. SOFTWARE LABORATORY 50 20 28 P C 50 PR 08. SIGNAL PROCESSING LABORATORY 25 10 19 P C 19. COMPUTER NETWORK TW 25 10 16 P C TW 50 20 32 P 09. SIGNAL PROCESSING LABORATORY 50 20 36 P C OR 20. COMPUTER NETWORK 10. HARDWARE LABORATORY 25 10 19 P C 21. SOFTWARE DEVELOPMENT TOOLS LAB. TW 50 20 32 P C TW 11. HARDWARE LABORATORY PR 50 20 27 P C 22. SEMINAR AND TECHNICAL COMMUNI. TW 50 20 42 P C GRAND TOTAL = 801/1500, RESULT: SECOND CLASS ORDN. 1 MARKS:

DATE: 31 JAN. 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 54 (477)

NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER KALPANA , 71045534J , T8054288 , PICT T80054363 MUSALE SAGAR RAJENDRA , т80054363 01. DATABASE MANAGEMENT SYSTEMS 12. PRINCIPLES OF PROGRAMMING LANG. PP 100 40 55 P C PP 100 40 52 P C 13. COMPUTER NETWORKS 02. DATA COMMUNICATION 40 41 P C PP 100 PP 100 100 40 52 P C 100 40 46 P C 03. MICROPROCESSORS & MICROCONTROLLERPP 14. FINANCE & MANAGEMENT INFORMA.SYS.PP 04. DIGITAL SIGNAL PROCESSING 100 40 46 P C 15. SYSTEMS PROGRAMMING & OPERA.SYS. PP 100 52 P C 40 05. THEORY OF COMPUTATION 100 40 40 P C 100 40 41 P.C 16. SOFTWARE ENGINEERING 50 20 40 P C 25 10 19 P C 06. RDBMS & VISUAL PROGRAMMING LAB. TW 17. SOFTWARE LABORATORY TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 39 P C 50 20 42 P C 18. SOFTWARE LABORATORY PR 25 10 17 P C 19. COMPUTER NETWORK 25 21 P C 08. SIGNAL PROCESSING LABORATORY TW TW 10 50 20 34 P C 50 20 30 P 09. SIGNAL PROCESSING LABORATORY 20. COMPUTER NETWORK OR OR 10. HARDWARE LABORATORY 25 10 19 P C 50 20 34 P C TW 21. SOFTWARE DEVELOPMENT TOOLS LAB. TW PR 50 20 38 P C 11. HARDWARE LABORATORY 22. SEMINAR AND TECHNICAL COMMUNI. TW 50 20 42 P C GRAND TOTAL = 840/1500, RESULT: HIGHER SECOND CLASS ORDN. 1 MARKS: MEERA , 71045540C , T8054292 , PICT T80054364 NAJAN KAUSTUBH GORAKSHANATH , т80054364 01. DATABASE MANAGEMENT SYSTEMS 40 P C 12. PRINCIPLES OF PROGRAMMING LANG. PP 100 40 40 PC 100 40 13. COMPUTER NETWORKS PP 100 02. DATA COMMUNICATION 100 40 42 P C 40 41 P C 40 40 P C 03. MICROPROCESSORS & MICROCONTROLLERPP 100 100 40 40 P C 14. FINANCE & MANAGEMENT INFORMA.SYS.PP 04. DIGITAL SIGNAL PROCESSING 100 100 40 40 P C 15. SYSTEMS PROGRAMMING & OPERA.SYS. PP 40 45 P C 41 P C 100 05. THEORY OF COMPUTATION 100 40 16. SOFTWARE ENGINEERING 40 49 P C 20 25 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 45 P C 17. SOFTWARE LABORATORY 10 20 P C TW 50 20 50 20 07. RDBMS & VISUAL PROGRAMMING LAB. PR 34 P C 18. SOFTWARE LABORATORY PR 40 P C 08. SIGNAL PROCESSING LABORATORY TW 25 10 17 P C 19. COMPUTER NETWORK TW 25 10 21 P C 50 20 28 P 09. SIGNAL PROCESSING LABORATORY OR 50 20 20 P C 20. COMPUTER NETWORK OR 10. HARDWARE LABORATORY 25 10 20 P C 50 20 35 P C TW 21. SOFTWARE DEVELOPMENT TOOLS LAB. TW 11. HARDWARE LABORATORY PR 50 20 35 P C 22. SEMINAR AND TECHNICAL COMMUNI. TW 50 20 36 P.C. GRAND TOTAL = 769/1500, RESULT: SECOND CLASS ORDN. 1 MARKS: , 71045541M , T8054294 , PICT KAMLESH T80054365 NANWANI SHRUTI RAMESH , т80054365 01. DATABASE MANAGEMENT SYSTEMS PP 100 40 67 P C 12. PRINCIPLES OF PROGRAMMING LANG. PP 100 40 60 P C 02. DATA COMMUNICATION PP 100 40 45 P C 13. COMPUTER NETWORKS PP 100 40 58 P C 100 40 62 P C 100 40 59 P C 03. MICROPROCESSORS & MICROCONTROLLERPP 14. FINANCE & MANAGEMENT INFORMA.SYS.PP 04. DIGITAL SIGNAL PROCESSING 100 40 61 P C 15. SYSTEMS PROGRAMMING & OPERA.SYS. PP 100 40 60 P C 05. THEORY OF COMPUTATION 100 40 62 P C PP 100 40 PP 16. SOFTWARE ENGINEERING 66 P C 25 50 20 40 P C 10 17 P C 06. RDBMS & VISUAL PROGRAMMING LAB. TW 17. SOFTWARE LABORATORY TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 20 39 P C 50 20 36 P C 50 18. SOFTWARE LABORATORY PR 08. SIGNAL PROCESSING LABORATORY 25 10 18 P C 19. COMPUTER NETWORK TW 25 10 19 P C TW 09. SIGNAL PROCESSING LABORATORY 50 20 38 P C 50 20 33 P OR 20. COMPUTER NETWORK 10. HARDWARE LABORATORY 25 10 20 P C 50 20 25 P C TW 21. SOFTWARE DEVELOPMENT TOOLS LAB. TW 11. HARDWARE LABORATORY PR 50 20 31 P C 22. SEMINAR AND TECHNICAL COMMUNI. TW 50 20 39 P C GRAND TOTAL = 955/1500, RESULT: FIRST CLASS ORDN. 1 MARKS:

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NOTE: FIRST LINE : SEAT NO., NAME												
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					· · · \RASWAT		, 71045550L , T8054299				T8005	
01. DATABASE MANAGEMENT SYSTEMS	DD	100	40		P C		PRINCIPLES OF PROGRAMMING LANG.		100	, 40		P C
02. DATA COMMUNICATION		100	40		PC				100	40		PC
	PP		_				COMPUTER NETWORKS FINANCE & MANAGEMENT INFORMA.SYS	PP				PC
03. MICROPROCESSORS & MICROCONTROLL		100	40		P C				100	40		
04. DIGITAL SIGNAL PROCESSING	PP	100	40	60			SYSTEMS PROGRAMMING & OPERA.SYS		100	40		PC
05. THEORY OF COMPUTATION		100	40		P C		SOFTWARE ENGINEERING	PP Tu	100	40		PC
06. RDBMS & VISUAL PROGRAMMING LAB.		50	20	38	PC		SOFTWARE LABORATORY	TW	25	10		PC
07. RDBMS & VISUAL PROGRAMMING LAB.		50	20	43			SOFTWARE LABORATORY	PR	50	20		P C
08. SIGNAL PROCESSING LABORATORY	TW	25	10	20	_		COMPUTER NETWORK	TW	25	10	_	P C
09. SIGNAL PROCESSING LABORATORY		50	20	26			COMPUTER NETWORK	OR	50	20	_	P
10. HARDWARE LABORATORY		25	10	20			SOFTWARE DEVELOPMENT TOOLS LAB.		50	20		P C
11. HARDWARE LABORATORY	PR	50	20	35	РС	22.	SEMINAR AND TECHNICAL COMMUNI.	I W	50	20	43	РС
GRAND TOTAL = 888/1500, RESULT: HIGH	ER SE	COND C	LASS									
ORDN. 1 MARKS :												
T80054367 PANDYA PARTH VIPUL		100	40		SHA	4.2	, 71045553E , T8054300			-	T8005	
01. DATABASE MANAGEMENT SYSTEMS		100	40		РС		PRINCIPLES OF PROGRAMMING LANG.		100	40		РС
02. DATA COMMUNICATION	PP	100	40		РС		COMPUTER NETWORKS		100	40		РС
03. MICROPROCESSORS & MICROCONTROLL		100	40	48			FINANCE & MANAGEMENT INFORMA.SYS		100	40		РС
04. DIGITAL SIGNAL PROCESSING	PP	100	40	40	_		SYSTEMS PROGRAMMING & OPERA.SYS		100	40		РС
05. THEORY OF COMPUTATION		100	40	60			SOFTWARE ENGINEERING	PP	100	40		РС
06. RDBMS & VISUAL PROGRAMMING LAB.		50	20	28	РС		SOFTWARE LABORATORY	TW	25	10		РС
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	33	РС		SOFTWARE LABORATORY	PR	50	20		РС
08. SIGNAL PROCESSING LABORATORY	TW	25	10	14			COMPUTER NETWORK	TW	25	10		РС
09. SIGNAL PROCESSING LABORATORY	OR	50	20	33	_		COMPUTER NETWORK	OR	50	20	40	
10. HARDWARE LABORATORY	TW	25	10		РС		SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20		РС
11. HARDWARE LABORATORY	PR	50	20	25	РС	22.	SEMINAR AND TECHNICAL COMMUNI.	TW	50	20	31	РС
GRAND TOTAL = $807/1500$, RESULT: SECO	ND CLA	ASS										
ORDN. 1 MARKS :												
T80054368 PATIL SWAPNIL PRABHAKAR				UR	RMILA		, 70601435к , т8054380		ICT	,	Т8005	
01. DATABASE MANAGEMENT SYSTEMS	PP	100	40		PС	12.	PRINCIPLES OF PROGRAMMING LANG.	PP	100	40		РС
02. DATA COMMUNICATION	PP	100	40	40	PС	13.	COMPUTER NETWORKS	PP	100	40		F
03. MICROPROCESSORS & MICROCONTROLL	ERPP	100	40	01	F	14.	FINANCE & MANAGEMENT INFORMA.SYS	S.PP	100	40	46	РС
04. DIGITAL SIGNAL PROCESSING	PP	100	40	AA	F	15.	SYSTEMS PROGRAMMING & OPERA.SYS	. PP	100	40	42	РС
05. THEORY OF COMPUTATION	PP	100	40	AA	F	16.	SOFTWARE ENGINEERING	PP	100	40	40	РС
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	21	PC	17.	SOFTWARE LABORATORY	TW	25	10	10	РС
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	25	PC	18.	SOFTWARE LABORATORY	PR	50	20	24	РС
08. SIGNAL PROCESSING LABORATORY	TW	25	10	13	PC	19.	COMPUTER NETWORK	TW	25	10	14	РС
09. SIGNAL PROCESSING LABORATORY	OR	50	20	21	PC	20.	COMPUTER NETWORK	OR	50	20	22	РС
10. HARDWARE LABORATORY	TW	25	10	14	PC	21.	SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	32	РС
11. HARDWARE LABORATORY	PR	50	20	24	PC	22.	SEMINAR AND TECHNICAL COMMUNI.	TW	50	20	37	РС
GRAND TOTAL = 522/1500, RESULT: FAIL	S											
ORDN. 1 MARKS :												

DATE: 31 JAN. 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 56 (479)

NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 70601438D , T8054381 , PICT T80054369 PATIL VINEET JAYANT PRAJAKTA , т80054369 PP 100 40 46 P C 12. PRINCIPLES OF PROGRAMMING LANG. PP 100 40 45 PC 01. DATABASE MANAGEMENT SYSTEMS 13. COMPUTER NETWORKS PP 100 02. DATA COMMUNICATION PP 100 40 AA F 100 40 AA F 14. FINANCE & MANAGEMENT INFORMA.SYS.PP 100 40 03. MICROPROCESSORS & MICROCONTROLLERPP 46 P C 04. DIGITAL SIGNAL PROCESSING 100 40 41 P C 15. SYSTEMS PROGRAMMING & OPERA.SYS. PP 100 62 P C 40 05. THEORY OF COMPUTATION 100 40 44 P C 100 40 40 P.C 16. SOFTWARE ENGINEERING 50 20 20 P C 25 10 10 P C 06. RDBMS & VISUAL PROGRAMMING LAB. TW 17. SOFTWARE LABORATORY TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 AA F 50 20 AA F 18. SOFTWARE LABORATORY PR 08. SIGNAL PROCESSING LABORATORY 25 10 11 P C 19. COMPUTER NETWORK 25 10 12 P C TW TW 50 20 22 P C 50 20 21 P C 09. SIGNAL PROCESSING LABORATORY 20. COMPUTER NETWORK OR OR 10. HARDWARE LABORATORY 25 10 10 P C 50 20 21 P C TW 21. SOFTWARE DEVELOPMENT TOOLS LAB. TW 11. HARDWARE LABORATORY PR 50 20 27 P C 22. SEMINAR AND TECHNICAL COMMUNI. TW 50 20 21 P C GRAND TOTAL = 543/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS: VIDYA , 71045570E , T8054307 , PICT T80054370 PAWAR AJINKYA NANDU , т80054370 PP 100 40 45 P C 12. PRINCIPLES OF PROGRAMMING LANG. PP 100 40 AA F 01. DATABASE MANAGEMENT SYSTEMS 02. DATA COMMUNICATION 51 P C 13. COMPUTER NETWORKS PP 100 PP 100 40 40 AA F 40 41 P C 40 03. MICROPROCESSORS & MICROCONTROLLERPP 100 14. FINANCE & MANAGEMENT INFORMA.SYS.PP 100 50 P C 04. DIGITAL SIGNAL PROCESSING 100 46 P C PP 100 40 AA F 15. SYSTEMS PROGRAMMING & OPERA.SYS. PP 40 40 40 P C 100 05. THEORY OF COMPUTATION 100 16. SOFTWARE ENGINEERING 40 42 P C 20 24 P C 25 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 17. SOFTWARE LABORATORY 10 10 P C TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 28 P C 50 20 05 F 18. SOFTWARE LABORATORY PR 25 08. SIGNAL PROCESSING LABORATORY TW 25 10 11 P C 19. COMPUTER NETWORK TW 10 11 P C 50 20 20 P C 50 20 09. SIGNAL PROCESSING LABORATORY OR 20. COMPUTER NETWORK OR 29 P 10. HARDWARE LABORATORY 25 10 11 P C 50 20 20 P C TW 21. SOFTWARE DEVELOPMENT TOOLS LAB. TW 11. HARDWARE LABORATORY PR 50 20 22 P C 22. SEMINAR AND TECHNICAL COMMUNI. TW 50 20 29 P.C. GRAND TOTAL = 535/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS: , 71132438н , Т8054308 , РІСТ RENUKA T80054371 PAWAR HARISH MANGESH , т80054371 01. DATABASE MANAGEMENT SYSTEMS PP 100 40 50 P C 12. PRINCIPLES OF PROGRAMMING LANG. PP 100 40 60 P C 02. DATA COMMUNICATION PP 100 40 49 P C 13. COMPUTER NETWORKS PP 100 40 59 P.C 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 52 P C 100 40 64 P C 14. FINANCE & MANAGEMENT INFORMA.SYS.PP 04. DIGITAL SIGNAL PROCESSING 100 40 53 P C 15. SYSTEMS PROGRAMMING & OPERA.SYS. PP 100 40 67 P C 05. THEORY OF COMPUTATION 100 40 40 P C PP 100 40 72 P C PP 16. SOFTWARE ENGINEERING 25 50 20 35 P C 10 14 P C 06. RDBMS & VISUAL PROGRAMMING LAB. TW 17. SOFTWARE LABORATORY TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 38 P C 18. SOFTWARE LABORATORY 50 20 25 P C PR 08. SIGNAL PROCESSING LABORATORY TW 25 10 14 P C 19. COMPUTER NETWORK TW 25 10 14 P C 50 20 29 P 09. SIGNAL PROCESSING LABORATORY 50 20 25 P C OR 20. COMPUTER NETWORK OR 10. HARDWARE LABORATORY 25 10 16 P C 21. SOFTWARE DEVELOPMENT TOOLS LAB. TW 50 20 29 P C TW PR 50 20 33 P C 11. HARDWARE LABORATORY 22. SEMINAR AND TECHNICAL COMMUNI. TW 50 20 28 P C GRAND TOTAL = 866/1500, RESULT: HIGHER SECOND CLASS ORDN. 1 MARKS:

DATE: 31 JAN. 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 57 (480)

NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER ROHINI , 70925566H , Т8054310 , РІСТ T80054372 POORVI ARVIND DHARWAD , т80054372 01. DATABASE MANAGEMENT SYSTEMS 45 P 12. PRINCIPLES OF PROGRAMMING LANG. PP 100 40 P PP 100 40 13. COMPUTER NETWORKS PP 100 02. DATA COMMUNICATION PP 100 40 40 P C 100 40 40 P C 14. FINANCE & MANAGEMENT INFORMA.SYS.PP 100 40 03. MICROPROCESSORS & MICROCONTROLLERPP 40 P C 04. DIGITAL SIGNAL PROCESSING 100 40 40 P 15. SYSTEMS PROGRAMMING & OPERA.SYS. PP 100 40 31# P 05. THEORY OF COMPUTATION 100 40 40 P C 100 40 40 P.C 16. SOFTWARE ENGINEERING 50 20 32 P C 25 10 18 P C 06. RDBMS & VISUAL PROGRAMMING LAB. TW 17. SOFTWARE LABORATORY TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 26 P C 50 20 21 P 18. SOFTWARE LABORATORY PR 25 10 20 P C 19. COMPUTER NETWORK 25 10 17 P C 08. SIGNAL PROCESSING LABORATORY TW TW 50 20 20 P C 50 20 32 P 09. SIGNAL PROCESSING LABORATORY 20. COMPUTER NETWORK OR OR 10. HARDWARE LABORATORY 25 10 21 P C 50 20 29 P C TW 21. SOFTWARE DEVELOPMENT TOOLS LAB. TW 11. HARDWARE LABORATORY PR 50 20 21 P 22. SEMINAR AND TECHNICAL COMMUNI. TW 50 20 41 P C GRAND TOTAL = 694/1500, RESULT: PASS CLASS # [0.4] RESULT RESERVED FOR BKLG ORDN. 1 MARKS: , 71045588H , Т8054317 , РІСТ T80054373 RAWALE SUHAS SUDAMRAO RATNMALA , т80054373 40 P C 12. PRINCIPLES OF PROGRAMMING LANG. PP 100 40 43 P C 01. DATABASE MANAGEMENT SYSTEMS PP 100 40 13. COMPUTER NETWORKS PP 100 02. DATA COMMUNICATION PP 100 40 43 P C 40 46 P C 40 40 P C 14. FINANCE & MANAGEMENT INFORMA.SYS.PP 100 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 42 P C 04. DIGITAL SIGNAL PROCESSING 40 100 PP 100 54 P C 15. SYSTEMS PROGRAMMING & OPERA.SYS. PP 40 47 P C 40 40 P C 100 59 P C 05. THEORY OF COMPUTATION 100 16. SOFTWARE ENGINEERING 40 20 35 P C 25 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 17. SOFTWARE LABORATORY 10 14 P C TW 50 20 32 P 20 25 P 07. RDBMS & VISUAL PROGRAMMING LAB. PR 18. SOFTWARE LABORATORY PR 50 08. SIGNAL PROCESSING LABORATORY TW 25 10 14 P C 19. COMPUTER NETWORK TW 25 10 14 P C 50 20 28 P 09. SIGNAL PROCESSING LABORATORY OR 50 20 25 P C 20. COMPUTER NETWORK OR 10. HARDWARE LABORATORY 25 10 13 P C 50 20 24 P C TW 21. SOFTWARE DEVELOPMENT TOOLS LAB. TW 11. HARDWARE LABORATORY 50 20 23 P C 22. SEMINAR AND TECHNICAL COMMUNI. TW 50 20 32 P C PR GRAND TOTAL = 733/1500, RESULT: PASS CLASS ORDN. 1 MARKS: , 71045593D , T8054318 , PICT MADHURI T80054374 SAKORE MITHILA RAMNATH , т80054374 01. DATABASE MANAGEMENT SYSTEMS PP 100 40 71 P C 12. PRINCIPLES OF PROGRAMMING LANG. PP 100 40 56 P C 02. DATA COMMUNICATION PP 100 40 63 P C 13. COMPUTER NETWORKS PP 100 40 58 P C 100 40 49 P C 100 40 65 P C 03. MICROPROCESSORS & MICROCONTROLLERPP 14. FINANCE & MANAGEMENT INFORMA.SYS.PP 04. DIGITAL SIGNAL PROCESSING 100 40 40 P C 15. SYSTEMS PROGRAMMING & OPERA.SYS. PP 100 40 61 P C 05. THEORY OF COMPUTATION 100 40 65 P C PP 100 40 76 P C PP 16. SOFTWARE ENGINEERING 25 50 20 39 P C 10 14 P C 06. RDBMS & VISUAL PROGRAMMING LAB. TW 17. SOFTWARE LABORATORY TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 20 28 P C 18. SOFTWARE LABORATORY 50 20 29 P C 50 PR 15 P C 08. SIGNAL PROCESSING LABORATORY 25 10 18 P C 19. COMPUTER NETWORK TW 25 10 TW 20 32 P 09. SIGNAL PROCESSING LABORATORY 50 20 24 P C 50 OR 20. COMPUTER NETWORK OR 10. HARDWARE LABORATORY 25 10 19 P C 50 20 29 P C TW 21. SOFTWARE DEVELOPMENT TOOLS LAB. TW 11. HARDWARE LABORATORY PR 50 20 21 P C 22. SEMINAR AND TECHNICAL COMMUNI. TW 50 20 40 P C GRAND TOTAL = 912/1500, RESULT: FIRST CLASS ORDN. 1 MARKS:

DATE: 31 JAN. 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 58 (481)

NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 70925587L , T8054319 , PICT T80054375 SANDEEP AGARWAL RITA , т80054375 47 P C 12. PRINCIPLES OF PROGRAMMING LANG. PP 100 40 19 F 01. DATABASE MANAGEMENT SYSTEMS PP 100 40 13. COMPUTER NETWORKS PP 100 02. DATA COMMUNICATION 100 40 40 P C 41 P C 100 40 AA F 14. FINANCE & MANAGEMENT INFORMA.SYS.PP 100 40 40 P.C 03. MICROPROCESSORS & MICROCONTROLLERPP 04. DIGITAL SIGNAL PROCESSING 100 40 13 F 15. SYSTEMS PROGRAMMING & OPERA.SYS. PP 100 11 F 40 05. THEORY OF COMPUTATION 100 40 50 P C 100 40 16. SOFTWARE ENGINEERING 20 34 P C 25 10 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 17. SOFTWARE LABORATORY TW 10 P C 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 20 P C 50 20 22 P C 18. SOFTWARE LABORATORY PR 25 10 15 P C 19. COMPUTER NETWORK 25 10 11 P C 08. SIGNAL PROCESSING LABORATORY TW TW 50 20 22 P C 50 20 21 P C 09. SIGNAL PROCESSING LABORATORY 20. COMPUTER NETWORK OR OR 10. HARDWARE LABORATORY 25 10 17 P C 50 20 20 P C TW 21. SOFTWARE DEVELOPMENT TOOLS LAB. TW 11. HARDWARE LABORATORY 50 20 30 P C 22. SEMINAR AND TECHNICAL COMMUNI. TW 50 20 24 P C PR GRAND TOTAL = 551/1500, RESULT: FAILS RESULT RESERVED FOR BKLG ORDN. 1 MARKS: , 70925598F , T8054326 , PICT T80054376 SHASHANK KUMAR GUPTA ANJANI , т80054376 PP 100 40 44 P C 12. PRINCIPLES OF PROGRAMMING LANG. PP 100 40 40 PC 01. DATABASE MANAGEMENT SYSTEMS 02. DATA COMMUNICATION PP 100 13. COMPUTER NETWORKS PP 100 40 44 P C 40 43 P C 40 40 P C 14. FINANCE & MANAGEMENT INFORMA.SYS.PP 100 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 48 P C 04. DIGITAL SIGNAL PROCESSING 40 100 PP 100 44 P C 15. SYSTEMS PROGRAMMING & OPERA.SYS. PP 40 42 P C 40 45 P C 100 05. THEORY OF COMPUTATION 100 16. SOFTWARE ENGINEERING 40 57 P C 25 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 20 25 P C 17. SOFTWARE LABORATORY 10 10 P C TW 20 33 P C 20 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 18. SOFTWARE LABORATORY PR 50 22 P C 08. SIGNAL PROCESSING LABORATORY TW 25 10 12 P C 19. COMPUTER NETWORK TW 25 10 11 P C 09. SIGNAL PROCESSING LABORATORY OR 50 20 20 P C 20. COMPUTER NETWORK OR 50 20 28 P 10. HARDWARE LABORATORY 25 10 16 P C 50 20 20 P C TW 21. SOFTWARE DEVELOPMENT TOOLS LAB. TW 11. HARDWARE LABORATORY 50 20 20 P C PR 22. SEMINAR AND TECHNICAL COMMUNI. TW 50 20 23 P.C. GRAND TOTAL = 687/1500, RESULT: PASS CLASS ORDN. 1 MARKS: , 70925601к , т8054328 , РІСТ SMITA T80054377 SHERKAR ANIRUDH UMAKANT , т80054377 01. DATABASE MANAGEMENT SYSTEMS PP 100 40 40 P C 12. PRINCIPLES OF PROGRAMMING LANG. PP 100 40 AA F PP 100 02. DATA COMMUNICATION 40 40 P C 13. COMPUTER NETWORKS PP 100 40 40 P C 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 40 P C 100 40 40 P C 14. FINANCE & MANAGEMENT INFORMA.SYS.PP 04. DIGITAL SIGNAL PROCESSING 100 40 23 F 15. SYSTEMS PROGRAMMING & OPERA.SYS. PP 100 40 43 P.C. 05. THEORY OF COMPUTATION 100 40 42 P C PP 100 40 44 P C PP 16. SOFTWARE ENGINEERING 25 50 20 40 P C 10 19 P C 06. RDBMS & VISUAL PROGRAMMING LAB. TW 17. SOFTWARE LABORATORY TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 42 P C 18. SOFTWARE LABORATORY 50 20 34 P C PR 08. SIGNAL PROCESSING LABORATORY 25 10 19 P C 19. COMPUTER NETWORK TW 25 10 15 P C TW 28 P 09. SIGNAL PROCESSING LABORATORY 50 20 32 P C 50 20 OR 20. COMPUTER NETWORK OR 10. HARDWARE LABORATORY 25 10 21 P C 21. SOFTWARE DEVELOPMENT TOOLS LAB. TW 50 20 29 P C TW 11. HARDWARE LABORATORY PR 50 20 33 P C 22. SEMINAR AND TECHNICAL COMMUNI. TW 50 20 38 P C GRAND TOTAL = 702/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS:

DATE: 31 JAN. 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 59 (482)

NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , т80054378 SANGEETA , 71045614L , T8054329 , PICT T80054378 SHINDE AMIT KEDARNATH PP 100 40 52 P C 01. DATABASE MANAGEMENT SYSTEMS 12. PRINCIPLES OF PROGRAMMING LANG. PP 100 40 PC 13. COMPUTER NETWORKS 02. DATA COMMUNICATION 100 40 42 P C PP 100 100 40 56 P 100 40 42 P C 03. MICROPROCESSORS & MICROCONTROLLERPP 14. FINANCE & MANAGEMENT INFORMA.SYS.PP 04. DIGITAL SIGNAL PROCESSING 100 40 26 F 15. SYSTEMS PROGRAMMING & OPERA.SYS. PP 100 40 43 P C 05. THEORY OF COMPUTATION 100 40 45 P C 100 40 49 P.C 16. SOFTWARE ENGINEERING 50 20 38 P C 25 10 06. RDBMS & VISUAL PROGRAMMING LAB. TW 17. SOFTWARE LABORATORY TW 21 P C 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 28 P C 50 20 40 P C 18. SOFTWARE LABORATORY PR 08. SIGNAL PROCESSING LABORATORY 25 10 15 P C 19. COMPUTER NETWORK 25 10 17 P C TW TW 50 20 20 P C 50 20 41 P C 09. SIGNAL PROCESSING LABORATORY 20. COMPUTER NETWORK OR OR 10. HARDWARE LABORATORY 25 10 17 P C 50 20 37 P C TW 21. SOFTWARE DEVELOPMENT TOOLS LAB. TW 11. HARDWARE LABORATORY PR 50 20 36 P C 22. SEMINAR AND TECHNICAL COMMUNI. TW 50 20 33 P C GRAND TOTAL = 778/1500, RESULT: FAILS A.T.K.T. RESULT RESERVED FOR BKLG ORDN. 1 MARKS: T80054379 SHUBHAM SINGH , 70601498H , T8054387 , PICT , T80054379 JAISHREE 01. DATABASE MANAGEMENT SYSTEMS PP 100 40 AA F 02. DATA COMMUNICATION PP 100 40 AA F AA F 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 40 04. DIGITAL SIGNAL PROCESSING 100 AA F 40 AA F 05. THEORY OF COMPUTATION 100 20 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 20 P C 07. RDBMS & VISUAL PROGRAMMING LAB. PR 20 50 AA F 25 10 11 P C 08. SIGNAL PROCESSING LABORATORY TW 09. SIGNAL PROCESSING LABORATORY 50 20 20 P C OR 10. HARDWARE LABORATORY 25 10 10 P C TW 50 20 20 P C 11. HARDWARE LABORATORY PR FIRST TERM TOTAL = 81/750. ORDN. 1 MARKS: , 71045634E , T8054336 , PICT KALPANA , т80054380 T80054380 SOUNDANKAR KOMAL RAJENDRA 01. DATABASE MANAGEMENT SYSTEMS 100 40 40 P C 12. PRINCIPLES OF PROGRAMMING LANG. PP 100 40 45 P C 02. DATA COMMUNICATION PP 100 40 42 P C 13. COMPUTER NETWORKS PP 100 40 48 P.C 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 42 P C 100 40 53 P C 14. FINANCE & MANAGEMENT INFORMA.SYS.PP 04. DIGITAL SIGNAL PROCESSING 100 40 40 P C 15. SYSTEMS PROGRAMMING & OPERA.SYS. PP 100 40 54 P C 05. THEORY OF COMPUTATION 100 40 40 P C 100 40 60 P C PP 16. SOFTWARE ENGINEERING PP 25 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 20 34 P C 10 14 P C 17. SOFTWARE LABORATORY TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 24 P C 18. SOFTWARE LABORATORY 50 20 32 P C PR 08. SIGNAL PROCESSING LABORATORY 25 10 15 P C 19. COMPUTER NETWORK TW 25 10 15 P C TW 20 27 P 09. SIGNAL PROCESSING LABORATORY 50 20 21 P C 50 OR 20. COMPUTER NETWORK 10. HARDWARE LABORATORY 25 10 19 P C 21. SOFTWARE DEVELOPMENT TOOLS LAB. TW 50 20 25 P C TW 11. HARDWARE LABORATORY PR 50 20 25 P C 22. SEMINAR AND TECHNICAL COMMUNI. TW 50 20 41 P C GRAND TOTAL = 756/1500, RESULT: SECOND CLASS ORDN. 1 MARKS:

DATE: 31 JAN. 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 60 (483)

							R TECHNOLOGY, PUNE.		GE NO.		•	•
NOTE: FIRST LINE : SEAT NO., NAME O												
OTHER LINES: HEAD OF PASSING,	MAX.	MARK	S, M	IN. P	ASS MARKS	, MAF	KS OBTAINED, P/F:PASS/FAIL, C:P	REVI	OUS CAI	RRY O	/ER	
T80054381 THAKARE SAGAR SATISH				_	YASHREE		, 70925632к , т8054343 ,		ICT	-	г8005	
01. DATABASE MANAGEMENT SYSTEMS	PP	100	40		PC		PRINCIPLES OF PROGRAMMING LANG.	PP	100	40		РС
02. DATA COMMUNICATION	PP	100	40	47			COMPUTER NETWORKS	PP	100	40	40	
03. MICROPROCESSORS & MICROCONTROLLE	RPP	100	40	40			FINANCE & MANAGEMENT INFORMA.SYS		100	40		РС
04. DIGITAL SIGNAL PROCESSING	PP	100	40	23	F		SYSTEMS PROGRAMMING & OPERA.SYS.	PP	100	40	59	РС
05. THEORY OF COMPUTATION	PP	100	40	40	PC	16.	SOFTWARE ENGINEERING	PP	100	40		РС
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	30	PC	17.	SOFTWARE LABORATORY	TW	25	10		РС
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	AA	F	18.	SOFTWARE LABORATORY	PR	50	20		PC
08. SIGNAL PROCESSING LABORATORY	TW	25	10	12	PC	19.	COMPUTER NETWORK	TW	25	10	11	PС
09. SIGNAL PROCESSING LABORATORY	OR	50	20	25	PC	20.	COMPUTER NETWORK	OR	50	20	21	PС
10. HARDWARE LABORATORY	TW	25	10	15	PC	21.	SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	20	PС
11. HARDWARE LABORATORY	PR	50	20	21	Р	22.	SEMINAR AND TECHNICAL COMMUNI.	TW	50	20	33	PС
GRAND TOTAL = 649/1500, RESULT: FAILS	A.T.	K.T.	[\$	0.1]				RES	ULT RE	SERVE) FOR	BKLG
ORDN. 1 MARKS : (18)2,												
T80054382 UTKARSH MISHRA				MA	MTA		, 71045654к , т8054346 ,	P.	ICT	, 7	г8005	4382
01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	56	PC	12.	PRINCIPLES OF PROGRAMMING LANG.	PP	100	40		РС
02. DATA COMMUNICATION	PP	100	40	62	PC		COMPUTER NETWORKS	PP	100	40	42	РС
03. MICROPROCESSORS & MICROCONTROLLE	RPP	100	40	40	PC	14.	FINANCE & MANAGEMENT INFORMA.SYS	.PP	100	40	58	PС
04. DIGITAL SIGNAL PROCESSING	PP	100	40	50	PC	15.	SYSTEMS PROGRAMMING & OPERA.SYS.	PP	100	40	67	PС
05. THEORY OF COMPUTATION	PP	100	40	46	PC	16.	SOFTWARE ENGINEERING	PP	100	40		РС
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	20	PC	17.	SOFTWARE LABORATORY	TW	25	10	10	PС
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	26	PC	18.	SOFTWARE LABORATORY	PR	50	20	26	Р
08. SIGNAL PROCESSING LABORATORY	TW	25	10	14	PC	19.	COMPUTER NETWORK	TW	25	10	11	РС
09. SIGNAL PROCESSING LABORATORY	OR	50	20	20	PC	20.	COMPUTER NETWORK	OR	50	20	21	РС
10. HARDWARE LABORATORY	TW	25	10	16	P C	21.	SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	20	PC
11. HARDWARE LABORATORY	PR	50	20	22	Р	22.	SEMINAR AND TECHNICAL COMMUNI.	TW	50	20	21	РС
GRAND TOTAL = 750/1500, RESULT: SECON	D CLA	SS										
ORDN. 1 MARKS :												

DATE: 31 JAN. 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 01 (484) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71100711L , , , PICT , T80058501 T80058501 AAYUSH FUSKELAY SHASHI 01. OPERATING SYSTEM 100 40 72 P 02. THEORY OF COMPUTATION 100 40 54 P PP 03. COMPUTER NETWORK TECHNOLOGY 100 40 57 P PP 04. DATBASE MANAGEMENT SYSTEMS 100 40 71 P PP 05. SOFTWARE ENGINEERING 100 40 53 P 25 10 21 P 06. OPERATING SYSTEM DESIGN LAB. TW 50 20 35 P 07. OPERATING SYSTEM DESIGN LAB. 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 18 P 50 20 38 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 10. NETWORK LABORATORY 25 10 18 P TW 11. NETWORK LABORATORY 50 20 39 P OR 25 10 18 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 494/750. ORDN. 1 MARKS: T80058502 ABHISHEK BHATTACHARJEE MOUSUMI , 71100713G , PICT , т80058502 01. OPERATING SYSTEM 40 71 P 100 02. THEORY OF COMPUTATION 100 40 74 P PP 03. COMPUTER NETWORK TECHNOLOGY 100 40 68 P PP 100 40 65 P 04. DATBASE MANAGEMENT SYSTEMS PP 100 40 53 P 05. SOFTWARE ENGINEERING PP 25 10 22 P 06. OPERATING SYSTEM DESIGN LAB. TW 50 20 40 P 07. OPERATING SYSTEM DESIGN LAB. PR 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 20 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 40 P 25 10 20 P 10. NETWORK LABORATORY TW 50 20 40 P 11. NETWORK LABORATORY OR 25 10 20 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 533/750. ORDN. 1 MARKS: T80058503 ADEP AKASH RAJENDRA KALPANA , 71100717K , PICT , T80058503 01. OPERATING SYSTEM 100 40 30 F 02. THEORY OF COMPUTATION 100 40 44 P PP 03. COMPUTER NETWORK TECHNOLOGY 100 40 51 P PP 100 04. DATBASE MANAGEMENT SYSTEMS 40 48 P 05. SOFTWARE ENGINEERING 100 40 47 P PP 06. OPERATING SYSTEM DESIGN LAB. 25 10 19 P TW 50 20 31 P 07. OPERATING SYSTEM DESIGN LAB. 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 19 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 36 P 25 10 13 P 10. NETWORK LABORATORY 11. NETWORK LABORATORY OR 50 20 31 P 12. SOFT SKILLS LABORATORY TW 25 10 16 P FIRST TERM TOTAL = 385/750. ORDN. 1 MARKS:

DATE: 31 JAN. 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 02 (485) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER DIMPLE , 71100719F T80058504 ADITYA SINGH SOLANKI , , PICT , T80058504 01. OPERATING SYSTEM PP 100 40 40 P 02. THEORY OF COMPUTATION PP 100 40 25 F 03. COMPUTER NETWORK TECHNOLOGY 100 40 46 P PP 04. DATBASE MANAGEMENT SYSTEMS 100 40 40 P PP 05. SOFTWARE ENGINEERING 100 40 43 P 25 10 15 P 06. OPERATING SYSTEM DESIGN LAB. TW 50 20 10 F 07. OPERATING SYSTEM DESIGN LAB. 25 10 13 P 08. INFORMATION SYSTEMS DESIGN LAB. TW 50 20 05 F 09. INFORMATION SYSTEMS DESIGN LAB. OR 10. NETWORK LABORATORY 25 10 15 P TW 11. NETWORK LABORATORY 50 20 10 F OR 25 10 14 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 276/750. ORDN. 1 MARKS: T80058505 AGARWAL RUHI DINESH **RADHA** , 71100720K , PICT , T80058505 01. OPERATING SYSTEM 40 81 P 100 100 40 58 P 02. THEORY OF COMPUTATION PP 03. COMPUTER NETWORK TECHNOLOGY 100 40 63 P PP 100 40 70 P 04. DATBASE MANAGEMENT SYSTEMS PP 100 40 62 P 05. SOFTWARE ENGINEERING PP 25 10 24 P 06. OPERATING SYSTEM DESIGN LAB. TW 50 20 42 P 07. OPERATING SYSTEM DESIGN LAB. PR 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 24 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 45 P 25 10 24 P 10. NETWORK LABORATORY TW 50 20 45 P 11. NETWORK LABORATORY OR 25 10 24 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 562/750. ORDN. 1 MARKS: T80058506 AHERKAR SHRIJEET CHANDRASHEKHAR MANGALA , 71100722F , PICT , T80058506 01. OPERATING SYSTEM 100 40 49 P 02. THEORY OF COMPUTATION 100 40 42 P PP 03. COMPUTER NETWORK TECHNOLOGY 100 40 71 P PP 100 62 P 04. DATBASE MANAGEMENT SYSTEMS 40 05. SOFTWARE ENGINEERING 100 40 55 P PP 06. OPERATING SYSTEM DESIGN LAB. 25 10 21 P TW 50 20 38 P 07. OPERATING SYSTEM DESIGN LAB. 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 20 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 38 P 25 10 18 P 10. NETWORK LABORATORY 11. NETWORK LABORATORY OR 50 20 36 P 12. SOFT SKILLS LABORATORY TW 25 10 20 P FIRST TERM TOTAL = 470/750. ORDN. 1 MARKS:

DATE: 31 JAN. 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 03 (486) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER BUDDHO , 71241751G T80058507 ALAMWALE SAMEER GAMA , , PICT , T80058507 01. OPERATING SYSTEM PP 100 40 48 P 02. THEORY OF COMPUTATION PP 100 40 40 P 03. COMPUTER NETWORK TECHNOLOGY 100 40 54 P PP 04. DATBASE MANAGEMENT SYSTEMS 100 40 61 P PP 05. SOFTWARE ENGINEERING 100 40 45 P 25 10 24 P 06. OPERATING SYSTEM DESIGN LAB. TW 50 20 07. OPERATING SYSTEM DESIGN LAB. 07 F 25 10 20 P 08. INFORMATION SYSTEMS DESIGN LAB. TW 50 20 25 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 10. NETWORK LABORATORY 25 10 21 P TW 11. NETWORK LABORATORY 50 20 28 P OR 25 10 21 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 394/750. ORDN. 1 MARKS: T80058508 ALHAT ANIKETH KANILAL MANGAL , 71241752E , PICT , T80058508 40 84 P 01. OPERATING SYSTEM 100 100 40 73 P 02. THEORY OF COMPUTATION PP 03. COMPUTER NETWORK TECHNOLOGY 100 40 72 P PP 100 40 69 P 04. DATBASE MANAGEMENT SYSTEMS PP 100 40 56 P 05. SOFTWARE ENGINEERING PP 25 10 24 P 06. OPERATING SYSTEM DESIGN LAB. TW 50 20 45 P 07. OPERATING SYSTEM DESIGN LAB. PR 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 24 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 48 P 25 10 24 P 10. NETWORK LABORATORY TW 50 20 45 P 11. NETWORK LABORATORY OR 25 10 24 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 588/750. ORDN. 1 MARKS: , 71100729C , , PICT T80058509 ANIL BAJAJ KAMAL , T80058509 01. OPERATING SYSTEM 100 40 41 P 02. THEORY OF COMPUTATION 100 40 30 F PP 03. COMPUTER NETWORK TECHNOLOGY 100 40 56 P PP 100 04. DATBASE MANAGEMENT SYSTEMS 40 55 P 05. SOFTWARE ENGINEERING 100 40 50 P PP 06. OPERATING SYSTEM DESIGN LAB. 25 10 19 P TW 50 20 35 P 07. OPERATING SYSTEM DESIGN LAB. 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 19 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 39 P 25 10 17 P 10. NETWORK LABORATORY 11. NETWORK LABORATORY OR 50 20 32 P 12. SOFT SKILLS LABORATORY TW 25 10 20 P FIRST TERM TOTAL = 413/750. ORDN. 1 MARKS:

DATE: 31 JAN. 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 04 (487) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71100730G T80058510 ANIRUDH SHISHODIA VIMLESH , , PICT , T80058510 01. OPERATING SYSTEM PP 100 40 62 P 02. THEORY OF COMPUTATION PP 100 40 63 P 03. COMPUTER NETWORK TECHNOLOGY 100 40 57 P PP 04. DATBASE MANAGEMENT SYSTEMS 100 40 66 P PP 05. SOFTWARE ENGINEERING 100 40 49 P 25 10 24 P 06. OPERATING SYSTEM DESIGN LAB. TW 50 20 38 P 07. OPERATING SYSTEM DESIGN LAB. 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 21 P 50 20 39 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 10. NETWORK LABORATORY 25 10 22 P TW 11. NETWORK LABORATORY 50 20 38 P OR 25 10 21 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 500/750. ORDN. 1 MARKS: T80058511 ANUPAMA TRIVEDI SANGITA , 71100732C , PICT , T80058511 01. OPERATING SYSTEM 40 79 P 100 100 40 61 P 02. THEORY OF COMPUTATION PP 03. COMPUTER NETWORK TECHNOLOGY 100 40 80 P PP 100 40 71 P 04. DATBASE MANAGEMENT SYSTEMS PP 100 40 65 P 05. SOFTWARE ENGINEERING PP 25 22 P 06. OPERATING SYSTEM DESIGN LAB. TW 10 50 20 35 P 07. OPERATING SYSTEM DESIGN LAB. PR 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 21 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 40 P 25 10 22 P 10. NETWORK LABORATORY TW 50 20 43 P 11. NETWORK LABORATORY OR 25 10 22 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 561/750. ORDN. 1 MARKS: T80058513 ASHUTOSH PANDEY MAMTA , 71100733M , PICT , T80058513 01. OPERATING SYSTEM 100 40 42 P 02. THEORY OF COMPUTATION 100 40 26 F PP 03. COMPUTER NETWORK TECHNOLOGY 100 40 64 P PP 100 04. DATBASE MANAGEMENT SYSTEMS 40 51 P 05. SOFTWARE ENGINEERING 100 40 40 P PP 06. OPERATING SYSTEM DESIGN LAB. 25 10 18 P TW 50 20 27 P 07. OPERATING SYSTEM DESIGN LAB. 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 18 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 14 F 25 10 10. NETWORK LABORATORY 16 P 11. NETWORK LABORATORY OR 50 20 20 P 12. SOFT SKILLS LABORATORY TW 25 10 19 P FIRST TERM TOTAL = 355/750. ORDN. 1 MARKS:

DATE: 31 JAN. 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 05 (488) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER AARTI , 71100734K T80058514 ASHWIN AJAY HABBU , , PICT , T80058514 01. OPERATING SYSTEM PP 100 40 59 P 02. THEORY OF COMPUTATION PP 100 40 69 P 03. COMPUTER NETWORK TECHNOLOGY 100 40 68 P PP 04. DATBASE MANAGEMENT SYSTEMS 100 40 68 P PP 05. SOFTWARE ENGINEERING 100 40 55 P 25 10 20 P 06. OPERATING SYSTEM DESIGN LAB. TW 50 20 33 P 07. OPERATING SYSTEM DESIGN LAB. 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 16 P 50 20 31 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 10. NETWORK LABORATORY 25 10 14 P TW 11. NETWORK LABORATORY 50 20 32 P OR 25 10 16 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 481/750. ORDN. 1 MARKS: T80058515 AUTADE SAGAR ASHOK INDIRA , 71045376M , PICT , T80058515 01. OPERATING SYSTEM 40 45 P 100 02. THEORY OF COMPUTATION 100 40 40 P PP 03. COMPUTER NETWORK TECHNOLOGY 100 40 46 P PP 100 40 40 P 04. DATBASE MANAGEMENT SYSTEMS PP 100 40 42 P 05. SOFTWARE ENGINEERING PP 06. OPERATING SYSTEM DESIGN LAB. 25 10 21 P TW 50 20 39 P 07. OPERATING SYSTEM DESIGN LAB. PR 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 22 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 41 P 25 10 21 P 10. NETWORK LABORATORY TW 50 20 17 F 11. NETWORK LABORATORY OR 25 10 22 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 396/750. ORDN. 1 MARKS: T80058516 AYMAN MUSHTAQ AHMAD VIKAR UN NISA , 71100736F , PICT , T80058516 01. OPERATING SYSTEM 100 40 59 P 02. THEORY OF COMPUTATION 100 40 48 P PP 03. COMPUTER NETWORK TECHNOLOGY 100 40 62 P PP 100 40 58 P 04. DATBASE MANAGEMENT SYSTEMS 05. SOFTWARE ENGINEERING 100 40 45 P PP 06. OPERATING SYSTEM DESIGN LAB. 25 10 18 P TW 50 20 29 P 07. OPERATING SYSTEM DESIGN LAB. 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 19 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 36 P 25 10 18 P 10. NETWORK LABORATORY 11. NETWORK LABORATORY OR 50 20 28 P 12. SOFT SKILLS LABORATORY TW 25 10 20 P FIRST TERM TOTAL = 440/750. ORDN. 1 MARKS:

DATE: 31 JAN. 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 06 (489) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71100739L T80058517 BAHETI MAHESH RAJENDRA CHANDA , , PICT , T80058517 01. OPERATING SYSTEM PP 100 40 62 P 02. THEORY OF COMPUTATION PP 100 40 30 F 03. COMPUTER NETWORK TECHNOLOGY 100 40 56 P PP 04. DATBASE MANAGEMENT SYSTEMS 100 40 45 P PP 05. SOFTWARE ENGINEERING 100 40 52 P 25 10 22 P 06. OPERATING SYSTEM DESIGN LAB. TW 50 20 34 P 07. OPERATING SYSTEM DESIGN LAB. 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 23 P 50 20 42 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 10. NETWORK LABORATORY 25 10 20 P TW 11. NETWORK LABORATORY 50 20 35 P OR 25 10 20 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 441/750. ORDN. 1 MARKS: T80058518 BHAKKAD MOHIT KISHANLALJI PO0JA , 71100747M , PICT , T80058518 40 69 P 01. OPERATING SYSTEM PP 100 02. THEORY OF COMPUTATION 100 40 55 P PP 03. COMPUTER NETWORK TECHNOLOGY 100 40 71 P 100 40 59 P 04. DATBASE MANAGEMENT SYSTEMS PP 100 40 52 P 05. SOFTWARE ENGINEERING PP 06. OPERATING SYSTEM DESIGN LAB. 25 23 P TW 10 50 20 35 P 07. OPERATING SYSTEM DESIGN LAB. PR 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 22 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 40 P 25 10 23 P 10. NETWORK LABORATORY TW 50 20 38 P 11. NETWORK LABORATORY OR 25 10 23 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 510/750. ORDN. 1 MARKS: T80058519 BHAND PRITI PARAJI RUKHMINI , 71100748K , PICT , T80058519 01. OPERATING SYSTEM 100 40 47 P 02. THEORY OF COMPUTATION PP 100 40 55 P 03. COMPUTER NETWORK TECHNOLOGY 100 40 58 P PP 100 40 50 P 04. DATBASE MANAGEMENT SYSTEMS 05. SOFTWARE ENGINEERING 100 40 47 P PP 06. OPERATING SYSTEM DESIGN LAB. 25 10 23 P TW 50 20 07 F 07. OPERATING SYSTEM DESIGN LAB. 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 20 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 36 P 25 10 20 P 10. NETWORK LABORATORY 11. NETWORK LABORATORY OR 50 20 22 P 12. SOFT SKILLS LABORATORY TW 25 10 20 P FIRST TERM TOTAL = 405/750. ORDN. 1 MARKS:

DATE: 31 JAN. 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 07 (490) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71100749H , , PICT , T80058520 T80058520 BHANDWALKAR ONKAR MADHUKAR PRABHAVATI PP 100 40 75 P 01. OPERATING SYSTEM 02. THEORY OF COMPUTATION PP 100 40 69 P 03. COMPUTER NETWORK TECHNOLOGY PP 100 40 66 P 04. DATBASE MANAGEMENT SYSTEMS 100 40 57 P PP 05. SOFTWARE ENGINEERING 100 40 57 P 25 10 22 P 06. OPERATING SYSTEM DESIGN LAB. TW 50 20 31 P 07. OPERATING SYSTEM DESIGN LAB. 25 10 23 P 08. INFORMATION SYSTEMS DESIGN LAB. TW 50 20 40 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 10. NETWORK LABORATORY 25 10 22 P TW 11. NETWORK LABORATORY 50 20 44 P OR 25 10 23 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 529/750. ORDN. 1 MARKS: T80058521 BHOIR KIRTI RAMCHANDRA ROHINI , 71241753C , PICT , T80058521 40 70 P 01. OPERATING SYSTEM 100 02. THEORY OF COMPUTATION 100 40 51 P PP 03. COMPUTER NETWORK TECHNOLOGY 100 40 66 P PP 100 40 58 P 04. DATBASE MANAGEMENT SYSTEMS PP 100 40 65 P 05. SOFTWARE ENGINEERING PP 06. OPERATING SYSTEM DESIGN LAB. 25 24 P TW 10 50 20 34 P 07. OPERATING SYSTEM DESIGN LAB. PR 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 23 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 39 P 25 10 24 P 10. NETWORK LABORATORY TW 50 20 40 P 11. NETWORK LABORATORY OR 25 10 23 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 517/750. ORDN. 1 MARKS: T80058522 BORHADE BHUSHAN MALHARI SUREKHA , 71241754M , PICT , T80058522 01. OPERATING SYSTEM 100 40 75 P 02. THEORY OF COMPUTATION 100 40 53 P PP 03. COMPUTER NETWORK TECHNOLOGY 100 40 60 P PP 100 40 63 P 04. DATBASE MANAGEMENT SYSTEMS 05. SOFTWARE ENGINEERING 100 40 63 P PP 06. OPERATING SYSTEM DESIGN LAB. 25 10 24 P TW 50 20 06 F 07. OPERATING SYSTEM DESIGN LAB. 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 23 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 44 P 25 10 21 P 10. NETWORK LABORATORY 11. NETWORK LABORATORY OR 50 20 38 P 12. SOFT SKILLS LABORATORY TW 25 10 22 P FIRST TERM TOTAL = 492/750. ORDN. 1 MARKS:

DATE: 31 JAN. 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 08 (491) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71100755в T80058523 BOTHARA PRIYANKA RAMESH , , PICT , T80058523 VIJAYA 01. OPERATING SYSTEM 100 40 66 P PP 02. THEORY OF COMPUTATION PP 100 40 61 P 03. COMPUTER NETWORK TECHNOLOGY 100 40 80 P PP 04. DATBASE MANAGEMENT SYSTEMS 100 40 69 P PP 05. SOFTWARE ENGINEERING 100 40 66 P 25 10 24 P 06. OPERATING SYSTEM DESIGN LAB. TW 50 20 07. OPERATING SYSTEM DESIGN LAB. 36 P 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 24 P 50 20 09. INFORMATION SYSTEMS DESIGN LAB. OR 41 P 10. NETWORK LABORATORY 25 10 21 P TW 11. NETWORK LABORATORY 50 20 40 P OR 25 10 22 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 550/750. ORDN. 1 MARKS: T80058524 BOTHRA PAYAL SUMATILAL SANDHYA , 71100756L , PICT , T80058524 40 67 P 01. OPERATING SYSTEM 100 02. THEORY OF COMPUTATION 100 40 PP 66 P 03. COMPUTER NETWORK TECHNOLOGY 100 40 72 P PP 100 40 04. DATBASE MANAGEMENT SYSTEMS PP 68 P 100 40 48 P 05. SOFTWARE ENGINEERING PP 25 21 P 06. OPERATING SYSTEM DESIGN LAB. TW 10 50 20 34 P 07. OPERATING SYSTEM DESIGN LAB. PR 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 23 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 39 P 25 10 20 P 10. NETWORK LABORATORY TW 50 20 29 P 11. NETWORK LABORATORY OR 25 10 21 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 508/750. ORDN. 1 MARKS: T80058525 BURSE RASHMI KIRAN MADHURI , 71100758G , PICT , T80058525 01. OPERATING SYSTEM 100 40 71 P 02. THEORY OF COMPUTATION 100 40 60 P PP 03. COMPUTER NETWORK TECHNOLOGY 100 40 72 P PP 100 40 64 P 04. DATBASE MANAGEMENT SYSTEMS 05. SOFTWARE ENGINEERING 100 40 59 P PP 06. OPERATING SYSTEM DESIGN LAB. 25 10 21 P TW 50 20 12 F 07. OPERATING SYSTEM DESIGN LAB. 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 23 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 42 P 25 10 22 P 10. NETWORK LABORATORY 11. NETWORK LABORATORY OR 50 20 35 P 12. SOFT SKILLS LABORATORY TW 25 10 23 P FIRST TERM TOTAL = 504/750. ORDN. 1 MARKS:

DATE: 31 JAN. 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 09 (492) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71241755K T80058526 CHANDAN PRITI DILIP MALATI , , PICT , T80058526 01. OPERATING SYSTEM PP 100 40 70 P 02. THEORY OF COMPUTATION PP 100 40 52 P 03. COMPUTER NETWORK TECHNOLOGY 100 40 56 P PP 04. DATBASE MANAGEMENT SYSTEMS 100 40 60 P PP 05. SOFTWARE ENGINEERING 100 40 56 P 25 10 23 P 06. OPERATING SYSTEM DESIGN LAB. TW 50 20 27 P 07. OPERATING SYSTEM DESIGN LAB. 25 10 24 P 08. INFORMATION SYSTEMS DESIGN LAB. TW 50 20 46 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 10. NETWORK LABORATORY 25 10 24 P TW 11. NETWORK LABORATORY 50 20 41 P OR 25 10 23 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 502/750. ORDN. 1 MARKS: T80058527 CHAUDHARI PRANITA RAVISHANKAR MEENA , 71100764M , PICT , T80058527 40 40 P 01. OPERATING SYSTEM PP 100 02. THEORY OF COMPUTATION 100 40 23 F PP 03. COMPUTER NETWORK TECHNOLOGY 100 40 40 P 100 40 22 F 04. DATBASE MANAGEMENT SYSTEMS PP 100 40 24 F 05. SOFTWARE ENGINEERING PP 25 21 P 06. OPERATING SYSTEM DESIGN LAB. TW 10 50 20 30 P 07. OPERATING SYSTEM DESIGN LAB. PR 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 18 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 33 P 25 10 18 P 10. NETWORK LABORATORY TW 50 20 28 P 11. NETWORK LABORATORY OR 25 10 19 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 316/750. ORDN. 1 MARKS: T80058528 CHAVAN SHRIKANT SHIVAJI , 71241756н , PICT , T80058528 MALAN 01. OPERATING SYSTEM 100 40 77 P 02. THEORY OF COMPUTATION 100 40 55 P PP 03. COMPUTER NETWORK TECHNOLOGY 100 40 73 P PP 100 04. DATBASE MANAGEMENT SYSTEMS 40 66 P 05. SOFTWARE ENGINEERING 100 40 59 P PP 06. OPERATING SYSTEM DESIGN LAB. 25 10 24 P TW 50 20 44 P 07. OPERATING SYSTEM DESIGN LAB. 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 22 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 43 P 25 10 22 P 10. NETWORK LABORATORY 11. NETWORK LABORATORY OR 50 20 31 P 12. SOFT SKILLS LABORATORY TW 25 10 22 P FIRST TERM TOTAL = 538/750. ORDN. 1 MARKS:

DATE: 31 JAN. 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 10 (493) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71241757F T80058529 DAHAWAD SUNIL NAMDEO VANITA , , PICT , T80058529 01. OPERATING SYSTEM PP 100 40 43 P 02. THEORY OF COMPUTATION PP 100 40 31 F 03. COMPUTER NETWORK TECHNOLOGY 100 40 57 P PP 04. DATBASE MANAGEMENT SYSTEMS 100 40 31 F PP 05. SOFTWARE ENGINEERING 100 40 40 P 25 10 18 P 06. OPERATING SYSTEM DESIGN LAB. TW 50 20 10 F 07. OPERATING SYSTEM DESIGN LAB. 25 10 18 P 08. INFORMATION SYSTEMS DESIGN LAB. TW 50 20 12 F 09. INFORMATION SYSTEMS DESIGN LAB. OR 10. NETWORK LABORATORY 25 10 17 P TW 11. NETWORK LABORATORY 50 20 14 F OR 25 10 19 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 310/750. ORDN. 1 MARKS: T80058530 DARDA MAYOOR MONISH KALPANA , 71045410E , PICT , T80058530 40 52 P 01. OPERATING SYSTEM 100 100 40 25 F 02. THEORY OF COMPUTATION PP 03. COMPUTER NETWORK TECHNOLOGY 100 40 60 P PP 100 40 40 P 04. DATBASE MANAGEMENT SYSTEMS PP 100 40 53 P 05. SOFTWARE ENGINEERING PP 25 10 10 P 06. OPERATING SYSTEM DESIGN LAB. TW 50 20 AA F 07. OPERATING SYSTEM DESIGN LAB. PR 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 11 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 AA F 25 10 13 P 10. NETWORK LABORATORY TW 50 20 AA F 11. NETWORK LABORATORY OR 25 10 13 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 277/750. ORDN. 1 MARKS: T80058531 DHANAVE PRATIK BALASAHEB UJJWALLA , 71100783H , , PICT , T80058531 01. OPERATING SYSTEM 100 40 42 P 02. THEORY OF COMPUTATION 100 40 17 F PP 03. COMPUTER NETWORK TECHNOLOGY 100 40 51 P PP 100 04. DATBASE MANAGEMENT SYSTEMS 40 43 P 05. SOFTWARE ENGINEERING 100 40 48 P PP 06. OPERATING SYSTEM DESIGN LAB. 25 10 12 P TW 50 20 AA F 07. OPERATING SYSTEM DESIGN LAB. 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 16 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 35 P 25 10 13 P 10. NETWORK LABORATORY 11. NETWORK LABORATORY OR 50 20 AA F 12. SOFT SKILLS LABORATORY TW 25 10 13 P FIRST TERM TOTAL = 290/750. ORDN. 1 MARKS:

DATE: 31 JAN. 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 11 (494) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71100784F T80058532 DHAWALE PRACHI PRAMOD MEENA , , PICT , T80058532 01. OPERATING SYSTEM 100 40 69 P 02. THEORY OF COMPUTATION PP 100 40 66 P 03. COMPUTER NETWORK TECHNOLOGY 100 40 79 P PP 04. DATBASE MANAGEMENT SYSTEMS 100 40 67 P PP 05. SOFTWARE ENGINEERING 100 40 55 P 25 10 21 P 06. OPERATING SYSTEM DESIGN LAB. TW 07. OPERATING SYSTEM DESIGN LAB. 50 20 12 F 25 10 22 P 08. INFORMATION SYSTEMS DESIGN LAB. TW 50 20 40 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 10. NETWORK LABORATORY 25 10 18 P TW 11. NETWORK LABORATORY 50 20 31 P OR 25 10 19 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 499/750. ORDN. 1 MARKS: T80058533 DHEPLE ARCHANA HARIBHAU MEENA , 71100785D , PICT , T80058533 01. OPERATING SYSTEM 100 40 48 P 02. THEORY OF COMPUTATION 100 40 57 P PP 03. COMPUTER NETWORK TECHNOLOGY 100 40 64 P PP 100 40 49 P 04. DATBASE MANAGEMENT SYSTEMS PP 100 40 46 P 05. SOFTWARE ENGINEERING PP 25 10 18 P 06. OPERATING SYSTEM DESIGN LAB. TW 50 20 34 P 07. OPERATING SYSTEM DESIGN LAB. PR 25 10 08. INFORMATION SYSTEMS DESIGN LAB. TW 20 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 30 P 25 10 18 P 10. NETWORK LABORATORY TW 50 20 25 P 11. NETWORK LABORATORY OR 25 10 20 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 429/750. ORDN. 1 MARKS: T80058534 GAIKWAD NETRANJALI SHAMRAO MOHINI , 71241758D , PICT , T80058534 01. OPERATING SYSTEM 100 40 75 P 02. THEORY OF COMPUTATION 100 40 55 P PP 03. COMPUTER NETWORK TECHNOLOGY 100 40 60 P PP 100 04. DATBASE MANAGEMENT SYSTEMS 40 63 P 05. SOFTWARE ENGINEERING 100 40 60 P PP 06. OPERATING SYSTEM DESIGN LAB. 25 10 23 P TW 50 20 38 P 07. OPERATING SYSTEM DESIGN LAB. 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 24 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 44 P 25 10 24 P 10. NETWORK LABORATORY 11. NETWORK LABORATORY OR 50 20 45 P 12. SOFT SKILLS LABORATORY TW 25 10 24 P FIRST TERM TOTAL = 535/750. ORDN. 1 MARKS:

DATE: 31 JAN. 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 12 (495) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71100793E T80058535 GAIKWAD PIYUSH GHANASHYAM VASUDHA , , PICT , T80058535 01. OPERATING SYSTEM PP 100 40 40 P 02. THEORY OF COMPUTATION PP 100 40 25 F 03. COMPUTER NETWORK TECHNOLOGY 100 40 41 P PP 04. DATBASE MANAGEMENT SYSTEMS 100 40 40 P PP 05. SOFTWARE ENGINEERING 100 40 48 P 25 10 10 P 06. OPERATING SYSTEM DESIGN LAB. TW 50 20 31 P 07. OPERATING SYSTEM DESIGN LAB. 25 10 15 P 08. INFORMATION SYSTEMS DESIGN LAB. TW 50 20 06 F 09. INFORMATION SYSTEMS DESIGN LAB. OR 10. NETWORK LABORATORY 25 10 14 P TW 11. NETWORK LABORATORY 50 20 17 F OR 25 10 16 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 303/750. ORDN. 1 MARKS: T80058536 GANGARDE MAYURI MUKUND PRAMILA , 71100796к , PICT , T80058536 62 P 01. OPERATING SYSTEM 100 40 02. THEORY OF COMPUTATION 100 40 46 P PP 03. COMPUTER NETWORK TECHNOLOGY 100 40 61 P PP 100 40 40 P 04. DATBASE MANAGEMENT SYSTEMS PP 100 40 55 P 05. SOFTWARE ENGINEERING PP 25 10 22 P 06. OPERATING SYSTEM DESIGN LAB. TW 50 20 25 P 07. OPERATING SYSTEM DESIGN LAB. PR 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 23 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 34 P 25 10 24 P 10. NETWORK LABORATORY TW 50 20 23 P 11. NETWORK LABORATORY OR 25 10 24 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 439/750. ORDN. 1 MARKS: T80058537 GAWANDE ANAGHA ARVIND MAYA , 71100800M , PICT , T80058537 01. OPERATING SYSTEM 100 40 52 P 02. THEORY OF COMPUTATION 100 40 45 P PP 03. COMPUTER NETWORK TECHNOLOGY 100 40 50 P PP 100 40 67 P 04. DATBASE MANAGEMENT SYSTEMS 05. SOFTWARE ENGINEERING 100 40 52 P PP 06. OPERATING SYSTEM DESIGN LAB. 25 10 23 P TW 50 20 12 F 07. OPERATING SYSTEM DESIGN LAB. 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 19 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 39 P 25 10 18 P 10. NETWORK LABORATORY 11. NETWORK LABORATORY OR 50 20 25 P 12. SOFT SKILLS LABORATORY TW 25 10 18 P FIRST TERM TOTAL = 420/750. ORDN. 1 MARKS:

DATE: 31 JAN. 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 13 (496) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71100802H , , PICT , T80058538 T80058538 GHODAKE PRAJAKTA DHANANJAY SWATI 01. OPERATING SYSTEM PP 100 40 51 P 02. THEORY OF COMPUTATION PP 100 40 24 F 03. COMPUTER NETWORK TECHNOLOGY PP 100 40 40 P 04. DATBASE MANAGEMENT SYSTEMS 100 40 40 P PP 05. SOFTWARE ENGINEERING 100 40 42 P 25 10 18 P 06. OPERATING SYSTEM DESIGN LAB. TW 50 20 05 F 07. OPERATING SYSTEM DESIGN LAB. 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 18 P 50 20 24 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 10. NETWORK LABORATORY 25 10 16 P TW 11. NETWORK LABORATORY 50 20 20 P OR 25 10 18 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 316/750. ORDN. 1 MARKS: T80058539 GHODE AMAR PRADEEPKUMAR SINDHU , 71045436J , PICT , T80058539 40 53 P 01. OPERATING SYSTEM 100 02. THEORY OF COMPUTATION 100 40 18 F PP 03. COMPUTER NETWORK TECHNOLOGY 100 40 48 P PP 100 40 40 P 04. DATBASE MANAGEMENT SYSTEMS PP 100 40 52 P 05. SOFTWARE ENGINEERING PP 06. OPERATING SYSTEM DESIGN LAB. 25 10 17 P TW 50 20 07. OPERATING SYSTEM DESIGN LAB. PR AA F 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 17 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 04 F 25 10 12 P 10. NETWORK LABORATORY TW 50 20 AA F 11. NETWORK LABORATORY OR 25 10 15 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 276/750. ORDN. 1 MARKS: T80058540 GHODESWAR MAYUR DEEPAK RATNAPRABHA , 71241759в , PICT , T80058540 01. OPERATING SYSTEM 100 40 51 P 02. THEORY OF COMPUTATION PP 100 40 46 P 03. COMPUTER NETWORK TECHNOLOGY 100 40 66 P PP 100 40 65 P 04. DATBASE MANAGEMENT SYSTEMS 05. SOFTWARE ENGINEERING 100 40 67 P PP 06. OPERATING SYSTEM DESIGN LAB. 25 10 21 P TW 50 20 37 P 07. OPERATING SYSTEM DESIGN LAB. 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 20 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 12 F 25 10 19 P 10. NETWORK LABORATORY 11. NETWORK LABORATORY OR 50 20 10 F 12. SOFT SKILLS LABORATORY TW 25 10 20 P FIRST TERM TOTAL = 434/750. ORDN. 1 MARKS:

DATE: 31 JAN. 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 14 (497) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71241760F T80058541 GHODKE AMRUTA PRAKASH JYOTI , , PICT , T80058541 01. OPERATING SYSTEM PP 100 40 79 P 02. THEORY OF COMPUTATION PP 100 40 66 P 03. COMPUTER NETWORK TECHNOLOGY 100 40 68 P PP 04. DATBASE MANAGEMENT SYSTEMS 100 40 62 P PP 05. SOFTWARE ENGINEERING 100 40 58 P 25 10 23 P 06. OPERATING SYSTEM DESIGN LAB. TW 50 20 07. OPERATING SYSTEM DESIGN LAB. 40 P 25 10 24 P 08. INFORMATION SYSTEMS DESIGN LAB. TW 50 20 45 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 10. NETWORK LABORATORY 25 10 22 P TW 11. NETWORK LABORATORY 50 20 45 P OR 25 10 24 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 556/750. ORDN. 1 MARKS: T80058542 GOENKA PALAK DILIP MANJU , 71100807J , PICT , T80058542 01. OPERATING SYSTEM 40 52 P 100 100 40 44 P 02. THEORY OF COMPUTATION PP 100 40 61 P 03. COMPUTER NETWORK TECHNOLOGY PP 100 40 64 P 04. DATBASE MANAGEMENT SYSTEMS PP 100 40 57 P 05. SOFTWARE ENGINEERING PP 25 10 17 P 06. OPERATING SYSTEM DESIGN LAB. TW 50 20 07. OPERATING SYSTEM DESIGN LAB. PR 38 P 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 17 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 41 P 25 10 17 P 10. NETWORK LABORATORY TW 50 20 32 P 11. NETWORK LABORATORY OR 25 10 18 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 458/750. ORDN. 1 MARKS: T80058543 GUJRATHI DHANESH NARESH JYOTI , 71100811G , PICT , T80058543 01. OPERATING SYSTEM 100 40 73 P 02. THEORY OF COMPUTATION 100 40 62 P PP 03. COMPUTER NETWORK TECHNOLOGY 100 40 68 P PP 100 04. DATBASE MANAGEMENT SYSTEMS 40 61 P 05. SOFTWARE ENGINEERING 100 40 55 P PP 06. OPERATING SYSTEM DESIGN LAB. 25 10 22 P TW 50 20 40 P 07. OPERATING SYSTEM DESIGN LAB. 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 21 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 40 P 25 10 22 P 10. NETWORK LABORATORY 11. NETWORK LABORATORY OR 50 20 41 P 12. SOFT SKILLS LABORATORY TW 25 10 22 P FIRST TERM TOTAL = 527/750. ORDN. 1 MARKS:

DATE: 31 JAN. 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 15 (498) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71100815K T80058544 GUTTE ASHWINI ASHOKRAO SUNANDA , , PICT , T80058544 01. OPERATING SYSTEM PP 100 40 49 P 02. THEORY OF COMPUTATION PP 100 40 51 P 03. COMPUTER NETWORK TECHNOLOGY 100 40 61 P PP 04. DATBASE MANAGEMENT SYSTEMS 100 40 54 P PP 05. SOFTWARE ENGINEERING 100 40 54 P 25 10 22 P 06. OPERATING SYSTEM DESIGN LAB. TW 50 20 10 F 07. OPERATING SYSTEM DESIGN LAB. 25 10 19 P 08. INFORMATION SYSTEMS DESIGN LAB. TW 50 20 10 F 09. INFORMATION SYSTEMS DESIGN LAB. OR 10. NETWORK LABORATORY 25 10 20 P TW 11. NETWORK LABORATORY 50 20 26 P OR 25 10 21 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 397/750. ORDN. 1 MARKS: T80058545 HARAL SMITA DATTATRAYA SARITA , 71100816н , PICT , T80058545 40 71 P 01. OPERATING SYSTEM 100 02. THEORY OF COMPUTATION 100 40 67 P PP 03. COMPUTER NETWORK TECHNOLOGY 100 40 67 P PP 100 40 57 P 04. DATBASE MANAGEMENT SYSTEMS PP 100 40 55 P 05. SOFTWARE ENGINEERING PP 25 10 22 P 06. OPERATING SYSTEM DESIGN LAB. TW 50 20 30 P 07. OPERATING SYSTEM DESIGN LAB. PR 25 10 21 P 08. INFORMATION SYSTEMS DESIGN LAB. TW 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 39 P 25 10 22 P 10. NETWORK LABORATORY TW 50 20 37 P 11. NETWORK LABORATORY OR 25 10 23 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 511/750. ORDN. 1 MARKS: T80058546 JADHWANI KANCHAN OMPRAKASH NIRMALAL , 71100824J , PICT , T80058546 01. OPERATING SYSTEM 100 40 75 P 02. THEORY OF COMPUTATION 100 40 51 P PP 03. COMPUTER NETWORK TECHNOLOGY 100 40 66 P PP 100 04. DATBASE MANAGEMENT SYSTEMS 40 65 P 05. SOFTWARE ENGINEERING 100 40 56 P PP 06. OPERATING SYSTEM DESIGN LAB. 25 10 22 P TW 50 20 32 P 07. OPERATING SYSTEM DESIGN LAB. 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 21 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 42 P 25 10 21 P 10. NETWORK LABORATORY 11. NETWORK LABORATORY OR 50 20 38 P 12. SOFT SKILLS LABORATORY TW 25 10 21 P FIRST TERM TOTAL = 510/750. ORDN. 1 MARKS:

DATE: 31 JAN. 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 16 (499) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71100826E T80058547 JAIN BHUSHAN SUMATILAL MANISHA , , PICT , T80058547 01. OPERATING SYSTEM PP 100 40 76 P 02. THEORY OF COMPUTATION PP 100 40 72 P 03. COMPUTER NETWORK TECHNOLOGY 100 40 69 P PP 04. DATBASE MANAGEMENT SYSTEMS 100 40 70 P PP 05. SOFTWARE ENGINEERING 100 40 64 P 25 10 20 P 06. OPERATING SYSTEM DESIGN LAB. TW 50 20 35 P 07. OPERATING SYSTEM DESIGN LAB. 25 10 22 P 08. INFORMATION SYSTEMS DESIGN LAB. TW 50 20 43 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 10. NETWORK LABORATORY 25 10 20 P TW 11. NETWORK LABORATORY 50 20 36 P OR 25 10 20 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 547/750. ORDN. 1 MARKS: T80058548 JAIPURIA ROHIT SHARADKUMAR AMITA , 71100828M , PICT , T80058548 40 40 P 01. OPERATING SYSTEM 100 02. THEORY OF COMPUTATION 100 40 29 F PP 03. COMPUTER NETWORK TECHNOLOGY 100 40 45 P 100 40 43 P 04. DATBASE MANAGEMENT SYSTEMS PP 100 40 42 P 05. SOFTWARE ENGINEERING PP 25 10 19 P 06. OPERATING SYSTEM DESIGN LAB. TW 50 20 07. OPERATING SYSTEM DESIGN LAB. PR AA F 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 18 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 12 F 25 10 19 P 10. NETWORK LABORATORY TW 50 20 28 P 11. NETWORK LABORATORY OR 25 10 20 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 315/750. ORDN. 1 MARKS: T80058549 JAMBHULKAR SAYALI SUNIL SHUBHAANGI , 71100829к , PICT , T80058549 01. OPERATING SYSTEM 100 40 66 P 02. THEORY OF COMPUTATION 100 40 49 P PP 03. COMPUTER NETWORK TECHNOLOGY 100 40 73 P PP 100 04. DATBASE MANAGEMENT SYSTEMS 40 54 P 05. SOFTWARE ENGINEERING 100 40 56 P PP 06. OPERATING SYSTEM DESIGN LAB. 25 10 20 P TW 50 20 42 P 07. OPERATING SYSTEM DESIGN LAB. 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 19 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 40 P 25 10 19 P 10. NETWORK LABORATORY 11. NETWORK LABORATORY OR 50 20 39 P 12. SOFT SKILLS LABORATORY TW 25 10 20 P FIRST TERM TOTAL = 497/750. ORDN. 1 MARKS:

DATE: 31 JAN. 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 17 (500) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER NEHA , 71100833H , , PICT , T80058550 T80058550 JOSHI AKHIL NIRANJAN 01. OPERATING SYSTEM PP 100 40 74 P 02. THEORY OF COMPUTATION PP 100 40 55 P 03. COMPUTER NETWORK TECHNOLOGY 100 40 69 P PP 04. DATBASE MANAGEMENT SYSTEMS 100 40 68 P PP 05. SOFTWARE ENGINEERING 100 40 54 P 25 10 20 P 06. OPERATING SYSTEM DESIGN LAB. TW 50 20 07. OPERATING SYSTEM DESIGN LAB. 44 P 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 18 P 50 20 40 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 10. NETWORK LABORATORY 25 10 17 P TW 11. NETWORK LABORATORY 50 20 39 P OR 25 10 18 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 516/750. ORDN. 1 MARKS: T80058551 KABRE AMEYA ATUL MANGALA , 71100838J , PICT , T80058551 01. OPERATING SYSTEM 40 72 P 100 100 40 43 P 02. THEORY OF COMPUTATION PP 03. COMPUTER NETWORK TECHNOLOGY 100 40 74 P PP 100 40 57 P 04. DATBASE MANAGEMENT SYSTEMS PP 100 40 59 P 05. SOFTWARE ENGINEERING PP 25 10 24 P 06. OPERATING SYSTEM DESIGN LAB. TW 50 20 37 P 07. OPERATING SYSTEM DESIGN LAB. PR 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 23 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 43 P 25 10 24 P 10. NETWORK LABORATORY TW 50 20 45 P 11. NETWORK LABORATORY OR 25 10 23 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 524/750. ORDN. 1 MARKS: T80058552 KADAM CHIRAG RAM **APSARA** , 71100839G , PICT , T80058552 01. OPERATING SYSTEM 100 40 76 P 02. THEORY OF COMPUTATION 100 40 61 P PP 03. COMPUTER NETWORK TECHNOLOGY 100 40 68 P PP 100 40 53 P 04. DATBASE MANAGEMENT SYSTEMS 05. SOFTWARE ENGINEERING 100 40 65 P PP 06. OPERATING SYSTEM DESIGN LAB. 25 10 24 P TW 50 20 42 P 07. OPERATING SYSTEM DESIGN LAB. 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 22 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 38 P 25 10 19 P 10. NETWORK LABORATORY 11. NETWORK LABORATORY OR 50 20 43 P 12. SOFT SKILLS LABORATORY TW 25 10 23 P FIRST TERM TOTAL = 534/750. ORDN. 1 MARKS:

DATE: 31 JAN. 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 18 (501) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71100841J T80058553 KAHATE SAUMITRA SHYAMKANT JYOTI , , PICT , T80058553 01. OPERATING SYSTEM 100 40 74 P PP 02. THEORY OF COMPUTATION 100 40 64 P 03. COMPUTER NETWORK TECHNOLOGY 100 40 69 P PP 04. DATBASE MANAGEMENT SYSTEMS 100 40 61 P PP 05. SOFTWARE ENGINEERING 100 40 60 P 25 10 21 P 06. OPERATING SYSTEM DESIGN LAB. TW 50 20 07. OPERATING SYSTEM DESIGN LAB. 40 P 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 22 P 50 20 39 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 10. NETWORK LABORATORY 25 10 23 P TW 11. NETWORK LABORATORY 50 20 40 P OR 25 10 22 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 535/750. ORDN. 1 MARKS: T80058554 KAUTHALE TRUPTI SAMPAT JAYASHRI , 71100853B , PICT , T80058554 01. OPERATING SYSTEM 100 40 56 P 02. THEORY OF COMPUTATION 100 40 53 P PP 03. COMPUTER NETWORK TECHNOLOGY 100 40 63 P PP 100 40 62 P 04. DATBASE MANAGEMENT SYSTEMS PP 100 40 62 P 05. SOFTWARE ENGINEERING PP 06. OPERATING SYSTEM DESIGN LAB. 25 10 20 P TW 50 20 35 P 07. OPERATING SYSTEM DESIGN LAB. PR 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 21 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 42 P 25 10 21 P 10. NETWORK LABORATORY TW 50 20 20 P 11. NETWORK LABORATORY OR 25 10 21 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 476/750. ORDN. 1 MARKS: T80058555 KHADKIWALA HATIM JAINUDDIN MASUMA , 71100856G , PICT , T80058555 01. OPERATING SYSTEM 100 40 67 P 02. THEORY OF COMPUTATION 100 40 68 P PP 03. COMPUTER NETWORK TECHNOLOGY 100 40 70 P PP 100 40 55 P 04. DATBASE MANAGEMENT SYSTEMS 05. SOFTWARE ENGINEERING 100 40 59 P PP 06. OPERATING SYSTEM DESIGN LAB. 25 10 24 P TW 50 20 46 P 07. OPERATING SYSTEM DESIGN LAB. 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 23 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 45 P 25 10 24 P 10. NETWORK LABORATORY 11. NETWORK LABORATORY OR 50 20 46 P 12. SOFT SKILLS LABORATORY TW 25 10 23 P FIRST TERM TOTAL = 550/750. ORDN. 1 MARKS:

DATE: 31 JAN. 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 19 (502) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER PRAMILA , 71100857E T80058556 KHADSE MAYUR DILIP , , PICT , T80058556 01. OPERATING SYSTEM PP 100 40 48 P 02. THEORY OF COMPUTATION PP 100 40 42 P 03. COMPUTER NETWORK TECHNOLOGY 100 40 53 P PP 04. DATBASE MANAGEMENT SYSTEMS 100 40 41 P PP 05. SOFTWARE ENGINEERING 100 40 40 P 25 10 17 P 06. OPERATING SYSTEM DESIGN LAB. TW 50 20 07. OPERATING SYSTEM DESIGN LAB. 40 P 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 19 P 50 20 41 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 10. NETWORK LABORATORY 25 10 16 P TW 11. NETWORK LABORATORY 50 20 25 P OR 25 10 17 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 399/750. ORDN. 1 MARKS: T80058557 KHAN RUQAIYA IQBAL SHAHEEN , 71100860E , PICT , T80058557 01. OPERATING SYSTEM 40 74 P 100 02. THEORY OF COMPUTATION 100 40 59 P PP 03. COMPUTER NETWORK TECHNOLOGY 100 40 68 P PP 100 40 65 P 04. DATBASE MANAGEMENT SYSTEMS PP 100 40 52 P 05. SOFTWARE ENGINEERING PP 06. OPERATING SYSTEM DESIGN LAB. 25 10 24 P TW 50 20 42 P 07. OPERATING SYSTEM DESIGN LAB. PR 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 24 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 46 P 25 10 24 P 10. NETWORK LABORATORY TW 50 20 38 P 11. NETWORK LABORATORY OR 25 10 24 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 540/750. ORDN. 1 MARKS: T80058558 KODITKAR POOJA NAMDEO JYOTI , 71241761D , PICT , T80058558 01. OPERATING SYSTEM 100 40 68 P 02. THEORY OF COMPUTATION PP 100 40 51 P 03. COMPUTER NETWORK TECHNOLOGY 100 40 67 P PP 100 40 59 P 04. DATBASE MANAGEMENT SYSTEMS 05. SOFTWARE ENGINEERING 100 40 67 P PP 06. OPERATING SYSTEM DESIGN LAB. 25 10 23 P TW 50 20 25 P 07. OPERATING SYSTEM DESIGN LAB. 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 23 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 34 P 25 10 24 P 10. NETWORK LABORATORY 11. NETWORK LABORATORY OR 50 20 37 P 12. SOFT SKILLS LABORATORY TW 25 10 23 P FIRST TERM TOTAL = 501/750. ORDN. 1 MARKS:

DATE: 31 JAN. 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 20 (503) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71045485G T80058559 KOSHTI ANUJA RAMESH MANGAL , , PICT , T80058559 01. OPERATING SYSTEM PP 100 40 46 P 02. THEORY OF COMPUTATION PP 100 40 40 P 03. COMPUTER NETWORK TECHNOLOGY 100 40 45 P PP 04. DATBASE MANAGEMENT SYSTEMS 100 40 43 P PP 05. SOFTWARE ENGINEERING 100 40 52 P 25 10 20 P 06. OPERATING SYSTEM DESIGN LAB. TW 07. OPERATING SYSTEM DESIGN LAB. 50 20 AA F 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 18 P 50 20 10 F 09. INFORMATION SYSTEMS DESIGN LAB. OR 10. NETWORK LABORATORY 25 10 20 P TW 11. NETWORK LABORATORY 50 20 30 P OR 25 10 20 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 344/750. ORDN. 1 MARKS: T80058560 KULKARNI MITALI SUHAS **RUPA** , 71100872J , PICT , T80058560 01. OPERATING SYSTEM 40 66 P 100 02. THEORY OF COMPUTATION 100 40 54 P PP 03. COMPUTER NETWORK TECHNOLOGY 100 40 63 P 100 40 58 P 04. DATBASE MANAGEMENT SYSTEMS PP 100 40 48 P 05. SOFTWARE ENGINEERING PP 25 22 P 06. OPERATING SYSTEM DESIGN LAB. TW 10 50 20 27 P 07. OPERATING SYSTEM DESIGN LAB. PR 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 23 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 42 P 25 10 22 P 10. NETWORK LABORATORY TW 50 20 38 P 11. NETWORK LABORATORY OR 25 10 22 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 485/750. ORDN. 1 MARKS: T80058561 LAGWANKAR NIKHIL KISHOR RASHMI , 71100878н , PICT , T80058561 01. OPERATING SYSTEM 100 40 73 P 02. THEORY OF COMPUTATION 100 40 51 P PP 03. COMPUTER NETWORK TECHNOLOGY 100 40 67 P PP 100 04. DATBASE MANAGEMENT SYSTEMS 40 48 P 05. SOFTWARE ENGINEERING 100 40 54 P PP 06. OPERATING SYSTEM DESIGN LAB. 25 10 24 P TW 50 20 46 P 07. OPERATING SYSTEM DESIGN LAB. 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 24 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 46 P 25 10 24 P 10. NETWORK LABORATORY 11. NETWORK LABORATORY OR 50 20 44 P 12. SOFT SKILLS LABORATORY TW 25 10 24 P FIRST TERM TOTAL = 525/750. ORDN. 1 MARKS:

DATE: 31 JAN. 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 21 (504) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71100884в T80058562 LUV VARMA PARVEEN , , PICT , T80058562 01. OPERATING SYSTEM 100 40 71 P 02. THEORY OF COMPUTATION 100 40 59 P PP 03. COMPUTER NETWORK TECHNOLOGY 100 40 79 P PP 04. DATBASE MANAGEMENT SYSTEMS 100 40 71 P PP 05. SOFTWARE ENGINEERING 100 40 60 P 25 10 23 P 06. OPERATING SYSTEM DESIGN LAB. TW 50 20 35 P 07. OPERATING SYSTEM DESIGN LAB. 25 10 23 P 08. INFORMATION SYSTEMS DESIGN LAB. TW 50 20 43 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 10. NETWORK LABORATORY 25 10 21 P TW 11. NETWORK LABORATORY 50 20 36 P OR 25 10 22 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 543/750. ORDN. 1 MARKS: T80058563 MADGULWAR AMIT ASHOK VIJAYA , 71100886J , PICT , T80058563 01. OPERATING SYSTEM 100 40 81 P 02. THEORY OF COMPUTATION 100 40 67 P PP 03. COMPUTER NETWORK TECHNOLOGY 100 40 72 P PP 100 40 69 P 04. DATBASE MANAGEMENT SYSTEMS PP 100 40 63 P 05. SOFTWARE ENGINEERING PP 25 10 23 P 06. OPERATING SYSTEM DESIGN LAB. TW 50 20 40 P 07. OPERATING SYSTEM DESIGN LAB. PR 25 10 23 P 08. INFORMATION SYSTEMS DESIGN LAB. TW 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 44 P 25 10 23 P 10. NETWORK LABORATORY TW 50 20 34 P 11. NETWORK LABORATORY OR 25 10 23 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 562/750. ORDN. 1 MARKS: T80058564 MAHAJAN DEEPIKA UDDHAV , 71241762в , PICT , T80058564 MALATI 01. OPERATING SYSTEM 100 40 65 P 02. THEORY OF COMPUTATION PP 100 40 47 P 03. COMPUTER NETWORK TECHNOLOGY 100 40 69 P PP 100 40 53 P 04. DATBASE MANAGEMENT SYSTEMS 05. SOFTWARE ENGINEERING 100 40 59 P PP 06. OPERATING SYSTEM DESIGN LAB. 25 10 22 P TW 50 20 45 P 07. OPERATING SYSTEM DESIGN LAB. 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 22 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 37 P 25 10 22 P 10. NETWORK LABORATORY 11. NETWORK LABORATORY OR 50 20 31 P 12. SOFT SKILLS LABORATORY TW 25 10 22 P FIRST TERM TOTAL = 494/750. ORDN. 1 MARKS:

DATE: 31 JAN. 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 22 (505) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71241763L T80058565 MAHARSHI PAYAL VIJAY NIRMALA , , PICT , T80058565 01. OPERATING SYSTEM PP 100 40 53 P 02. THEORY OF COMPUTATION PP 100 40 52 P 03. COMPUTER NETWORK TECHNOLOGY 100 40 62 P PP 04. DATBASE MANAGEMENT SYSTEMS 100 40 56 P PP 05. SOFTWARE ENGINEERING 100 40 51 P 25 10 20 P 06. OPERATING SYSTEM DESIGN LAB. TW 50 20 35 P 07. OPERATING SYSTEM DESIGN LAB. 25 10 23 P 08. INFORMATION SYSTEMS DESIGN LAB. TW 50 20 40 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 10. NETWORK LABORATORY 25 10 20 P TW 11. NETWORK LABORATORY 50 20 41 P OR 25 10 20 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 473/750. ORDN. 1 MARKS: T80058566 MANSI MATHUR PREETI , 71100890G , PICT , T80058566 40 54 P 01. OPERATING SYSTEM 100 100 40 40 P 02. THEORY OF COMPUTATION PP 03. COMPUTER NETWORK TECHNOLOGY 100 40 60 P PP 100 40 50 P 04. DATBASE MANAGEMENT SYSTEMS PP 100 40 57 P 05. SOFTWARE ENGINEERING PP 25 10 21 P 06. OPERATING SYSTEM DESIGN LAB. TW 50 20 07. OPERATING SYSTEM DESIGN LAB. PR 12 F 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 17 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 24 P 25 10 17 P 10. NETWORK LABORATORY TW 50 20 22 P 11. NETWORK LABORATORY OR 25 10 18 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 392/750. ORDN. 1 MARKS: T80058567 MANTRI ANKIT JAGDISH SANGITA , 71100891E , , PICT , т80058567 01. OPERATING SYSTEM 100 40 60 P 02. THEORY OF COMPUTATION PP 100 40 48 P 03. COMPUTER NETWORK TECHNOLOGY 100 40 64 P PP 100 40 57 P 04. DATBASE MANAGEMENT SYSTEMS 05. SOFTWARE ENGINEERING 100 40 49 P PP 06. OPERATING SYSTEM DESIGN LAB. 25 10 22 P TW 50 20 44 P 07. OPERATING SYSTEM DESIGN LAB. 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 23 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 42 P 25 10 24 P 10. NETWORK LABORATORY 11. NETWORK LABORATORY OR 50 20 45 P 12. SOFT SKILLS LABORATORY TW 25 10 22 P FIRST TERM TOTAL = 500/750. ORDN. 1 MARKS:

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DATE: 31 JAN. 2013
                           CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.
                                                                            PAGE NO. 23 ( 506)
NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
     OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
, 71100892C
 T80058568 MATHANGI KRISHNAMURTHI
                                       K JEYANTHY
                                                               , , PICT , T80058568
 01. OPERATING SYSTEM
                           PP 100 40 79 P
 02. THEORY OF COMPUTATION
                           PP 100
                                   40
                                      48 P
 03. COMPUTER NETWORK TECHNOLOGY
                               100
                                   40 71 P
                           PP
 04. DATBASE MANAGEMENT SYSTEMS
                               100
                                  40 66 P
                           PP
 05. SOFTWARE ENGINEERING
                               100
                                  40 65 P
                               25 10 24 P
 06. OPERATING SYSTEM DESIGN LAB.
                           TW
                               50 20
 07. OPERATING SYSTEM DESIGN LAB.
                                      46 P
 08. INFORMATION SYSTEMS DESIGN LAB. TW
                               25 10
                                      24 P
                               50 20
                                      47 P
 09. INFORMATION SYSTEMS DESIGN LAB. OR
 10. NETWORK LABORATORY
                               25 10
                                      24 P
                           TW
 11. NETWORK LABORATORY
                               50 20
                                      44 P
                           OR
                               25 10
                                      24 P
 12. SOFT SKILLS LABORATORY
                           TW
 FIRST TERM TOTAL = 562/750.
ORDN. 1 MARKS:
T80058569 MATNANI PRIYA SHIVADAS
                                       UPASANA
                                                      , 71100893м
                                                                , PICT
                                                                                    , T80058569
 01. OPERATING SYSTEM
                                   40 73 P
                              100
 02. THEORY OF COMPUTATION
                               100
                                   40 73 P
                           PP
 03. COMPUTER NETWORK TECHNOLOGY
                               100
                                   40 77 P
                           PP
                               100
                                   40
 04. DATBASE MANAGEMENT SYSTEMS
                           PP
                                      68 P
                               100
                                  40 68 P
 05. SOFTWARE ENGINEERING
                           PP
 06. OPERATING SYSTEM DESIGN LAB.
                               25
                                  10
                                      23 P
                           TW
                               50 20
                                      38 P
 07. OPERATING SYSTEM DESIGN LAB.
                           PR
 08. INFORMATION SYSTEMS DESIGN LAB. TW
                               25 10
                                      24 P
 09. INFORMATION SYSTEMS DESIGN LAB. OR
                               50 20
                                      42 P
                               25 10 21 P
 10. NETWORK LABORATORY
                           TW
                               50 20 44 P
 11. NETWORK LABORATORY
                           OR
                               25 10 22 P
 12. SOFT SKILLS LABORATORY
                           TW
 FIRST TERM TOTAL = 573/750.
ORDN. 1 MARKS:
T80058570 MONGA MANPREET KAUR DARSHAN SINGH
                                       GURPREET KAUR
                                                      , 71100898в
                                                                , PICT
                                                                                    , т80058570
 01. OPERATING SYSTEM
                               100
                                   40
                                      57 P
 02. THEORY OF COMPUTATION
                               100
                                   40
                                      41 P
                           PP
 03. COMPUTER NETWORK TECHNOLOGY
                               100
                                   40 65 P
                           PP
                               100
 04. DATBASE MANAGEMENT SYSTEMS
                                   40 65 P
 05. SOFTWARE ENGINEERING
                               100
                                   40
                                      55 P
                           PP
 06. OPERATING SYSTEM DESIGN LAB.
                               25 10 20 P
                           TW
                                50 20
                                      39 P
 07. OPERATING SYSTEM DESIGN LAB.
 08. INFORMATION SYSTEMS DESIGN LAB. TW
                               25 10
                                      21 P
 09. INFORMATION SYSTEMS DESIGN LAB. OR
                                50 20
                                      41 P
                               25 10 19 P
 10. NETWORK LABORATORY
 11. NETWORK LABORATORY
                           OR
                               50 20
                                      35 P
 12. SOFT SKILLS LABORATORY
                           TW
                               25 10
                                      20 P
 FIRST TERM TOTAL = 478/750.
ORDN. 1 MARKS:
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DATE: 31 JAN. 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 24 (507) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER ANITA , 71241764J T80058571 MORE SWATI ANNARAO , , PICT , T80058571 01. OPERATING SYSTEM PP 100 40 68 P 02. THEORY OF COMPUTATION PP 100 40 71 P 03. COMPUTER NETWORK TECHNOLOGY 100 40 70 P PP 04. DATBASE MANAGEMENT SYSTEMS 100 40 69 P PP 05. SOFTWARE ENGINEERING 100 40 67 P 25 10 24 P 06. OPERATING SYSTEM DESIGN LAB. TW 50 20 07. OPERATING SYSTEM DESIGN LAB. 40 P 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 24 P 50 20 44 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 10. NETWORK LABORATORY 25 10 24 P TW 11. NETWORK LABORATORY 50 20 40 P OR 25 10 24 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 565/750. ORDN. 1 MARKS: T80058572 MULCHANDANI POOJA OM PRITI , 71100901F , PICT , T80058572 01. OPERATING SYSTEM 40 56 P 100 02. THEORY OF COMPUTATION 100 40 59 P PP 03. COMPUTER NETWORK TECHNOLOGY 100 40 51 P PP 100 40 44 P 04. DATBASE MANAGEMENT SYSTEMS PP 100 40 47 P 05. SOFTWARE ENGINEERING PP 25 10 23 P 06. OPERATING SYSTEM DESIGN LAB. TW 50 20 07. OPERATING SYSTEM DESIGN LAB. PR 38 P 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 21 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 37 P 25 10 21 P 10. NETWORK LABORATORY TW 50 20 23 P 11. NETWORK LABORATORY OR 25 10 21 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 441/750. ORDN. 1 MARKS: T80058573 MULLA ASMA MUSA RABIYA , 71134970D , PICT , T80058573 01. OPERATING SYSTEM 100 40 54 P 02. THEORY OF COMPUTATION 100 40 40 P PP 03. COMPUTER NETWORK TECHNOLOGY 100 40 60 P PP 100 40 45 P 04. DATBASE MANAGEMENT SYSTEMS 05. SOFTWARE ENGINEERING 100 40 59 P PP 06. OPERATING SYSTEM DESIGN LAB. 25 10 18 P TW 50 20 38 P 07. OPERATING SYSTEM DESIGN LAB. 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 18 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 37 P 25 10 17 P 10. NETWORK LABORATORY 11. NETWORK LABORATORY OR 50 20 24 P 12. SOFT SKILLS LABORATORY TW 25 10 18 P FIRST TERM TOTAL = 428/750. ORDN. 1 MARKS:

DATE: 31 JAN. 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 25 (508) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71241765G T80058574 MUPADE PRIYA SHIVAJI KANTABAI , , PICT , T80058574 01. OPERATING SYSTEM PP 100 40 52 P 02. THEORY OF COMPUTATION PP 100 40 49 P 03. COMPUTER NETWORK TECHNOLOGY PP 100 40 47 P 04. DATBASE MANAGEMENT SYSTEMS 100 40 51 P PP 05. SOFTWARE ENGINEERING 100 40 57 P 25 10 20 P 06. OPERATING SYSTEM DESIGN LAB. TW 50 20 10 F 07. OPERATING SYSTEM DESIGN LAB. 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 16 P 50 20 10 F 09. INFORMATION SYSTEMS DESIGN LAB. OR 10. NETWORK LABORATORY 25 10 19 P TW 11. NETWORK LABORATORY 50 20 12 F OR 25 10 19 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 362/750. ORDN. 1 MARKS: T80058575 MUTHA VINITA VINOD SUREKHA , 71100904L , PICT , т80058575 01. OPERATING SYSTEM 40 49 P 100 100 40 42 P 02. THEORY OF COMPUTATION PP 100 40 49 P 03. COMPUTER NETWORK TECHNOLOGY PP 100 40 47 P 04. DATBASE MANAGEMENT SYSTEMS PP 100 40 50 P 05. SOFTWARE ENGINEERING PP 25 10 16 P 06. OPERATING SYSTEM DESIGN LAB. TW 50 20 38 P 07. OPERATING SYSTEM DESIGN LAB. PR 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 20 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 40 P 25 10 15 P 10. NETWORK LABORATORY TW 50 20 34 P 11. NETWORK LABORATORY OR 25 10 16 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 416/750. ORDN. 1 MARKS: , 71100907E , , PICT T80058576 NAIK RASHMI BHARAT MEENA , T80058576 01. OPERATING SYSTEM 100 40 40 P 02. THEORY OF COMPUTATION 100 40 43 P PP 03. COMPUTER NETWORK TECHNOLOGY 100 40 51 P PP 100 40 40 P 04. DATBASE MANAGEMENT SYSTEMS 05. SOFTWARE ENGINEERING 100 40 56 P PP 06. OPERATING SYSTEM DESIGN LAB. 25 10 18 P TW 50 20 27 P 07. OPERATING SYSTEM DESIGN LAB. 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 21 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 39 P 25 10 21 P 10. NETWORK LABORATORY 11. NETWORK LABORATORY OR 50 20 25 P 12. SOFT SKILLS LABORATORY TW 25 10 21 P FIRST TERM TOTAL = 402/750. ORDN. 1 MARKS:

DATE: 31 JAN. 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 26 (509) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71100911C T80058577 NARAD POONAM DEEPAK VIDYA , , PICT , T80058577 01. OPERATING SYSTEM PP 100 40 54 P 02. THEORY OF COMPUTATION PP 100 40 40 P 03. COMPUTER NETWORK TECHNOLOGY 100 40 55 P PP 04. DATBASE MANAGEMENT SYSTEMS 100 40 40 P PP 05. SOFTWARE ENGINEERING 100 40 50 P 25 10 20 P 06. OPERATING SYSTEM DESIGN LAB. TW 50 20 27 P 07. OPERATING SYSTEM DESIGN LAB. 25 10 17 P 08. INFORMATION SYSTEMS DESIGN LAB. TW 50 20 22 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 10. NETWORK LABORATORY 25 10 16 P TW 11. NETWORK LABORATORY 50 20 30 P OR 25 10 20 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 391/750. ORDN. 1 MARKS: T80058578 NARANG KARISHMA KISHOR SUNITA , 71100912M , PICT , T80058578 01. OPERATING SYSTEM 40 74 P 100 100 40 62 P 02. THEORY OF COMPUTATION PP 100 40 62 P 03. COMPUTER NETWORK TECHNOLOGY PP 100 40 62 P 04. DATBASE MANAGEMENT SYSTEMS PP 100 40 64 P 05. SOFTWARE ENGINEERING PP 25 10 18 P 06. OPERATING SYSTEM DESIGN LAB. TW 50 20 31 P 07. OPERATING SYSTEM DESIGN LAB. PR 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 19 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 40 P 25 10 18 P 10. NETWORK LABORATORY TW 50 20 40 P 11. NETWORK LABORATORY OR 25 10 18 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 508/750. ORDN. 1 MARKS: T80058579 NEVATIA SAKSHI SUNIL , 71100916D , PICT , т80058579 ANJU 01. OPERATING SYSTEM 100 40 59 P 02. THEORY OF COMPUTATION 100 40 56 P PP 03. COMPUTER NETWORK TECHNOLOGY 100 40 61 P PP 100 63 P 04. DATBASE MANAGEMENT SYSTEMS 40 05. SOFTWARE ENGINEERING 100 40 65 P PP 06. OPERATING SYSTEM DESIGN LAB. 25 10 21 P TW 32 P 50 20 07. OPERATING SYSTEM DESIGN LAB. 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 21 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 37 P 25 10 21 P 10. NETWORK LABORATORY 11. NETWORK LABORATORY OR 50 20 41 P 12. SOFT SKILLS LABORATORY TW 25 10 21 P FIRST TERM TOTAL = 498/750. ORDN. 1 MARKS:

DATE: 31 JAN. 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 27 (510) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71100918L T80058580 OKA TANVI VISHWAS MEDHA , , PICT , T80058580 01. OPERATING SYSTEM PP 100 40 76 P 02. THEORY OF COMPUTATION PP 100 40 73 P 03. COMPUTER NETWORK TECHNOLOGY 100 40 71 P PP 04. DATBASE MANAGEMENT SYSTEMS 100 40 64 P PP 05. SOFTWARE ENGINEERING 100 40 65 P 25 10 23 P 06. OPERATING SYSTEM DESIGN LAB. TW 50 20 29 P 07. OPERATING SYSTEM DESIGN LAB. 25 10 19 P 08. INFORMATION SYSTEMS DESIGN LAB. TW 50 20 40 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 10. NETWORK LABORATORY 25 10 17 P TW 11. NETWORK LABORATORY 50 20 41 P OR 25 10 17 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 535/750. ORDN. 1 MARKS: T80058581 OSWAL DARSHAN MANISH NEETA , 71100919J , PICT , T80058581 01. OPERATING SYSTEM 40 66 P 100 100 40 69 P 02. THEORY OF COMPUTATION PP 03. COMPUTER NETWORK TECHNOLOGY 100 40 62 P PP 100 40 55 P 04. DATBASE MANAGEMENT SYSTEMS PP 100 40 55 P 05. SOFTWARE ENGINEERING PP 25 23 P 06. OPERATING SYSTEM DESIGN LAB. TW 10 50 20 07. OPERATING SYSTEM DESIGN LAB. PR 33 P 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 22 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 43 P 25 10 22 P 10. NETWORK LABORATORY TW 50 20 31 P 11. NETWORK LABORATORY OR 25 10 22 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 503/750. ORDN. 1 MARKS: T80058582 PAGAR NILESH DADAJI YOGITA , 71100922J , PICT , T80058582 01. OPERATING SYSTEM 100 40 61 P 02. THEORY OF COMPUTATION 100 40 51 P PP 03. COMPUTER NETWORK TECHNOLOGY 100 40 72 P PP 100 04. DATBASE MANAGEMENT SYSTEMS 40 63 P 05. SOFTWARE ENGINEERING 100 40 67 P PP 06. OPERATING SYSTEM DESIGN LAB. 25 10 23 P TW 50 20 36 P 07. OPERATING SYSTEM DESIGN LAB. 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 23 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 38 P 25 10 22 P 10. NETWORK LABORATORY 11. NETWORK LABORATORY OR 50 20 40 P 12. SOFT SKILLS LABORATORY TW 25 10 22 P FIRST TERM TOTAL = 518/750. ORDN. 1 MARKS:

UNIVERSITY O		-	-			NFORMATION TECHNOLOGY) EXAMINATION NOV. 2012 E OF COMPUTER TECHNOLOGY, PUNE. PAG	E NO. 28 (511)
						R, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEG	
OTHER LINES: HEAD OF PASSING,	MAX	. MARK	S, M	IN. P	ASS	MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIO	US CARRY OVER
T80058583 PANDAY PRASHANT RAMDHAR						, 71100925C , PI	
01. OPERATING SYSTEM	PP	100	40	66		, ,, ,, ,,	,
02. THEORY OF COMPUTATION		100	40	63			
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	67			
04. DATBASE MANAGEMENT SYSTEMS	PP	100	40	62			
05. SOFTWARE ENGINEERING	PP	100	40	61	Р		
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	20	Р		
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	29	Р		
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	18			
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	43			
10. NETWORK LABORATORY	TW	25	10	18			
11. NETWORK LABORATORY	OR	50	20	38			
12. SOFT SKILLS LABORATORY	TW	25	10	18			
FIRST TERM TOTAL = 503/750.							
ORDN. 1 MARKS :							
T80058584 PANDIT SNEHALATA GANAPA	ΤΙ			SH	IOBH/		
01. OPERATING SYSTEM	PP	100	40	70		,	,
02. THEORY OF COMPUTATION	PP	100	40	73			
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	72			
04. DATBASE MANAGEMENT SYSTEMS	PP	100	40	56	Р		
05. SOFTWARE ENGINEERING	PP	100	40	72	Р		
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	24	Р		
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	41	Р		
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	24	Р		
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	44	Р		
10. NETWORK LABORATORY	TW	25	10	24	Р		
11. NETWORK LABORATORY	OR	50	20	33	Р		
12. SOFT SKILLS LABORATORY	TW	25	10	23	Р		
FIRST TERM TOTAL = 556/750.							
ORDN. 1 MARKS :							
T80058585 PARSE JYOTI SHRIKRISHNA				AM	BIKA	A , 71241767C , PI	ст , т80058585
01. OPERATING SYSTEM	PP	100	40	77	Р		
02. THEORY OF COMPUTATION	PP	100	40	65	Р		
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	69	Р		
04. DATBASE MANAGEMENT SYSTEMS	PP	100	40	56	Р		
05. SOFTWARE ENGINEERING	PP	100	40	63	Р		
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	23	Р		
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	35	Р		
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	22	Р		
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	43	Р		
10. NETWORK LABORATORY	TW	25	10	19	Р		
11. NETWORK LABORATORY	OR	50	20	23	Р		
12. SOFT SKILLS LABORATORY	TW	25	10	20	Р		
FIRST TERM TOTAL = 515/750.							
ORDN. 1 MARKS :							

DATE: 31 JAN. 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 29 (512) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71100933D T80058586 PATEL MANISH UTTAM KAILAS , , PICT , T80058586 01. OPERATING SYSTEM PP 100 40 80 P 02. THEORY OF COMPUTATION PP 100 40 64 P 03. COMPUTER NETWORK TECHNOLOGY 100 40 66 P PP 04. DATBASE MANAGEMENT SYSTEMS 100 40 69 P PP 05. SOFTWARE ENGINEERING 100 40 70 P 25 10 24 P 06. OPERATING SYSTEM DESIGN LAB. TW 50 20 07. OPERATING SYSTEM DESIGN LAB. 31 P 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 24 P 50 20 40 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 10. NETWORK LABORATORY 25 10 23 P TW 11. NETWORK LABORATORY 50 20 38 P OR 25 10 23 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 552/750. ORDN. 1 MARKS: T80058587 PATHAK ANUJA PRAVIN UJJWALA , 71100934в , PICT , T80058587 40 49 P 01. OPERATING SYSTEM 100 02. THEORY OF COMPUTATION 100 40 57 P PP 100 40 53 P 03. COMPUTER NETWORK TECHNOLOGY PP 100 40 41 P 04. DATBASE MANAGEMENT SYSTEMS PP 100 40 47 P 05. SOFTWARE ENGINEERING PP 25 10 19 P 06. OPERATING SYSTEM DESIGN LAB. TW 50 20 29 P 07. OPERATING SYSTEM DESIGN LAB. PR 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 18 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 36 P 25 10 17 P 10. NETWORK LABORATORY TW 50 20 29 P 11. NETWORK LABORATORY OR 25 10 17 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 412/750. ORDN. 1 MARKS: T80058588 PATIL AMOL MADHUKAR JYOTI , 71100938E , PICT , T80058588 01. OPERATING SYSTEM 100 40 54 P 02. THEORY OF COMPUTATION 100 40 44 P PP 03. COMPUTER NETWORK TECHNOLOGY 100 40 45 P PP 100 40 45 P 04. DATBASE MANAGEMENT SYSTEMS 05. SOFTWARE ENGINEERING 100 40 52 P PP 06. OPERATING SYSTEM DESIGN LAB. 25 10 14 P TW 50 20 32 P 07. OPERATING SYSTEM DESIGN LAB. 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 18 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 38 P 25 10 12 P 10. NETWORK LABORATORY 11. NETWORK LABORATORY OR 50 20 08 F 12. SOFT SKILLS LABORATORY TW 25 10 12 P FIRST TERM TOTAL = 374/750. ORDN. 1 MARKS:

DATE: 31 JAN. 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 30 (513) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71100941E , , PICT , T80058589 T80058589 PATIL PRIYA KISHOR PUSHPA 01. OPERATING SYSTEM PP 100 40 60 P 02. THEORY OF COMPUTATION PP 100 40 55 P 03. COMPUTER NETWORK TECHNOLOGY 100 40 57 P PP 04. DATBASE MANAGEMENT SYSTEMS 100 40 65 P PP 05. SOFTWARE ENGINEERING 100 40 54 P 25 10 20 P 06. OPERATING SYSTEM DESIGN LAB. TW 50 20 30 P 07. OPERATING SYSTEM DESIGN LAB. 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 19 P 50 20 35 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 10. NETWORK LABORATORY 25 10 19 P TW 11. NETWORK LABORATORY 50 20 25 P OR 25 10 19 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 458/750. ORDN. 1 MARKS: T80058590 PATIL RUPESHSING RAMESH KANCHAN , 71100943M , PICT , T80058590 40 56 P 01. OPERATING SYSTEM 100 02. THEORY OF COMPUTATION 100 40 PP 61 P 03. COMPUTER NETWORK TECHNOLOGY 100 40 59 P PP 100 40 64 P 04. DATBASE MANAGEMENT SYSTEMS PP 100 40 58 P 05. SOFTWARE ENGINEERING PP 25 10 14 P 06. OPERATING SYSTEM DESIGN LAB. TW 50 20 25 P 07. OPERATING SYSTEM DESIGN LAB. PR 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 18 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 35 P 25 10 15 P 10. NETWORK LABORATORY TW 50 20 10 F 11. NETWORK LABORATORY OR 25 10 17 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 432/750. ORDN. 1 MARKS: T80058591 PATIL RUSHIKESH VASANTRAO SHOBHANA , 71100944K , PICT , т80058591 01. OPERATING SYSTEM 100 40 66 P 02. THEORY OF COMPUTATION 100 40 70 P PP 03. COMPUTER NETWORK TECHNOLOGY 100 40 63 P PP 100 40 64 P 04. DATBASE MANAGEMENT SYSTEMS 05. SOFTWARE ENGINEERING 100 40 53 P PP 06. OPERATING SYSTEM DESIGN LAB. 25 10 20 P TW 50 20 32 P 07. OPERATING SYSTEM DESIGN LAB. 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 19 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 43 P 25 10 19 P 10. NETWORK LABORATORY 11. NETWORK LABORATORY OR 50 20 39 P 12. SOFT SKILLS LABORATORY TW 25 10 19 P FIRST TERM TOTAL = 507/750. ORDN. 1 MARKS:

DATE: 31 JAN. 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 31 (514) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 70925555В T80058592 PAWAR CHANDAN DILIP REKHA , , PICT , T80058592 01. OPERATING SYSTEM PP 100 40 72 P 02. THEORY OF COMPUTATION PP 100 40 43 P 03. COMPUTER NETWORK TECHNOLOGY 100 40 61 P PP 04. DATBASE MANAGEMENT SYSTEMS 100 40 52 P PP 05. SOFTWARE ENGINEERING 100 40 57 P 25 10 16 P 06. OPERATING SYSTEM DESIGN LAB. TW 07. OPERATING SYSTEM DESIGN LAB. 50 20 AA F 25 10 17 P 08. INFORMATION SYSTEMS DESIGN LAB. TW 50 20 04 F 09. INFORMATION SYSTEMS DESIGN LAB. OR 10. NETWORK LABORATORY 25 10 14 P TW 11. NETWORK LABORATORY 50 20 37 P OR 25 10 13 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 386/750. ORDN. 1 MARKS: T80058593 PAWAR PRASAD GORAKSH MEENAKSHI , 71100951B , PICT , T80058593 01. OPERATING SYSTEM 100 40 42 P 02. THEORY OF COMPUTATION 100 40 PP 21 F 03. COMPUTER NETWORK TECHNOLOGY 100 40 40 P PP 100 40 40 P 04. DATBASE MANAGEMENT SYSTEMS PP 100 40 40 P 05. SOFTWARE ENGINEERING PP 25 10 18 P 06. OPERATING SYSTEM DESIGN LAB. TW 50 20 33 P 07. OPERATING SYSTEM DESIGN LAB. PR 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 15 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 37 P 25 10 12 P 10. NETWORK LABORATORY TW 50 20 05 F 11. NETWORK LABORATORY OR 25 10 12 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 315/750. ORDN. 1 MARKS: T80058594 PAWAR SNEHA SADASHIV JYOTI , 71241768M , PICT , т80058594 01. OPERATING SYSTEM 100 40 88 P 02. THEORY OF COMPUTATION 100 40 59 P PP 03. COMPUTER NETWORK TECHNOLOGY 100 40 73 P PP 100 40 75 P 04. DATBASE MANAGEMENT SYSTEMS 05. SOFTWARE ENGINEERING 100 40 75 P PP 06. OPERATING SYSTEM DESIGN LAB. 25 10 23 P TW 50 20 41 P 07. OPERATING SYSTEM DESIGN LAB. 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 23 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 44 P 25 10 22 P 10. NETWORK LABORATORY 11. NETWORK LABORATORY OR 50 20 22 P 12. SOFT SKILLS LABORATORY TW 25 10 23 P FIRST TERM TOTAL = 568/750. ORDN. 1 MARKS:

DATE: 31 JAN. 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 32 (515) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71241769K T80058595 PAYGUDE GAUTAMI SANJAY SUMAN , , PICT , T80058595 01. OPERATING SYSTEM PP 100 40 73 P 02. THEORY OF COMPUTATION PP 100 40 45 P 03. COMPUTER NETWORK TECHNOLOGY 100 40 67 P PP 04. DATBASE MANAGEMENT SYSTEMS 100 40 54 P PP 05. SOFTWARE ENGINEERING 100 40 66 P 25 10 24 P 06. OPERATING SYSTEM DESIGN LAB. TW 50 20 07. OPERATING SYSTEM DESIGN LAB. 39 P 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 24 P 50 20 38 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 10. NETWORK LABORATORY 25 10 24 P TW 11. NETWORK LABORATORY 50 20 40 P OR 25 10 24 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 518/750. ORDN. 1 MARKS: T80058596 PHADKE MUGDHA RAVINDRA PRADNYA , 71100954G , PICT , T80058596 40 57 P 01. OPERATING SYSTEM 100 02. THEORY OF COMPUTATION 100 40 44 P PP 03. COMPUTER NETWORK TECHNOLOGY 100 40 56 P PP 100 40 55 P 04. DATBASE MANAGEMENT SYSTEMS PP 100 40 49 P 05. SOFTWARE ENGINEERING PP 25 22 P 06. OPERATING SYSTEM DESIGN LAB. TW 10 50 20 35 P 07. OPERATING SYSTEM DESIGN LAB. PR 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 21 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 37 P 25 10 21 P 10. NETWORK LABORATORY TW 50 20 06 F 11. NETWORK LABORATORY OR 25 10 22 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 425/750. ORDN. 1 MARKS: SUNITA T80058597 POTE ISHWAR VITTHAL , 71100957M , PICT , т80058597 01. OPERATING SYSTEM 100 40 66 P 02. THEORY OF COMPUTATION 100 40 40 P PP 03. COMPUTER NETWORK TECHNOLOGY 100 40 44 P PP 100 40 44 P 04. DATBASE MANAGEMENT SYSTEMS 05. SOFTWARE ENGINEERING 100 40 44 P PP 06. OPERATING SYSTEM DESIGN LAB. 25 10 19 P TW 50 20 33 P 07. OPERATING SYSTEM DESIGN LAB. 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 15 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 37 P 25 10 16 P 10. NETWORK LABORATORY 11. NETWORK LABORATORY OR 50 20 38 P 12. SOFT SKILLS LABORATORY TW 25 10 16 P FIRST TERM TOTAL = 412/750. ORDN. 1 MARKS:

DATE: 31 JAN. 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 33 (516) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71100962H , , PICT , T80058598 T80058598 RAHUL BAIJAL SADHNA 01. OPERATING SYSTEM 100 40 80 P 02. THEORY OF COMPUTATION 100 40 67 P PP 03. COMPUTER NETWORK TECHNOLOGY 100 40 70 P PP 04. DATBASE MANAGEMENT SYSTEMS 100 40 69 P PP 05. SOFTWARE ENGINEERING 100 40 64 P 25 10 21 P 06. OPERATING SYSTEM DESIGN LAB. TW 50 20 07. OPERATING SYSTEM DESIGN LAB. 40 P 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 22 P 50 20 44 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 10. NETWORK LABORATORY 25 10 18 P TW 11. NETWORK LABORATORY 50 20 33 P OR 25 10 21 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 549/750. ORDN. 1 MARKS: T80058599 RAHUL SHARMA PROMILA , 71100963F , PICT , T80058599 40 59 P 01. OPERATING SYSTEM 100 100 40 66 P 02. THEORY OF COMPUTATION PP 03. COMPUTER NETWORK TECHNOLOGY 100 40 46 P PP 100 40 51 P 04. DATBASE MANAGEMENT SYSTEMS PP 100 40 43 P 05. SOFTWARE ENGINEERING PP 25 12 P 06. OPERATING SYSTEM DESIGN LAB. TW 10 50 20 07. OPERATING SYSTEM DESIGN LAB. PR AA F 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 11 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 AA F 25 10 13 P 10. NETWORK LABORATORY TW 50 20 AA F 11. NETWORK LABORATORY OR 25 10 12 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 313/750. ORDN. 1 MARKS: T80058600 RAMPURAWALA MARIYA FIROZ FARIDA , 71100964D , PICT , т80058600 01. OPERATING SYSTEM 100 40 70 P 02. THEORY OF COMPUTATION 100 40 73 P PP 03. COMPUTER NETWORK TECHNOLOGY 100 40 48 P PP 100 40 56 P 04. DATBASE MANAGEMENT SYSTEMS 05. SOFTWARE ENGINEERING 100 40 57 P PP 06. OPERATING SYSTEM DESIGN LAB. 25 10 20 P TW 50 20 32 P 07. OPERATING SYSTEM DESIGN LAB. 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 22 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 39 P 25 10 19 P 10. NETWORK LABORATORY 11. NETWORK LABORATORY OR 50 20 07 F 12. SOFT SKILLS LABORATORY TW 25 10 20 P FIRST TERM TOTAL = 463/750. ORDN. 1 MARKS:

DATE: 31 JAN. 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 34 (517) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER KANCHAN , 71100966L T80058601 RANADIVE ABHIJEET JANARDAN , , PICT , T80058601 01. OPERATING SYSTEM PP 100 40 56 P 02. THEORY OF COMPUTATION PP 100 40 58 P 03. COMPUTER NETWORK TECHNOLOGY 100 40 53 P PP 04. DATBASE MANAGEMENT SYSTEMS 100 40 56 P PP 05. SOFTWARE ENGINEERING 100 40 55 P 25 10 23 P 06. OPERATING SYSTEM DESIGN LAB. TW 50 20 31 P 07. OPERATING SYSTEM DESIGN LAB. 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 21 P 50 20 40 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 10. NETWORK LABORATORY 25 10 19 P TW 11. NETWORK LABORATORY 50 20 43 P OR 25 10 19 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 474/750. ORDN. 1 MARKS: T80058602 RATHOD ASHWINKUMAR AMARSING RANJANA , 71241770C , PICT , T80058602 100 40 71 P 01. OPERATING SYSTEM 02. THEORY OF COMPUTATION 100 40 62 P PP 03. COMPUTER NETWORK TECHNOLOGY 100 40 67 P 100 40 74 P 04. DATBASE MANAGEMENT SYSTEMS PP 100 40 72 P 05. SOFTWARE ENGINEERING PP 25 21 P 06. OPERATING SYSTEM DESIGN LAB. TW 10 50 20 36 P 07. OPERATING SYSTEM DESIGN LAB. PR 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 20 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 41 P 25 10 20 P 10. NETWORK LABORATORY TW 50 20 41 P 11. NETWORK LABORATORY OR 25 10 20 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 545/750. ORDN. 1 MARKS: T80058603 RATTAN NAKUL VISHWAS BIMLA , 71100971G , PICT , т80058603 01. OPERATING SYSTEM 100 40 66 P 02. THEORY OF COMPUTATION 100 40 53 P PP 03. COMPUTER NETWORK TECHNOLOGY 100 40 49 P PP 100 40 42 P 04. DATBASE MANAGEMENT SYSTEMS 05. SOFTWARE ENGINEERING 100 40 44 P PP 06. OPERATING SYSTEM DESIGN LAB. 25 10 22 P TW 50 20 36 P 07. OPERATING SYSTEM DESIGN LAB. 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 22 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 34 P 25 10 19 P 10. NETWORK LABORATORY 11. NETWORK LABORATORY OR 50 20 40 P 12. SOFT SKILLS LABORATORY TW 25 10 19 P FIRST TERM TOTAL = 446/750. ORDN. 1 MARKS:

DATE: 31 JAN. 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 35 (518) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER T80058604 RIA NARAYAN , 71100975K MANJU , , PICT , T80058604 01. OPERATING SYSTEM 100 40 73 P 02. THEORY OF COMPUTATION 100 40 73 P PP 03. COMPUTER NETWORK TECHNOLOGY 100 40 69 P PP 04. DATBASE MANAGEMENT SYSTEMS 100 40 66 P PP 05. SOFTWARE ENGINEERING 100 40 65 P 25 10 18 P 06. OPERATING SYSTEM DESIGN LAB. TW 50 20 07. OPERATING SYSTEM DESIGN LAB. 41 P 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 19 P 50 20 39 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 10. NETWORK LABORATORY 25 10 18 P TW 11. NETWORK LABORATORY 50 20 38 P OR 25 10 19 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 538/750. ORDN. 1 MARKS: T80058605 ROMIL MEHTA SHEELA , 71100978D , PICT , T80058605 40 69 P 01. OPERATING SYSTEM 100 100 40 74 P 02. THEORY OF COMPUTATION PP 03. COMPUTER NETWORK TECHNOLOGY 100 40 66 P PP 100 40 68 P 04. DATBASE MANAGEMENT SYSTEMS PP 100 40 63 P 05. SOFTWARE ENGINEERING PP 25 10 23 P 06. OPERATING SYSTEM DESIGN LAB. TW 50 20 29 P 07. OPERATING SYSTEM DESIGN LAB. PR 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 22 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 36 P 25 10 21 P 10. NETWORK LABORATORY TW 50 20 41 P 11. NETWORK LABORATORY OR 25 10 21 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 533/750. ORDN. 1 MARKS: , PICT T80058606 SABALE BHUSHAN BALASAHEB SUMAN , 71100980F , T80058606 01. OPERATING SYSTEM 100 40 70 P 02. THEORY OF COMPUTATION 100 40 57 P PP 03. COMPUTER NETWORK TECHNOLOGY 100 40 65 P PP 100 04. DATBASE MANAGEMENT SYSTEMS 40 56 P 05. SOFTWARE ENGINEERING 100 40 62 P PP 06. OPERATING SYSTEM DESIGN LAB. 25 10 21 P TW 50 20 36 P 07. OPERATING SYSTEM DESIGN LAB. 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 21 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 44 P 25 10 21 P 10. NETWORK LABORATORY 11. NETWORK LABORATORY OR 50 20 42 P 12. SOFT SKILLS LABORATORY TW 25 10 21 P FIRST TERM TOTAL = 516/750. ORDN. 1 MARKS:

DATE: 31 JAN. 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 36 (519) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71100981D T80058607 SAGAR SABIR DAMANI SHAHIN , , PICT , T80058607 01. OPERATING SYSTEM PP 100 40 79 P 02. THEORY OF COMPUTATION PP 100 40 64 P 03. COMPUTER NETWORK TECHNOLOGY 100 40 74 P PP 04. DATBASE MANAGEMENT SYSTEMS 100 40 64 P PP 05. SOFTWARE ENGINEERING 100 40 65 P 25 10 24 P 06. OPERATING SYSTEM DESIGN LAB. TW 50 20 07. OPERATING SYSTEM DESIGN LAB. 39 P 25 10 24 P 08. INFORMATION SYSTEMS DESIGN LAB. TW 50 20 46 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 10. NETWORK LABORATORY 25 10 23 P TW 11. NETWORK LABORATORY 50 20 29 P OR 25 10 24 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 555/750. ORDN. 1 MARKS: T80058608 SAHARE NUPUR ARVIND MAMTA , 71100982в , PICT , T80058608 40 74 P 01. OPERATING SYSTEM 100 100 40 56 P 02. THEORY OF COMPUTATION PP 100 40 65 P 03. COMPUTER NETWORK TECHNOLOGY PP 100 40 53 P 04. DATBASE MANAGEMENT SYSTEMS PP 100 40 64 P 05. SOFTWARE ENGINEERING PP 25 10 19 P 06. OPERATING SYSTEM DESIGN LAB. TW 50 20 15 F 07. OPERATING SYSTEM DESIGN LAB. PR 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 20 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 32 P 25 10 19 P 10. NETWORK LABORATORY TW 50 20 32 P 11. NETWORK LABORATORY OR 25 10 19 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 468/750. ORDN. 1 MARKS: T80058609 SARAF DARPAN RAJENDRA AMITA , 71100986E , PICT , т80058609 01. OPERATING SYSTEM 100 40 62 P 02. THEORY OF COMPUTATION 100 40 62 P PP 03. COMPUTER NETWORK TECHNOLOGY 100 40 73 P PP 100 04. DATBASE MANAGEMENT SYSTEMS 40 60 P 05. SOFTWARE ENGINEERING 100 40 62 P PP 06. OPERATING SYSTEM DESIGN LAB. 25 10 19 P TW 50 20 33 P 07. OPERATING SYSTEM DESIGN LAB. 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 19 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 44 P 25 10 19 P 10. NETWORK LABORATORY 11. NETWORK LABORATORY OR 50 20 25 P 12. SOFT SKILLS LABORATORY TW 25 10 19 P FIRST TERM TOTAL = 497/750. ORDN. 1 MARKS:

DATE: 31 JAN. 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 37 (520) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71045599C T80058610 SARWAR PRIYANKA ASHOK MEERA , , PICT , T80058610 01. OPERATING SYSTEM PP 100 40 60 P 02. THEORY OF COMPUTATION PP 100 40 44 P 03. COMPUTER NETWORK TECHNOLOGY 100 40 73 P PP 04. DATBASE MANAGEMENT SYSTEMS 100 40 49 P PP 05. SOFTWARE ENGINEERING 100 40 57 P 25 10 18 P 06. OPERATING SYSTEM DESIGN LAB. TW 50 20 15 F 07. OPERATING SYSTEM DESIGN LAB. 25 10 19 P 08. INFORMATION SYSTEMS DESIGN LAB. TW 50 20 37 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 10. NETWORK LABORATORY 25 10 15 P TW 11. NETWORK LABORATORY 50 20 28 P OR 25 10 18 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 433/750. ORDN. 1 MARKS: T80058611 SASANE PRANITA RAMESH , 71100989к , PICT , T80058611 ALKA 40 55 P 01. OPERATING SYSTEM 100 100 40 50 P 02. THEORY OF COMPUTATION PP 03. COMPUTER NETWORK TECHNOLOGY 100 40 42 P PP 100 40 40 P 04. DATBASE MANAGEMENT SYSTEMS PP 100 40 40 P 05. SOFTWARE ENGINEERING PP 25 10 21 P 06. OPERATING SYSTEM DESIGN LAB. TW 50 20 30 P 07. OPERATING SYSTEM DESIGN LAB. PR 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 21 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 34 P 25 10 21 P 10. NETWORK LABORATORY TW 50 20 21 P 11. NETWORK LABORATORY OR 25 10 21 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 396/750. ORDN. 1 MARKS: T80058612 SAYYED FAKHRUDDIN KAMALUDDIN RAZIYA KHANAM , 71241771M , PICT , т80058612 01. OPERATING SYSTEM 100 40 75 P 02. THEORY OF COMPUTATION 100 40 56 P PP 03. COMPUTER NETWORK TECHNOLOGY 100 40 63 P PP 100 04. DATBASE MANAGEMENT SYSTEMS 40 55 P 05. SOFTWARE ENGINEERING 100 40 61 P PP 06. OPERATING SYSTEM DESIGN LAB. 25 10 24 P TW 50 20 43 P 07. OPERATING SYSTEM DESIGN LAB. 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 22 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 42 P 25 10 21 P 10. NETWORK LABORATORY 11. NETWORK LABORATORY OR 50 20 47 P 12. SOFT SKILLS LABORATORY TW 25 10 23 P FIRST TERM TOTAL = 532/750. ORDN. 1 MARKS:

DATE: 31 JAN. 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 38 (521) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71100999G T80058613 SHARMA LAKSHMI KANAK , , PICT , T80058613 01. OPERATING SYSTEM 100 40 60 P 02. THEORY OF COMPUTATION 100 40 67 P PP 03. COMPUTER NETWORK TECHNOLOGY 100 40 73 P PP 04. DATBASE MANAGEMENT SYSTEMS 100 40 57 P PP 05. SOFTWARE ENGINEERING 100 40 67 P 25 10 24 P 06. OPERATING SYSTEM DESIGN LAB. TW 50 20 39 P 07. OPERATING SYSTEM DESIGN LAB. 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 23 P 50 20 09. INFORMATION SYSTEMS DESIGN LAB. OR 41 P 10. NETWORK LABORATORY 25 10 19 P TW 11. NETWORK LABORATORY 50 20 28 P OR 25 10 23 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 521/750. ORDN. 1 MARKS: T80058614 SHINDE NUTAN BALASAHEB MANISHA , 71101006E , PICT , T80058614 40 79 P 01. OPERATING SYSTEM 100 100 40 55 P 02. THEORY OF COMPUTATION PP 100 40 67 P 03. COMPUTER NETWORK TECHNOLOGY PP 100 40 57 P 04. DATBASE MANAGEMENT SYSTEMS PP 100 40 69 P 05. SOFTWARE ENGINEERING PP 25 24 P 06. OPERATING SYSTEM DESIGN LAB. TW 10 50 20 07. OPERATING SYSTEM DESIGN LAB. PR 38 P 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 23 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 40 P 25 10 23 P 10. NETWORK LABORATORY TW 50 20 33 P 11. NETWORK LABORATORY OR 25 10 22 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 530/750. ORDN. 1 MARKS: T80058615 SHINDE SUSHANT GAUTAM SANGITA , 71241772K , PICT , T80058615 01. OPERATING SYSTEM 100 40 60 P 02. THEORY OF COMPUTATION PP 100 40 46 P 03. COMPUTER NETWORK TECHNOLOGY 100 40 67 P PP 100 40 59 P 04. DATBASE MANAGEMENT SYSTEMS 05. SOFTWARE ENGINEERING 100 40 65 P PP 06. OPERATING SYSTEM DESIGN LAB. 25 10 24 P TW 50 20 35 P 07. OPERATING SYSTEM DESIGN LAB. 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 24 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 46 P 25 10 23 P 10. NETWORK LABORATORY 11. NETWORK LABORATORY OR 50 20 26 P 12. SOFT SKILLS LABORATORY TW 25 10 24 P FIRST TERM TOTAL = 499/750. ORDN. 1 MARKS:

UNIVERSITY OF DATE: 31 JAN. 2013						FORMATION TECHNOLOGY) EXAMIN OF COMPUTER TECHNOLOGY, PUN			39 (522)
NOTE: FIRST LINE : SEAT NO., NAME (
OTHER LINES: HEAD OF PASSING,	MAX	. MARK	S, M	IN. P	ASS N	MARKS, MARKS OBTAINED, P/F	::PASS/FAIL,	C:PREVIOUS CAR	RY OVER
T80058616 SHIRA VISHAL BHAGWANSING					 LTA				
01. OPERATING SYSTEM	PP	100	40	59		, 71101003K	,	, FICI	, 100030010
02. THEORY OF COMPUTATION		100	40	51					
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	61					
04. DATBASE MANAGEMENT SYSTEMS	PP	100	40	40					
05. SOFTWARE ENGINEERING	PP	100	40	59					
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	18					
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	32					
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	19	Р				
09. INFORMATION SYSTEMS DESIGN LAB.		50	20	42					
10. NETWORK LABORATORY	TW	25	10	16					
11. NETWORK LABORATORY	OR	50	20	08					
12. SOFT SKILLS LABORATORY	_	25	10	20					
FIRST TERM TOTAL = 425/750.									
ORDN. 1 MARKS :									
T80058617 SHIRBHATE ABHILASH DNYA					JSHRI		,		, т80058617
01. OPERATING SYSTEM		100	40	46		•	,	•	,
02. THEORY OF COMPUTATION	PP	100	40	43	Р				
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	62					
04. DATBASE MANAGEMENT SYSTEMS	PP	100	40	45	Р				
05. SOFTWARE ENGINEERING	PP	100	40	49	Р				
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	17	Р				
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	21	Р				
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	15	Р				
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	39	Р				
10. NETWORK LABORATORY	TW	25	10	12	Р				
11. NETWORK LABORATORY	OR	50	20	32	Р				
12. SOFT SKILLS LABORATORY	TW	25	10	14	Р				
FIRST TERM TOTAL = 395/750.									
ORDN. 1 MARKS :									
T80058618 SHUKLA MANISH NITYANAND				MA	DHUR	, 71045622м	,	, PICT	, т80058618
01. OPERATING SYSTEM	PP	100	40	63	Р				
02. THEORY OF COMPUTATION	PP	100	40	50	Р				
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	63	Р				
04. DATBASE MANAGEMENT SYSTEMS	PP	100	40	51	Р				
05. SOFTWARE ENGINEERING	PP	100	40	55	Р				
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	17	Р				
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	29	Р				
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	16	Р				
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	41	Р				
10. NETWORK LABORATORY	TW	25	10	14	Р				
11. NETWORK LABORATORY	OR	50	20	29	Р				
12. SOFT SKILLS LABORATORY	TW	25	10	17	Р				
FIRST TERM TOTAL = $445/750$.									
ORDN. 1 MARKS :									

DATE: 31 JAN. 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 40 (523) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71101015D T80058619 SIDDHANT BANSAL SUJATA , , PICT , T80058619 01. OPERATING SYSTEM 100 40 69 P 02. THEORY OF COMPUTATION PP 100 40 60 P 03. COMPUTER NETWORK TECHNOLOGY 100 40 67 P PP 04. DATBASE MANAGEMENT SYSTEMS 100 40 71 P PP 05. SOFTWARE ENGINEERING 100 40 57 P 25 10 18 P 06. OPERATING SYSTEM DESIGN LAB. TW 50 20 35 P 07. OPERATING SYSTEM DESIGN LAB. 25 10 19 P 08. INFORMATION SYSTEMS DESIGN LAB. TW 50 20 40 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 10. NETWORK LABORATORY 25 10 16 P TW 11. NETWORK LABORATORY 50 20 10 F OR 25 10 18 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 480/750. ORDN. 1 MARKS: T80058620 SOLANKE BHUSHAN NARENDRA REKHA , 71101017L , PICT , T80058620 40 42 P 01. OPERATING SYSTEM PP 100 02. THEORY OF COMPUTATION 100 40 48 P PP 100 40 42 P 03. COMPUTER NETWORK TECHNOLOGY 100 40 40 P 04. DATBASE MANAGEMENT SYSTEMS PP 100 40 56 P 05. SOFTWARE ENGINEERING PP 25 10 22 P 06. OPERATING SYSTEM DESIGN LAB. TW 50 20 27 P 07. OPERATING SYSTEM DESIGN LAB. PR 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 16 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 27 P 25 10 15 P 10. NETWORK LABORATORY TW 50 20 11 F 11. NETWORK LABORATORY OR 25 10 16 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 362/750. ORDN. 1 MARKS: T80058621 SONAWANE JAGDISH RAJENDRA HIRABAI , 71241773H , , PICT , т80058621 01. OPERATING SYSTEM 100 40 64 P 02. THEORY OF COMPUTATION 100 40 50 P PP 03. COMPUTER NETWORK TECHNOLOGY 100 40 72 P PP 100 04. DATBASE MANAGEMENT SYSTEMS 40 65 P 05. SOFTWARE ENGINEERING 100 40 65 P PP 06. OPERATING SYSTEM DESIGN LAB. 25 10 19 P TW 50 20 30 P 07. OPERATING SYSTEM DESIGN LAB. 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 17 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 43 P 25 10 18 P 10. NETWORK LABORATORY 11. NETWORK LABORATORY OR 50 20 43 P 12. SOFT SKILLS LABORATORY TW 25 10 19 P FIRST TERM TOTAL = 505/750. ORDN. 1 MARKS:

DATE: 31 JAN. 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 41 (524) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71101022G T80058622 STUTI MITTAL BHAVNA , , PICT , T80058622 01. OPERATING SYSTEM 100 40 61 P 02. THEORY OF COMPUTATION 100 40 40 P PP 03. COMPUTER NETWORK TECHNOLOGY 100 40 54 P PP 04. DATBASE MANAGEMENT SYSTEMS 100 40 49 P PP 05. SOFTWARE ENGINEERING 100 40 55 P 25 10 19 P 06. OPERATING SYSTEM DESIGN LAB. TW 50 20 31 P 07. OPERATING SYSTEM DESIGN LAB. 25 10 22 P 08. INFORMATION SYSTEMS DESIGN LAB. TW 50 20 25 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 10. NETWORK LABORATORY 25 10 18 P TW 11. NETWORK LABORATORY 50 20 42 P OR 25 10 18 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 434/750. ORDN. 1 MARKS: T80058623 SWETAMBARI , 71101024C , PICT , T80058623 ANJU 40 78 P 01. OPERATING SYSTEM 100 100 40 57 P 02. THEORY OF COMPUTATION PP 03. COMPUTER NETWORK TECHNOLOGY 100 40 77 P PP 100 40 04. DATBASE MANAGEMENT SYSTEMS PP 67 P 100 40 59 P 05. SOFTWARE ENGINEERING PP 25 10 23 P 06. OPERATING SYSTEM DESIGN LAB. TW 50 20 32 P 07. OPERATING SYSTEM DESIGN LAB. PR 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 24 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 40 P 25 10 24 P 10. NETWORK LABORATORY TW 50 20 43 P 11. NETWORK LABORATORY OR 25 10 24 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 548/750. ORDN. 1 MARKS: T80058624 TEKE AMRUTA PRAKASH SHALINEE , 71101028F , PICT , т80058624 01. OPERATING SYSTEM 100 40 46 P 02. THEORY OF COMPUTATION 100 40 40 P PP 03. COMPUTER NETWORK TECHNOLOGY 100 40 66 P PP 100 40 04. DATBASE MANAGEMENT SYSTEMS 60 P 05. SOFTWARE ENGINEERING 100 40 61 P PP 06. OPERATING SYSTEM DESIGN LAB. 25 10 19 P TW 50 20 21 P 07. OPERATING SYSTEM DESIGN LAB. 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 19 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 38 P 25 10 16 P 10. NETWORK LABORATORY 11. NETWORK LABORATORY OR 50 20 12 F 12. SOFT SKILLS LABORATORY TW 25 10 19 P FIRST TERM TOTAL = 417/750. ORDN. 1 MARKS:

DATE: 31 JAN. 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 42 (525) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71101034L T80058625 THORAT AKSHAY RAMAKANT SUNANDA , , PICT , T80058625 01. OPERATING SYSTEM 100 40 56 P PP 02. THEORY OF COMPUTATION PP 100 40 40 P 03. COMPUTER NETWORK TECHNOLOGY 100 40 62 P PP 04. DATBASE MANAGEMENT SYSTEMS 100 40 46 P PP 05. SOFTWARE ENGINEERING 100 40 46 P 25 10 21 P 06. OPERATING SYSTEM DESIGN LAB. TW 50 20 07. OPERATING SYSTEM DESIGN LAB. 30 P 25 10 20 P 08. INFORMATION SYSTEMS DESIGN LAB. TW 50 20 09. INFORMATION SYSTEMS DESIGN LAB. OR 43 P 10. NETWORK LABORATORY 25 10 18 P TW 11. NETWORK LABORATORY 50 20 44 P OR 25 10 20 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 446/750. ORDN. 1 MARKS: T80058626 THOSARE YOGESH YASHWANT USHADEVI , 71101035J , PICT , T80058626 01. OPERATING SYSTEM 100 40 58 P 02. THEORY OF COMPUTATION 100 40 44 P PP 03. COMPUTER NETWORK TECHNOLOGY 100 40 56 P 100 40 54 P 04. DATBASE MANAGEMENT SYSTEMS PP 100 40 70 P 05. SOFTWARE ENGINEERING PP 25 21 P 06. OPERATING SYSTEM DESIGN LAB. TW 10 50 20 43 P 07. OPERATING SYSTEM DESIGN LAB. PR 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 19 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 37 P 25 10 20 P 10. NETWORK LABORATORY TW 50 20 25 P 11. NETWORK LABORATORY OR 25 10 20 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 467/750. ORDN. 1 MARKS: T80058627 TOSHNIWAL VIVEK ANILKUMAR SANJAYA , 71101039м , PICT , T80058627 01. OPERATING SYSTEM 100 40 59 P 02. THEORY OF COMPUTATION 100 40 55 P PP 03. COMPUTER NETWORK TECHNOLOGY 100 40 62 P PP 100 40 40 P 04. DATBASE MANAGEMENT SYSTEMS 05. SOFTWARE ENGINEERING 100 40 46 P PP 06. OPERATING SYSTEM DESIGN LAB. 25 10 19 P TW 50 20 23 P 07. OPERATING SYSTEM DESIGN LAB. 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 20 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 43 P 25 10 18 P 10. NETWORK LABORATORY 11. NETWORK LABORATORY OR 50 20 29 P 12. SOFT SKILLS LABORATORY TW 25 10 18 P FIRST TERM TOTAL = 432/750. ORDN. 1 MARKS:

DATE: 31 JAN. 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 43 (526) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71101040E T80058628 TRIPATHI SHREYA BHARAT REENA , , PICT , T80058628 01. OPERATING SYSTEM PP 100 40 66 P 02. THEORY OF COMPUTATION PP 100 40 47 P 03. COMPUTER NETWORK TECHNOLOGY 100 40 63 P PP 04. DATBASE MANAGEMENT SYSTEMS 100 40 52 P PP 05. SOFTWARE ENGINEERING 100 40 50 P 25 10 18 P 06. OPERATING SYSTEM DESIGN LAB. TW 50 20 22 P 07. OPERATING SYSTEM DESIGN LAB. 25 10 21 P 08. INFORMATION SYSTEMS DESIGN LAB. TW 50 20 40 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 10. NETWORK LABORATORY 25 10 16 P TW 11. NETWORK LABORATORY 50 20 28 P OR 25 10 18 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 441/750. ORDN. 1 MARKS: T80058629 UTSAV DUSAD LAXMI , 71101042M , PICT , T80058629 40 65 P 01. OPERATING SYSTEM 100 100 40 59 P 02. THEORY OF COMPUTATION PP 03. COMPUTER NETWORK TECHNOLOGY 100 40 70 P PP 100 40 57 P 04. DATBASE MANAGEMENT SYSTEMS PP 100 40 51 P 05. SOFTWARE ENGINEERING PP 25 24 P 06. OPERATING SYSTEM DESIGN LAB. TW 10 50 20 07. OPERATING SYSTEM DESIGN LAB. PR 33 P 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 24 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 44 P 25 10 19 P 10. NETWORK LABORATORY TW 50 20 08 F 11. NETWORK LABORATORY OR 25 10 23 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 477/750. ORDN. 1 MARKS: T80058630 VARAT KAUSTUBH SOMKANT TILOTTAMA , 71101046D , PICT , т80058630 01. OPERATING SYSTEM 100 40 27 F 02. THEORY OF COMPUTATION 100 40 45 P PP 03. COMPUTER NETWORK TECHNOLOGY 100 40 40 P PP 100 40 50 P 04. DATBASE MANAGEMENT SYSTEMS 05. SOFTWARE ENGINEERING 100 40 45 P PP 06. OPERATING SYSTEM DESIGN LAB. 25 10 10 P TW 50 20 AA F 07. OPERATING SYSTEM DESIGN LAB. 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 12 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 AA F 25 10 10 P 10. NETWORK LABORATORY 11. NETWORK LABORATORY OR 50 20 AA F 12. SOFT SKILLS LABORATORY TW 25 10 12 P FIRST TERM TOTAL = 251/750. ORDN. 1 MARKS:

DATE: 31 JAN. 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 44 (527) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71101047в T80058631 VICHARE GAURAV GIRISH VINA , , PICT , T80058631 01. OPERATING SYSTEM PP 100 40 43 P 02. THEORY OF COMPUTATION PP 100 40 44 P 03. COMPUTER NETWORK TECHNOLOGY 100 40 52 P PP 04. DATBASE MANAGEMENT SYSTEMS 100 40 40 P PP 05. SOFTWARE ENGINEERING 100 40 40 P 25 10 21 P 06. OPERATING SYSTEM DESIGN LAB. TW 50 20 23 P 07. OPERATING SYSTEM DESIGN LAB. 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 24 P 50 20 09. INFORMATION SYSTEMS DESIGN LAB. OR 41 P 10. NETWORK LABORATORY 25 10 20 P TW 11. NETWORK LABORATORY 50 20 31 P OR 25 10 21 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 400/750. ORDN. 1 MARKS: T80058632 VIMAL BHAT KIRAN , 71101050B , PICT , T80058632 40 72 P 01. OPERATING SYSTEM 100 100 40 61 P 02. THEORY OF COMPUTATION PP 03. COMPUTER NETWORK TECHNOLOGY 100 40 70 P PP 100 40 61 P 04. DATBASE MANAGEMENT SYSTEMS PP 100 40 71 P 05. SOFTWARE ENGINEERING PP 25 22 P 06. OPERATING SYSTEM DESIGN LAB. TW 10 50 20 33 P 07. OPERATING SYSTEM DESIGN LAB. PR 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 20 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 42 P 25 10 20 P 10. NETWORK LABORATORY TW 50 20 31 P 11. NETWORK LABORATORY OR 25 10 21 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 524/750. ORDN. 1 MARKS: SUMAN T80058634 WADILE NITIN ADHAR , 71101052J , PICT , т80058634 01. OPERATING SYSTEM 100 40 40 P 02. THEORY OF COMPUTATION 100 40 43 P PP 03. COMPUTER NETWORK TECHNOLOGY 100 40 48 P PP 100 40 04. DATBASE MANAGEMENT SYSTEMS 40 P 05. SOFTWARE ENGINEERING 100 40 51 P PP 06. OPERATING SYSTEM DESIGN LAB. 25 10 10 P TW 50 20 AA F 07. OPERATING SYSTEM DESIGN LAB. 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 12 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 AA F 25 10 10 P 10. NETWORK LABORATORY 11. NETWORK LABORATORY OR 50 20 AA F 12. SOFT SKILLS LABORATORY TW 25 10 13 P FIRST TERM TOTAL = 267/750. ORDN. 1 MARKS:

DATE: 31 JAN. 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 45 (528) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71101054E T80058635 WALKE AMRUTA GHANSHYAM , , PICT , T80058635 ASHA 100 40 67 P 01. OPERATING SYSTEM PP 02. THEORY OF COMPUTATION PP 100 40 58 P 03. COMPUTER NETWORK TECHNOLOGY 100 40 59 P PP 04. DATBASE MANAGEMENT SYSTEMS 100 40 57 P PP 05. SOFTWARE ENGINEERING 100 40 63 P 25 10 24 P 06. OPERATING SYSTEM DESIGN LAB. TW 50 20 21 P 07. OPERATING SYSTEM DESIGN LAB. 25 10 23 P 08. INFORMATION SYSTEMS DESIGN LAB. TW 50 20 38 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 10. NETWORK LABORATORY 25 10 23 P TW 11. NETWORK LABORATORY 50 20 25 P OR 25 10 22 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 480/750. ORDN. 1 MARKS: T80058636 WANKHADE POOJA SUNIL VIDYA , 71101056M , PICT , T80058636 01. OPERATING SYSTEM 40 68 P 100 02. THEORY OF COMPUTATION 100 40 72 P PP 03. COMPUTER NETWORK TECHNOLOGY 100 40 69 P PP 100 40 69 P 04. DATBASE MANAGEMENT SYSTEMS PP 100 40 62 P 05. SOFTWARE ENGINEERING PP 25 10 23 P 06. OPERATING SYSTEM DESIGN LAB. TW 50 20 32 P 07. OPERATING SYSTEM DESIGN LAB. PR 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 23 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 40 P 25 10 20 P 10. NETWORK LABORATORY TW 50 20 41 P 11. NETWORK LABORATORY OR 25 10 23 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 542/750. ORDN. 1 MARKS: T80058637 WAVAL BHAGYASHREE RAMESH MEGHA , 71241774F , PICT , т80058637 01. OPERATING SYSTEM 100 40 78 P 02. THEORY OF COMPUTATION 100 40 51 P PP 03. COMPUTER NETWORK TECHNOLOGY 100 40 76 P PP 100 04. DATBASE MANAGEMENT SYSTEMS 40 58 P 05. SOFTWARE ENGINEERING 100 40 68 P PP 06. OPERATING SYSTEM DESIGN LAB. 25 10 23 P TW 50 20 44 P 07. OPERATING SYSTEM DESIGN LAB. 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 24 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 40 P 25 10 24 P 10. NETWORK LABORATORY 11. NETWORK LABORATORY OR 50 20 37 P 12. SOFT SKILLS LABORATORY TW 25 10 23 P FIRST TERM TOTAL = 546/750. ORDN. 1 MARKS:

DATE: 31 JAN. 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 46 (529) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER CHANDRIKA , 71101058H , , PICT , T80058638 T80058638 WHABI JAIDEEP MAHESH 01. OPERATING SYSTEM PP 100 40 69 P 02. THEORY OF COMPUTATION PP 100 40 40 P 03. COMPUTER NETWORK TECHNOLOGY 100 40 56 P PP 04. DATBASE MANAGEMENT SYSTEMS 100 40 55 P PP 05. SOFTWARE ENGINEERING 100 40 52 P 25 10 23 P 06. OPERATING SYSTEM DESIGN LAB. TW 50 20 32 P 07. OPERATING SYSTEM DESIGN LAB. 25 10 20 P 08. INFORMATION SYSTEMS DESIGN LAB. TW 50 20 42 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 10. NETWORK LABORATORY 25 10 21 P TW 11. NETWORK LABORATORY 50 20 06 F OR 25 10 21 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 437/750. ORDN. 1 MARKS: T80058639 YEOLE SHRADDHA CHANDRASHEKHAR HEMLATA , 71101063D , PICT , T80058639 40 75 P 01. OPERATING SYSTEM PP 100 02. THEORY OF COMPUTATION 100 40 55 P PP 03. COMPUTER NETWORK TECHNOLOGY 100 40 64 P 100 40 61 P 04. DATBASE MANAGEMENT SYSTEMS PP 100 40 62 P 05. SOFTWARE ENGINEERING PP 25 21 P 06. OPERATING SYSTEM DESIGN LAB. TW 10 50 20 38 P 07. OPERATING SYSTEM DESIGN LAB. PR 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 22 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 39 P 25 10 17 P 10. NETWORK LABORATORY TW 50 20 30 P 11. NETWORK LABORATORY OR 25 10 21 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 505/750. ORDN. 1 MARKS: T80058640 YUSRA AIJAZ SAHAF SAMINA FARHAT , 71101066J , , PICT , т80058640 01. OPERATING SYSTEM 100 40 60 P 02. THEORY OF COMPUTATION 100 40 44 P PP 03. COMPUTER NETWORK TECHNOLOGY 100 40 57 P PP 100 40 40 P 04. DATBASE MANAGEMENT SYSTEMS 05. SOFTWARE ENGINEERING 100 40 47 P PP 06. OPERATING SYSTEM DESIGN LAB. 25 10 17 P TW 50 20 28 P 07. OPERATING SYSTEM DESIGN LAB. 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 22 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 10 F 25 10 18 P 10. NETWORK LABORATORY 11. NETWORK LABORATORY OR 50 20 09 F 12. SOFT SKILLS LABORATORY TW 25 10 18 P FIRST TERM TOTAL = 370/750. ORDN. 1 MARKS:

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DATE : 31 JAN. 2013

NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 70801315F , T8058650 , PICT , т80058641 T80058641 ABHINAV CHATURVEDI ABHA 13. SYSTEM SOFTWARE PROGRAMMING 01. OPERATING SYSTEM PP 100 40 40 P C PP 100 40 40 P C PP 100 40 41 P C 14. MANAGEMENT INFORMATION SYSTEMS PP 100 48 P C 02. THEORY OF COMPUTATION 40 PP 100 40 41 P C 100 40 41 P C 03. COMPUTER NETWORK TECHNOLOGY 15. PROGRAMMING PARADIGMS PP 04. DATBASE MANAGEMENT SYSTEMS 100 40 100 40 45 P C PP AA F 16. DESIGN & ANALYSIS OF ALGORITHMS PP 05. SOFTWARE ENGINEERING 100 40 40 P C 17. HUMAN COMPU.INTERACTION & USABI. PP 100 40 47 P C PP 06. OPERATING SYSTEM DESIGN LAB. TW 25 10 10 P C 50 20 20 P C 18. SOFTWARE DESIGN LABORATORY TW 07. OPERATING SYSTEM DESIGN LAB. 50 20 50 20 24 P C PR AA F 19. SOFTWARE DESIGN LABORATORY PR 25 10 10 P C 20. SOFTWARE DEVELOPMENT TOOLS LAB. TW 50 20 P C 08. INFORMATION SYSTEMS DESIGN LAB. TW 50 20 27 P C 21. SOFTWARE DEVELOPMENT TOOLS LAB. OR 50 20 32 P C 09. INFORMATION SYSTEMS DESIGN LAB. OR 22. SEMINAR AND TECHNICAL COMMUN. TW 50 20 P C 25 10 10 P C 10. NETWORK LABORATORY TW 50 20 26 P C 11. NETWORK LABORATORY OR TW 25 10 10 P C 12. SOFT SKILLS LABORATORY GRAND TOTAL = 592/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS: T80058642 BHALERAO MANGESH GANESH , T8058510 , PICT , т80058642 NANDA , 70925361D 01. OPERATING SYSTEM PP 100 40 40 P C 13. SYSTEM SOFTWARE PROGRAMMING PP 100 40 53 P C 02. THEORY OF COMPUTATION 40 P C PP 100 40 100 40 46 P 14. MANAGEMENT INFORMATION SYSTEMS PP 03. COMPUTER NETWORK TECHNOLOGY 100 PP 100 40 40 P C 15. PROGRAMMING PARADIGMS 40 40 P 04. DATBASE MANAGEMENT SYSTEMS PP 100 40 40 P C 16. DESIGN & ANALYSIS OF ALGORITHMS PP 100 40 10 F 100 40 05. SOFTWARE ENGINEERING PP 40 P C 17. HUMAN COMPU.INTERACTION & USABI. PP 100 40 44 P 25 15 P C 06. OPERATING SYSTEM DESIGN LAB. TW 10 18. SOFTWARE DESIGN LABORATORY TW 50 20 20 P C 50 20 15 F 50 20 23 P 07. OPERATING SYSTEM DESIGN LAB. PR 19. SOFTWARE DESIGN LABORATORY PR 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 15 P C 20. SOFTWARE DEVELOPMENT TOOLS LAB. TW 50 20 30 P C 50 20 41 P C 50 20 10 F 09. INFORMATION SYSTEMS DESIGN LAB. OR 21. SOFTWARE DEVELOPMENT TOOLS LAB. OR 25 10 16 P C 22. SEMINAR AND TECHNICAL COMMUN. TW 50 20 37 P C 10. NETWORK LABORATORY TW 50 20 32 P C 11. NETWORK LABORATORY OR 25 10 21 P C 12. SOFT SKILLS LABORATORY TW GRAND TOTAL = 668/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS: , 71045398B , T8058514 , PICT T80058643 BORDE ROHAN SATISHRAO UJWALA , T80058643 100 40 51 P C 40 66 P C 01. OPERATING SYSTEM 13. SYSTEM SOFTWARE PROGRAMMING 100 02. THEORY OF COMPUTATION PP 100 40 61 P C 14. MANAGEMENT INFORMATION SYSTEMS PP 100 40 59 P C 100 40 68 P C 15. PROGRAMMING PARADIGMS 100 40 66 P C 03. COMPUTER NETWORK TECHNOLOGY PP PP 100 40 65 P C 100 40 63 P C 04. DATBASE MANAGEMENT SYSTEMS 16. DESIGN & ANALYSIS OF ALGORITHMS PP 05. SOFTWARE ENGINEERING 100 40 59 P C 17. HUMAN COMPU.INTERACTION & USABI. PP 100 40 58 P C PP 06. OPERATING SYSTEM DESIGN LAB. 25 10 19 P C 18. SOFTWARE DESIGN LABORATORY TW 50 20 33 P C TW 50 20 30 P C 50 20 40 P 07. OPERATING SYSTEM DESIGN LAB. 19. SOFTWARE DESIGN LABORATORY 25 10 19 P C 50 20 44 P C 08. INFORMATION SYSTEMS DESIGN LAB. TW 20. SOFTWARE DEVELOPMENT TOOLS LAB. TW 50 20 25 P C 50 20 33 P C 09. INFORMATION SYSTEMS DESIGN LAB. OR 21. SOFTWARE DEVELOPMENT TOOLS LAB. OR 25 10 10. NETWORK LABORATORY 19 P C 22. SEMINAR AND TECHNICAL COMMUN. TW 50 20 35 P C 11. NETWORK LABORATORY OR 50 20 40 P C 12. SOFT SKILLS LABORATORY 25 10 20 P C TW GRAND TOTAL = 973/1500, RESULT: FIRST CLASS ORDN. 1 MARKS:

DATE : 31 JAN. 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 48 (531) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71134947K , T8058522 , PICT , т80058644 T80058644 CHAVAN PUSHPAK VILAS SUREKHA 13. SYSTEM SOFTWARE PROGRAMMING 01. OPERATING SYSTEM PP 100 40 57 P C PP 100 40 63 P C PP 100 40 48 P C 14. MANAGEMENT INFORMATION SYSTEMS PP 100 56 P C 02. THEORY OF COMPUTATION 40 PP 100 40 64 P C 100 40 74 P C 03. COMPUTER NETWORK TECHNOLOGY 15. PROGRAMMING PARADIGMS PP 04. DATBASE MANAGEMENT SYSTEMS 100 40 57 P C 100 40 49 P C PP 16. DESIGN & ANALYSIS OF ALGORITHMS PP 05. SOFTWARE ENGINEERING 100 40 69 P C 17. HUMAN COMPU.INTERACTION & USABI. PP 100 40 54 P C PP 06. OPERATING SYSTEM DESIGN LAB. 25 10 19 P C 50 20 33 P C 18. SOFTWARE DESIGN LABORATORY TW TW 07. OPERATING SYSTEM DESIGN LAB. 50 20 34 P C 50 20 32 P PR 19. SOFTWARE DESIGN LABORATORY PR 25 10 17 P C 50 20 41 P C 08. INFORMATION SYSTEMS DESIGN LAB. TW 20. SOFTWARE DEVELOPMENT TOOLS LAB. TW 50 20 22 P C 21. SOFTWARE DEVELOPMENT TOOLS LAB. OR 50 20 25 P C 09. INFORMATION SYSTEMS DESIGN LAB. OR 22. SEMINAR AND TECHNICAL COMMUN. TW 50 20 40 P C 25 10 17 P C 10. NETWORK LABORATORY TW 50 20 35 P C 11. NETWORK LABORATORY OR TW 25 10 21 P C 12. SOFT SKILLS LABORATORY GRAND TOTAL = 927/1500, RESULT: FIRST CLASS ORDN. 1 MARKS: T80058645 DEORE NIKHIL BAPUSAHEB , T8058525 , PICT , т80058645 MANDAKINI , 70801397L 100 40 42 P C 01. OPERATING SYSTEM 13. SYSTEM SOFTWARE PROGRAMMING PP 100 40 59 P C 02. THEORY OF COMPUTATION 40 P C PP 100 40 40 P C 100 40 14. MANAGEMENT INFORMATION SYSTEMS PP 03. COMPUTER NETWORK TECHNOLOGY 100 PP 100 40 50 P C 15. PROGRAMMING PARADIGMS 40 40 P 04. DATBASE MANAGEMENT SYSTEMS PP 100 40 41 P C 16. DESIGN & ANALYSIS OF ALGORITHMS PP 100 40 40 P 100 100 05. SOFTWARE ENGINEERING PP 40 43 P C 17. HUMAN COMPU.INTERACTION & USABI. PP 40 42 P C 25 20 20 P C 06. OPERATING SYSTEM DESIGN LAB. TW 10 14 P C 18. SOFTWARE DESIGN LABORATORY TW 50 50 20 50 20 10 F 07. OPERATING SYSTEM DESIGN LAB. PR 20 P C 19. SOFTWARE DESIGN LABORATORY PR 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 13 P C 20. SOFTWARE DEVELOPMENT TOOLS LAB. TW 50 20 20 P C 50 20 35 P C 50 20 08 F 09. INFORMATION SYSTEMS DESIGN LAB. OR 21. SOFTWARE DEVELOPMENT TOOLS LAB. OR 25 10 12 P C 22. SEMINAR AND TECHNICAL COMMUN. TW 50 20 P C 10. NETWORK LABORATORY TW 11. NETWORK LABORATORY 50 20 24 P C OR 25 10 20 P C 12. SOFT SKILLS LABORATORY TW GRAND TOTAL = 653/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS: , 71045418L , T8058530 , PICT T80058646 DIKE ASHISH VIJAY RAJANI , T80058646 100 40 59 P C 40 55 P C 01. OPERATING SYSTEM 13. SYSTEM SOFTWARE PROGRAMMING 100 02. THEORY OF COMPUTATION 100 40 46 P C 14. MANAGEMENT INFORMATION SYSTEMS PP 100 40 43 P C PP 03. COMPUTER NETWORK TECHNOLOGY 100 40 45 P C 15. PROGRAMMING PARADIGMS 100 40 50 P C PP PP 100 40 40 P C 100 40 40 P C 04. DATBASE MANAGEMENT SYSTEMS PP 16. DESIGN & ANALYSIS OF ALGORITHMS PP 05. SOFTWARE ENGINEERING 100 40 46 P C 17. HUMAN COMPU.INTERACTION & USABI. PP 100 40 41 P C PP 20 20 P C 06. OPERATING SYSTEM DESIGN LAB. 25 10 12 P C 18. SOFTWARE DESIGN LABORATORY TW 50 TW 50 20 20 P C 50 20 AA F 07. OPERATING SYSTEM DESIGN LAB. 19. SOFTWARE DESIGN LABORATORY 25 10 10 P C 50 20 25 P C 08. INFORMATION SYSTEMS DESIGN LAB. TW 20. SOFTWARE DEVELOPMENT TOOLS LAB. TW 50 20 21 P 50 20 12 F 09. INFORMATION SYSTEMS DESIGN LAB. OR 21. SOFTWARE DEVELOPMENT TOOLS LAB. OR 25 10 10 P C 50 20 20 P C 10. NETWORK LABORATORY TW 22. SEMINAR AND TECHNICAL COMMUN. TW 11. NETWORK LABORATORY OR 50 20 23 P C 12. SOFT SKILLS LABORATORY TW 25 10 15 P C GRAND TOTAL = 653/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS:

DATE : 31 JAN. 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 49 (532)

NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , T8058535 , PICT , т80058647 T80058647 GAIKWAD AKSHAY SHAHAJI MAHANANDA , 71045430к 13. SYSTEM SOFTWARE PROGRAMMING 01. OPERATING SYSTEM PP 100 40 45 P C PP 100 40 57 P C PP 100 40 48 P C 14. MANAGEMENT INFORMATION SYSTEMS PP 100 51 P C 02. THEORY OF COMPUTATION 40 PP 100 40 51 P C 100 40 59 P C 03. COMPUTER NETWORK TECHNOLOGY 15. PROGRAMMING PARADIGMS PP 04. DATBASE MANAGEMENT SYSTEMS 100 40 52 P C 100 40 50 P C PP 16. DESIGN & ANALYSIS OF ALGORITHMS PP 05. SOFTWARE ENGINEERING 100 40 44 P C 17. HUMAN COMPU.INTERACTION & USABI. PP 100 40 47 P C PP 25 10 10 P C 50 20 20 P C 06. OPERATING SYSTEM DESIGN LAB. TW 18. SOFTWARE DESIGN LABORATORY TW 07. OPERATING SYSTEM DESIGN LAB. 50 20 20 P C 50 20 01 F PR 19. SOFTWARE DESIGN LABORATORY PR 25 10 10 P C 20. SOFTWARE DEVELOPMENT TOOLS LAB. TW 50 20 25 P C 08. INFORMATION SYSTEMS DESIGN LAB. TW 50 20 21 P 21. SOFTWARE DEVELOPMENT TOOLS LAB. OR 50 20 22 P C 09. INFORMATION SYSTEMS DESIGN LAB. OR 22. SEMINAR AND TECHNICAL COMMUN. TW 50 20 P C 25 10 10 P C 10. NETWORK LABORATORY TW 50 20 29 P C 11. NETWORK LABORATORY OR TW 25 10 15 P C 12. SOFT SKILLS LABORATORY GRAND TOTAL = 707/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS: T80058648 JADHAV DEEPANJAN MADHAV , T8058662 , PICT , т80058648 ARUNA , 70925445J PP 100 40 45 P C 01. OPERATING SYSTEM 13. SYSTEM SOFTWARE PROGRAMMING PP 100 40 59 P C 02. THEORY OF COMPUTATION 47 P C PP 100 40 52 P C 100 40 14. MANAGEMENT INFORMATION SYSTEMS PP 03. COMPUTER NETWORK TECHNOLOGY 72 P C 100 PP 100 40 15. PROGRAMMING PARADIGMS 40 40 P C 04. DATBASE MANAGEMENT SYSTEMS PP 100 40 53 P C 16. DESIGN & ANALYSIS OF ALGORITHMS PP 100 40 43 P C 100 100 05. SOFTWARE ENGINEERING PP 40 47 P C 17. HUMAN COMPU.INTERACTION & USABI. PP 40 55 P C 25 06. OPERATING SYSTEM DESIGN LAB. TW 10 13 P C 18. SOFTWARE DESIGN LABORATORY TW 50 20 24 P C 50 20 07. OPERATING SYSTEM DESIGN LAB. PR AA F 19. SOFTWARE DESIGN LABORATORY PR 50 20 22 P 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 12 P C 20. SOFTWARE DEVELOPMENT TOOLS LAB. TW 50 20 20 P C 50 20 22 P C 50 20 20 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 21. SOFTWARE DEVELOPMENT TOOLS LAB. OR 25 10 16 P C 22. SEMINAR AND TECHNICAL COMMUN. TW 50 20 34 P C 10. NETWORK LABORATORY TW 50 20 42 P C 11. NETWORK LABORATORY OR 25 10 16 P C 12. SOFT SKILLS LABORATORY TW GRAND TOTAL = 754/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS: T80058649 KAKADE POOJA DILIPRAO SUNITA , 71134965H , T8058554 , PICT , T80058649 100 40 59 P C 40 74 P C 01. OPERATING SYSTEM 13. SYSTEM SOFTWARE PROGRAMMING PP 100 02. THEORY OF COMPUTATION 100 40 64 P C 14. MANAGEMENT INFORMATION SYSTEMS PP 100 40 45 P C PP 100 40 74 P C 15. PROGRAMMING PARADIGMS 100 40 61 P C 03. COMPUTER NETWORK TECHNOLOGY PP PP 100 40 58 P C 100 40 63 P C 04. DATBASE MANAGEMENT SYSTEMS PP 16. DESIGN & ANALYSIS OF ALGORITHMS PP 05. SOFTWARE ENGINEERING 100 40 55 P C 17. HUMAN COMPU.INTERACTION & USABI. PP 100 40 54 P C PP 06. OPERATING SYSTEM DESIGN LAB. 25 10 23 P C 18. SOFTWARE DESIGN LABORATORY TW 50 20 45 P C TW 50 20 37 P C 50 20 37 P C 07. OPERATING SYSTEM DESIGN LAB. 19. SOFTWARE DESIGN LABORATORY 25 10 24 P C 50 20 38 P C 08. INFORMATION SYSTEMS DESIGN LAB. TW 20. SOFTWARE DEVELOPMENT TOOLS LAB. TW 50 20 32 P C 50 20 22 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 21. SOFTWARE DEVELOPMENT TOOLS LAB. OR 25 10 22 P C 10. NETWORK LABORATORY 22. SEMINAR AND TECHNICAL COMMUN. TW 50 20 38 P C 11. NETWORK LABORATORY OR 50 20 36 P C 12. SOFT SKILLS LABORATORY 25 10 23 P C TW GRAND TOTAL = 984/1500, RESULT: FIRST CLASS ORDN. 1 MARKS:

DATE : 21 JAN 2012

DATE : 31 JAN. 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 50 (533) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER SUNITA , 70925476J , T8058562 , PICT , т80058650 T80058650 KHANDAGALE SURAJ PANDURANG PP 100 40 45 P C 13. SYSTEM SOFTWARE PROGRAMMING 01. OPERATING SYSTEM PP 100 40 61 P C PP 100 40 57 P C 14. MANAGEMENT INFORMATION SYSTEMS PP 100 51 P C 02. THEORY OF COMPUTATION 40 03. COMPUTER NETWORK TECHNOLOGY PP 100 40 62 P C 100 40 45 P C 15. PROGRAMMING PARADIGMS PP 04. DATBASE MANAGEMENT SYSTEMS 100 40 51 P C 100 40 51 P C PP 16. DESIGN & ANALYSIS OF ALGORITHMS PP 05. SOFTWARE ENGINEERING 100 40 40 P C 17. HUMAN COMPU.INTERACTION & USABI. PP 100 40 44 P C PP 25 10 19 P C 50 20 35 P C 06. OPERATING SYSTEM DESIGN LAB. TW 18. SOFTWARE DESIGN LABORATORY TW 07. OPERATING SYSTEM DESIGN LAB. 50 20 28 P C 50 20 38 P PR 19. SOFTWARE DESIGN LABORATORY PR 25 10 19 P C 50 20 37 P C 08. INFORMATION SYSTEMS DESIGN LAB. TW 20. SOFTWARE DEVELOPMENT TOOLS LAB. TW 50 20 40 P C 50 20 27 P C 09. INFORMATION SYSTEMS DESIGN LAB. OR 21. SOFTWARE DEVELOPMENT TOOLS LAB. OR 22. SEMINAR AND TECHNICAL COMMUN. TW 50 20 40 P C 25 10 18 P C 10. NETWORK LABORATORY TW 50 20 37 P C 11. NETWORK LABORATORY OR 25 10 17 P C 12. SOFT SKILLS LABORATORY TW GRAND TOTAL = 862/1500, RESULT: HIGHER SECOND CLASS ORDN. 1 MARKS: T80058651 KOTHAWALE AKASH SANJAY , T8058563 , PICT , т80058651 ANITA , 71045486E 100 40 52 P C 01. OPERATING SYSTEM 13. SYSTEM SOFTWARE PROGRAMMING PP 100 40 45 P C 02. THEORY OF COMPUTATION PP 100 40 100 40 42 P C 61 P C 14. MANAGEMENT INFORMATION SYSTEMS PP 03. COMPUTER NETWORK TECHNOLOGY 100 PP 100 40 69 P C 15. PROGRAMMING PARADIGMS 40 43 P C 40 53 P C 04. DATBASE MANAGEMENT SYSTEMS PP 100 40 42 P C 16. DESIGN & ANALYSIS OF ALGORITHMS PP 100 100 05. SOFTWARE ENGINEERING PP 40 51 P C 17. HUMAN COMPU.INTERACTION & USABI. PP 100 40 48 P C 25 23 P C 06. OPERATING SYSTEM DESIGN LAB. TW 10 18. SOFTWARE DESIGN LABORATORY TW 50 20 39 P C 50 20 50 20 25 P 07. OPERATING SYSTEM DESIGN LAB. PR 26 P C 19. SOFTWARE DESIGN LABORATORY PR 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 22 P C 20. SOFTWARE DEVELOPMENT TOOLS LAB. TW 50 20 36 P C 50 20 41 P C 50 20 40 P C 09. INFORMATION SYSTEMS DESIGN LAB. OR 21. SOFTWARE DEVELOPMENT TOOLS LAB. OR 25 10 22 P C 22. SEMINAR AND TECHNICAL COMMUN. TW 50 20 41 P C 10. NETWORK LABORATORY TW 50 20 37 P C 11. NETWORK LABORATORY OR 12. SOFT SKILLS LABORATORY TW 25 10 22 P C GRAND TOTAL = 880/1500, RESULT: HIGHER SECOND CLASS ORDN. 1 MARKS: SUMATI T80058652 KULKARNI PRAMOD SHIVAJIRAO , 71045672H , T8058565 , PICT , T80058652 100 40 40 P C 40 60 P C 01. OPERATING SYSTEM 13. SYSTEM SOFTWARE PROGRAMMING PP 100 02. THEORY OF COMPUTATION 100 40 40 P C 14. MANAGEMENT INFORMATION SYSTEMS PP 100 40 44 P C PP 100 40 61 P C 15. PROGRAMMING PARADIGMS 100 40 42 P C 03. COMPUTER NETWORK TECHNOLOGY PP PP 100 40 49 P C 100 40 41 P C 04. DATBASE MANAGEMENT SYSTEMS 16. DESIGN & ANALYSIS OF ALGORITHMS PP 05. SOFTWARE ENGINEERING 100 40 40 P C 17. HUMAN COMPU.INTERACTION & USABI. PP 100 40 46 P C PP 06. OPERATING SYSTEM DESIGN LAB. 25 10 12 P C 18. SOFTWARE DESIGN LABORATORY TW 50 20 25 P C TW 50 20 20 P C 50 20 12 F 07. OPERATING SYSTEM DESIGN LAB. 19. SOFTWARE DESIGN LABORATORY 25 10 10 P C 50 20 22 P C 08. INFORMATION SYSTEMS DESIGN LAB. TW 20. SOFTWARE DEVELOPMENT TOOLS LAB. TW 50 20 25 P C 50 20 25 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 21. SOFTWARE DEVELOPMENT TOOLS LAB. OR 25 10 10 P C 50 20 20 P C 10. NETWORK LABORATORY 22. SEMINAR AND TECHNICAL COMMUN. TW 11. NETWORK LABORATORY OR 50 20 26 P C 12. SOFT SKILLS LABORATORY TW 25 10 12 P C GRAND TOTAL = 682/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS:

ONIVERSITY OF FORE THE COOK PATY (INFORMATION FEEDINGLOST) EXAMINATION NOV. 2012

DATE : 31 JAN. 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 51 (534) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 70701521K , T8058569 , PICT , т80058653 T80058653 MAHAJAN SACHIN RAVINDRA SARALA 13. SYSTEM SOFTWARE PROGRAMMING 01. OPERATING SYSTEM PP 100 40 43 P C PP 100 40 64 P C PP 100 40 14. MANAGEMENT INFORMATION SYSTEMS PP 100 52 P C 02. THEORY OF COMPUTATION AA F 40 61 P C PP 100 40 15. PROGRAMMING PARADIGMS PP 100 40 43 P C 03. COMPUTER NETWORK TECHNOLOGY 04. DATBASE MANAGEMENT SYSTEMS 100 40 42 P C 100 40 44 P PP 16. DESIGN & ANALYSIS OF ALGORITHMS PP 05. SOFTWARE ENGINEERING 100 40 40 P C 17. HUMAN COMPU.INTERACTION & USABI. PP 100 40 47 P C PP 25 10 10 P C 50 20 20 P C 06. OPERATING SYSTEM DESIGN LAB. TW 18. SOFTWARE DESIGN LABORATORY TW 07. OPERATING SYSTEM DESIGN LAB. 50 20 20 P C 50 20 AA F PR 19. SOFTWARE DESIGN LABORATORY PR 25 10 10 P C 50 20 20 P C 08. INFORMATION SYSTEMS DESIGN LAB. TW 20. SOFTWARE DEVELOPMENT TOOLS LAB. TW 50 20 25 P C 50 20 20 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 21. SOFTWARE DEVELOPMENT TOOLS LAB. OR 25 10 10 P C 22. SEMINAR AND TECHNICAL COMMUN. TW 50 20 20 P C 10. NETWORK LABORATORY TW 50 20 32 P C 11. NETWORK LABORATORY OR TW 25 10 10 P C 12. SOFT SKILLS LABORATORY GRAND TOTAL = 633/1500, RESULT: FAILS A.T.K.T. RESULT RESERVED FOR BKLG ORDN. 1 MARKS: T80058654 MULAY AMIT YESHWANT , т80058654 SNEHAL , 71045530F , T8058575 , PICT 100 40 40 P C PP 100 40 40 P C 01. OPERATING SYSTEM 13. SYSTEM SOFTWARE PROGRAMMING 02. THEORY OF COMPUTATION PP 100 40 52 P C 40 14. MANAGEMENT INFORMATION SYSTEMS PP 100 67 P 03. COMPUTER NETWORK TECHNOLOGY 15. PROGRAMMING PARADIGMS 58 P C 100 PP 100 40 40 50 P C 04. DATBASE MANAGEMENT SYSTEMS PP 100 40 58 P C 16. DESIGN & ANALYSIS OF ALGORITHMS PP 100 40 50 P C 100 05. SOFTWARE ENGINEERING PP 40 46 P C 17. HUMAN COMPU.INTERACTION & USABI. PP 100 40 46 P C 25 15 P C 06. OPERATING SYSTEM DESIGN LAB. TW 10 18. SOFTWARE DESIGN LABORATORY TW 50 20 20 P C 50 20 23 P 07. OPERATING SYSTEM DESIGN LAB. PR 19. SOFTWARE DESIGN LABORATORY PR 50 20 AA F 50 20 21 P C 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 15 P C 20. SOFTWARE DEVELOPMENT TOOLS LAB. TW 50 20 20 P C 50 20 10 F 09. INFORMATION SYSTEMS DESIGN LAB. OR 21. SOFTWARE DEVELOPMENT TOOLS LAB. OR 25 10 14 P C 50 20 20 P C 10. NETWORK LABORATORY TW 22. SEMINAR AND TECHNICAL COMMUN. TW 50 20 23 P C 11. NETWORK LABORATORY OR TW 25 10 10 P C 12. SOFT SKILLS LABORATORY GRAND TOTAL = 698/1500, RESULT: FAILS A.T.K.T. RESULT RESERVED FOR BKLG ORDN. 1 MARKS: T80058655 NAIKNAWARE UTKARSH MARUTI , 71045538M , T8058579 , PICT , T80058655 MAHANANDA 100 40 40 P C 64 P C 01. OPERATING SYSTEM 13. SYSTEM SOFTWARE PROGRAMMING PP 100 40 02. THEORY OF COMPUTATION 100 40 48 P C 14. MANAGEMENT INFORMATION SYSTEMS PP 100 40 59 P C PP 03. COMPUTER NETWORK TECHNOLOGY 100 40 52 P C 15. PROGRAMMING PARADIGMS 100 40 62 P C PP PP 100 40 50 P C 100 40 40 P C 04. DATBASE MANAGEMENT SYSTEMS PP 16. DESIGN & ANALYSIS OF ALGORITHMS PP 05. SOFTWARE ENGINEERING 100 40 40 P C 17. HUMAN COMPU.INTERACTION & USABI. PP 100 40 41 P C PP 06. OPERATING SYSTEM DESIGN LAB. 25 10 23 P C 18. SOFTWARE DESIGN LABORATORY TW 50 20 37 P C TW 50 20 32 P C 50 20 22 P 07. OPERATING SYSTEM DESIGN LAB. 19. SOFTWARE DESIGN LABORATORY 25 10 20 P C 50 20 38 P C 08. INFORMATION SYSTEMS DESIGN LAB. TW 20. SOFTWARE DEVELOPMENT TOOLS LAB. TW 50 20 30 P C 50 20 42 P C 09. INFORMATION SYSTEMS DESIGN LAB. OR 21. SOFTWARE DEVELOPMENT TOOLS LAB. OR 25 10 22 P C 10. NETWORK LABORATORY 22. SEMINAR AND TECHNICAL COMMUN. TW 50 20 40 P C 11. NETWORK LABORATORY OR 50 20 35 P C 21 P C 12. SOFT SKILLS LABORATORY 25 10 TW GRAND TOTAL = 858/1500, RESULT: HIGHER SECOND CLASS ORDN. 1 MARKS:

CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.

DATE : 31 JAN. 2013 PAGE NO. 52 (535) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER T80058656 NY GARIN YI MALIVATTEY , 70925519F , T8058583 , PICT , т80058656 13. SYSTEM SOFTWARE PROGRAMMING 01. OPERATING SYSTEM PP 100 40 48 P C PP 100 40 61 P C PP 100 40 44 P C 14. MANAGEMENT INFORMATION SYSTEMS PP 100 48 P C 02. THEORY OF COMPUTATION 40 03. COMPUTER NETWORK TECHNOLOGY 100 40 46 P C 100 40 56 P C PP 15. PROGRAMMING PARADIGMS PP 04. DATBASE MANAGEMENT SYSTEMS 100 40 40 P C 100 40 45 P C PP 16. DESIGN & ANALYSIS OF ALGORITHMS PP 05. SOFTWARE ENGINEERING 100 40 46 P C 17. HUMAN COMPU.INTERACTION & USABI. PP 100 40 46 P C PP 06. OPERATING SYSTEM DESIGN LAB. 25 10 19 P C 50 20 28 P C 18. SOFTWARE DESIGN LABORATORY TW TW 07. OPERATING SYSTEM DESIGN LAB. 50 20 27 P C 50 20 21 P PR 19. SOFTWARE DESIGN LABORATORY PR 25 10 20 P C 50 20 32 P C 08. INFORMATION SYSTEMS DESIGN LAB. TW 20. SOFTWARE DEVELOPMENT TOOLS LAB. TW 50 20 31 P C 50 20 36 P C 09. INFORMATION SYSTEMS DESIGN LAB. OR 21. SOFTWARE DEVELOPMENT TOOLS LAB. OR 22. SEMINAR AND TECHNICAL COMMUN. TW 50 20 41 P C 25 10 18 P C 10. NETWORK LABORATORY TW 50 20 23 P C 11. NETWORK LABORATORY OR 25 10 19 P C 12. SOFT SKILLS LABORATORY TW GRAND TOTAL = 795/1500, RESULT: SECOND CLASS ORDN. 1 MARKS: T80058657 PANSARE ROHAN SAMPAT , T8058585 , PICT MANGAL , 71045554C , T80058657 01. OPERATING SYSTEM 100 40 50 P C 13. SYSTEM SOFTWARE PROGRAMMING PP 100 40 65 P C 02. THEORY OF COMPUTATION 100 40 49 P C 100 40 PP 14. MANAGEMENT INFORMATION SYSTEMS PP 54 P C 100 75 P C 03. COMPUTER NETWORK TECHNOLOGY PP 100 40 69 P C 15. PROGRAMMING PARADIGMS 40 04. DATBASE MANAGEMENT SYSTEMS PP 100 40 60 P C 16. DESIGN & ANALYSIS OF ALGORITHMS PP 100 40 45 P C 100 40 05. SOFTWARE ENGINEERING PP 49 P C 17. HUMAN COMPU.INTERACTION & USABI. PP 100 40 55 P C 25 22 P C 06. OPERATING SYSTEM DESIGN LAB. TW 10 18. SOFTWARE DESIGN LABORATORY TW 50 20 36 P C 50 20 40 P 07. OPERATING SYSTEM DESIGN LAB. PR 39 P C 19. SOFTWARE DESIGN LABORATORY PR 50 20 50 20 38 P C 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 20 P C 20. SOFTWARE DEVELOPMENT TOOLS LAB. TW 50 20 28 P C 50 20 34 P C 09. INFORMATION SYSTEMS DESIGN LAB. OR 21. SOFTWARE DEVELOPMENT TOOLS LAB. OR 25 10 20 P C 22. SEMINAR AND TECHNICAL COMMUN. TW 50 20 41 P C 10. NETWORK LABORATORY TW 50 20 36 P C 11. NETWORK LABORATORY OR 25 10 21 P C 12. SOFT SKILLS LABORATORY TW GRAND TOTAL = 946/1500, RESULT: FIRST CLASS ORDN. 1 MARKS: T80058658 PARTH SARTHI PIPLANI SUREKHA , 71045560H , T8058589 , PICT , T80058658 100 40 45 P C 40 54 P C 01. OPERATING SYSTEM 13. SYSTEM SOFTWARE PROGRAMMING 100 02. THEORY OF COMPUTATION 100 40 40 P C 14. MANAGEMENT INFORMATION SYSTEMS PP 100 40 48 P C PP 100 40 40 P C 15. PROGRAMMING PARADIGMS 100 40 46 P 03. COMPUTER NETWORK TECHNOLOGY PP PP 100 40 40 P C 100 40 53 P C 04. DATBASE MANAGEMENT SYSTEMS PP 16. DESIGN & ANALYSIS OF ALGORITHMS PP 05. SOFTWARE ENGINEERING 100 40 41 P C 17. HUMAN COMPU.INTERACTION & USABI. PP 100 40 40 P C PP 06. OPERATING SYSTEM DESIGN LAB. 25 10 16 P C 18. SOFTWARE DESIGN LABORATORY TW 50 20 20 P C TW 50 20 22 P 50 20 20 P 07. OPERATING SYSTEM DESIGN LAB. 19. SOFTWARE DESIGN LABORATORY 25 10 10 P C 50 20 24 P C 08. INFORMATION SYSTEMS DESIGN LAB. TW 20. SOFTWARE DEVELOPMENT TOOLS LAB. TW 50 20 21 P 50 20 32 P C 09. INFORMATION SYSTEMS DESIGN LAB. OR 21. SOFTWARE DEVELOPMENT TOOLS LAB. OR 25 10 12 P C 10. NETWORK LABORATORY 22. SEMINAR AND TECHNICAL COMMUN. TW 50 20 22 P C 11. NETWORK LABORATORY OR 50 20 32 P C 12. SOFT SKILLS LABORATORY 25 10 15 P C TW GRAND TOTAL = 693/1500, RESULT: PASS CLASS ORDN. 1 MARKS:

DATE : 31 JAN. 2013 CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 53 (536)

NOTE: FIRST LINE : SEAT NO., NAME (
OTHER LINES: HEAD OF PASSING,	MAX	MARK	S, M	IN. P	PASS	MARKS, MAR	RKS OBTAINED, P/F:PASS/FAIL, C:F	REVI	OUS CAI	RRY O	VER	
T80058659 PATHAK ANIKET SANJAY				AS	SHA		, 70925542L , т8058672 ,	P	ICT	,	т8005	8659
01. OPERATING SYSTEM	PP	100	40	40	РС	13	SYSTEM SOFTWARE PROGRAMMING	PP	100	40	56	РС
02. THEORY OF COMPUTATION	PP	100	40	49	Р	14	MANAGEMENT INFORMATION SYSTEMS	PP	100	40	49	РС
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	57	РС	15	PROGRAMMING PARADIGMS	PP	100	40	40	РС
04. DATBASE MANAGEMENT SYSTEMS	PP	100	40	40	РС	16	DESIGN & ANALYSIS OF ALGORITHMS	PP	100	40	42	РС
05. SOFTWARE ENGINEERING	PP	100	40	47	РС	17	HUMAN COMPU.INTERACTION & USABI.	PP	100	40	51	РС
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	12	РС	18	SOFTWARE DESIGN LABORATORY	TW	50	20	26	РС
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	20	РС	19	SOFTWARE DESIGN LABORATORY	PR	50	20	32	РС
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	12	РС	20	SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	24	РС
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	33	РС	21.	SOFTWARE DEVELOPMENT TOOLS LAB.	OR	50	20	36	РС
10. NETWORK LABORATORY	TW	25	10	12	РС	22.	SEMINAR AND TECHNICAL COMMUN.	TW	50	20	32	РС
11. NETWORK LABORATORY	OR	50	20	30	РС							
12. SOFT SKILLS LABORATORY	TW	25	10	18	РС							
GRAND TOTAL = 758/1500, RESULT: SECON	ND CLA	ASS										
ORDN. 1 MARKS :												
T80058660 POLE AKSHAYKUMAR RAHUL				SH	IEELA		, 71045576D , T8058596 ,	P	ICT	,	т8005	8660
01. OPERATING SYSTEM	PP	100	40	40	РС	13	SYSTEM SOFTWARE PROGRAMMING	PP	100	40	51	РС
02. THEORY OF COMPUTATION	PP	100	40	40	РС	14.	MANAGEMENT INFORMATION SYSTEMS	PP	100	40	52	РС
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	55	РС	15.	PROGRAMMING PARADIGMS	PP	100	40	54	Р
04. DATBASE MANAGEMENT SYSTEMS	PP	100	40	42	РС	16	DESIGN & ANALYSIS OF ALGORITHMS	PP	100	40	47	РС
05. SOFTWARE ENGINEERING	PP	100	40	40	РС	17	HUMAN COMPU.INTERACTION & USABI.	PP	100	40	52	РС
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	12	РС	18	SOFTWARE DESIGN LABORATORY	TW	50	20	37	РС
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	20	РС	19	SOFTWARE DESIGN LABORATORY	PR	50	20	22	РС
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	10	РС	20	SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	32	РС
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	28	РС	21.	SOFTWARE DEVELOPMENT TOOLS LAB.	OR	50	20	38	РС
10. NETWORK LABORATORY	TW	25	10	19	РС	22	SEMINAR AND TECHNICAL COMMUN.	TW	50	20	41	РС
11. NETWORK LABORATORY	OR	50	20	26	РС							
12. SOFT SKILLS LABORATORY	TW	25	10	20	РС							
GRAND TOTAL = 778/1500, RESULT: SECON	ND CLA	ASS										
ORDN. 1 MARKS :												
T80058661 POTEY AKSHAY MADHUKAR				NI	LIMA		, 71045578L , т8058598 ,	P.	ICT	,	т8005	8661
01. OPERATING SYSTEM	PP	100	40	AA	F							
02. THEORY OF COMPUTATION	PP	100	40	42	РС							
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	66	РС							
04. DATBASE MANAGEMENT SYSTEMS	PP	100	40	47	РС							
05. SOFTWARE ENGINEERING	PP	100	40	40	РС							
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	18	РС							
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	30	РС							
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	15	РС							
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	AA	F							
10. NETWORK LABORATORY	TW	25	10	11	РС							
11. NETWORK LABORATORY	OR	50	20	25	Р							
12. SOFT SKILLS LABORATORY	TW	25	10	15	РС							
FIRST TERM TOTAL = 309/750.												
ORDN. 1 MARKS:												

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DATE : 31 JAN. 2013
                              CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.
                                                                                     PAGE NO. 54 (537)
NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
      OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
, т80058662
 T80058662 PUNTAMBEKAR SHREYA SHAILESH
                                            SEEMA
                                                            , 71045584E , T8058600 , PICT
                                         45 P
                                                       13. SYSTEM SOFTWARE PROGRAMMING
 01. OPERATING SYSTEM
                              PP 100 40
                                                                                    PP 100 40 50 P C
 02. THEORY OF COMPUTATION
                              PP 100 40
                                          28 F
                                                       14. MANAGEMENT INFORMATION SYSTEMS PP 100
                                                                                            40
                                                                                                47 P C
 03. COMPUTER NETWORK TECHNOLOGY
                                  100
                                      40
                                          46 P C
                                                                                        100
                                                                                            40 41 P C
                              PP
                                                      15. PROGRAMMING PARADIGMS
                                                                                    PP
 04. DATBASE MANAGEMENT SYSTEMS
                                  100
                                      40
                                          40 P C
                                                                                        100
                                                                                            40
                              PP
                                                       16. DESIGN & ANALYSIS OF ALGORITHMS PP
                                                                                                AA F
 05. SOFTWARE ENGINEERING
                                  100
                                      40
                                          40 P C
                                                       17. HUMAN COMPU.INTERACTION & USABI. PP
                                                                                        100
                                                                                           40
                                                                                                43 P C
                              PP
 06. OPERATING SYSTEM DESIGN LAB.
                                  25 10
                                          18 P C
                                                                                        50
                                                                                            20 29 P C
                                                       18. SOFTWARE DESIGN LABORATORY
                                                                                    TW
                              TW
 07. OPERATING SYSTEM DESIGN LAB.
                                   50 20
                                          20 P C
                                                                                         50 20
                              PR
                                                       19. SOFTWARE DESIGN LABORATORY
                                                                                    PR
                                                                                                AA F
                                   25 10
                                          18 P C
                                                       20. SOFTWARE DEVELOPMENT TOOLS LAB. TW 50 20 33 P C
 08. INFORMATION SYSTEMS DESIGN LAB. TW
                                   50 20
                                          38 P C
                                                       21. SOFTWARE DEVELOPMENT TOOLS LAB. OR 50 20 40 P C
 09. INFORMATION SYSTEMS DESIGN LAB. OR
                                                       22. SEMINAR AND TECHNICAL COMMUN. TW 50 20 40 P C
                                   25 10 19 P C
 10. NETWORK LABORATORY
                              TW
                                  50 20 28 P C
 11. NETWORK LABORATORY
                              OR
                              TW 25 10 17 P C
 12. SOFT SKILLS LABORATORY
GRAND TOTAL = 680/1500, RESULT: FAILS A.T.K.T.
ORDN. 1 MARKS:
T80058663 RAJOLE VISHAL VILASRAO
                                                                         , T8058602 , PICT
                                            SUNITA
                                                            , 71134976C
                                                                                              , T80058663
                                                                                    PP 100 40
 01. OPERATING SYSTEM
                              PP 100 40 53 P C
                                                       13. SYSTEM SOFTWARE PROGRAMMING
                                                                                                68 P C
 02. THEORY OF COMPUTATION
                              PP 100
                                       40
                                                                                        100
                                                                                            40
                                          72 P C
                                                       14. MANAGEMENT INFORMATION SYSTEMS PP
                                                                                                61 P C
 03. COMPUTER NETWORK TECHNOLOGY
                                          71 P C
                                                                                        100
                                                                                                70 P C
                              PP
                                  100
                                       40
                                                       15. PROGRAMMING PARADIGMS
                                                                                            40
 04. DATBASE MANAGEMENT SYSTEMS
                              PP
                                  100
                                       40
                                           60 P C
                                                       16. DESIGN & ANALYSIS OF ALGORITHMS PP
                                                                                        100
                                                                                            40
                                                                                                60 P C
                                  100
                                       40
                                                                                        100
 05. SOFTWARE ENGINEERING
                              PP
                                           60 P C
                                                       17. HUMAN COMPU.INTERACTION & USABI. PP
                                                                                            40 49 P C
                                   25
                                           22 P C
 06. OPERATING SYSTEM DESIGN LAB.
                              TW
                                     10
                                                       18. SOFTWARE DESIGN LABORATORY
                                                                                    TW
                                                                                         50
                                                                                            20 39 P C
                                   50 20
                                                                                         50 20 39 P
 07. OPERATING SYSTEM DESIGN LAB.
                              PR
                                          41 P C
                                                       19. SOFTWARE DESIGN LABORATORY
                                                                                    PR
 08. INFORMATION SYSTEMS DESIGN LAB. TW
                                   25 10 21 P C
                                                       20. SOFTWARE DEVELOPMENT TOOLS LAB. TW
                                                                                        50 20 36 P C
                                   50 20 40 P C
                                                                                         50 20 45 P C
 09. INFORMATION SYSTEMS DESIGN LAB. OR
                                                       21. SOFTWARE DEVELOPMENT TOOLS LAB. OR
                                  25 10 22 P C
                                                       22. SEMINAR AND TECHNICAL COMMUN. TW 50 20 45 P C
 10. NETWORK LABORATORY
                              TW
                                   50 20 35 P C
 11. NETWORK LABORATORY
                              OR
 12. SOFT SKILLS LABORATORY
                              TW
                                   25 10 20 P C
GRAND TOTAL = 1029/1500, RESULT: FIRST CLASS WITH DISTINCTION
ORDN. 1 MARKS:
T80058664 RISHABH PANDITA
                                           USHA
                                                            , 71045589F , T8058603 , PICT
                                                                                              , T80058664
                                  100
                                       40
                                          52 P C
                                                                                            40
                                                                                                65 P C
 01. OPERATING SYSTEM
                                                       13. SYSTEM SOFTWARE PROGRAMMING
                                                                                        100
 02. THEORY OF COMPUTATION
                                  100
                                       40
                                          59 P C
                                                       14. MANAGEMENT INFORMATION SYSTEMS PP
                                                                                        100
                                                                                            40
                                                                                                58 P C
                              PP
 03. COMPUTER NETWORK TECHNOLOGY
                                  100
                                      40
                                          67 P C
                                                       15. PROGRAMMING PARADIGMS
                                                                                        100
                                                                                            40 43 P C
                              PP
                                                                                    PP
                                  100
                                      40
                                          60 P C
                                                                                        100
                                                                                            40
                                                                                                61 P C
 04. DATBASE MANAGEMENT SYSTEMS
                              PP
                                                       16. DESIGN & ANALYSIS OF ALGORITHMS PP
 05. SOFTWARE ENGINEERING
                                  100
                                       40
                                          46 P C
                                                       17. HUMAN COMPU.INTERACTION & USABI. PP
                                                                                        100
                                                                                            40
                                                                                                56 P C
                              PP
 06. OPERATING SYSTEM DESIGN LAB.
                                   25 10 21 P C
                                                       18. SOFTWARE DESIGN LABORATORY
                                                                                    TW
                                                                                        50
                                                                                           20
                                                                                                21 P C
                              TW
                                   50 20
                                          41 P C
                                                                                         50
                                                                                            20
                                                                                                36 P
 07. OPERATING SYSTEM DESIGN LAB.
                                                       19. SOFTWARE DESIGN LABORATORY
                                   25 10
                                          21 P C
                                                                                        50 20 34 P C
 08. INFORMATION SYSTEMS DESIGN LAB. TW
                                                       20. SOFTWARE DEVELOPMENT TOOLS LAB. TW
                                   50 20 30 P C
                                                                                       50 20 43 P C
 09. INFORMATION SYSTEMS DESIGN LAB. OR
                                                       21. SOFTWARE DEVELOPMENT TOOLS LAB. OR
                                   25 10 20 P C
 10. NETWORK LABORATORY
                                                       22. SEMINAR AND TECHNICAL COMMUN. TW
                                                                                         50 20 41 P C
 11. NETWORK LABORATORY
                              OR
                                   50 20 44 P C
 12. SOFT SKILLS LABORATORY
                                  25 10 21 P C
                              TW
GRAND TOTAL = 940/1500, RESULT: FIRST CLASS
ORDN. 1 MARKS:
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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER MEENAKSHI T80058665 ROHAN MARWADI , 71045590к , Т8058604 , РІСТ , т80058665 13. SYSTEM SOFTWARE PROGRAMMING 01. OPERATING SYSTEM PP 100 40 56 P C PP 100 40 60 P C PP 100 40 57 P C 14. MANAGEMENT INFORMATION SYSTEMS PP 100 42 P C 02. THEORY OF COMPUTATION 40 100 40 40 P C 100 40 58 P 03. COMPUTER NETWORK TECHNOLOGY PP 15. PROGRAMMING PARADIGMS PP 04. DATBASE MANAGEMENT SYSTEMS 100 40 52 P C 100 40 42 P C PP 16. DESIGN & ANALYSIS OF ALGORITHMS PP 05. SOFTWARE ENGINEERING 100 40 45 P C 17. HUMAN COMPU.INTERACTION & USABI. PP 100 40 43 P C PP 06. OPERATING SYSTEM DESIGN LAB. 25 10 17 P C 50 20 21 P C 18. SOFTWARE DESIGN LABORATORY TW TW 07. OPERATING SYSTEM DESIGN LAB. 50 20 32 P C 50 20 21 P PR 19. SOFTWARE DESIGN LABORATORY PR 25 10 19 P C 20. SOFTWARE DEVELOPMENT TOOLS LAB. TW 50 20 30 P C 08. INFORMATION SYSTEMS DESIGN LAB. TW 50 20 40 P C 21. SOFTWARE DEVELOPMENT TOOLS LAB. OR 50 20 40 P C 09. INFORMATION SYSTEMS DESIGN LAB. OR 22. SEMINAR AND TECHNICAL COMMUN. TW 50 20 35 P C 25 10 19 P C 10. NETWORK LABORATORY TW 50 20 22 P C 11. NETWORK LABORATORY OR 25 10 18 P C 12. SOFT SKILLS LABORATORY TW GRAND TOTAL = 809/1500, RESULT: SECOND CLASS ORDN. 1 MARKS: T80058666 SARTHAK MAJITHIA , T8058608 , PICT , т80058666 POOJA , 71045598E 100 40 40 P C 01. OPERATING SYSTEM 13. SYSTEM SOFTWARE PROGRAMMING PP 100 40 AA F 02. THEORY OF COMPUTATION 100 40 100 40 PP 45 P C 14. MANAGEMENT INFORMATION SYSTEMS PP 61 P C 03. COMPUTER NETWORK TECHNOLOGY 15. PROGRAMMING PARADIGMS 100 47 P C PP 100 40 40 P C 40 40 55 P C 04. DATBASE MANAGEMENT SYSTEMS PP 100 40 51 P C 16. DESIGN & ANALYSIS OF ALGORITHMS PP 100 100 05. SOFTWARE ENGINEERING PP 40 AA F 17. HUMAN COMPU.INTERACTION & USABI. PP 100 40 50 P C 25 06. OPERATING SYSTEM DESIGN LAB. TW 10 11 P C 18. SOFTWARE DESIGN LABORATORY TW 50 20 20 P C 50 20 07. OPERATING SYSTEM DESIGN LAB. PR 22 P C 19. SOFTWARE DESIGN LABORATORY PR 50 20 AA F 50 20 22 P C 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 12 P C 20. SOFTWARE DEVELOPMENT TOOLS LAB. TW 50 20 50 20 32 P C 09. INFORMATION SYSTEMS DESIGN LAB. OR AA F 21. SOFTWARE DEVELOPMENT TOOLS LAB. OR 25 10 10 P C 10. NETWORK LABORATORY TW 22. SEMINAR AND TECHNICAL COMMUN. TW 50 20 22 P C 50 20 24 P C 11. NETWORK LABORATORY OR 25 10 18 P C 12. SOFT SKILLS LABORATORY TW GRAND TOTAL = 582/1500, RESULT: FAILS A.T.K.T. RESULT RESERVED FOR BKLG ORDN. 1 MARKS: , 71045600L , T8058611 , PICT T80058667 SATPUTE SWAPNIL BHASKAR SUREKHA , т80058667 100 40 46 P C 40 50 P C 01. OPERATING SYSTEM 13. SYSTEM SOFTWARE PROGRAMMING PP 100 02. THEORY OF COMPUTATION 100 40 42 P C 14. MANAGEMENT INFORMATION SYSTEMS PP 100 40 47 P C PP 100 40 50 P C 100 40 40 P C 03. COMPUTER NETWORK TECHNOLOGY PP 15. PROGRAMMING PARADIGMS PP 100 40 44 P C 100 40 40 P C 04. DATBASE MANAGEMENT SYSTEMS 16. DESIGN & ANALYSIS OF ALGORITHMS PP 05. SOFTWARE ENGINEERING 100 40 40 P C 17. HUMAN COMPU.INTERACTION & USABI. PP 100 40 40 P C PP 06. OPERATING SYSTEM DESIGN LAB. 25 10 15 P C 18. SOFTWARE DESIGN LABORATORY TW 50 20 20 P C TW 50 20 38 P C 50 20 20 P 07. OPERATING SYSTEM DESIGN LAB. 19. SOFTWARE DESIGN LABORATORY 25 10 15 P C 50 20 30 P C 08. INFORMATION SYSTEMS DESIGN LAB. TW 20. SOFTWARE DEVELOPMENT TOOLS LAB. TW 50 20 30 P C 50 20 35 P C 09. INFORMATION SYSTEMS DESIGN LAB. OR 21. SOFTWARE DEVELOPMENT TOOLS LAB. OR 25 10 15 P C 10. NETWORK LABORATORY 22. SEMINAR AND TECHNICAL COMMUN. TW 50 20 21 P C 11. NETWORK LABORATORY OR 50 20 35 P C 12. SOFT SKILLS LABORATORY 25 10 19 P C TW GRAND TOTAL = 732/1500, RESULT: PASS CLASS ORDN. 1 MARKS:

UNIVERSITY OF PUNE ,T.E. (2008 PAT) (INFORMATION TECHNOLOGY) EXAMINATION NOV. 2012 DATE : 31 JAN. 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 56 (539) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER T80058668 SHIVADE PRATHAMESH SHRIKANT NILIMA , 70925608G , T8058621 , PICT , т80058668 13. SYSTEM SOFTWARE PROGRAMMING 01. OPERATING SYSTEM PP 100 40 47 P C PP 100 40 59 P C PP 100 40 43 P C 14. MANAGEMENT INFORMATION SYSTEMS PP 100 41 P C 02. THEORY OF COMPUTATION 40 03. COMPUTER NETWORK TECHNOLOGY 100 40 50 P C 100 40 45 P C PP 15. PROGRAMMING PARADIGMS PР 04. DATBASE MANAGEMENT SYSTEMS 100 40 40 P C 100 40 45 P PP 16. DESIGN & ANALYSIS OF ALGORITHMS PP 05. SOFTWARE ENGINEERING 100 40 40 P C 17. HUMAN COMPU.INTERACTION & USABI. PP 100 40 52 P C PP 06. OPERATING SYSTEM DESIGN LAB. 25 10 14 P C 50 20 20 P C 18. SOFTWARE DESIGN LABORATORY TW TW 07. OPERATING SYSTEM DESIGN LAB. 50 20 40 P C 50 20 20\$ P C PR 19. SOFTWARE DESIGN LABORATORY 25 10 15 P C 50 20 29 P C 08. INFORMATION SYSTEMS DESIGN LAB. TW 20. SOFTWARE DEVELOPMENT TOOLS LAB. TW 50 20 41 P C 50 20 38 P C 09. INFORMATION SYSTEMS DESIGN LAB. OR 21. SOFTWARE DEVELOPMENT TOOLS LAB. OR 25 10 14 P C 22. SEMINAR AND TECHNICAL COMMUN. TW 50 20 34 P C 10. NETWORK LABORATORY TW 50 20 24 P C 11. NETWORK LABORATORY OR 25 10 20 P C 12. SOFT SKILLS LABORATORY TW GRAND TOTAL = 771/1500, RESULT: SECOND CLASS [\$ 0.1] ORDN. 1 MARKS: (19)2, T80058669 SURYAWANSHI NIRAJ NITIN , т8058630 , , т80058669 NEHA , 70701667D PICT 01. OPERATING SYSTEM PP 100 40 AA F 13. SYSTEM SOFTWARE PROGRAMMING 100 40 AA F 02. THEORY OF COMPUTATION PP 100 100 40 40 AA F 14. MANAGEMENT INFORMATION SYSTEMS PP 40 P C 100 40 40 P C 03. COMPUTER NETWORK TECHNOLOGY PP 100 40 40 P C 15. PROGRAMMING PARADIGMS 100 04. DATBASE MANAGEMENT SYSTEMS PP 40 44 P C 16. DESIGN & ANALYSIS OF ALGORITHMS PP 100 40 AA F 100 05. SOFTWARE ENGINEERING PP 40 AA F 100 40 15 F 17. HUMAN COMPU.INTERACTION & USABI. PP 25 20 28 P C 06. OPERATING SYSTEM DESIGN LAB. TW 10 12 P C 18. SOFTWARE DESIGN LABORATORY TW 50 50 20 07. OPERATING SYSTEM DESIGN LAB. PR AA F 19. SOFTWARE DESIGN LABORATORY PR 50 20 AA F 50 20 32 P C 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 14 P C 20. SOFTWARE DEVELOPMENT TOOLS LAB. TW 50 20 50 20 38 P C 09. INFORMATION SYSTEMS DESIGN LAB. OR 20 P C 21. SOFTWARE DEVELOPMENT TOOLS LAB. OR 25 10 11 P C 10. NETWORK LABORATORY TW 22. SEMINAR AND TECHNICAL COMMUN. TW 50 20 22 P C 11. NETWORK LABORATORY 50 20 28 P C OR 12. SOFT SKILLS LABORATORY TW 25 10 18 P C GRAND TOTAL = 402/1500, RESULT: FAILS RESULT RESERVED FOR BKLG ORDN. 1 MARKS: T80058670 SUYASH PANDEY SHAILA , 70925625G , T8058631 , PICT 100 40 55 P 01. OPERATING SYSTEM 02. THEORY OF COMPUTATION 100 40 24 F PP PP 100 40 46 P 03. COMPUTER NETWORK TECHNOLOGY 100 40 40 P C 04. DATBASE MANAGEMENT SYSTEMS PP 05. SOFTWARE ENGINEERING 100 40 40 P C PP 06. OPERATING SYSTEM DESIGN LAB. 25 10 11 P C TW 50 20 30 P 07. OPERATING SYSTEM DESIGN LAB. 25 10 14 P C 08. INFORMATION SYSTEMS DESIGN LAB. TW 50 20 28 P C 09. INFORMATION SYSTEMS DESIGN LAB. OR 25 10 10 P C 10. NETWORK LABORATORY 11. NETWORK LABORATORY OR 50 20 32 P 12. SOFT SKILLS LABORATORY TW 25 10 15 P C FIRST TERM TOTAL = 345/750. ORDN. 1 MARKS:

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DATE : 31 JAN. 2013
                              CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.
                                                                                     PAGE NO. 57 ( 540)
NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
      OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
MANDA
                                                            , 70801648M
                                                                                             , т80058671
 T80058671 TANAWADE AMOL SHAMRAO
                                                                       , T8058677 , PICT
                                                       13. SYSTEM SOFTWARE PROGRAMMING
 01. OPERATING SYSTEM
                              PP 100 40
                                          40 P C
                                                                                    PP 100 40 59 P C
                              PP 100
                                       40
                                           40 P C
                                                       14. MANAGEMENT INFORMATION SYSTEMS PP
                                                                                        100
                                                                                                 45 P C
 02. THEORY OF COMPUTATION
                                                                                            40
 03. COMPUTER NETWORK TECHNOLOGY
                                  100
                                       40
                                          43 P C
                                                                                        100
                                                                                            40
                                                                                                 41 P C
                              PP
                                                       15. PROGRAMMING PARADIGMS
                                                                                   PP
 04. DATBASE MANAGEMENT SYSTEMS
                                  100
                                      40
                                          40 P C
                                                                                        100
                                                                                            40
                                                                                                40 P C
                              PP
                                                       16. DESIGN & ANALYSIS OF ALGORITHMS PP
 05. SOFTWARE ENGINEERING
                                  100
                                      40
                                          40 P C
                                                       17. HUMAN COMPU.INTERACTION & USABI. PP
                                                                                        100
                                                                                            40 57 P C
                              PP
 06. OPERATING SYSTEM DESIGN LAB. TW
                                   25 10
                                           12 P C
                                                                                        50
                                                                                            20 39 P C
                                                       18. SOFTWARE DESIGN LABORATORY
                                                                                    TW
 07. OPERATING SYSTEM DESIGN LAB.
                                   50 20
                                          13 F
                                                                                         50 20 30 P C
                              PR
                                                       19. SOFTWARE DESIGN LABORATORY
                                                                                    PR
                                   25 10
                                          12 P C
                                                       20. SOFTWARE DEVELOPMENT TOOLS LAB. TW 50 20 36 P C
 08. INFORMATION SYSTEMS DESIGN LAB. TW
                                   50 20 27 P C
                                                       21. SOFTWARE DEVELOPMENT TOOLS LAB. OR 50 20 36 P C
 09. INFORMATION SYSTEMS DESIGN LAB. OR
                                                       22. SEMINAR AND TECHNICAL COMMUN. TW 50 20 41 P C
                                   25 10 10 P C
 10. NETWORK LABORATORY
                              TW
                                   50 20 33 P C
 11. NETWORK LABORATORY
                              OR
                              TW 25 10 14 P C
 12. SOFT SKILLS LABORATORY
GRAND TOTAL = 748/1500, RESULT: FAILS A.T.K.T.
ORDN. 1 MARKS:
T80058672 THAKUR BHAVANA PRAKASH
                                                                         , T8058634 , PICT
                                                                                              , т80058672
                                            ARCHANA
                                                             , 70925633H
                                  100 40 43 P C
 01. OPERATING SYSTEM
                                                       13. SYSTEM SOFTWARE PROGRAMMING
                                                                                    PP 100 40 42 P C
 02. THEORY OF COMPUTATION
                              PP 100
                                       40
                                                                                        100
                                                                                            40
                                                                                                 40 P
                                          52 P C
                                                       14. MANAGEMENT INFORMATION SYSTEMS PP
 03. COMPUTER NETWORK TECHNOLOGY
                                                                                        100
                              PP
                                  100
                                       40
                                          46 P C
                                                       15. PROGRAMMING PARADIGMS
                                                                                            40
                                                                                                 48 P
                                                                                        100
 04. DATBASE MANAGEMENT SYSTEMS
                              PP
                                  100
                                       40
                                          41 P C
                                                       16. DESIGN & ANALYSIS OF ALGORITHMS PP
                                                                                            40
                                                                                                 44 P C
                                  100
                                                                                        100
                                                                                            40 52 P
 05. SOFTWARE ENGINEERING
                              PP
                                       40
                                           41 P C
                                                       17. HUMAN COMPU.INTERACTION & USABI. PP
                                   25
 06. OPERATING SYSTEM DESIGN LAB.
                              TW
                                       10
                                           17 P C
                                                       18. SOFTWARE DESIGN LABORATORY
                                                                                    TW
                                                                                         50
                                                                                             20
                                                                                                31 P C
                                   50 20
 07. OPERATING SYSTEM DESIGN LAB.
                              PR
                                          35 P C
                                                       19. SOFTWARE DESIGN LABORATORY
                                                                                    PR
                                                                                         50 20 38 P C
 08. INFORMATION SYSTEMS DESIGN LAB. TW
                                   25 10
                                          17 P C
                                                       20. SOFTWARE DEVELOPMENT TOOLS LAB. TW
                                                                                        50 20 30 P C
                                   50 20 27 P C
                                                                                         50 20 36 P C
 09. INFORMATION SYSTEMS DESIGN LAB. OR
                                                       21. SOFTWARE DEVELOPMENT TOOLS LAB. OR
                                   25 10 18 P C
                                                                                       50 20 33 P C
 10. NETWORK LABORATORY
                              TW
                                                       22. SEMINAR AND TECHNICAL COMMUN. TW
 11. NETWORK LABORATORY
                                   50 20 24 P C
                              OR
                                  25 10 21 P C
 12. SOFT SKILLS LABORATORY
                              TW
GRAND TOTAL = 776/1500, RESULT: SECOND CLASS
ORDN. 1 MARKS:
, 71045649C , T8058635 , PICT
 T80058673 THOMBRE PRIYANKA MILIND
                                            JAYMALA
                                                                                              , T80058673
                                  100
                                       40 43 P C
                                                                                             40 52 P C
 01. OPERATING SYSTEM
                                                       13. SYSTEM SOFTWARE PROGRAMMING
                                                                                        100
 02. THEORY OF COMPUTATION
                                  100
                                       40
                                           56 P C
                                                       14. MANAGEMENT INFORMATION SYSTEMS PP
                                                                                        100
                                                                                            40
                                                                                                40 P C
                              PP
                                  100
                                       40
                                          40 P C
                                                       15. PROGRAMMING PARADIGMS
                                                                                        100
                                                                                            40 45 P C
 03. COMPUTER NETWORK TECHNOLOGY
                              PP
                                                                                    PP
                                  100
                                      40
                                          44 P C
                                                                                        100
                                                                                            40 46 P C
 04. DATBASE MANAGEMENT SYSTEMS
                                                       16. DESIGN & ANALYSIS OF ALGORITHMS PP
 05. SOFTWARE ENGINEERING
                                  100
                                       40
                                           42 P C
                                                       17. HUMAN COMPU.INTERACTION & USABI. PP
                                                                                        100
                                                                                            40
                                                                                                 54 P
                              PP
 06. OPERATING SYSTEM DESIGN LAB.
                                   25 10
                                          17 P C
                                                       18. SOFTWARE DESIGN LABORATORY
                                                                                    TW
                                                                                         50 20
                                                                                                32 P C
                              TW
                                   50 20
                                           35 P C
                                                                                         50 20
                                                                                                33 P C
 07. OPERATING SYSTEM DESIGN LAB.
                                                       19. SOFTWARE DESIGN LABORATORY
                                   25 10
                                           15 P C
                                                                                         50 20 31 P C
 08. INFORMATION SYSTEMS DESIGN LAB. TW
                                                       20. SOFTWARE DEVELOPMENT TOOLS LAB. TW
                                   50 20
                                          38 P C
                                                                                       50 20 37 P C
 09. INFORMATION SYSTEMS DESIGN LAB. OR
                                                       21. SOFTWARE DEVELOPMENT TOOLS LAB. OR
                                   25 10 18 P C
 10. NETWORK LABORATORY
                                                       22. SEMINAR AND TECHNICAL COMMUN. TW
                                                                                         50 20 20 P C
 11. NETWORK LABORATORY
                              OR
                                   50
                                       20
                                          29 P C
 12. SOFT SKILLS LABORATORY
                                   25 10 20 P C
                              TW
GRAND TOTAL = 787/1500, RESULT: SECOND CLASS
ORDN. 1 MARKS:
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DATE: 31 JAN. 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 58 (541)

NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71045653M , T8058638 , PICT T80058674 UPPOD BALAJI SAMBHAJI SHOBHA , т80058674 13. SYSTEM SOFTWARE PROGRAMMING 01. OPERATING SYSTEM PP 100 40 40 P C PP 100 40 41 P C PP 100 40 40 P C 14. MANAGEMENT INFORMATION SYSTEMS PP 100 40 P C 02. THEORY OF COMPUTATION 40 PP 100 40 54 P C 100 40 59 P 03. COMPUTER NETWORK TECHNOLOGY 15. PROGRAMMING PARADIGMS PP 04. DATBASE MANAGEMENT SYSTEMS 100 40 41 P C 100 40 43 P C PP 16. DESIGN & ANALYSIS OF ALGORITHMS PP 05. SOFTWARE ENGINEERING 100 40 44 P 17. HUMAN COMPU.INTERACTION & USABI. PP 100 40 46 P C PP 06. OPERATING SYSTEM DESIGN LAB. TW 25 10 15 P C 50 20 20 P C 18. SOFTWARE DESIGN LABORATORY TW 07. OPERATING SYSTEM DESIGN LAB. 50 20 28 P C 50 20 12 F PR 19. SOFTWARE DESIGN LABORATORY PR 25 10 17 P C 50 20 20 P C 08. INFORMATION SYSTEMS DESIGN LAB. TW 20. SOFTWARE DEVELOPMENT TOOLS LAB. TW 50 20 41 P C 21. SOFTWARE DEVELOPMENT TOOLS LAB. OR 50 20 38 P C 09. INFORMATION SYSTEMS DESIGN LAB. OR 22. SEMINAR AND TECHNICAL COMMUN. TW 50 20 P C 25 10 15 P C 10. NETWORK LABORATORY TW 50 20 29 P C 11. NETWORK LABORATORY OR TW 25 10 20 P C 12. SOFT SKILLS LABORATORY GRAND TOTAL = 723/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS: T80058675 VED PRAKASH SINGH , T8058679 , PICT , т80058675 RAJ LAXMI , 70801665M 01. OPERATING SYSTEM 100 40 AA F 13. SYSTEM SOFTWARE PROGRAMMING PP 100 40 40 P C 02. THEORY OF COMPUTATION PP 100 100 40 40 52 P C 14. MANAGEMENT INFORMATION SYSTEMS PP 46 P C 03. COMPUTER NETWORK TECHNOLOGY 46 P C 100 PP 100 40 15. PROGRAMMING PARADIGMS 40 AA F 04. DATBASE MANAGEMENT SYSTEMS PP 100 40 AA F 16. DESIGN & ANALYSIS OF ALGORITHMS PP 100 40 40 P C 100 05. SOFTWARE ENGINEERING PP 40 40 P C 17. HUMAN COMPU.INTERACTION & USABI. PP 100 40 44 P C 25 06. OPERATING SYSTEM DESIGN LAB. TW 10 10 P C 18. SOFTWARE DESIGN LABORATORY TW 50 20 21 P C 50 20 07. OPERATING SYSTEM DESIGN LAB. PR 20 P C 19. SOFTWARE DESIGN LABORATORY PR 50 20 AA F 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 10 P C 20. SOFTWARE DEVELOPMENT TOOLS LAB. TW 50 20 20 P C 50 20 21 P C 50 20 AA F 09. INFORMATION SYSTEMS DESIGN LAB. OR 21. SOFTWARE DEVELOPMENT TOOLS LAB. OR 25 10 10 P C 22. SEMINAR AND TECHNICAL COMMUN. TW 50 20 22 P C 10. NETWORK LABORATORY TW 50 20 26 P C 11. NETWORK LABORATORY OR 25 10 10 P C 12. SOFT SKILLS LABORATORY TW GRAND TOTAL = 478/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS: T80058676 VIVEK KUMAR SUMAN , 70925651F , T8058643 , PICT , т80058676 100 40 46 P C 40 49 P C 01. OPERATING SYSTEM 13. SYSTEM SOFTWARE PROGRAMMING PP 100 02. THEORY OF COMPUTATION 100 40 53 P C 14. MANAGEMENT INFORMATION SYSTEMS PP 100 40 55 P C PP 03. COMPUTER NETWORK TECHNOLOGY 100 40 59 P C 100 40 43 P C PP 15. PROGRAMMING PARADIGMS PP 100 40 52 P C 100 40 50 P 04. DATBASE MANAGEMENT SYSTEMS PP 16. DESIGN & ANALYSIS OF ALGORITHMS PP 05. SOFTWARE ENGINEERING 100 40 40 P C 17. HUMAN COMPU.INTERACTION & USABI. PP 100 40 47 P C PP 06. OPERATING SYSTEM DESIGN LAB. 25 10 21 P C 18. SOFTWARE DESIGN LABORATORY TW 50 20 27 P C TW 50 20 39 P C 50 20 27 P 07. OPERATING SYSTEM DESIGN LAB. 19. SOFTWARE DESIGN LABORATORY 25 10 18 P C 50 20 34 P C 08. INFORMATION SYSTEMS DESIGN LAB. TW 20. SOFTWARE DEVELOPMENT TOOLS LAB. TW 50 20 28 P C 50 20 38 P C 09. INFORMATION SYSTEMS DESIGN LAB. OR 21. SOFTWARE DEVELOPMENT TOOLS LAB. OR 25 10 20 P C 10. NETWORK LABORATORY 22. SEMINAR AND TECHNICAL COMMUN. TW 50 20 40 P C 11. NETWORK LABORATORY OR 50 20 30 P C 12. SOFT SKILLS LABORATORY 25 10 20 P C TW GRAND TOTAL = 836/1500, RESULT: HIGHER SECOND CLASS ORDN. 1 MARKS: