DATE : 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 01 (365) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER SINDHU , 71200748C , , PICT , T80053001 T80053001 ABHISHEK ANAND 01. CONTROL SYSTEMS 100 40 60 P 02. DIGITAL COMMUNICATION 100 40 45 P 03. DIGITAL COMMUNICATION 50 20 28 P PR 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 48 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 32 P 100 40 43 P 06. MICROCONTROLLERS & APPLICATION PP 07. MICROCONTROLLERS & APPLICATION PR 50 20 28 P 08. DIGITAL SIGNAL PROCESSING 100 40 49 P 50 20 38 P 09. DIGITAL SIGNAL PROCESSING OR 10. ELECTRONIC DESIGN PRACTICE 50 20 26 P OR FIRST TERM TOTAL = 397/750. ORDN. 1 MARKS: T80053002 AGRAWAL MUKUL RAVINDRA ASHA , 71200751C , , PICT , т80053002 01. CONTROL SYSTEMS 100 40 79 P 02. DIGITAL COMMUNICATION 100 40 65 P 03. DIGITAL COMMUNICATION 50 20 36 P 100 40 77 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 50 20 40 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 100 40 06. MICROCONTROLLERS & APPLICATION PP 68 P 50 20 32 P 07. MICROCONTROLLERS & APPLICATION 08. DIGITAL SIGNAL PROCESSING 100 40 70 P 50 20 39 P 09. DIGITAL SIGNAL PROCESSING OR 10. ELECTRONIC DESIGN PRACTICE 50 20 40 P FIRST TERM TOTAL = 546/750. ORDN. 1 MARKS: T80053003 AGRAWAL PRANAT ADITYA , T80053007 , PICT ASHA , 71100721H , т80053003 100 40 47 P C 11. SIGNAL CODING & ESTIMATION THEORYPP 100 40 45 P C 01. CONTROL SYSTEMS 50 20 32 P C 02. DIGITAL COMMUNICATION 100 40 40 P C 12. SIGNAL CODING & ESTIMATION THEORYPR PP 03. DIGITAL COMMUNICATION 50 20 20 P C 13. SYSTEM PROGRA. & OPERATING SYS. PP 100 40 54 P C 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 47 P C 14. SYSTEM PROGRA. OPERATING SYS. TW 50 20 30 P C 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 36 P C 15. COMPUTER ORGANIZATION & ARCHITEC PP 100 40 46 P C 06. MICROCONTROLLERS & APPLICATION PP 100 40 47 P C 16. INDUSTRIAL MANAGEMENT 100 40 58 P C 07. MICROCONTROLLERS & APPLICATION PR 50 20 21 P C 17. WAVE THEORY & ANTENNA 100 40 58 P PΡ 100 40 42 P C 50 20 23 P C 08. DIGITAL SIGNAL PROCESSING 18. WAVE THEORY & ANTENNA 09. DIGITAL SIGNAL PROCESSING 50 20 30 P C 19. MINI PROJECT & SEMINAR 50 20 35 P C OR OR 10. ELECTRONIC DESIGN PRACTICE OR 50 20 24 P C 20. TEST & MEASUREMENT TECHNIQUES OR 50 20 32 P C GRAND TOTAL = 767/1500, RESULT: SECOND CLASS ORDN. 1 MARKS:

DATE: 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 02 (366) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER LAXMIBAI , 71344345G , , PICT , T80053004 T80053004 AHIRE BHUSHAN SURESH 01. CONTROL SYSTEMS 100 40 60 P 02. DIGITAL COMMUNICATION 100 40 43 P 03. DIGITAL COMMUNICATION 50 20 35 P PR 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 63 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 36 P 100 40 56 P 06. MICROCONTROLLERS & APPLICATION PP 07. MICROCONTROLLERS & APPLICATION PR 50 20 31 P 08. DIGITAL SIGNAL PROCESSING 100 40 32 F 50 20 37 P 09. DIGITAL SIGNAL PROCESSING OR 10. ELECTRONIC DESIGN PRACTICE 50 20 35 P OR FIRST TERM TOTAL = 428/750. ORDN. 1 MARKS: T80053005 ALGUR GAURAV ISHWAR VASANTI , т80053005 01. CONTROL SYSTEMS 100 40 52 P 02. DIGITAL COMMUNICATION 100 40 AA F 03. DIGITAL COMMUNICATION 50 20 06 F PR 100 40 04. NETWORK SYNTHESIS & FILTER DESIGNPP AA F 50 20 28 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 100 40 15 F 06. MICROCONTROLLERS & APPLICATION PP 50 20 28 P 07. MICROCONTROLLERS & APPLICATION 08. DIGITAL SIGNAL PROCESSING 100 40 27 F 50 20 36 P 09. DIGITAL SIGNAL PROCESSING OR 10. ELECTRONIC DESIGN PRACTICE OR 50 20 22 P FIRST TERM TOTAL = 214/750. ORDN. 1 MARKS: , T80053009 , PICT T80053006 ALI ASGER MODI ZEENAT , 71100724в , т80053006 11. SIGNAL CODING & ESTIMATION THEORYPP 100 40 40 P 01. CONTROL SYSTEMS 100 40 40 P C 100 40 40 P C 50 20 35 P C 02. DIGITAL COMMUNICATION 12. SIGNAL CODING & ESTIMATION THEORYPR PP 03. DIGITAL COMMUNICATION 50 20 30 P C 13. SYSTEM PROGRA. & OPERATING SYS. PP 100 40 52 P C 100 40 04. NETWORK SYNTHESIS & FILTER DESIGNPP 48 P C 14. SYSTEM PROGRA. OPERATING SYS. TW 50 20 35 P C 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 34 P C 15. COMPUTER ORGANIZATION & ARCHITEC PP 100 40 44 P C 06. MICROCONTROLLERS & APPLICATION PP 100 40 56 P C 16. INDUSTRIAL MANAGEMENT 100 40 63 P C 07. MICROCONTROLLERS & APPLICATION PR 50 20 27 P C 17. WAVE THEORY & ANTENNA 100 40 40 P C PP 100 40 52 P C 50 20 30 P C 08. DIGITAL SIGNAL PROCESSING 18. WAVE THEORY & ANTENNA 09. DIGITAL SIGNAL PROCESSING 50 20 23 P C 19. MINI PROJECT & SEMINAR 50 20 39 P C OR OR 10. ELECTRONIC DESIGN PRACTICE OR 50 20 22 P C 20. TEST & MEASUREMENT TECHNIQUES OR 50 20 36 P C GRAND TOTAL = 786/1500, RESULT: SECOND CLASS ORDN. 1 MARKS:

UNIVERSITY OF	PUNE	,T.E.	(2008	PAT	.)(EL	ECTRON	ICS &	TELEC.) E	XAMINA	TION NOV. 2	013		
DATE : 11 MAR. 2014	CENT	RE : Pl	JNE II	NSTI	TUTE (OF COM	PUTER 7	TECHNOLOG	Y, PUNI	Ε.	P	AGE NO	. 03 (367)
NOTE: FIRST LINE : SEAT NO., NAME CONTHER LINES: HEAD OF PASSING,	F THE	CANDI	DATE,	МО	THER,	PERMA	NENT RI	EG. NO.,	PREVIO	_	., COLL	EGE,	
T80053007 ANIRUDDHA SINGH CHOUHAN					 NUKA			, 71200					, т80053007
01. CONTROL SYSTEMS	PP	100	40	62				, /1200	// OIL	,	,	rici	, 100033007
02. DIGITAL COMMUNICATION	PP	100		44									
03. DIGITAL COMMUNICATION	PR		20	22									
04. NETWORK SYNTHESIS & FILTER DESIG		100		48									
05. NETWORK SYNTHESIS & FILTER DESIG			20	29									
06. MICROCONTROLLERS & APPLICATION	PP		40	62									
07. MICROCONTROLLERS & APPLICATION	PR		20	22									
08. DIGITAL SIGNAL PROCESSING	PP	100		57									
09. DIGITAL SIGNAL PROCESSING	OR	50	20	34									
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	15	F								
FIRST TERM TOTAL = 395/750.													
ORDN. 1 MARKS :													
T80053008 ASHISH VILAS PATIL				SH	IAILA	PATIL		, 71344	346E	,	,	PICT	, т80053008
01. CONTROL SYSTEMS	PP	100	40	82	Р								
02. DIGITAL COMMUNICATION	PP	100	40	76	Р								
03. DIGITAL COMMUNICATION	PR	50	20	45	Р								
04. NETWORK SYNTHESIS & FILTER DESIG	NPP	100	40	83	Р								
05. NETWORK SYNTHESIS & FILTER DESIG	MTW	50	20	47	Р								
06. MICROCONTROLLERS & APPLICATION	PP	100	40	76	Р								
07. MICROCONTROLLERS & APPLICATION	PR	50	20	40	Р								
08. DIGITAL SIGNAL PROCESSING	PP	100	40	74	Р								
09. DIGITAL SIGNAL PROCESSING	OR	50	20	41	Р								
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	44	Р								
FIRST TERM TOTAL = $608/750$.													
ORDN. 1 MARKS :													
T80053009 ATHAWALE SHRADDHA MAHADE	:V				YΑ			, 71212	091C	,	,	PICT	, т80053009
01. CONTROL SYSTEMS	PP	100		62									
02. DIGITAL COMMUNICATION	PP	100		42									
03. DIGITAL COMMUNICATION	PR		20	42									
04. NETWORK SYNTHESIS & FILTER DESIG		100		47									
05. NETWORK SYNTHESIS & FILTER DESIG	INTW		20	38									
06. MICROCONTROLLERS & APPLICATION	PP	100		53									
07. MICROCONTROLLERS & APPLICATION	PR	50		24									
08. DIGITAL SIGNAL PROCESSING	PP	100		58									
09. DIGITAL SIGNAL PROCESSING	OR	50		33									
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	36	Р								
FIRST TERM TOTAL = $435/750$.													
ORDN. 1 MARKS :													

DATE: 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 04 (368) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER KAVERI T80053010 AWACHAR MOHAN UDDHAO , 71200767к , , , , рІСТ , Т80053010 01. CONTROL SYSTEMS 100 40 67 P 02. DIGITAL COMMUNICATION 100 40 57 P 03. DIGITAL COMMUNICATION 50 20 37 P PR 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 75 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 37 P 06. MICROCONTROLLERS & APPLICATION PP 100 40 60 P 07. MICROCONTROLLERS & APPLICATION PR 50 20 28 P 08. DIGITAL SIGNAL PROCESSING 100 40 48 P 09. DIGITAL SIGNAL PROCESSING 50 20 35 P OR 50 20 34 P 10. ELECTRONIC DESIGN PRACTICE OR FIRST TERM TOTAL = 478/750. ORDN. 1 MARKS: , т80053011 T80053011 B KUMAR ANUPAM LAKSHMI PRIYA , 71200768H , , PICT 01. CONTROL SYSTEMS 100 40 43 P 02. DIGITAL COMMUNICATION 100 40 29 F 03. DIGITAL COMMUNICATION 50 20 20 P PR 100 40 40 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 50 20 29 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 100 40 32 F 06. MICROCONTROLLERS & APPLICATION PP 50 20 05 F 07. MICROCONTROLLERS & APPLICATION 08. DIGITAL SIGNAL PROCESSING 100 40 46 P 09. DIGITAL SIGNAL PROCESSING 50 20 33 P OR 10. ELECTRONIC DESIGN PRACTICE OR 50 20 12 F FIRST TERM TOTAL = 289/750. ORDN. 1 MARKS: T80053012 BADE ABHIJIT ARJUN **ASHA** , 71200769F , т80053012 , PICT 100 40 66 P 01. CONTROL SYSTEMS 100 40 51 P 02. DIGITAL COMMUNICATION PP 03. DIGITAL COMMUNICATION 50 20 30 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 61 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 36 P 06. MICROCONTROLLERS & APPLICATION 100 40 56 P 07. MICROCONTROLLERS & APPLICATION PR 50 20 24 P 100 40 48 P 08. DIGITAL SIGNAL PROCESSING 09. DIGITAL SIGNAL PROCESSING 50 20 37 P OR 10. ELECTRONIC DESIGN PRACTICE OR 50 20 40 P FIRST TERM TOTAL = 449/750. ORDN. 1 MARKS:

DATE: 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 05 (369) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER T80053013 BAGDE PRAJAKTA ASHOK , 71200770к , , , рІСТ , Т80053013 MANISHA 01. CONTROL SYSTEMS 100 40 55 P 02. DIGITAL COMMUNICATION 100 40 52 P 03. DIGITAL COMMUNICATION 50 20 40 P PR 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 63 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 42 P 06. MICROCONTROLLERS & APPLICATION PP 100 40 62 P 50 20 32 P 07. MICROCONTROLLERS & APPLICATION PR 08. DIGITAL SIGNAL PROCESSING 100 40 61 P 09. DIGITAL SIGNAL PROCESSING 50 20 35 P OR 10. ELECTRONIC DESIGN PRACTICE 50 20 38 P OR FIRST TERM TOTAL = 480/750. ORDN. 1 MARKS: T80053014 BAIRAGI SURAJ KACHARDAS SULOCHANA , T80053014 01. CONTROL SYSTEMS 100 40 78 P 02. DIGITAL COMMUNICATION 100 40 71 P 03. DIGITAL COMMUNICATION 50 20 44 P 100 40 82 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 50 20 45 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 100 40 71 P 06. MICROCONTROLLERS & APPLICATION PP 50 20 40 P 07. MICROCONTROLLERS & APPLICATION PR 08. DIGITAL SIGNAL PROCESSING 100 40 69 P 09. DIGITAL SIGNAL PROCESSING 50 20 38 P OR 10. ELECTRONIC DESIGN PRACTICE OR 50 20 38 P FIRST TERM TOTAL = 576/750. ORDN. 1 MARKS: T80053015 BAJAJ POOJA BALAPRASAD SAVITA , 71200776J , т80053015 , PICT 100 40 70 P 01. CONTROL SYSTEMS 02. DIGITAL COMMUNICATION 100 40 25 F PP 03. DIGITAL COMMUNICATION 50 20 20 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 44 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 36 P 06. MICROCONTROLLERS & APPLICATION PP 100 40 47 P 07. MICROCONTROLLERS & APPLICATION PR 50 20 22 P 100 40 50 P 08. DIGITAL SIGNAL PROCESSING 09. DIGITAL SIGNAL PROCESSING 50 20 32 P OR 10. ELECTRONIC DESIGN PRACTICE OR 50 20 39 P FIRST TERM TOTAL = 385/750. ORDN. 1 MARKS:

DATE: 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 06 (370) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER T80053016 BAKRE ANUJ RAMESH , 71200777G , , PICT , T80053016 JAYASHREE 01. CONTROL SYSTEMS 100 40 76 P 02. DIGITAL COMMUNICATION 100 40 72 P 03. DIGITAL COMMUNICATION 50 20 39 P PR 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 65 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 38 P 06. MICROCONTROLLERS & APPLICATION PP 100 40 70 P 07. MICROCONTROLLERS & APPLICATION PR 50 20 36 P 08. DIGITAL SIGNAL PROCESSING 100 40 73 P 50 20 39 P 09. DIGITAL SIGNAL PROCESSING OR 50 20 39 P 10. ELECTRONIC DESIGN PRACTICE OR FIRST TERM TOTAL = 547/750. ORDN. 1 MARKS: , т80053017 T80053017 BARKADE VIJAYA VASANT JAYASHRI 01. CONTROL SYSTEMS 100 40 75 P 02. DIGITAL COMMUNICATION 100 40 56 P 03. DIGITAL COMMUNICATION 50 20 20 P PR 100 40 75 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 50 20 40 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 100 40 73 P 06. MICROCONTROLLERS & APPLICATION PP 50 20 36 P 07. MICROCONTROLLERS & APPLICATION PR 08. DIGITAL SIGNAL PROCESSING 100 40 64 P 09. DIGITAL SIGNAL PROCESSING 50 20 33 P OR 10. ELECTRONIC DESIGN PRACTICE OR 50 20 34 P FIRST TERM TOTAL = 506/750. ORDN. 1 MARKS: T80053018 BASHIRABADKAR SAURABH MOHANKUMAR ANUPAMA , 71200785H , T80053018 , PICT 100 40 61 P 01. CONTROL SYSTEMS 100 40 02. DIGITAL COMMUNICATION 60 P 03. DIGITAL COMMUNICATION 50 20 27 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 49 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 35 P 06. MICROCONTROLLERS & APPLICATION PP 100 40 50 P 07. MICROCONTROLLERS & APPLICATION PR 50 20 29 P 100 40 54 P 08. DIGITAL SIGNAL PROCESSING 09. DIGITAL SIGNAL PROCESSING 50 20 35 P OR 10. ELECTRONIC DESIGN PRACTICE OR 50 20 26 P FIRST TERM TOTAL = 426/750. ORDN. 1 MARKS:

DATE: 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 07 (371) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER T80053019 BAVKAR INDRANEEL NANDKUMAR , 71200787D , , PICT , T80053019 RADHIKA 01. CONTROL SYSTEMS 100 40 64 P 02. DIGITAL COMMUNICATION 100 40 40 P 03. DIGITAL COMMUNICATION 50 20 33 P PR 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 47 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 39 P 06. MICROCONTROLLERS & APPLICATION PP 100 40 51 P 07. MICROCONTROLLERS & APPLICATION PR 50 20 30 P 08. DIGITAL SIGNAL PROCESSING 100 40 55 P 09. DIGITAL SIGNAL PROCESSING 50 20 40 P OR 10. ELECTRONIC DESIGN PRACTICE 50 20 36 P OR FIRST TERM TOTAL = 435/750. ORDN. 1 MARKS: T80053020 BAYAS DIPALI SUNILSING URMILA , 71344348M , , PICT , т80053020 01. CONTROL SYSTEMS 100 40 78 P 02. DIGITAL COMMUNICATION 100 40 60 P 03. DIGITAL COMMUNICATION 50 20 32 P 100 40 76 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 50 20 42 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 100 40 72 P 06. MICROCONTROLLERS & APPLICATION PP 50 20 31 P 07. MICROCONTROLLERS & APPLICATION 08. DIGITAL SIGNAL PROCESSING 100 40 62 P 50 20 36 P 09. DIGITAL SIGNAL PROCESSING OR 10. ELECTRONIC DESIGN PRACTICE 50 20 38 P FIRST TERM TOTAL = 527/750. ORDN. 1 MARKS: T80053021 BERAD SUPRIYA LAXMIKANT SUNANDA , 71200788в , T80053021 , PICT 01. CONTROL SYSTEMS 100 40 68 P 02. DIGITAL COMMUNICATION 100 40 63 P PP 03. DIGITAL COMMUNICATION 50 20 20 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 75 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 43 P 06. MICROCONTROLLERS & APPLICATION PP 100 40 71 P 07. MICROCONTROLLERS & APPLICATION PR 50 20 33 P 100 40 66 P 08. DIGITAL SIGNAL PROCESSING 09. DIGITAL SIGNAL PROCESSING 50 20 38 P OR 10. ELECTRONIC DESIGN PRACTICE OR 50 20 34 P FIRST TERM TOTAL = 511/750. ORDN. 1 MARKS:

DATE: 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 08 (372) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER T80053022 BHALERAO MAHESH GOPAL SANGITA , 71344349K , , , PICT , T80053022 01. CONTROL SYSTEMS 100 40 67 P 02. DIGITAL COMMUNICATION 100 40 46 P 03. DIGITAL COMMUNICATION 50 20 35 P PR 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 61 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 39 P 06. MICROCONTROLLERS & APPLICATION PP 100 40 70 P 50 20 30 P 07. MICROCONTROLLERS & APPLICATION PR 08. DIGITAL SIGNAL PROCESSING 100 40 68 P 09. DIGITAL SIGNAL PROCESSING 50 20 32 P OR 50 20 38 P 10. ELECTRONIC DESIGN PRACTICE OR FIRST TERM TOTAL = 486/750. ORDN. 1 MARKS: , т80053023 T80053023 BHANDARI AISHWARYA ABHAY SANGITA , 71200791B , , PICT 01. CONTROL SYSTEMS 100 40 60 P 02. DIGITAL COMMUNICATION 100 40 62 P 03. DIGITAL COMMUNICATION 50 20 33 P PR 100 40 40 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 50 20 37 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 100 40 73 P 06. MICROCONTROLLERS & APPLICATION PP 50 20 38 P 07. MICROCONTROLLERS & APPLICATION PR 08. DIGITAL SIGNAL PROCESSING 100 40 74 P 09. DIGITAL SIGNAL PROCESSING 50 20 39 P OR 10. ELECTRONIC DESIGN PRACTICE OR 50 20 38 P FIRST TERM TOTAL = 494/750. ORDN. 1 MARKS: T80053024 BHARGANDE SAURABH VIJAYKUMAR SUSHMA , 71200794G , т80053024 , PICT 01. CONTROL SYSTEMS 100 40 63 P 02. DIGITAL COMMUNICATION 100 40 27 F 03. DIGITAL COMMUNICATION 50 20 34 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 50 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 35 P 06. MICROCONTROLLERS & APPLICATION PP 100 40 44 P 07. MICROCONTROLLERS & APPLICATION PR 50 20 06 F 100 40 40 P 08. DIGITAL SIGNAL PROCESSING 09. DIGITAL SIGNAL PROCESSING 50 20 38 P OR 10. ELECTRONIC DESIGN PRACTICE OR 50 20 38 P FIRST TERM TOTAL = 375/750. ORDN. 1 MARKS:

UNIVERSITY O	F PUNE	T.E.	(200	8 PA	T.)(ELECTRO	NICS & T	ELEC.) EXAMINA	TION NOV. 20	13	
DATE : 11 MAR. 2014	CENT	RE : F	PUNE	INST	ITUTE OF CO	MPUTER T	ECHNOLOGY, PUNI	E.	PAGE NO.	09 (373)
NOTE: FIRST LINE : SEAT NO., NAME										
OTHER LINES: HEAD OF PASSING,										
T80053025 BHAVSAR RUCHIKA KISHOR			• •		AVITA			,		, т80053025
01. CONTROL SYSTEMS	PP	100	40		P		, 713113300	,	, 1101	, 100033023
02. DIGITAL COMMUNICATION		100			P					
03. DIGITAL COMMUNICATION	PR		20		Р					
04. NETWORK SYNTHESIS & FILTER DESI	GNPP	100			Р					
05. NETWORK SYNTHESIS & FILTER DESI	GNTW	50	20	45	Р					
06. MICROCONTROLLERS & APPLICATION	PP	100	40	67	Р					
07. MICROCONTROLLERS & APPLICATION	PR	50	20	38	Р					
08. DIGITAL SIGNAL PROCESSING	PP	100	40	58	Р					
09. DIGITAL SIGNAL PROCESSING	OR	50	20	39	Р					
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	34	Р					
FIRST TERM TOTAL = 536/750.										
ORDN. 1 MARKS :										
T80053026 BHINGE ABHIJEET SOMESHW	AR			RI	JPALI		, 71200795E	,	, PICT	, т80053026
01. CONTROL SYSTEMS	PP	100	40	75	Р					
02. DIGITAL COMMUNICATION	PP	100	40	73	Р					
03. DIGITAL COMMUNICATION	PR	50	20	45	Р					
04. NETWORK SYNTHESIS & FILTER DESI	GNPP	100	40	79	Р					
05. NETWORK SYNTHESIS & FILTER DESI	GNTW	50	20	47	Р					
06. MICROCONTROLLERS & APPLICATION	PP	100		70	Р					
07. MICROCONTROLLERS & APPLICATION	PR	50	20	44	Р					
08. DIGITAL SIGNAL PROCESSING	PP	100		77	Р					
09. DIGITAL SIGNAL PROCESSING	OR		20	44	Р					
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	45	Р					
FIRST TERM TOTAL = 599/750.										
ORDN. 1 MARKS :										
T20052027 PHORDE MONALT MARKET										
T80053027 BHORDE MONALI MARUTI	חח	100	40		JNANDA		, 71344351M	,	, PICT	, т80053027
01. CONTROL SYSTEMS 02. DIGITAL COMMUNICATION	PP PP	100 100		71 83						
03. DIGITAL COMMUNICATION	PP PR		20	83 44						
04. NETWORK SYNTHESIS & FILTER DESI		100		71						
05. NETWORK SYNTHESIS & FILTER DESI			20	40						
06. MICROCONTROLLERS & APPLICATION	PP	100		72						
07. MICROCONTROLLERS & APPLICATION	PR PR		20	41						
08. DIGITAL SIGNAL PROCESSING	PP	100		63						
09. DIGITAL SIGNAL PROCESSING	OR		20	39						
10. ELECTRONIC DESIGN PRACTICE	OR		20	44						
FIRST TERM TOTAL = 568/750.	ΟIX	30	20	77	ı					
ORDN. 1 MARKS :										
SIGNI I PARIO I										

DATE: 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 10 (374) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER T80053028 BHOSALE JUILEE UMESH ANITA , 71200797M , , PICT , T80053028 01. CONTROL SYSTEMS 100 40 74 P 02. DIGITAL COMMUNICATION 100 40 67 P 03. DIGITAL COMMUNICATION 50 20 43 P PR 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 72 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 39 P 06. MICROCONTROLLERS & APPLICATION PP 100 40 55 P 07. MICROCONTROLLERS & APPLICATION PR 50 20 41 P 08. DIGITAL SIGNAL PROCESSING 100 40 70 P 09. DIGITAL SIGNAL PROCESSING 50 20 38 P OR 50 20 37 P 10. ELECTRONIC DESIGN PRACTICE OR FIRST TERM TOTAL = 536/750. ORDN. 1 MARKS: T80053029 BIDE DADA VITHOBA , 71200799н , , , ріст , т80053029 JIJABAI 01. CONTROL SYSTEMS 100 40 57 P 02. DIGITAL COMMUNICATION 100 40 AA F 03. DIGITAL COMMUNICATION 50 20 07 F PR 100 40 57 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 50 20 36 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 100 40 67 P 06. MICROCONTROLLERS & APPLICATION PP 50 20 07 F 07. MICROCONTROLLERS & APPLICATION 08. DIGITAL SIGNAL PROCESSING 100 40 AA F 09. DIGITAL SIGNAL PROCESSING 50 20 34 P OR 10. ELECTRONIC DESIGN PRACTICE 50 20 13 F FIRST TERM TOTAL = 278/750. ORDN. 1 MARKS: T80053030 BOBADE AJINKYA CHANDRAKANT CHHAYA , 71200802M , T80053030 , PICT 100 40 55 P 01. CONTROL SYSTEMS 30 F 02. DIGITAL COMMUNICATION 100 40 03. DIGITAL COMMUNICATION 50 20 40 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 33 F 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 38 P 06. MICROCONTROLLERS & APPLICATION PP 100 40 45 P 07. MICROCONTROLLERS & APPLICATION PR 50 20 26 P 100 40 50 P 08. DIGITAL SIGNAL PROCESSING 09. DIGITAL SIGNAL PROCESSING 50 20 35 P OR 10. ELECTRONIC DESIGN PRACTICE OR 50 20 28 P FIRST TERM TOTAL = 380/750. ORDN. 1 MARKS:

DATE: 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 11 (375) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER T80053031 BONDE KAJAL SURESH KALPANA , 71344352к , , , , рІСТ , Т80053031 01. CONTROL SYSTEMS 100 40 59 P 02. DIGITAL COMMUNICATION 100 40 40 P 03. DIGITAL COMMUNICATION 50 20 39 P PR 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 49 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 39 P 06. MICROCONTROLLERS & APPLICATION PP 100 40 58 P 07. MICROCONTROLLERS & APPLICATION PR 50 20 34 P 08. DIGITAL SIGNAL PROCESSING 100 40 45 P 09. DIGITAL SIGNAL PROCESSING 50 20 34 P OR 50 20 42 P 10. ELECTRONIC DESIGN PRACTICE OR FIRST TERM TOTAL = 439/750. ORDN. 1 MARKS: , т80053032 T80053032 BOOB SURABHI RAMDHAN SANGEETA , 71200803K , PICT 01. CONTROL SYSTEMS 100 40 65 P 02. DIGITAL COMMUNICATION 100 40 64 P 03. DIGITAL COMMUNICATION 50 20 41 P PR 100 40 66 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 50 20 39 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 100 40 72 P 06. MICROCONTROLLERS & APPLICATION PP 50 20 41 P 07. MICROCONTROLLERS & APPLICATION 08. DIGITAL SIGNAL PROCESSING 100 40 71 P 09. DIGITAL SIGNAL PROCESSING 50 20 40 P OR 10. ELECTRONIC DESIGN PRACTICE OR 50 20 42 P FIRST TERM TOTAL = 541/750. ORDN. 1 MARKS: T80053033 BORSE SWATI NANA KALPANA , 71100754D , т80053033 , PICT 100 40 40 P 01. CONTROL SYSTEMS 100 40 40 P 02. DIGITAL COMMUNICATION PP 03. DIGITAL COMMUNICATION 50 20 25 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 53 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 32 P 06. MICROCONTROLLERS & APPLICATION PP 100 40 31 F 07. MICROCONTROLLERS & APPLICATION PR 50 20 AA F 100 40 40 P 08. DIGITAL SIGNAL PROCESSING 09. DIGITAL SIGNAL PROCESSING 50 20 33 P OR 10. ELECTRONIC DESIGN PRACTICE OR 50 20 28 P FIRST TERM TOTAL = 322/750. ORDN. 1 MARKS:

DATE: 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 12 (376) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71100760J , , PICT , T80053034 T80053034 CHANDAK ANKIT PANDURANGJI MAYA 01. CONTROL SYSTEMS 100 40 42 P 02. DIGITAL COMMUNICATION 100 40 07 F 03. DIGITAL COMMUNICATION 50 20 34 P PR 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 45 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 32 P 100 40 24 F 06. MICROCONTROLLERS & APPLICATION PP 07. MICROCONTROLLERS & APPLICATION PR 50 20 03 F 08. DIGITAL SIGNAL PROCESSING 100 40 28 F 50 20 34 P 09. DIGITAL SIGNAL PROCESSING OR 10. ELECTRONIC DESIGN PRACTICE 50 20 26 P OR FIRST TERM TOTAL = 275/750. ORDN. 1 MARKS: T80053035 CHANDAN PANCHLORIA SUMITRADEVI , 71100761G , T80053020 , PICT , T80053035 45 P C 01. CONTROL SYSTEMS 100 40 11. SIGNAL CODING & ESTIMATION THEORYPP 100 40 40 P C 100 40 43 P C 12. SIGNAL CODING & ESTIMATION THEORYPR 50 20 27 P 02. DIGITAL COMMUNICATION 03. DIGITAL COMMUNICATION 50 20 32 P C 13. SYSTEM PROGRA. & OPERATING SYS. PP 100 40 PR 63 P C 100 40 58 P C 50 20 04. NETWORK SYNTHESIS & FILTER DESIGNPP 14. SYSTEM PROGRA. & OPERATING SYS. TW 33 P C 50 20 40 P C 100 40 48 P C 05. NETWORK SYNTHESIS & FILTER DESIGNTW 15. COMPUTER ORGANIZATION & ARCHITEC PP 100 40 40 P C 100 40 70 P C 06. MICROCONTROLLERS & APPLICATION PP 16. INDUSTRIAL MANAGEMENT 50 20 33 P C 100 40 40 P C 07. MICROCONTROLLERS & APPLICATION PR 17. WAVE THEORY & ANTENNA PP 08. DIGITAL SIGNAL PROCESSING 100 40 50 20 20 P C 44 P C 18. WAVE THEORY & ANTENNA 50 20 21 P C 50 20 38 P C 09. DIGITAL SIGNAL PROCESSING OR 19. MINI PROJECT & SEMINAR OR 10. ELECTRONIC DESIGN PRACTICE OR 50 20 28 P C 20. TEST & MEASUREMENT TECHNIQUES OR 50 20 32 P C GRAND TOTAL = 795/1500, RESULT: SECOND CLASS ORDN. 1 MARKS: T80053036 CHASKAR DIPALI SHRIDHAR , 71200810в , T80053036 ALKA , PICT 01. CONTROL SYSTEMS 100 40 58 P 02. DIGITAL COMMUNICATION 100 40 40 P 03. DIGITAL COMMUNICATION 50 20 36 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 50 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 39 P 06. MICROCONTROLLERS & APPLICATION PP 100 40 56 P 07. MICROCONTROLLERS & APPLICATION PR 50 20 22 P 59 P 08. DIGITAL SIGNAL PROCESSING 100 40 09. DIGITAL SIGNAL PROCESSING 50 20 33 P OR 10. ELECTRONIC DESIGN PRACTICE OR 50 20 28 P FIRST TERM TOTAL = 421/750. ORDN. 1 MARKS:

DATE: 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 13 (377) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71200816M , , PICT , T80053037 T80053037 CHAVAN ROHIT UTTAM MEENA 01. CONTROL SYSTEMS 100 40 66 P 02. DIGITAL COMMUNICATION 100 40 85 P 03. DIGITAL COMMUNICATION 50 20 35 P PR 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 70 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 43 P 100 40 82 P 06. MICROCONTROLLERS & APPLICATION PP 07. MICROCONTROLLERS & APPLICATION PR 50 20 41 P 08. DIGITAL SIGNAL PROCESSING 100 40 79 P 50 20 38 P 09. DIGITAL SIGNAL PROCESSING OR 10. ELECTRONIC DESIGN PRACTICE 50 20 38 P OR FIRST TERM TOTAL = 577/750. ORDN. 1 MARKS: T80053038 CHIRANIYA ANKITA GOPAL RAKHI , 71200821H , , PICT , т80053038 01. CONTROL SYSTEMS 100 40 61 P 02. DIGITAL COMMUNICATION 100 40 40 P 03. DIGITAL COMMUNICATION 50 20 29 P PR 100 40 62 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 50 20 29 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 100 40 45 P 06. MICROCONTROLLERS & APPLICATION PP 50 20 07. MICROCONTROLLERS & APPLICATION 05 F 08. DIGITAL SIGNAL PROCESSING 100 40 59 P 50 20 35 P 09. DIGITAL SIGNAL PROCESSING OR 10. ELECTRONIC DESIGN PRACTICE 50 20 26 P FIRST TERM TOTAL = 391/750. ORDN. 1 MARKS: , т80053025 , T80053039 CHITALE JANHAVI GOVIND **VARSHA** , 71100769в , т80053039 PICT 100 40 40 P C 01. CONTROL SYSTEMS 100 40 56 P C 11. SIGNAL CODING & ESTIMATION THEORYPP 50 20 39 P C 02. DIGITAL COMMUNICATION 100 40 48 P C 12. SIGNAL CODING & ESTIMATION THEORYPR PP 03. DIGITAL COMMUNICATION 50 20 41 P C 13. SYSTEM PROGRA. & OPERATING SYS. PP 100 40 53 P C 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 55 P C 14. SYSTEM PROGRA. OPERATING SYS. TW 50 20 40 P C 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 42 P C 15. COMPUTER ORGANIZATION & ARCHITEC PP 100 40 52 P C 06. MICROCONTROLLERS & APPLICATION PP 100 40 57 P C 16. INDUSTRIAL MANAGEMENT 100 40 61 P C 07. MICROCONTROLLERS & APPLICATION PR 50 20 38 P C 17. WAVE THEORY & ANTENNA 100 40 61 P PΡ 100 40 50 20 08. DIGITAL SIGNAL PROCESSING 47 P C 18. WAVE THEORY & ANTENNA 38 P C 09. DIGITAL SIGNAL PROCESSING 50 20 42 P C 19. MINI PROJECT & SEMINAR 50 20 42 P C OR OR 10. ELECTRONIC DESIGN PRACTICE OR 50 20 25 P C 20. TEST & MEASUREMENT TECHNIQUES OR 50 20 41 P C GRAND TOTAL = 918/1500, RESULT: FIRST CLASS ORDN. 1 MARKS:

UNIVERSITY OF	F PUNE	,T.E.	(200	8 PAT	.)(EL	ECTRONICS & TELEC	C.) EXAMINA	TION NOV. 20	13	
DATE : 11 MAR. 2014	CENT	RE : P	UNE	INSTI	TUTE	OF COMPUTER TECHN	NOLOGY, PUN	E.	PAGE NO	. 14 (378)
NOTE: ETDST LINE: SEAT NO NAME (DEDMANENT DEC.				
NOTE: FIRST LINE : SEAT NO., NAME (OTHER LINES: HEAD OF PASSING,				-	-		_	_		
T80053040 CHOWDHARY NOOPUR YUVRAJI			•		RSHA			,		, т80053040
01. CONTROL SYSTEMS	PP	100	40	62		,		,	,	,
02. DIGITAL COMMUNICATION	PP	100		44	Р					
03. DIGITAL COMMUNICATION	PR	50	20	24						
04. NETWORK SYNTHESIS & FILTER DESIG	GNPP	100	40	56	Р					
05. NETWORK SYNTHESIS & FILTER DESIG	GNTW	50	20	38	Р					
06. MICROCONTROLLERS & APPLICATION	PP	100	40	49	Р					
07. MICROCONTROLLERS & APPLICATION	PR	50	20	06	F					
08. DIGITAL SIGNAL PROCESSING	PP	100	40	51	Р					
09. DIGITAL SIGNAL PROCESSING	OR	50	20	36	Р					
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	36	Р					
FIRST TERM TOTAL = $402/750$.										
ORDN. 1 MARKS :										
T80053041 DAKE ABOLI BALASAHEB				AR	UNA	, 7	71200826〕	,	, PICT	, т80053041
01. CONTROL SYSTEMS	PP	100	40	64	Р					
02. DIGITAL COMMUNICATION	PP	100	40	40	Р					
03. DIGITAL COMMUNICATION	PR	50	20	80	F					
04. NETWORK SYNTHESIS & FILTER DESIG	GNPP	100	40	67	Р					
05. NETWORK SYNTHESIS & FILTER DESIG	GNTW	50	20	35	Р					
06. MICROCONTROLLERS & APPLICATION	PP	100	40	57	Р					
07. MICROCONTROLLERS & APPLICATION	PR	50	20	26	Р					
08. DIGITAL SIGNAL PROCESSING	PP	100		64						
09. DIGITAL SIGNAL PROCESSING	OR	50	20	28						
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	26	Р					
FIRST TERM TOTAL = $415/750$.										
ORDN. 1 MARKS :										
T80053042 DESHMUKH ASHUTOSH VILASI		100	40		INITA -	, ,	71100780C	,	, PICT	, т80053042
01. CONTROL SYSTEMS	PP	100		60						
02. DIGITAL COMMUNICATION	PP	100		43						
03. DIGITAL COMMUNICATION	PR	50	20	AA						
04. NETWORK SYNTHESIS & FILTER DESIG		100		49						
05. NETWORK SYNTHESIS & FILTER DESIG		50	_	20						
06. MICROCONTROLLERS & APPLICATION	PP	100		26						
07. MICROCONTROLLERS & APPLICATION	PR		20	AA						
08. DIGITAL SIGNAL PROCESSING	PP	100	40	48						
09. DIGITAL SIGNAL PROCESSING	OR		20	AA						
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	AA	F					
FIRST TERM TOTAL = 246/750.										
ORDN. 1 MARKS :										

UNIVERSITY OF PUNE ,T.E.(2008 PAT.)(ELECTRONICS & TELEC.) EXAMINATION NOV. 2013 DATE : 11 MAR. 2014 CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 15 (379)

DATE . II MAR. 2014	CENT	KE . P	UNE	TIVOIT			R TECHNOLOGY, PONE.				-	-
NOTE: FIRST LINE : SEAT NO., NAME OF PASSING					OTHER, PE	ERMANENT	REG. NO., PREVIOUS SEAT NO., C KS OBTAINED, P/F:PASS/FAIL, C:P	OLLEGE	E, S	SEAT	NO.	
			-			•						
T80053043 DESHPANDE ADITI VIDHYAD	HAR			ΑL	LKA		, 71100781м , т80053028 ,	PI	СТ	,	т8005	3043
01. CONTROL SYSTEMS	PP	100	40	58	PC	11.	SIGNAL CODING & ESTIMATION THEOR	YPP	100	40	40	РС
02. DIGITAL COMMUNICATION	PP	100	40	42	PC	12.	SIGNAL CODING & ESTIMATION THEOR	YPR	50	20	38	P C
03. DIGITAL COMMUNICATION	PR	50	20	27	PC	13.	SYSTEM PROGRA.& OPERATING SYS.	PP	100	40	54	P C
04. NETWORK SYNTHESIS & FILTER DESI	GNPP	100	40	46	PC	14.	SYSTEM PROGRA.& OPERATING SYS.	TW	50	20	40	P C
05. NETWORK SYNTHESIS & FILTER DESI	GNTW	50	20	38	PC	15.	COMPUTER ORGANIZATION & ARCHITEC	PP	100	40	53	P C
06. MICROCONTROLLERS & APPLICATION	PP	100	40	51	PC	16.	INDUSTRIAL MANAGEMENT	PP	100	40	61	P C
07. MICROCONTROLLERS & APPLICATION	PR	50	20	30	PC	17.	WAVE THEORY & ANTENNA	PP	100	40	52	P C
08. DIGITAL SIGNAL PROCESSING	PP	100	_	51	PC		WAVE THEORY & ANTENNA	PR	50	20	22	P C
09. DIGITAL SIGNAL PROCESSING	OR		20	26	PC		MINI PROJECT & SEMINAR	OR			39	РС
10. ELECTRONIC DESIGN PRACTICE	OR		20	34	PC	20.	TEST & MEASUREMENT TECHNIQUES	OR	50	20	25	Р
GRAND TOTAL = $827/1500$, RESULT: HIGH	ER SEC	OND CL	ASS									
ORDN. 1 MARKS :												
T20052044 DECUDANDE CHATTANYA DUA							712009226				 -0005	
T80053044 DESHPANDE CHAITANYA DHA			40		WATI		, 71200832C , , ,	PIC	C I	,	т8005	3044
01. CONTROL SYSTEMS 02. DIGITAL COMMUNICATION	PP	100			P							
			40		Р							
03. DIGITAL COMMUNICATION	PR		20		Р							
04. NETWORK SYNTHESIS & FILTER DESI		100		64								
05. NETWORK SYNTHESIS & FILTER DESI			20		Р							
06. MICROCONTROLLERS & APPLICATION	PP		40 20	68 44								
07. MICROCONTROLLERS & APPLICATION	PR PP		_									
08. DIGITAL SIGNAL PROCESSING 09. DIGITAL SIGNAL PROCESSING	OR	100	20		P P							
	OR	50	20	34								
<pre>10. ELECTRONIC DESIGN PRACTICE FIRST TERM TOTAL = 523/750.</pre>	UK	30	20	34	r							
ORDN. 1 MARKS :												
T80053045 DESHPANDE KAUSTUBH SURY					ANIK		712000224	PI(T8005	
01. CONTROL SYSTEMS	PP	100	40	63			, , ,			,		
02. DIGITAL COMMUNICATION	PP	100			Р							
03. DIGITAL COMMUNICATION	PR		20	39								
04. NETWORK SYNTHESIS & FILTER DESI	GNPP		40	62								
05. NETWORK SYNTHESIS & FILTER DESI			20	36								
06. MICROCONTROLLERS & APPLICATION	PP	100	40	55								
07. MICROCONTROLLERS & APPLICATION	PR		20	30								
08. DIGITAL SIGNAL PROCESSING	PP	100		52								
09. DIGITAL SIGNAL PROCESSING	OR		20		Р							
10. ELECTRONIC DESIGN PRACTICE	OR	50	20		Р							
FIRST TERM TOTAL = 459/750.												
ORDN. 1 MARKS :												

UNIVERSITY	OF PUNE	,T.E.	(200	8 PAT	Γ.)(ELECTRO	ONICS & 7	TELEC.) EXAM	INATION	NOV. 201	13	
DATE : 11 MAR. 2014	CENT	RE : F	PUNE	INST	ITUTE OF CO	OMPUTER 1	ΓΕCHNOLOGY,	PUNE.		PAGE NO	. 16 (380)
NOTE: FIRST LINE : SEAT NO., NAME											SEAT NO.
OTHER LINES: HEAD OF PASSING								_			
											T90052046
T80053046 DESHPANDE NIKITA KISHO		100	40	55	EETA		, /1200834	·K ,		, PICT	, т80053046
<pre>01. CONTROL SYSTEMS 02. DIGITAL COMMUNICATION</pre>	PP	100									
03. DIGITAL COMMUNICATION	PR PR	50		46 30							
04. NETWORK SYNTHESIS & FILTER DES		100		59							
05. NETWORK SYNTHESIS & FILTER DES			20	33							
06. MICROCONTROLLERS & APPLICATION		100		56							
07. MICROCONTROLLERS & APPLICATION		50		31							
08. DIGITAL SIGNAL PROCESSING	PP	100		51							
	OR	50		32							
10. ELECTRONIC DESIGN PRACTICE	OR		20	22							
FIRST TERM TOTAL = 415/750.	OK	30	20	~ ~	1						
ORDN. 1 MARKS :											
T80053047 DESHPANDE SUBODH SANJA	YRAO			SU	JPRIYA		, 71200835	н,		, PICT	, т80053047
01. CONTROL SYSTEMS	PP	100	40	57			,	ŕ		•	,
02. DIGITAL COMMUNICATION	PP	100	40	43	Р						
03. DIGITAL COMMUNICATION	PR	50	20	32	Р						
04. NETWORK SYNTHESIS & FILTER DES	IGNPP	100	40	55	Р						
05. NETWORK SYNTHESIS & FILTER DES	IGNTW	50	20	36	Р						
06. MICROCONTROLLERS & APPLICATION	PP	100	40	50	Р						
07. MICROCONTROLLERS & APPLICATION	PR	50	20	33	Р						
08. DIGITAL SIGNAL PROCESSING	PP	100	40	40	Р						
09. DIGITAL SIGNAL PROCESSING	OR	50	20	38	Р						
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	37	Р						
FIRST TERM TOTAL = $421/750$.											
ORDN. 1 MARKS :											
T80053048 DHAMAL NAMRATA MOHAN					AVITA		, 71344353	Н ,		, PICT	, т80053048
01. CONTROL SYSTEMS	PP	100		74							
02. DIGITAL COMMUNICATION	PP	100		57							
03. DIGITAL COMMUNICATION	PR		20	35							
04. NETWORK SYNTHESIS & FILTER DES		100		55							
05. NETWORK SYNTHESIS & FILTER DES			20	43							
06. MICROCONTROLLERS & APPLICATION		100		71							
07. MICROCONTROLLERS & APPLICATION			20	40							
08. DIGITAL SIGNAL PROCESSING	PP	100		66							
09. DIGITAL SIGNAL PROCESSING	OR		20	38							
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	38	Р						
FIRST TERM TOTAL = 517/750.											
ORDN. 1 MARKS :											

DATE: 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 17 (381) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER T80053049 DHODI SHRADDHA MADHU , 71045415F , , , PICT , T80053049 ARUNA 01. CONTROL SYSTEMS 100 40 44 P 02. DIGITAL COMMUNICATION 100 40 23 F 03. DIGITAL COMMUNICATION 50 20 04 F PR 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 48 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 27 P 06. MICROCONTROLLERS & APPLICATION PP 100 40 32 F 05 F 07. MICROCONTROLLERS & APPLICATION PR 50 20 08. DIGITAL SIGNAL PROCESSING 100 40 40 P 09. DIGITAL SIGNAL PROCESSING 50 20 37 P OR 50 20 14 F 10. ELECTRONIC DESIGN PRACTICE OR FIRST TERM TOTAL = 274/750. ORDN. 1 MARKS: T80053050 DHORE PRASHANT SHRIKRISHNA RANJANA , 71200842L , , PICT , т80053050 01. CONTROL SYSTEMS 100 40 60 P 02. DIGITAL COMMUNICATION 100 40 31 F 03. DIGITAL COMMUNICATION 50 20 27 P 100 40 57 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 50 20 28 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 100 40 43 P 06. MICROCONTROLLERS & APPLICATION PP 50 20 30 P 07. MICROCONTROLLERS & APPLICATION PR 08. DIGITAL SIGNAL PROCESSING 100 40 54 P 09. DIGITAL SIGNAL PROCESSING 50 20 38 P OR 10. ELECTRONIC DESIGN PRACTICE OR 50 20 34 P FIRST TERM TOTAL = 402/750. ORDN. 1 MARKS: T80053051 DHOTEY RUTVIJ SUDHIR SANGITA , 71200843J , т80053051 , PICT 100 40 56 P 01. CONTROL SYSTEMS 02. DIGITAL COMMUNICATION 100 40 51 P PP 03. DIGITAL COMMUNICATION 50 20 33 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 64 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 39 P 06. MICROCONTROLLERS & APPLICATION 100 40 53 P 07. MICROCONTROLLERS & APPLICATION PR 50 20 30 P 100 40 61 P 08. DIGITAL SIGNAL PROCESSING 09. DIGITAL SIGNAL PROCESSING 50 20 34 P OR 10. ELECTRONIC DESIGN PRACTICE OR 50 20 38 P FIRST TERM TOTAL = 459/750. ORDN. 1 MARKS:

DATE: 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 18 (382) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71236310G , , PICT , T80053052 T80053052 DOLAS LUBDHA RAJU SUNITA 01. CONTROL SYSTEMS 100 40 57 P 02. DIGITAL COMMUNICATION 100 40 25 F 03. DIGITAL COMMUNICATION 50 20 23 P PR 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 51 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 34 P 06. MICROCONTROLLERS & APPLICATION PP 100 40 47 P 07. MICROCONTROLLERS & APPLICATION PR 50 20 05 F 08. DIGITAL SIGNAL PROCESSING 100 40 46 P 09. DIGITAL SIGNAL PROCESSING 50 20 32 P OR 50 20 32 P 10. ELECTRONIC DESIGN PRACTICE OR FIRST TERM TOTAL = 352/750. ORDN. 1 MARKS: T80053053 GAIKWAD GAYATRI DILIP DIPALI , 71100792G , , PICT , T80053053 01. CONTROL SYSTEMS 100 40 40 P 02. DIGITAL COMMUNICATION 100 40 30 F 03. DIGITAL COMMUNICATION 50 20 06 F 100 40 40 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 50 20 36 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 100 40 40 P 06. MICROCONTROLLERS & APPLICATION PP 50 20 26 P 07. MICROCONTROLLERS & APPLICATION PR 08. DIGITAL SIGNAL PROCESSING 100 40 47 P 50 20 28 P 09. DIGITAL SIGNAL PROCESSING OR 10. ELECTRONIC DESIGN PRACTICE 50 20 24 P FIRST TERM TOTAL = 317/750. ORDN. 1 MARKS: T80053054 GAIKWAD VAISHALI RAMESH SHITAL , 71344354F , T80053054 , PICT 01. CONTROL SYSTEMS 100 40 56 P 02. DIGITAL COMMUNICATION 100 40 41 P PP 03. DIGITAL COMMUNICATION 50 20 22 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 52 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 41 P 06. MICROCONTROLLERS & APPLICATION PP 100 40 54 P 07. MICROCONTROLLERS & APPLICATION PR 50 20 30 P 100 40 63 P 08. DIGITAL SIGNAL PROCESSING 09. DIGITAL SIGNAL PROCESSING 50 20 31 P OR 10. ELECTRONIC DESIGN PRACTICE OR 50 20 34 P FIRST TERM TOTAL = 424/750. ORDN. 1 MARKS:

DATE: 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 19 (383) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71200857J , , PICT , T80053055 T80053055 GANGAL AKSHAY PARAG RAJASHREE 01. CONTROL SYSTEMS 100 40 77 P 02. DIGITAL COMMUNICATION 100 40 57 P 03. DIGITAL COMMUNICATION 50 20 37 P PR 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 77 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 46 P 100 40 73 P 06. MICROCONTROLLERS & APPLICATION PP 07. MICROCONTROLLERS & APPLICATION PR 50 20 44 P 08. DIGITAL SIGNAL PROCESSING 100 40 76 P 50 20 40 P 09. DIGITAL SIGNAL PROCESSING OR 50 20 42 P 10. ELECTRONIC DESIGN PRACTICE OR FIRST TERM TOTAL = 569/750. ORDN. 1 MARKS: T80053056 GAVADE SACHIN SHIVAJI SNEHAL , 71100798F , T80053034 , PICT , T80053056 01. CONTROL SYSTEMS 100 40 49 P C 11. SIGNAL CODING & ESTIMATION THEORYPP 100 40 60 P C 02. DIGITAL COMMUNICATION 100 40 40 P C 12. SIGNAL CODING & ESTIMATION THEORYPR 50 20 20 P C 03. DIGITAL COMMUNICATION 50 20 30 P C 13. SYSTEM PROGRA. & OPERATING SYS. PP 100 40 46 P C PR 100 40 50 P C 50 20 34 P C 04. NETWORK SYNTHESIS & FILTER DESIGNPP 14. SYSTEM PROGRA. & OPERATING SYS. TW 50 20 39 P C 100 40 AA F 05. NETWORK SYNTHESIS & FILTER DESIGNTW 15. COMPUTER ORGANIZATION & ARCHITEC PP 100 40 48 P C 100 40 49 P C 06. MICROCONTROLLERS & APPLICATION PP 16. INDUSTRIAL MANAGEMENT 50 20 27 P C 100 40 41 P C 07. MICROCONTROLLERS & APPLICATION PR 17. WAVE THEORY & ANTENNA PP 08. DIGITAL SIGNAL PROCESSING 100 40 50 20 21 P C AA F 18. WAVE THEORY & ANTENNA 50 20 34 P C 50 20 38 P C 09. DIGITAL SIGNAL PROCESSING OR 19. MINI PROJECT & SEMINAR OR 50 20 36 P C 10. ELECTRONIC DESIGN PRACTICE OR 50 20 27 P C 20. TEST & MEASUREMENT TECHNIQUES OR GRAND TOTAL = 689/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS: T80053057 GENGAJE PRAJAKTA SHANTARAM SUREKHA , 71236312C , т80053057 , PICT 01. CONTROL SYSTEMS 100 40 49 P 02. DIGITAL COMMUNICATION 100 40 29 F 03. DIGITAL COMMUNICATION 50 20 08 F 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 29 F 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 41 P 06. MICROCONTROLLERS & APPLICATION PP 100 40 46 P 07. MICROCONTROLLERS & APPLICATION PR 50 20 21 P 100 40 08. DIGITAL SIGNAL PROCESSING 49 P 09. DIGITAL SIGNAL PROCESSING 50 20 25 P OR 10. ELECTRONIC DESIGN PRACTICE OR 50 20 26 P FIRST TERM TOTAL = 323/750. ORDN. 1 MARKS:

DATE: 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 20 (384) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71200863C , , PICT , T80053058 T80053058 GHADGE GITANJALI SUBHASH MANJUSHA 01. CONTROL SYSTEMS 100 40 53 P PP 02. DIGITAL COMMUNICATION 100 40 44 P 03. DIGITAL COMMUNICATION 50 20 40 P PR 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 58 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 41 P 100 40 50 P 06. MICROCONTROLLERS & APPLICATION PP 07. MICROCONTROLLERS & APPLICATION PR 50 20 35 P 08. DIGITAL SIGNAL PROCESSING 100 40 60 P 50 20 26 P 09. DIGITAL SIGNAL PROCESSING OR 10. ELECTRONIC DESIGN PRACTICE 50 20 28 P OR FIRST TERM TOTAL = 435/750. ORDN. 1 MARKS: T80053059 GHARDE SAGAR BHIMRAO SHARDA , 71236313M , T80053035 , PICT , T80053059 01. CONTROL SYSTEMS 100 40 40 P C 11. SIGNAL CODING & ESTIMATION THEORYPP 100 40 24 F 02. DIGITAL COMMUNICATION 100 40 48 P C 12. SIGNAL CODING & ESTIMATION THEORYPR 50 20 27 P C 03. DIGITAL COMMUNICATION 50 20 28 P C 13. SYSTEM PROGRA. & OPERATING SYS. PP 100 40 53 P C PR 100 40 41 P C 50 20 04. NETWORK SYNTHESIS & FILTER DESIGNPP 14. SYSTEM PROGRA. & OPERATING SYS. TW 39 P C 50 20 39 P C 100 40 41 P C 05. NETWORK SYNTHESIS & FILTER DESIGNTW 15. COMPUTER ORGANIZATION & ARCHITEC PP 100 40 57 P C 100 40 47 P C 06. MICROCONTROLLERS & APPLICATION PP 16. INDUSTRIAL MANAGEMENT 50 20 35 P C 100 40 40 P C 07. MICROCONTROLLERS & APPLICATION PR 17. WAVE THEORY & ANTENNA PP 08. DIGITAL SIGNAL PROCESSING 100 40 40 P C 50 20 35 P 18. WAVE THEORY & ANTENNA 50 20 22 P C 50 20 33 P C 09. DIGITAL SIGNAL PROCESSING OR 19. MINI PROJECT & SEMINAR OR 10. ELECTRONIC DESIGN PRACTICE OR 50 20 29 P C 20. TEST & MEASUREMENT TECHNIQUES OR 50 20 26 P C GRAND TOTAL = 744/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS: T80053060 GIRI PRATIK CHANDRAKANT SANDHYA GIRI , 71344355D , т80053060 , PICT 01. CONTROL SYSTEMS 100 40 81 P 02. DIGITAL COMMUNICATION 100 40 61 P PP 03. DIGITAL COMMUNICATION 50 20 42 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 53 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 45 P 06. MICROCONTROLLERS & APPLICATION PP 100 40 68 P 07. MICROCONTROLLERS & APPLICATION PR 50 20 35 P 100 40 62 P 08. DIGITAL SIGNAL PROCESSING 09. DIGITAL SIGNAL PROCESSING 50 20 38 P OR 10. ELECTRONIC DESIGN PRACTICE OR 50 20 38 P FIRST TERM TOTAL = 523/750. ORDN. 1 MARKS:

DATE: 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 21 (385) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71200866H , , , РІСТ , Т80053061 T80053061 GOKHALE SATYAJIT SHASHANK JAYASHREE 01. CONTROL SYSTEMS 100 40 50 P 02. DIGITAL COMMUNICATION 100 40 40 P 03. DIGITAL COMMUNICATION 50 20 43 P PR 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 52 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 41 P 06. MICROCONTROLLERS & APPLICATION PP 100 40 57 P 07. MICROCONTROLLERS & APPLICATION PR 50 20 36 P 08. DIGITAL SIGNAL PROCESSING 100 40 48 P 09. DIGITAL SIGNAL PROCESSING 50 20 33 P OR 50 20 36 P 10. ELECTRONIC DESIGN PRACTICE OR FIRST TERM TOTAL = 436/750. ORDN. 1 MARKS: T80053062 GOSAVI ABHISHEK MAHESH MANISHA , 71344356B , , PICT , т80053062 01. CONTROL SYSTEMS 100 40 82 P 02. DIGITAL COMMUNICATION 100 40 64 P 03. DIGITAL COMMUNICATION 50 20 43 P PR 100 40 59 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 50 20 37 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 100 40 06. MICROCONTROLLERS & APPLICATION PP 68 P 50 20 34 P 07. MICROCONTROLLERS & APPLICATION 08. DIGITAL SIGNAL PROCESSING 100 40 65 P 50 20 33 P 09. DIGITAL SIGNAL PROCESSING OR 10. ELECTRONIC DESIGN PRACTICE 50 20 32 P FIRST TERM TOTAL = 517/750. ORDN. 1 MARKS: T80053063 GUMBADE MAYURI RAGHUNATH , 71344357L , т80053063 LATA , PICT 100 40 57 P 01. CONTROL SYSTEMS 100 40 50 P 02. DIGITAL COMMUNICATION 03. DIGITAL COMMUNICATION 50 20 26 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 56 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 39 P 06. MICROCONTROLLERS & APPLICATION PP 100 40 61 P 07. MICROCONTROLLERS & APPLICATION PR 50 20 40 P 100 40 52 P 08. DIGITAL SIGNAL PROCESSING 09. DIGITAL SIGNAL PROCESSING 50 20 28 P OR 10. ELECTRONIC DESIGN PRACTICE OR 50 20 32 P FIRST TERM TOTAL = 441/750. ORDN. 1 MARKS:

UNIVERSITY OF PUNE ,T.E. (2008 PAT.) (ELECTRONICS & TELEC.) EXAMINATION NOV. 2013 DATE : 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 22 (386) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71200874J , , PICT , T80053064 T80053064 GUTTE KISHOR SHIVAJI USHABAI 01. CONTROL SYSTEMS 100 40 69 P 02. DIGITAL COMMUNICATION 100 40 49 P 03. DIGITAL COMMUNICATION 50 20 30 P PR 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 53 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 43 P 100 40 56 P 06. MICROCONTROLLERS & APPLICATION PP 07. MICROCONTROLLERS & APPLICATION PR 50 20 38 P 08. DIGITAL SIGNAL PROCESSING 100 40 66 P 50 20 36 P 09. DIGITAL SIGNAL PROCESSING OR 10. ELECTRONIC DESIGN PRACTICE 50 20 34 P OR FIRST TERM TOTAL = 474/750. ORDN. 1 MARKS: T80053065 HEGDE GAUTAMI GOPALKRISHNA APARNA , 71200880C , , PICT , т80053065 01. CONTROL SYSTEMS 100 40 71 P 02. DIGITAL COMMUNICATION 100 40 61 P 03. DIGITAL COMMUNICATION 50 20 36 P PR 100 40 04. NETWORK SYNTHESIS & FILTER DESIGNPP 71 P 50 20 41 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 100 40 67 P 06. MICROCONTROLLERS & APPLICATION PP 50 20 07. MICROCONTROLLERS & APPLICATION 31 P 08. DIGITAL SIGNAL PROCESSING 100 40 69 P 50 20 30 P 09. DIGITAL SIGNAL PROCESSING OR 10. ELECTRONIC DESIGN PRACTICE OR 50 20 42 P FIRST TERM TOTAL = 519/750. ORDN. 1 MARKS: T80053066 HIMANSHU JAIN , T80053039 , PICT RACHANA , 71100821D , т80053066 100 40 53 P C 11. SIGNAL CODING & ESTIMATION THEORYPP 100 40 51 P 01. CONTROL SYSTEMS 100 40 44 P C 50 20 32 P C 02. DIGITAL COMMUNICATION 12. SIGNAL CODING & ESTIMATION THEORYPR PP 03. DIGITAL COMMUNICATION 50 20 37 P C 13. SYSTEM PROGRA. & OPERATING SYS. PP 100 40 54 P C 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 42 P C 14. SYSTEM PROGRA. OPERATING SYS. TW 50 20 28 P C 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 25 P C 15. COMPUTER ORGANIZATION & ARCHITEC PP 100 40 50 P C 06. MICROCONTROLLERS & APPLICATION PP 100 40 56 P C 16. INDUSTRIAL MANAGEMENT 100 40 41 P C 07. MICROCONTROLLERS & APPLICATION PR 50 20 23 P C 17. WAVE THEORY & ANTENNA 100 40 46 P C PΡ 100 40 50 20 25 P C 08. DIGITAL SIGNAL PROCESSING 40 P C 18. WAVE THEORY & ANTENNA 09. DIGITAL SIGNAL PROCESSING 50 20 32 P C 19. MINI PROJECT & SEMINAR 50 20 34 P C OR OR 10. ELECTRONIC DESIGN PRACTICE OR 50 20 25 P C 20. TEST & MEASUREMENT TECHNIQUES OR 50 20 34 P C GRAND TOTAL = 772/1500, RESULT: SECOND CLASS ORDN. 1 MARKS:

DATE: 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 23 (387) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71200881M , , PICT , T80053067 T80053067 HINGANE SOURABH MADHUKAR MADHURI 01. CONTROL SYSTEMS 100 40 73 P PP 02. DIGITAL COMMUNICATION 100 40 47 P 03. DIGITAL COMMUNICATION 50 20 40 P PR 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 62 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 42 P 06. MICROCONTROLLERS & APPLICATION PP 100 40 59 P 07. MICROCONTROLLERS & APPLICATION PR 50 20 33 P 08. DIGITAL SIGNAL PROCESSING 100 40 59 P 09. DIGITAL SIGNAL PROCESSING 50 20 34 P OR 50 20 40 P 10. ELECTRONIC DESIGN PRACTICE OR FIRST TERM TOTAL = 489/750. ORDN. 1 MARKS: T80053068 HOWNI SAMAN WANN LAMBOK , т80053068 01. CONTROL SYSTEMS 100 40 46 P 02. DIGITAL COMMUNICATION 100 40 32 F 03. DIGITAL COMMUNICATION 50 20 20 P PR 100 40 29 F 04. NETWORK SYNTHESIS & FILTER DESIGNPP 50 20 28 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 100 40 21 F 06. MICROCONTROLLERS & APPLICATION PP 50 20 05 F 07. MICROCONTROLLERS & APPLICATION PR 08. DIGITAL SIGNAL PROCESSING 100 40 40 P 50 20 30 P 09. DIGITAL SIGNAL PROCESSING OR 10. ELECTRONIC DESIGN PRACTICE 50 20 12 F FIRST TERM TOTAL = 263/750. ORDN. 1 MARKS: T80053069 INGLE BHAVANA KRUSHNAJI SHAILA , 71344358J , т80053069 , PICT 100 40 67 P 01. CONTROL SYSTEMS 02. DIGITAL COMMUNICATION 100 40 41 P PP 03. DIGITAL COMMUNICATION 50 20 40 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 58 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 41 P 06. MICROCONTROLLERS & APPLICATION PP 100 40 70 P 07. MICROCONTROLLERS & APPLICATION PR 50 20 36 P 100 40 08. DIGITAL SIGNAL PROCESSING 61 P 09. DIGITAL SIGNAL PROCESSING 50 20 30 P OR 10. ELECTRONIC DESIGN PRACTICE OR 50 20 38 P FIRST TERM TOTAL = 482/750. ORDN. 1 MARKS:

DATE: 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 24 (388)

NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 70925442D , T80053040 , PICT T80053070 ISHTE PRAKASH BHAGVAN KAMAL , т80053070 01. CONTROL SYSTEMS 100 40 40 P C 11. SIGNAL CODING & ESTIMATION THEORYPP 100 40 PC 02. DIGITAL COMMUNICATION 100 40 40 P C 12. SIGNAL CODING & ESTIMATION THEORYPR 50 20 23 P 03. DIGITAL COMMUNICATION 50 20 20 P C 13. SYSTEM PROGRA. & OPERATING SYS. PP 100 40 43 P C PR 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 52 P C 14. SYSTEM PROGRA. OPERATING SYS. TW 50 20 28 P C 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 33 P C 15. COMPUTER ORGANIZATION & ARCHITEC PP 100 40 45 P C 100 40 40 P C 16. INDUSTRIAL MANAGEMENT 100 40 40 P C 06. MICROCONTROLLERS & APPLICATION PP PР 100 40 46 P C 07. MICROCONTROLLERS & APPLICATION PR 50 20 28 P C PΡ 17. WAVE THEORY & ANTENNA 08. DIGITAL SIGNAL PROCESSING 100 40 40 P C 18. WAVE THEORY & ANTENNA 50 20 22 P C PR 50 20 28 P C 50 20 21 P 09. DIGITAL SIGNAL PROCESSING 19. MINI PROJECT & SEMINAR OR OR 50 20 22 P C 50 20 22 P 10. ELECTRONIC DESIGN PRACTICE 20. TEST & MEASUREMENT TECHNIQUES OR OR GRAND TOTAL = 673/1500, RESULT: PASS CLASS ORDN. 1 MARKS: T80053071 JADHAV POONAM SANJAY SANGITA , T80053071 01. CONTROL SYSTEMS 100 40 55 P 02. DIGITAL COMMUNICATION 100 40 46 P 03. DIGITAL COMMUNICATION 50 20 40 P PR 100 40 52 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 50 20 40 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 100 40 06. MICROCONTROLLERS & APPLICATION PP 57 P 50 20 33 P 07. MICROCONTROLLERS & APPLICATION 100 40 40 P 08. DIGITAL SIGNAL PROCESSING 50 20 28 P 09. DIGITAL SIGNAL PROCESSING OR 10. ELECTRONIC DESIGN PRACTICE 50 20 38 P FIRST TERM TOTAL = 429/750. ORDN. 1 MARKS: T80053072 JAGTAP MANJIRI ARVIND , T80053041 , PICT SHUBHANGI , 71100825G , т80053072 100 40 43 P C 100 40 40 P C 01. CONTROL SYSTEMS 11. SIGNAL CODING & ESTIMATION THEORYPP 50 20 30 P C 02. DIGITAL COMMUNICATION 100 40 25 F 12. SIGNAL CODING & ESTIMATION THEORYPR PΡ 03. DIGITAL COMMUNICATION 50 20 21 P C 13. SYSTEM PROGRA. & OPERATING SYS. PP 100 40 43 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 52 P C 14. SYSTEM PROGRA. OPERATING SYS. TW 50 20 34 P C 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 36 P C 15. COMPUTER ORGANIZATION & ARCHITEC PP 100 40 49 P C 06. MICROCONTROLLERS & APPLICATION PP 100 40 46 P C 16. INDUSTRIAL MANAGEMENT 100 40 44 P C 07. MICROCONTROLLERS & APPLICATION PR 50 20 25 P C 17. WAVE THEORY & ANTENNA 100 40 50 P C PP 100 40 50 20 28 P C 08. DIGITAL SIGNAL PROCESSING 43 P C 18. WAVE THEORY & ANTENNA 09. DIGITAL SIGNAL PROCESSING 50 20 26 P C 19. MINI PROJECT & SEMINAR 50 20 37 P C OR OR 10. ELECTRONIC DESIGN PRACTICE OR 50 20 22 P C 20. TEST & MEASUREMENT TECHNIQUES OR 50 20 20 P C GRAND TOTAL = 714/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS:

DATE: 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 25 (389) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER T80053073 JAGTAP SHITAL LAXMAN LATA , 71344360L , , PICT , T80053073 01. CONTROL SYSTEMS 100 40 65 P 02. DIGITAL COMMUNICATION 100 40 44 P 03. DIGITAL COMMUNICATION 50 20 37 P PR 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 61 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 43 P 06. MICROCONTROLLERS & APPLICATION PP 100 40 62 P 50 20 35 P 07. MICROCONTROLLERS & APPLICATION PR 08. DIGITAL SIGNAL PROCESSING 100 40 60 P 09. DIGITAL SIGNAL PROCESSING 50 20 31 P OR 10. ELECTRONIC DESIGN PRACTICE 50 20 36 P OR FIRST TERM TOTAL = 474/750. ORDN. 1 MARKS: T80053074 JAIN KAJAL HEMANTKUMAR RAJANI , 71200887L , , PICT , т80053074 01. CONTROL SYSTEMS 100 40 71 P 02. DIGITAL COMMUNICATION 100 40 52 P 03. DIGITAL COMMUNICATION 50 20 32 P PR 100 40 56 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 50 20 36 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 100 40 53 P 06. MICROCONTROLLERS & APPLICATION PP 50 20 21 P 07. MICROCONTROLLERS & APPLICATION 08. DIGITAL SIGNAL PROCESSING 100 40 58 P 50 20 30 P 09. DIGITAL SIGNAL PROCESSING OR 10. ELECTRONIC DESIGN PRACTICE OR 50 20 29 P FIRST TERM TOTAL = 438/750. ORDN. 1 MARKS: T80053075 JAIN KRUNAL LALIT MANJU , 71200888J , т80053075 , PICT 100 40 64 P 01. CONTROL SYSTEMS 100 40 02. DIGITAL COMMUNICATION 63 P PP 03. DIGITAL COMMUNICATION 50 20 44 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 53 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 42 P 06. MICROCONTROLLERS & APPLICATION 100 40 63 P 07. MICROCONTROLLERS & APPLICATION PR 50 20 40 P 100 40 71 P 08. DIGITAL SIGNAL PROCESSING 09. DIGITAL SIGNAL PROCESSING 50 20 40 P OR 10. ELECTRONIC DESIGN PRACTICE OR 50 20 36 P FIRST TERM TOTAL = 516/750. ORDN. 1 MARKS:

UNIVERSITY OF	F PUNE	. T.E.	(200	8 PAT	Γ.)(ELE	ECTRONICS	& TELEC.)	EXAMINA	ATION NOV. 2	013	
DATE : 11 MAR. 2014	CENT	TRE : P	UNE	INST	ITUTE (OF COMPUT	ER TECHNOL	OGY, PUN	NE.	PAGE	NO. 26 (390)
NOTE: FIRST LINE : SEAT NO., NAME (OF THE	E CANDI	DATE	, MC	OTHER,	PERMANEN	T REG. NO.	, PREVI	OUS SEAT NO	., COLLEGE,	SEAT NO.
OTHER LINES: HEAD OF PASSING,	MAX.	. MARKS	, M	IN. F	PASS MA	ARKS, MA	RKS OBTAIN	ED, P/F	F:PASS/FAIL,	C:PREVIOUS	CARRY OVER
T80053076 JAVANJAL MAUSAMI SHIVSH	ANKAR				ANDHYA		, 713	44361J	,	, PICT	, т80053076
01. CONTROL SYSTEMS	PP	100		65							
02. DIGITAL COMMUNICATION	PP	100	40	54							
03. DIGITAL COMMUNICATION	PR	50	20	29							
04. NETWORK SYNTHESIS & FILTER DESIG	GNPP	100	40	57							
05. NETWORK SYNTHESIS & FILTER DESIG	GNTW	50	20	36							
06. MICROCONTROLLERS & APPLICATION	PP	100	40	70	Р						
07. MICROCONTROLLERS & APPLICATION	PR	50	20	39	Р						
08. DIGITAL SIGNAL PROCESSING	PP	100	40	59	Р						
09. DIGITAL SIGNAL PROCESSING	OR	50	20	36	Р						
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	30	Р						
FIRST TERM TOTAL = $475/750$.											
ORDN. 1 MARKS :											
T80053077 JAWALIKAR AMEY RAVINDRA				N]	IRMALA	RAVINDRA	JA , 713	44362G	,	, PICT	, т80053077
01. CONTROL SYSTEMS	PP	100	40	42	Р						
02. DIGITAL COMMUNICATION	PP	100	40	AA	F						
03. DIGITAL COMMUNICATION	PR	50	20	28	Р						
04. NETWORK SYNTHESIS & FILTER DESIG	GNPP	100	40	40	Р						
05. NETWORK SYNTHESIS & FILTER DESIG	GNTW	50	20	33	Р						
06. MICROCONTROLLERS & APPLICATION	PP	100	40	48	Р						
07. MICROCONTROLLERS & APPLICATION	PR	50	20	35	Р						
08. DIGITAL SIGNAL PROCESSING	PP	100	40	48	Р						
09. DIGITAL SIGNAL PROCESSING	OR	50	20	38	Р						
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	36	Р						
FIRST TERM TOTAL = 348/750.											
ORDN. 1 MARKS :											
T80053078 JOSHI NEEL RAJEEV				P/	ADMAJA		, 712	00894c	,	, PICT	, т80053078
01. CONTROL SYSTEMS	PP	100	40	58	Р		•		•	·	·
02. DIGITAL COMMUNICATION	PP	100	40	44	Р						
03. DIGITAL COMMUNICATION	PR	50	20	44							
04. NETWORK SYNTHESIS & FILTER DESIG		100		56							
05. NETWORK SYNTHESIS & FILTER DESIG		50		38							
06. MICROCONTROLLERS & APPLICATION	PP	100		55							
07. MICROCONTROLLERS & APPLICATION	PR		20	39							
08. DIGITAL SIGNAL PROCESSING	PP	100		66							
09. DIGITAL SIGNAL PROCESSING	OR		20	39							
10. ELECTRONIC DESIGN PRACTICE	OR		20	42							
FIRST TERM TOTAL = 481/750.	OIX.	30	20	12	•						
ORDN. 1 MARKS :											
CADITI I PINAMO I											

DATE: 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 27 (391) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER T80053079 JOSHI NIKITA SANJAY , 71200895M , , PICT , T80053079 MADHAVI 01. CONTROL SYSTEMS 100 40 40 P 02. DIGITAL COMMUNICATION 100 40 40 P 03. DIGITAL COMMUNICATION 50 20 32 P PR 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 47 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 41 P 06. MICROCONTROLLERS & APPLICATION PP 100 40 43 P 07. MICROCONTROLLERS & APPLICATION PR 50 20 23 P 08. DIGITAL SIGNAL PROCESSING 100 40 48 P 09. DIGITAL SIGNAL PROCESSING 50 20 38 P OR 50 20 28 P 10. ELECTRONIC DESIGN PRACTICE OR FIRST TERM TOTAL = 380/750. ORDN. 1 MARKS: T80053080 KADAM AKSHAY VITTHALRAO LALITA , 71200898F , , PICT , т80053080 01. CONTROL SYSTEMS 100 40 85 P 02. DIGITAL COMMUNICATION 100 40 63 P 03. DIGITAL COMMUNICATION 50 20 40 P 100 40 55 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 50 20 45 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 100 40 64 P 06. MICROCONTROLLERS & APPLICATION PP 50 20 38 P 07. MICROCONTROLLERS & APPLICATION 08. DIGITAL SIGNAL PROCESSING 100 40 68 P 09. DIGITAL SIGNAL PROCESSING 50 20 38 P OR 10. ELECTRONIC DESIGN PRACTICE OR 50 20 40 P FIRST TERM TOTAL = 536/750. ORDN. 1 MARKS: T80053081 KALE PRASHANT SUBHASH SHARDA , 71200902н , T80053081 , PICT 100 40 51 P 01. CONTROL SYSTEMS 50 P 02. DIGITAL COMMUNICATION 100 40 PP 03. DIGITAL COMMUNICATION 50 20 31 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 57 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 37 P 06. MICROCONTROLLERS & APPLICATION 100 40 69 P 07. MICROCONTROLLERS & APPLICATION PR 50 20 33 P 100 40 57 P 08. DIGITAL SIGNAL PROCESSING 09. DIGITAL SIGNAL PROCESSING 50 20 36 P OR 10. ELECTRONIC DESIGN PRACTICE OR 50 20 22 P FIRST TERM TOTAL = 443/750. ORDN. 1 MARKS:

DATE: 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 28 (392)

NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER KAMAL , 71236314K , T80053048 , PICT , T80053082 T80053082 KAMBLE DASHARATH SATAPPA 01. CONTROL SYSTEMS 100 40 50 P C 11. SIGNAL CODING & ESTIMATION THEORYPP 100 40 PC PP 02. DIGITAL COMMUNICATION 100 40 40 P C 12. SIGNAL CODING & ESTIMATION THEORYPR 50 20 24 P 03. DIGITAL COMMUNICATION 50 20 28 P C 13. SYSTEM PROGRA.& OPERATING SYS. PP 100 40 58 P C PR 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 55 P C 14. SYSTEM PROGRA.& OPERATING SYS. TW 50 20 40 P C 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 42 P C 15. COMPUTER ORGANIZATION & ARCHITEC PP 100 40 53 P C 100 40 62 P C 16. INDUSTRIAL MANAGEMENT 100 40 58 P C 06. MICROCONTROLLERS & APPLICATION PP PР 25 P C 100 40 59 P C 07. MICROCONTROLLERS & APPLICATION PR 50 20 17. WAVE THEORY & ANTENNA PP 08. DIGITAL SIGNAL PROCESSING 100 40 45 P C 18. WAVE THEORY & ANTENNA 50 20 35 P C PR 09. DIGITAL SIGNAL PROCESSING 50 20 25 P C 50 20 41 P C OR 19. MINI PROJECT & SEMINAR OR 50 20 25 P C 50 20 34 P C 10. ELECTRONIC DESIGN PRACTICE 20. TEST & MEASUREMENT TECHNIQUES OR OR GRAND TOTAL = 839/1500, RESULT: HIGHER SECOND CLASS ORDN. 1 MARKS: T80053083 KAMBLE PRATIK RAVINDRA SUMAN , 71100845M , T80053049 , PICT , T80053083 01. CONTROL SYSTEMS 100 40 54 P C 11. SIGNAL CODING & ESTIMATION THEORYPP 100 40 40 P C 100 40 45 P C 12. SIGNAL CODING & ESTIMATION THEORYPR 50 20 23 P 02. DIGITAL COMMUNICATION 03. DIGITAL COMMUNICATION 50 20 30 P C 13. SYSTEM PROGRA. & OPERATING SYS. PP 100 40 66 P C PR 100 40 58 P C 50 20 27 P C 04. NETWORK SYNTHESIS & FILTER DESIGNPP 14. SYSTEM PROGRA.& OPERATING SYS. TW 50 20 26 P C 100 40 51 P C 05. NETWORK SYNTHESIS & FILTER DESIGNTW 15. COMPUTER ORGANIZATION & ARCHITEC PP 100 40 48 P C 100 40 47 P C 06. MICROCONTROLLERS & APPLICATION PP 16. INDUSTRIAL MANAGEMENT 50 20 27 P C 100 40 54 P C 07. MICROCONTROLLERS & APPLICATION PR 17. WAVE THEORY & ANTENNA PP 08. DIGITAL SIGNAL PROCESSING 100 40 50 20 24 P C 44 P C 18. WAVE THEORY & ANTENNA 50 20 33 P C 50 20 36 P C 09. DIGITAL SIGNAL PROCESSING OR 19. MINI PROJECT & SEMINAR OR 50 20 28 P C 10. ELECTRONIC DESIGN PRACTICE OR 50 20 22 P C 20. TEST & MEASUREMENT TECHNIQUES OR GRAND TOTAL = 783/1500, RESULT: SECOND CLASS ORDN. 1 MARKS: T80053084 KAMBLE SANJEEVANI RAMESH JAYASHREE , 71200908G , PICT , т80053084 01. CONTROL SYSTEMS 100 40 40 P 02. DIGITAL COMMUNICATION 100 40 32 F 03. DIGITAL COMMUNICATION 50 20 20 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 49 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 42 P 06. MICROCONTROLLERS & APPLICATION PP 100 40 40 P 07. MICROCONTROLLERS & APPLICATION PR 50 20 20 P 100 40 08. DIGITAL SIGNAL PROCESSING 40 P 09. DIGITAL SIGNAL PROCESSING 50 20 34 P OR 10. ELECTRONIC DESIGN PRACTICE OR 50 20 22 P FIRST TERM TOTAL = 339/750. ORDN. 1 MARKS:

DATE: 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 29 (393) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER SAVITRI , 71200909E , , PICT , T80053085 T80053085 KAMBLE VIJAY SANJAY 01. CONTROL SYSTEMS 100 40 47 P 02. DIGITAL COMMUNICATION 100 40 65 P 03. DIGITAL COMMUNICATION 50 20 40 P PR 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 55 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 38 P 06. MICROCONTROLLERS & APPLICATION PP 100 40 58 P 50 20 28 P 07. MICROCONTROLLERS & APPLICATION PR 08. DIGITAL SIGNAL PROCESSING 100 40 54 P 09. DIGITAL SIGNAL PROCESSING 50 20 33 P OR 50 20 34 P 10. ELECTRONIC DESIGN PRACTICE OR FIRST TERM TOTAL = 452/750. ORDN. 1 MARKS: T80053086 KAPRE SURAJ DNYANOBA JAYSHREE , т80053086 01. CONTROL SYSTEMS 100 40 61 P 02. DIGITAL COMMUNICATION 100 40 29 F 03. DIGITAL COMMUNICATION 50 20 36 P PR 100 40 55 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 50 20 39 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 100 40 50 P 06. MICROCONTROLLERS & APPLICATION PP 50 20 28 P 07. MICROCONTROLLERS & APPLICATION 08. DIGITAL SIGNAL PROCESSING 100 40 57 P 09. DIGITAL SIGNAL PROCESSING 50 20 37 P OR 10. ELECTRONIC DESIGN PRACTICE OR 50 20 30 P FIRST TERM TOTAL = 422/750. ORDN. 1 MARKS: T80053087 KASAR SNEHAL NIVRUTTI SUREKHA , 71212196L , т80053087 , PICT 100 40 66 P 01. CONTROL SYSTEMS 100 40 02. DIGITAL COMMUNICATION 63 P PP 03. DIGITAL COMMUNICATION 50 20 36 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 72 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 37 P 06. MICROCONTROLLERS & APPLICATION 100 40 59 P 07. MICROCONTROLLERS & APPLICATION PR 50 20 25 P 100 40 56 P 08. DIGITAL SIGNAL PROCESSING 09. DIGITAL SIGNAL PROCESSING 50 20 35 P OR 10. ELECTRONIC DESIGN PRACTICE OR 50 20 31 P FIRST TERM TOTAL = 480/750. ORDN. 1 MARKS:

DATE: 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 30 (394) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71200915K , , PICT , T80053088 T80053088 KASLIKAR RASHMI MILIND SWATI 01. CONTROL SYSTEMS 100 40 67 P 02. DIGITAL COMMUNICATION 100 40 52 P 03. DIGITAL COMMUNICATION 50 20 30 P PR 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 55 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 37 P 06. MICROCONTROLLERS & APPLICATION PP 100 40 60 P 07. MICROCONTROLLERS & APPLICATION PR 50 20 33 P 08. DIGITAL SIGNAL PROCESSING 100 40 47 P 09. DIGITAL SIGNAL PROCESSING 50 20 36 P OR 50 20 32 P 10. ELECTRONIC DESIGN PRACTICE OR FIRST TERM TOTAL = 449/750. ORDN. 1 MARKS: T80053089 KHAIRE SHUBHAM SUNIL SANGITA , 71200921D , , PICT , т80053089 01. CONTROL SYSTEMS 100 40 67 P 02. DIGITAL COMMUNICATION 100 40 58 P 03. DIGITAL COMMUNICATION 50 20 32 P PR 100 40 71 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 50 20 37 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 100 40 41 P 06. MICROCONTROLLERS & APPLICATION PP 50 20 25 P 07. MICROCONTROLLERS & APPLICATION 08. DIGITAL SIGNAL PROCESSING 100 40 59 P 09. DIGITAL SIGNAL PROCESSING 50 20 38 P OR 10. ELECTRONIC DESIGN PRACTICE OR 50 20 22 P FIRST TERM TOTAL = 450/750. ORDN. 1 MARKS: T80053091 KORANNE RENUKA SUNIL SHUBHADA KORANNE , 71344363E , T80053091 , PICT 01. CONTROL SYSTEMS 100 40 56 P 100 40 40 P 02. DIGITAL COMMUNICATION PP 03. DIGITAL COMMUNICATION 50 20 37 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 59 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 38 P 06. MICROCONTROLLERS & APPLICATION 100 40 55 P 07. MICROCONTROLLERS & APPLICATION PR 50 20 36 P 100 40 45 P 08. DIGITAL SIGNAL PROCESSING 09. DIGITAL SIGNAL PROCESSING 50 20 39 P OR 10. ELECTRONIC DESIGN PRACTICE OR 50 20 43 P FIRST TERM TOTAL = 448/750. ORDN. 1 MARKS:

UNIVERSITY OF	PUNE	,T.E.	(200)8 PAT	T.)(EL	ECTRONICS & TELEC.)	EXAMINATION NOV. 20	13	
DATE : 11 MAR. 2014	CENT	RE : F	UNE	INST	TUTE	OF COMPUTER TECHNOLOG	GY, PUNE.	PAGE N	0. 31 (395)
NOTE: FIRST LINE : SEAT NO., NAME (
OTHER LINES: HEAD OF PASSING,							D, P/F:PASS/FAIL,	C:PREVIOUS	CARRY OVER
T80053092 KOTHARI GAURAO RAJENDRA			•		 IJAYA	, 71200			
01. CONTROL SYSTEMS	PP	100	40	55		, 71200	J932K ,	, PICT	, 180033032
02. DIGITAL COMMUNICATION	PP	100	40	66					
03. DIGITAL COMMUNICATION	PR	50	20	42					
04. NETWORK SYNTHESIS & FILTER DESIG		100	40	58					
05. NETWORK SYNTHESIS & FILTER DESIG		50	20	41					
06. MICROCONTROLLERS & APPLICATION	PP	100	40	59					
07. MICROCONTROLLERS & APPLICATION	PR	50	20	32					
08. DIGITAL SIGNAL PROCESSING	PP	100	40	58					
09. DIGITAL SIGNAL PROCESSING	OR	50	20	38					
10. ELECTRONIC DESIGN PRACTICE	OR	50		33					
FIRST TERM TOTAL = 482/750.	OK	30	20	33	'				
ORDN. 1 MARKS :									
ORDIT I PIARRO .									
T80053093 KRITI KESARWANI			•	 ΔΙ	 .KA	, 71200		, PICT	, т80053093
01. CONTROL SYSTEMS	PP	100	40	60		, 71200	,	,	, 100033033
02. DIGITAL COMMUNICATION	PP	100	_	51					
03. DIGITAL COMMUNICATION	PR	50	20	35					
04. NETWORK SYNTHESIS & FILTER DESIG		100		74					
05. NETWORK SYNTHESIS & FILTER DESIG		50	20	39					
06. MICROCONTROLLERS & APPLICATION	PP	100	40	55					
07. MICROCONTROLLERS & APPLICATION	PR	50	20	28					
08. DIGITAL SIGNAL PROCESSING	PP	100		57					
09. DIGITAL SIGNAL PROCESSING	OR		20						
	OR		20						
FIRST TERM TOTAL = 466/750.	OK	30	20	20	Г				
ORDN. 1 MARKS :									
ORDIN. I MARKS .									
T80053094 KULKARNI PARIMAL SHRIKRI	 ΓSHNΔ		•	 V2	 SUDHA			, PICT	, т80053094
01. CONTROL SYSTEMS	PP	100	40	71		, , , , , , , , , , , , , , , , , , , ,	,	,	, 100033031
02. DIGITAL COMMUNICATION	PP	100		68					
03. DIGITAL COMMUNICATION	PR	50	20	38					
04. NETWORK SYNTHESIS & FILTER DESIG		100		77					
05. NETWORK SYNTHESIS & FILTER DESIG		50		41					
06. MICROCONTROLLERS & APPLICATION	PP	100		50					
07. MICROCONTROLLERS & APPLICATION	PR		20	26					
08. DIGITAL SIGNAL PROCESSING	PP	100		59					
09. DIGITAL SIGNAL PROCESSING	OR		20	40					
10. ELECTRONIC DESIGN PRACTICE	OR		20						
FIRST TERM TOTAL = 504/750.	JI	30	20	74	•				
ORDN. 1 MARKS :									
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UNIVERSITY O	F PUNE	,T.E.	(200	8 PA	T.)(ELECTRO	ONICS &	TELEC.) EXAM	NATION NO	ov. 2013	3	
DATE : 11 MAR. 2014	CENT	RE : P	PUNE	INST	ITUTE OF CO	OMPUTER	TECHNOLOGY, F	PUNE.		PAGE NO	. 32 (396)
NOTE: FIRST LINE : SEAT NO., NAME OTHER LINES: HEAD OF PASSING,	OF THE	CANDI	DATE	, MC	OTHER, PERM	MANENT R	EG. NO., PRE	EVIOUS SEA	AT NO.,	COLLEGE,	
											T90052005
T80053095 KULKARNI VISHAL VINAYAK		100	40		ARSHA		, 712009390	,		, PICT	, т80053095
<pre>01. CONTROL SYSTEMS 02. DIGITAL COMMUNICATION</pre>	PP DD	100			P P						
03. DIGITAL COMMUNICATION	PR PR	50			P						
04. NETWORK SYNTHESIS & FILTER DESI		100			P						
05. NETWORK SYNTHESIS & FILTER DESI			20	42							
06. MICROCONTROLLERS & APPLICATION	PP	100			P						
07. MICROCONTROLLERS & APPLICATION	PR	50			P						
08. DIGITAL SIGNAL PROCESSING	PP	100			' P						
	OR	50		37							
	OR		20	_	' P						
FIRST TERM TOTAL = 517/750.	J.(30	_0		•						
ORDN. 1 MARKS :											
T80053096 LADIKKAR CHAITALI AVINA					EETA		, 712009420	i ,		, PICT	, т80053096
01. CONTROL SYSTEMS	PP	100	40	47	Р						
02. DIGITAL COMMUNICATION	PP	100	40	50	Р						
03. DIGITAL COMMUNICATION	PR	50	20	33	Р						
04. NETWORK SYNTHESIS & FILTER DESI	GNPP	100	40	58	Р						
05. NETWORK SYNTHESIS & FILTER DESI	GNTW	50	20	43	Р						
06. MICROCONTROLLERS & APPLICATION	PP	100	40	52	Р						
07. MICROCONTROLLERS & APPLICATION	PR	50	20	39	Р						
08. DIGITAL SIGNAL PROCESSING	PP	100	40	58	Р						
09. DIGITAL SIGNAL PROCESSING	OR	50	20	38	Р						
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	40	Р						
FIRST TERM TOTAL = $458/750$.											
ORDN. 1 MARKS :											
T80053097 LASRADO VINOD WILFRED	00	100	40		ARIA		, 712009440	,		, PICT	, т80053097
01. CONTROL SYSTEMS	PP	100		62							
02. DIGITAL COMMUNICATION	PP	100			P						
03. DIGITAL COMMUNICATION	PR		20		P						
04. NETWORK SYNTHESIS & FILTER DESI		100		81							
05. NETWORK SYNTHESIS & FILTER DESI			20	43							
06. MICROCONTROLLERS & APPLICATION	PP DD	100		63 41							
07. MICROCONTROLLERS & APPLICATION	PR DD	100	20 40	41 70							
08. DIGITAL SIGNAL PROCESSING 09. DIGITAL SIGNAL PROCESSING	PP OR		20	70 39	P P						
10. ELECTRONIC DESIGN PRACTICE	OR OR		20	45							
FIRST TERM TOTAL = 560/750.	UK	30	20	+3	г						
ORDN. 1 MARKS :											
ONDIA: I MANNO .											

DATE: 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 33 (397) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71200945M , , PICT , T80053098 T80053098 LOHIYA KRISHNA SUBHASH SUNITA 01. CONTROL SYSTEMS 100 40 32 F 02. DIGITAL COMMUNICATION 100 40 31 F 03. DIGITAL COMMUNICATION 50 20 25 P PR 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 40 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 31 P 06. MICROCONTROLLERS & APPLICATION PP 100 40 40 P 07. MICROCONTROLLERS & APPLICATION PR 50 20 05 F 08. DIGITAL SIGNAL PROCESSING 100 40 45 P 09. DIGITAL SIGNAL PROCESSING 50 20 39 P OR 50 20 12 F 10. ELECTRONIC DESIGN PRACTICE OR FIRST TERM TOTAL = 300/750. ORDN. 1 MARKS: T80053099 MANE MADHURI DILIP SHEELA , 71200954L , , PICT , т80053099 01. CONTROL SYSTEMS 100 40 79 P 02. DIGITAL COMMUNICATION 100 40 59 P 03. DIGITAL COMMUNICATION 50 20 38 P PR 100 40 80 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 50 20 46 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 100 40 68 P 06. MICROCONTROLLERS & APPLICATION PP 50 20 31 P 07. MICROCONTROLLERS & APPLICATION 08. DIGITAL SIGNAL PROCESSING 100 40 69 P 50 20 37 P 09. DIGITAL SIGNAL PROCESSING OR 10. ELECTRONIC DESIGN PRACTICE OR 50 20 32 P FIRST TERM TOTAL = 539/750. ORDN. 1 MARKS: T80053100 MANU MAKKAR , 71200958C , T80053100 INDU , PICT 100 40 49 P 01. CONTROL SYSTEMS 100 40 46 P 02. DIGITAL COMMUNICATION PP 03. DIGITAL COMMUNICATION 50 20 35 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 67 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 37 P 06. MICROCONTROLLERS & APPLICATION PP 100 40 56 P 07. MICROCONTROLLERS & APPLICATION PR 50 20 04 F 100 40 57 P 08. DIGITAL SIGNAL PROCESSING 09. DIGITAL SIGNAL PROCESSING 50 20 38 P OR 10. ELECTRONIC DESIGN PRACTICE OR 50 20 42 P FIRST TERM TOTAL = 431/750. ORDN. 1 MARKS:

DATE: 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 34 (398) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER SANGITA , 71200960E , , PICT , T80053101 T80053101 MHETRE ROSHANEE RAJENDRA 01. CONTROL SYSTEMS 100 40 52 P PP 02. DIGITAL COMMUNICATION 100 40 58 P 03. DIGITAL COMMUNICATION 50 20 32 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 62 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 44 P 06. MICROCONTROLLERS & APPLICATION PP 100 40 52 P 50 20 25 P 07. MICROCONTROLLERS & APPLICATION PR 08. DIGITAL SIGNAL PROCESSING 100 40 68 P 09. DIGITAL SIGNAL PROCESSING 50 20 38 P OR 50 20 30 P 10. ELECTRONIC DESIGN PRACTICE OR FIRST TERM TOTAL = 461/750. ORDN. 1 MARKS: , т80053102 T80053102 MIHIR BIDWAI URMILA , 71200961C , , PICT 01. CONTROL SYSTEMS 100 40 53 P 02. DIGITAL COMMUNICATION 100 40 40 P 03. DIGITAL COMMUNICATION 50 20 22 P PR 100 40 50 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 50 20 24 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 100 40 21 F 06. MICROCONTROLLERS & APPLICATION PP 50 20 28 P 07. MICROCONTROLLERS & APPLICATION PR 08. DIGITAL SIGNAL PROCESSING 100 40 60 P 09. DIGITAL SIGNAL PROCESSING 50 20 34 P OR 10. ELECTRONIC DESIGN PRACTICE OR 50 20 22 P FIRST TERM TOTAL = 354/750. ORDN. 1 MARKS: T80053103 MUNNA KUMAR URMILADEVI , 71200970в , т80053103 , PICT 01. CONTROL SYSTEMS 100 40 49 P 100 40 53 P 02. DIGITAL COMMUNICATION PΡ 03. DIGITAL COMMUNICATION 50 20 27 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 68 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 32 P 06. MICROCONTROLLERS & APPLICATION PP 100 40 40 P 07. MICROCONTROLLERS & APPLICATION PR 50 20 29 P 100 40 60 P 08. DIGITAL SIGNAL PROCESSING 09. DIGITAL SIGNAL PROCESSING 50 20 37 P OR 10. ELECTRONIC DESIGN PRACTICE OR 50 20 20 P FIRST TERM TOTAL = 415/750. ORDN. 1 MARKS:

UNIVERSITY OF PUNE ,T.E.(2008 PAT.)(ELECTRONICS & TELEC.) EXAMINATION NOV. 2013 DATE : 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 35 (399) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71236321B , T80053071 , PICT T80053104 NADEKAR SANJIV CHANDRAKANT ALKA , т80053104 01. CONTROL SYSTEMS 100 40 60 P C 11. SIGNAL CODING & ESTIMATION THEORYPP 100 40 57 P C 02. DIGITAL COMMUNICATION 100 40 44 P C 12. SIGNAL CODING & ESTIMATION THEORYPR 50 20 32 P C 03. DIGITAL COMMUNICATION 50 20 22 P C 13. SYSTEM PROGRA. & OPERATING SYS. PP 100 40 58 P C PR 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 50 P C 14. SYSTEM PROGRA. & OPERATING SYS. TW 50 20 37 P C 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 40 P C 15. COMPUTER ORGANIZATION & ARCHITEC PP 100 40 51 P C 100 40 44 P C 16. INDUSTRIAL MANAGEMENT 100 40 48 P 06. MICROCONTROLLERS & APPLICATION PP 100 40 47 P C 07. MICROCONTROLLERS & APPLICATION PR 50 20 34 P C 17. WAVE THEORY & ANTENNA PP 08. DIGITAL SIGNAL PROCESSING 100 40 55 P C 18. WAVE THEORY & ANTENNA 50 20 40 P PR 09. DIGITAL SIGNAL PROCESSING 50 20 35 P C 50 20 40 P C OR 19. MINI PROJECT & SEMINAR OR 50 20 27 P C 50 20 21 P C 10. ELECTRONIC DESIGN PRACTICE 20. TEST & MEASUREMENT TECHNIQUES OR OR GRAND TOTAL = 842/1500, RESULT: HIGHER SECOND CLASS ORDN. 1 MARKS: T80053105 NAGARE PRERANA EKNATH PUSHPA , 71200975C , , PICT , т80053105 73 P 01. CONTROL SYSTEMS 100 40 02. DIGITAL COMMUNICATION 100 40 52 P 03. DIGITAL COMMUNICATION 50 20 20 P PR 100 40 79 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 50 20 41 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 100 40 55 P 06. MICROCONTROLLERS & APPLICATION PP 50 20 25 P 07. MICROCONTROLLERS & APPLICATION PR 08. DIGITAL SIGNAL PROCESSING 100 40 50 P 50 20 38 P 09. DIGITAL SIGNAL PROCESSING OR 50 20 34 P 10. ELECTRONIC DESIGN PRACTICE FIRST TERM TOTAL = 467/750. ORDN. 1 MARKS: T80053106 NAIR PRATHMESH PRAKASH PRIYA , 71344364C , T80053106 , PICT 01. CONTROL SYSTEMS 100 40 50 P 02. DIGITAL COMMUNICATION 100 40 46 P PΡ 03. DIGITAL COMMUNICATION 50 20 42 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 46 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 41 P 06. MICROCONTROLLERS & APPLICATION PP 100 40 54 P 07. MICROCONTROLLERS & APPLICATION PR 50 20 29 P 100 40 55 P 08. DIGITAL SIGNAL PROCESSING 09. DIGITAL SIGNAL PROCESSING 50 20 40 P OR 10. ELECTRONIC DESIGN PRACTICE OR 50 20 36 P

ORDN. 1 MARKS:

FIRST TERM TOTAL = 439/750.

UNIVERSITY OF	PUNE	T.E.	(200	8 PAT	.)(EL	ECTRONICS & TELI	EC.) EXAMI	INATION NOV. 20	13	
DATE : 11 MAR. 2014	CENT	RE : P	UNE	INSTI	TUTE	OF COMPUTER TECH	HNOLOGY, F	PUNE.	PAGE N	o. 36 (400)
NOTE: FIRST LINE : SEAT NO., NAME O OTHER LINES: HEAD OF PASSING,	MAX.	MARKS	, M	IN. P	ASS M	MARKS, MARKS OB	TAINED, F	P/F:PASS/FAIL,	C:PREVIOUS	
T80053107 NAWATHALE MANOJ PANDURAN					 JANI		 71200982F	· · · · · · · · · · · · · · · · · · ·		
01. CONTROL SYSTEMS	PP	100	40	63		,		,	,	,
02. DIGITAL COMMUNICATION	PP	100	40	48	Р					
03. DIGITAL COMMUNICATION	PR	50	20	43	Р					
04. NETWORK SYNTHESIS & FILTER DESIG	SNPP	100	40	70	Р					
05. NETWORK SYNTHESIS & FILTER DESIG	SNTW	50	20	39	Р					
06. MICROCONTROLLERS & APPLICATION	PP	100	40	63	Р					
07. MICROCONTROLLERS & APPLICATION	PR	50	20	32	Р					
08. DIGITAL SIGNAL PROCESSING	PP	100	40	60	Р					
09. DIGITAL SIGNAL PROCESSING	OR	50	20	40	Р					
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	38	Р					
FIRST TERM TOTAL = $496/750$.										
ORDN. 1 MARKS :										
T80053108 NEVPURKAR MANDAR SANTOSH	ł			AN	IAGHA	,	71200985L	- ,	, PICT	, т80053108
01. CONTROL SYSTEMS	PP	100	40	72	Р					
02. DIGITAL COMMUNICATION	PP	100	40	61	Р					
03. DIGITAL COMMUNICATION	PR	50	20	44	Р					
04. NETWORK SYNTHESIS & FILTER DESIG	SNPP	100	40	65	Р					
05. NETWORK SYNTHESIS & FILTER DESIG	SNTW	50	20	39	Р					
06. MICROCONTROLLERS & APPLICATION	PP	100	40	66	Р					
07. MICROCONTROLLERS & APPLICATION	PR	50	20	36						
08. DIGITAL SIGNAL PROCESSING	PP	100		66						
09. DIGITAL SIGNAL PROCESSING	OR		20							
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	41	Р					
FIRST TERM TOTAL = $530/750$.										
ORDN. 1 MARKS :										
T80053109 NIMISHA NITIN MISHRA		100	40		RTI	,	712058475	Ι,	, PICT	, т80053109
01. CONTROL SYSTEMS	PP	100		64						
02. DIGITAL COMMUNICATION	PP 	100		60						
03. DIGITAL COMMUNICATION	PR	50	20	43						
04. NETWORK SYNTHESIS & FILTER DESIG		100		70						
05. NETWORK SYNTHESIS & FILTER DESIG		50	20	40						
06. MICROCONTROLLERS & APPLICATION	PP	100		70						
07. MICROCONTROLLERS & APPLICATION	PR	50 100		39 62						
08. DIGITAL SIGNAL PROCESSING	PP OB	100	40	62 27						
09. DIGITAL SIGNAL PROCESSING	OR OR	50 50	20	37 42						
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	43	Ρ					
FIRST TERM TOTAL = 528/750.										
ORDN. 1 MARKS :										

UNIVERSITY OF PUNE ,T.E.(2008 PAT.)(ELECTRONICS & TELEC.) EXAMINATION NOV. 2013 DATE: 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 37 (401) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 70801532J , T80053154 , PICT T80053110 NITESH KR SINGH RITA , т80053110 11. SIGNAL CODING & ESTIMATION THEORYPP 100 40 56 P 01. CONTROL SYSTEMS 100 40 50 P C 02. DIGITAL COMMUNICATION 100 40 42 P 12. SIGNAL CODING & ESTIMATION THEORYPR 50 20 22 P C 03. DIGITAL COMMUNICATION 50 20 23 P C 13. SYSTEM PROGRA. & OPERATING SYS. PP 100 40 40 P PR 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 44 P C 14. SYSTEM PROGRA. & OPERATING SYS. TW 50 20 26 P C 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 22 P C 15. COMPUTER ORGANIZATION & ARCHITEC PP 100 40 46 P C 100 40 44 P C 16. INDUSTRIAL MANAGEMENT 100 40 49 P C 06. MICROCONTROLLERS & APPLICATION PP 100 40 48 P C 07. MICROCONTROLLERS & APPLICATION PR 50 20 28 P C 17. WAVE THEORY & ANTENNA PΡ 08. DIGITAL SIGNAL PROCESSING 100 40 40 P C 18. WAVE THEORY & ANTENNA 50 20 25 P C PR 09. DIGITAL SIGNAL PROCESSING 50 20 26 P C 50 20 20 P C 19. MINI PROJECT & SEMINAR OR OR 50 20 28 P C 50 20 30 P C 10. ELECTRONIC DESIGN PRACTICE 20. TEST & MEASUREMENT TECHNIQUES OR OR GRAND TOTAL = 709/1500, RESULT: PASS CLASS ORDN. 1 MARKS: T80053111 OMKAR NAGESHKAR KRUPA , 71200991E , , PICT , т80053111 01. CONTROL SYSTEMS 100 40 60 P 02. DIGITAL COMMUNICATION 100 40 53 P 03. DIGITAL COMMUNICATION 50 20 44 P PR 100 40 59 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 50 20 39 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 100 40 40 P 06. MICROCONTROLLERS & APPLICATION PP 50 20 39 P 07. MICROCONTROLLERS & APPLICATION PR 08. DIGITAL SIGNAL PROCESSING 100 40 54 P 50 20 40 P 09. DIGITAL SIGNAL PROCESSING OR 10. ELECTRONIC DESIGN PRACTICE 50 20 40 P FIRST TERM TOTAL = 468/750. ORDN. 1 MARKS: T80053112 OSWAL ARCHA AMRUT ALKA OSWAL , 71344365M , т80053112 , PICT 01. CONTROL SYSTEMS 100 40 48 P 02. DIGITAL COMMUNICATION 100 40 26 F PΡ 03. DIGITAL COMMUNICATION 50 20 08 F 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 18 F 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 36 P 06. MICROCONTROLLERS & APPLICATION PP 100 40 21 F 07. MICROCONTROLLERS & APPLICATION PR 50 20 24 P 100 40 46 P 08. DIGITAL SIGNAL PROCESSING 09. DIGITAL SIGNAL PROCESSING 50 20 25 P OR 10. ELECTRONIC DESIGN PRACTICE OR 50 20 23 P FIRST TERM TOTAL = 275/750.

ORDN. 1 MARKS:

UNIVERSITY OF PUNE ,T.E.(2008 PAT.)(ELECTRONICS & TELEC.) EXAMINATION NOV. 2013 DATE: 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 38 (402) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71100928н , T80053083 , PICT T80053113 PANDILWAR RAMESHWAR KISHORE SUJATA , т80053113 01. CONTROL SYSTEMS 100 40 61 P C 11. SIGNAL CODING & ESTIMATION THEORYPP 100 40 PC 02. DIGITAL COMMUNICATION 100 40 40 P C 12. SIGNAL CODING & ESTIMATION THEORYPR 50 20 37 P C 03. DIGITAL COMMUNICATION 50 20 37 P C 13. SYSTEM PROGRA. & OPERATING SYS. PP 100 40 46 P C PR 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 51 P C 14. SYSTEM PROGRA. OPERATING SYS. TW 50 20 27 P C 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 21 P C 15. COMPUTER ORGANIZATION & ARCHITEC PP 100 40 44 P C 100 40 41 P C 16. INDUSTRIAL MANAGEMENT 100 40 58 P C 06. MICROCONTROLLERS & APPLICATION PP 100 40 58 P C 07. MICROCONTROLLERS & APPLICATION PR 50 20 34 P C 17. WAVE THEORY & ANTENNA PP 08. DIGITAL SIGNAL PROCESSING 100 40 43 P C 18. WAVE THEORY & ANTENNA 50 20 25 P C PR 09. DIGITAL SIGNAL PROCESSING 50 20 41 P C 50 20 AA F OR 19. MINI PROJECT & SEMINAR OR 50 20 22 P C 50 20 21 P C 10. ELECTRONIC DESIGN PRACTICE 20. TEST & MEASUREMENT TECHNIQUES OR OR GRAND TOTAL = 747/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS: T80053114 PANMAND SUHAS DASHRATH HIRA , 71344366K , , рІСТ , T80053114 01. CONTROL SYSTEMS 100 40 68 P 02. DIGITAL COMMUNICATION 100 40 68 P 03. DIGITAL COMMUNICATION 50 20 41 P PR 81 P 100 40 04. NETWORK SYNTHESIS & FILTER DESIGNPP 50 20 47 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 100 40 06. MICROCONTROLLERS & APPLICATION PP 63 P 50 20 39 P 07. MICROCONTROLLERS & APPLICATION PR 08. DIGITAL SIGNAL PROCESSING 100 40 74 P 50 20 38 P 09. DIGITAL SIGNAL PROCESSING OR 10. ELECTRONIC DESIGN PRACTICE 50 20 42 P FIRST TERM TOTAL = 561/750. ORDN. 1 MARKS: T80053115 PARAKH BHUSHAN RAJENDRA **PUSHPA** , 71344367H , T80053115 , PICT 01. CONTROL SYSTEMS 100 40 73 P 02. DIGITAL COMMUNICATION 100 40 61 P PP 03. DIGITAL COMMUNICATION 50 20 36 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 77 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 43 P 06. MICROCONTROLLERS & APPLICATION PP 100 40 62 P 07. MICROCONTROLLERS & APPLICATION PR 50 20 31 P 66 P 08. DIGITAL SIGNAL PROCESSING 100 40 09. DIGITAL SIGNAL PROCESSING 50 20 37 P OR 10. ELECTRONIC DESIGN PRACTICE OR 50 20 38 P FIRST TERM TOTAL = 524/750. ORDN. 1 MARKS:

DATE : 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 39 (403) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71344368F , , , PICT , T80053116 T80053116 PATEL KINNARY VIPIN VINAYA PATEL 01. CONTROL SYSTEMS 100 40 73 P 02. DIGITAL COMMUNICATION 100 40 70 P 03. DIGITAL COMMUNICATION 50 20 41 P PR 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 81 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 44 P 100 40 78 P 06. MICROCONTROLLERS & APPLICATION PP 07. MICROCONTROLLERS & APPLICATION PR 50 20 43 P 08. DIGITAL SIGNAL PROCESSING 100 40 69 P 50 20 38 P 09. DIGITAL SIGNAL PROCESSING OR 10. ELECTRONIC DESIGN PRACTICE 50 20 37 P OR FIRST TERM TOTAL = 574/750. ORDN. 1 MARKS: T80053117 PATIL AMOL HIRAMAN LATA , 71100937G , T80053086 , PICT , T80053117 01. CONTROL SYSTEMS 100 40 58 P C 11. SIGNAL CODING & ESTIMATION THEORYPP 100 40 40 P C 100 40 44 P C 12. SIGNAL CODING & ESTIMATION THEORYPR 50 20 37 P C 02. DIGITAL COMMUNICATION 03. DIGITAL COMMUNICATION 50 20 27 P C 13. SYSTEM PROGRA. & OPERATING SYS. PP 100 40 55 P C PR 100 40 45 P C 50 20 39 P C 04. NETWORK SYNTHESIS & FILTER DESIGNPP 14. SYSTEM PROGRA.& OPERATING SYS. TW 50 20 41 P C 100 40 60 P C 05. NETWORK SYNTHESIS & FILTER DESIGNTW 15. COMPUTER ORGANIZATION & ARCHITEC PP 100 40 54 P C 100 40 61 P C 06. MICROCONTROLLERS & APPLICATION PP 16. INDUSTRIAL MANAGEMENT 50 20 32 P C 100 40 07. MICROCONTROLLERS & APPLICATION PR 17. WAVE THEORY & ANTENNA PP 61 P C 08. DIGITAL SIGNAL PROCESSING 100 40 45 P C 50 20 40 P 18. WAVE THEORY & ANTENNA 50 20 35 P C 50 20 37 P C 09. DIGITAL SIGNAL PROCESSING OR 19. MINI PROJECT & SEMINAR OR 10. ELECTRONIC DESIGN PRACTICE OR 50 20 29 P C 20. TEST & MEASUREMENT TECHNIQUES OR 50 20 34 P C GRAND TOTAL = 874/1500, RESULT: HIGHER SECOND CLASS ORDN. 1 MARKS: PATIL SUVARNA SAMPAT , 71344369D , PICT T80053118 PATIL CHINAR SAMPATRAO , т80053118 01. CONTROL SYSTEMS 100 40 49 P 02. DIGITAL COMMUNICATION 100 40 54 P PΡ 03. DIGITAL COMMUNICATION 50 20 36 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 59 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 39 P 06. MICROCONTROLLERS & APPLICATION PP 100 40 63 P 07. MICROCONTROLLERS & APPLICATION PR 50 20 30 P 100 40 57 P 08. DIGITAL SIGNAL PROCESSING 09. DIGITAL SIGNAL PROCESSING 50 20 41 P OR 10. ELECTRONIC DESIGN PRACTICE OR 50 20 40 P FIRST TERM TOTAL = 468/750. ORDN. 1 MARKS:

DATE: 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 40 (404) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER NILIMA , 71201009C , , PICT , T80053119 T80053119 PATIL SAYALI ABHAY 01. CONTROL SYSTEMS 100 40 74 P 02. DIGITAL COMMUNICATION 100 40 48 P 03. DIGITAL COMMUNICATION 50 20 32 P PR 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 75 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 41 P 100 40 55 P 06. MICROCONTROLLERS & APPLICATION PP 07. MICROCONTROLLERS & APPLICATION PR 50 20 34 P 08. DIGITAL SIGNAL PROCESSING 100 40 64 P 50 20 41 P 09. DIGITAL SIGNAL PROCESSING OR 10. ELECTRONIC DESIGN PRACTICE 50 20 36 P OR FIRST TERM TOTAL = 500/750. ORDN. 1 MARKS: T80053120 PATIL TUSHAR TUKARAM KAMAL , 71201010G , , PICT , т80053120 01. CONTROL SYSTEMS 100 40 69 P 02. DIGITAL COMMUNICATION 100 40 55 P 03. DIGITAL COMMUNICATION 50 20 42 P PR 100 40 63 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 50 20 45 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 100 40 42 P 06. MICROCONTROLLERS & APPLICATION PP 50 20 33 P 07. MICROCONTROLLERS & APPLICATION 08. DIGITAL SIGNAL PROCESSING 100 40 70 P 50 20 37 P 09. DIGITAL SIGNAL PROCESSING OR 10. ELECTRONIC DESIGN PRACTICE OR 50 20 38 P FIRST TERM TOTAL = 494/750. ORDN. 1 MARKS: T80053121 PAVAN TENKALE , T80053091 , PICT SHIVKANTA , 71100946F , т80053121 100 40 56 P C 100 40 49 P 01. CONTROL SYSTEMS 11. SIGNAL CODING & ESTIMATION THEORYPP 100 40 44 P C 50 20 27 P 02. DIGITAL COMMUNICATION 12. SIGNAL CODING & ESTIMATION THEORYPR PP 03. DIGITAL COMMUNICATION 50 20 20 P C 13. SYSTEM PROGRA. & OPERATING SYS. PP 100 40 48 P C 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 51 P C 14. SYSTEM PROGRA. OPERATING SYS. TW 50 20 28 P C 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 25 P C 15. COMPUTER ORGANIZATION & ARCHITEC PP 100 40 45 P C 06. MICROCONTROLLERS & APPLICATION PP 100 40 40 P C 16. INDUSTRIAL MANAGEMENT 100 40 54 P C 07. MICROCONTROLLERS & APPLICATION PR 50 20 22 P C 17. WAVE THEORY & ANTENNA 100 40 44 P C PΡ 100 40 50 20 36 P 08. DIGITAL SIGNAL PROCESSING 58 P C 18. WAVE THEORY & ANTENNA 09. DIGITAL SIGNAL PROCESSING 50 20 20 P C 19. MINI PROJECT & SEMINAR 50 20 23 P OR OR 50 20 25 P C 10. ELECTRONIC DESIGN PRACTICE OR 50 20 20 P C 20. TEST & MEASUREMENT TECHNIQUES OR GRAND TOTAL = 735/1500, RESULT: PASS CLASS ORDN. 1 MARKS:

DATE: 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 41 (405) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER T80053122 PAWAR GAURAV GURUNATH , 71201012C , , PICT , T80053122 JYOTSNA 01. CONTROL SYSTEMS 100 40 65 P 02. DIGITAL COMMUNICATION 100 40 50 P 03. DIGITAL COMMUNICATION 50 20 34 P PR 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 68 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 44 P 06. MICROCONTROLLERS & APPLICATION PP 100 40 54 P 50 20 21 P 07. MICROCONTROLLERS & APPLICATION PR 08. DIGITAL SIGNAL PROCESSING 100 40 60 P 09. DIGITAL SIGNAL PROCESSING 50 20 35 P OR 50 20 42 P 10. ELECTRONIC DESIGN PRACTICE OR FIRST TERM TOTAL = 473/750. ORDN. 1 MARKS: , т80053123 T80053123 PHADKE ASHWINI SHEKHAR ANJALI , 71201013M , , PICT 01. CONTROL SYSTEMS 100 40 58 P 02. DIGITAL COMMUNICATION 100 40 49 P 03. DIGITAL COMMUNICATION 50 20 39 P 100 40 54 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 50 20 36 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 100 40 55 P 06. MICROCONTROLLERS & APPLICATION PP 50 20 25 P 07. MICROCONTROLLERS & APPLICATION PR 08. DIGITAL SIGNAL PROCESSING 100 40 46 P 09. DIGITAL SIGNAL PROCESSING 50 20 34 P OR 10. ELECTRONIC DESIGN PRACTICE OR 50 20 32 P FIRST TERM TOTAL = 428/750. ORDN. 1 MARKS: T80053124 PRANITA PRAMOD GHOLE SANGITA , 71201020D , т80053124 , PICT 100 40 74 P 01. CONTROL SYSTEMS 02. DIGITAL COMMUNICATION 100 40 58 P PP 03. DIGITAL COMMUNICATION 50 20 35 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 64 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 43 P 06. MICROCONTROLLERS & APPLICATION 100 40 66 P 07. MICROCONTROLLERS & APPLICATION PR 50 20 30 P 100 40 54 P 08. DIGITAL SIGNAL PROCESSING 09. DIGITAL SIGNAL PROCESSING 50 20 39 P OR 10. ELECTRONIC DESIGN PRACTICE OR 50 20 35 P FIRST TERM TOTAL = 498/750. ORDN. 1 MARKS:

DATE: 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 42 (406) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER T80053125 PRASHANT KUMAR , 71201021B , , PICT , T80053125 LALITA DEVI 01. CONTROL SYSTEMS 100 40 46 P 02. DIGITAL COMMUNICATION 100 40 40 P 03. DIGITAL COMMUNICATION 50 20 09 F PR 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 50 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 36 P 06. MICROCONTROLLERS & APPLICATION PP 100 40 65 P 50 20 27 P 07. MICROCONTROLLERS & APPLICATION PR 08. DIGITAL SIGNAL PROCESSING 100 40 51 P 09. DIGITAL SIGNAL PROCESSING 50 20 32 P OR 50 20 41 P 10. ELECTRONIC DESIGN PRACTICE OR FIRST TERM TOTAL = 397/750. ORDN. 1 MARKS: , т80053126 T80053126 PRIYANKA BALRAJ R MANORAMA 01. CONTROL SYSTEMS 100 40 77 P 02. DIGITAL COMMUNICATION 100 40 62 P 03. DIGITAL COMMUNICATION 50 20 42 P PR 100 40 74 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 50 20 42 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 100 40 70 P 06. MICROCONTROLLERS & APPLICATION PP 50 20 35 P 07. MICROCONTROLLERS & APPLICATION PR 08. DIGITAL SIGNAL PROCESSING 100 40 52 P 09. DIGITAL SIGNAL PROCESSING 50 20 37 P OR 10. ELECTRONIC DESIGN PRACTICE OR 50 20 25 P FIRST TERM TOTAL = 516/750. ORDN. 1 MARKS: T80053127 PULEKAR ADITYA CHANDRASHEKHAR MADHAVI , 71201025E , т80053127 , PICT 01. CONTROL SYSTEMS 100 40 69 P 100 40 02. DIGITAL COMMUNICATION 65 P 03. DIGITAL COMMUNICATION 50 20 43 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 62 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 35 P 06. MICROCONTROLLERS & APPLICATION PP 100 40 58 P 07. MICROCONTROLLERS & APPLICATION PR 50 20 26 P 100 40 56 P 08. DIGITAL SIGNAL PROCESSING 09. DIGITAL SIGNAL PROCESSING 50 20 37 P OR 10. ELECTRONIC DESIGN PRACTICE OR 50 20 32 P FIRST TERM TOTAL = 483/750. ORDN. 1 MARKS:

DATE: 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 43 (407) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71201026C , , PICT , T80053128 T80053128 R GOKUL KRISHNAN R RAJESWARI 01. CONTROL SYSTEMS 100 40 66 P 02. DIGITAL COMMUNICATION 100 40 53 P 03. DIGITAL COMMUNICATION 50 20 42 P PR 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 58 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 42 P 06. MICROCONTROLLERS & APPLICATION PP 100 40 65 P 07. MICROCONTROLLERS & APPLICATION PR 50 20 35 P 08. DIGITAL SIGNAL PROCESSING 100 40 53 P 09. DIGITAL SIGNAL PROCESSING 50 20 39 P OR 50 20 30 P 10. ELECTRONIC DESIGN PRACTICE OR FIRST TERM TOTAL = 483/750. ORDN. 1 MARKS: , т80053129 T80053129 RADHIKA RAJE VIDYA , 71201027M , , PICT 01. CONTROL SYSTEMS 100 40 74 P 02. DIGITAL COMMUNICATION 100 40 69 P 03. DIGITAL COMMUNICATION 50 20 39 P PR 100 40 48 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 50 20 41 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 100 40 71 P 06. MICROCONTROLLERS & APPLICATION PP 50 20 37 P 07. MICROCONTROLLERS & APPLICATION PR 08. DIGITAL SIGNAL PROCESSING 100 40 54 P 50 20 37 P 09. DIGITAL SIGNAL PROCESSING OR 10. ELECTRONIC DESIGN PRACTICE OR 50 20 28 P FIRST TERM TOTAL = 498/750. ORDN. 1 MARKS: T80053130 RAHEGAONKAR RUSHIKESH VIJAY LALITA , 71201028к , т80053130 , PICT 100 40 75 P 01. CONTROL SYSTEMS 100 40 47 P 02. DIGITAL COMMUNICATION 03. DIGITAL COMMUNICATION 50 20 40 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 73 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 38 P 06. MICROCONTROLLERS & APPLICATION PP 100 40 60 P 07. MICROCONTROLLERS & APPLICATION PR 50 20 33 P 100 40 58 P 08. DIGITAL SIGNAL PROCESSING 09. DIGITAL SIGNAL PROCESSING 50 20 36 P OR 10. ELECTRONIC DESIGN PRACTICE OR 50 20 29 P FIRST TERM TOTAL = 489/750. ORDN. 1 MARKS:

UNIVERSITY C	F PUNE	,T.E.	(200	8 PA	T.)(ELECTRO	NICS & TE	ELEC.) EXAMINAT	TION NOV. 20	13	
DATE : 11 MAR. 2014	CENT	RE : F	PUNE	INST	ITUTE OF COM	MPUTER TE	ECHNOLOGY, PUNE	Ξ.	PAGE NO.	44 (408)
NOTE: FIRST LINE : SEAT NO., NAME										
OTHER LINES: HEAD OF PASSING,										
T80053131 RAHUL MAHIPATI KALE					UVARNA		, 71201030м			 , т80053131
01. CONTROL SYSTEMS	PP	100	40		P		, 71201030M	,	, 1101	, 100033131
02. DIGITAL COMMUNICATION		100			P					
03. DIGITAL COMMUNICATION	PR		20		Р					
04. NETWORK SYNTHESIS & FILTER DESI	GNPP	100			Р					
05. NETWORK SYNTHESIS & FILTER DESI	IGNTW	50	20	42	Р					
06. MICROCONTROLLERS & APPLICATION	PP	100	40	52	Р					
07. MICROCONTROLLERS & APPLICATION	PR	50	20	28	Р					
08. DIGITAL SIGNAL PROCESSING	PP	100	40	44	Р					
09. DIGITAL SIGNAL PROCESSING	OR	50	20	37	Р					
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	35	Р					
FIRST TERM TOTAL = 463/750.										
ORDN. 1 MARKS :										
T80053132 RATHOD ABHIJIT KALURAM				Α	LKA		, 71201034D	,	, PICT	, т80053132
01. CONTROL SYSTEMS	PP	100	40	71	Р					
02. DIGITAL COMMUNICATION	PP	100	40	24	F					
03. DIGITAL COMMUNICATION	PR	50	20	30	Р					
04. NETWORK SYNTHESIS & FILTER DESI	GNPP	100	40	40	Р					
05. NETWORK SYNTHESIS & FILTER DESI	IGNTW	50	20	35	Р					
06. MICROCONTROLLERS & APPLICATION	PP	100	40	52	Р					
07. MICROCONTROLLERS & APPLICATION	PR	50	20	23	Р					
08. DIGITAL SIGNAL PROCESSING	PP	100	40	30	F					
09. DIGITAL SIGNAL PROCESSING	OR	50	20	38	Р					
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	30	Р					
FIRST TERM TOTAL = $373/750$.										
ORDN. 1 MARKS :										
T900F3132 DATUOD DRIVANIKA NEMTCHA							74204026			
T80053133 RATHOD PRIYANKA NEMICHA		100	40		HOBHA		, 71201036L	,	, PICT	, т80053133
01. CONTROL SYSTEMS	PP		40	_	Р					
02. DIGITAL COMMUNICATION	PP	100		40						
03. DIGITAL COMMUNICATION	PR		20		Р					
04. NETWORK SYNTHESIS & FILTER DESI		100			P					
05. NETWORK SYNTHESIS & FILTER DESI 06. MICROCONTROLLERS & APPLICATION		100	20 40		P P					
07. MICROCONTROLLERS & APPLICATION	PP PR		20	46 24						
08. DIGITAL SIGNAL PROCESSING	PK PP	100		40						
09. DIGITAL SIGNAL PROCESSING	OR		20		P P					
10. ELECTRONIC DESIGN PRACTICE	OR		20		P P					
FIRST TERM TOTAL = 360/750.	UK	30	20	22	Г					
ORDN. 1 MARKS :										
ONDITE FINANCE I										

UNIVERSITY OF PUNE ,T.E.(2008 PAT.)(ELECTRONICS & TELEC.) EXAMINATION NOV. 2013 DATE: 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 45 (409) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71236326C , T80053100 , PICT T80053134 RATHOD VIJAY HIRAMAN KANTA , т80053134 01. CONTROL SYSTEMS 100 40 60 P C 11. SIGNAL CODING & ESTIMATION THEORYPP 100 40 PC 02. DIGITAL COMMUNICATION 100 40 57 P C 12. SIGNAL CODING & ESTIMATION THEORYPR 50 20 03. DIGITAL COMMUNICATION 50 20 28 P C 13. SYSTEM PROGRA. & OPERATING SYS. PP 100 40 54 P C PR 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 52 P C 14. SYSTEM PROGRA. & OPERATING SYS. TW 50 20 38 P C 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 39 P C 15. COMPUTER ORGANIZATION & ARCHITEC PP 100 40 59 P C 100 40 61 P C 16. INDUSTRIAL MANAGEMENT 100 40 55 P C 06. MICROCONTROLLERS & APPLICATION PP 32 P C 100 40 52 P C 07. MICROCONTROLLERS & APPLICATION PR 50 20 17. WAVE THEORY & ANTENNA PΡ 08. DIGITAL SIGNAL PROCESSING 100 40 42 P C 18. WAVE THEORY & ANTENNA 50 20 25 P C PR 09. DIGITAL SIGNAL PROCESSING 50 20 34 P C 50 20 38 P C OR 19. MINI PROJECT & SEMINAR OR 50 20 36 P C 50 20 30 P C 10. ELECTRONIC DESIGN PRACTICE 20. TEST & MEASUREMENT TECHNIQUES OR OR GRAND TOTAL = 862/1500, RESULT: HIGHER SECOND CLASS ORDN. 1 MARKS: T80053135 RITAPURE KIRAN KAMLAKAR SUVARNA , т80053135 01. CONTROL SYSTEMS 100 40 59 P 100 40 51 P 02. DIGITAL COMMUNICATION 03. DIGITAL COMMUNICATION 50 20 38 P PR 100 40 43 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 50 20 38 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 100 40 06. MICROCONTROLLERS & APPLICATION PP 63 P 50 20 40 P 07. MICROCONTROLLERS & APPLICATION PR 08. DIGITAL SIGNAL PROCESSING 100 40 42 P 50 20 40 P 09. DIGITAL SIGNAL PROCESSING OR 50 20 36 P 10. ELECTRONIC DESIGN PRACTICE FIRST TERM TOTAL = 450/750. ORDN. 1 MARKS: T80053136 ROHANKAR AKSHAY ANILRAO REKHA , 71344371F , T80053136 , PICT 01. CONTROL SYSTEMS 100 40 64 P 02. DIGITAL COMMUNICATION 100 40 33 F PP 03. DIGITAL COMMUNICATION 50 20 37 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 46 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 39 P 06. MICROCONTROLLERS & APPLICATION PP 100 40 56 P 07. MICROCONTROLLERS & APPLICATION PR 50 20 05 F 100 40 40 P 08. DIGITAL SIGNAL PROCESSING 09. DIGITAL SIGNAL PROCESSING 50 20 36 P OR 10. ELECTRONIC DESIGN PRACTICE OR 50 20 36 P FIRST TERM TOTAL = 392/750.

ORDN. 1 MARKS:

DATE: 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 46 (410) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER T80053137 ROHIT GUPTA , 71201042E , , PICT , T80053137 GEETA 01. CONTROL SYSTEMS 100 40 69 P 02. DIGITAL COMMUNICATION 100 40 45 P 03. DIGITAL COMMUNICATION 50 20 28 P PR 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 54 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 27 P 06. MICROCONTROLLERS & APPLICATION PP 100 40 62 P 07. MICROCONTROLLERS & APPLICATION PR 50 20 06 F 08. DIGITAL SIGNAL PROCESSING 100 40 55 P 09. DIGITAL SIGNAL PROCESSING 50 20 35 P OR 10. ELECTRONIC DESIGN PRACTICE 50 20 14 F OR FIRST TERM TOTAL = 395/750. ORDN. 1 MARKS: , т80053138 T80053138 ROKADE SUYOG SUDARSHAN SUREKHA , 71201043C , , PICT 01. CONTROL SYSTEMS 100 40 54 P 02. DIGITAL COMMUNICATION 100 40 10 F 03. DIGITAL COMMUNICATION 50 20 34 P 100 40 21 F 04. NETWORK SYNTHESIS & FILTER DESIGNPP 50 20 34 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 100 40 AA F 06. MICROCONTROLLERS & APPLICATION PP 50 20 05 F 07. MICROCONTROLLERS & APPLICATION 08. DIGITAL SIGNAL PROCESSING 100 40 45 P 09. DIGITAL SIGNAL PROCESSING 50 20 27 P OR 50 20 30 P 10. ELECTRONIC DESIGN PRACTICE OR FIRST TERM TOTAL = 260/750. ORDN. 1 MARKS: T80053139 RUTUJA S JADHAV BHARATI , 71201045K , т80053139 , PICT 100 40 70 P 01. CONTROL SYSTEMS 100 40 02. DIGITAL COMMUNICATION 58 P PP 03. DIGITAL COMMUNICATION 50 20 32 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 53 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 38 P 06. MICROCONTROLLERS & APPLICATION 100 40 62 P 07. MICROCONTROLLERS & APPLICATION PR 50 20 37 P 100 40 49 P 08. DIGITAL SIGNAL PROCESSING 09. DIGITAL SIGNAL PROCESSING 50 20 27 P OR 10. ELECTRONIC DESIGN PRACTICE OR 50 20 34 P FIRST TERM TOTAL = 460/750. ORDN. 1 MARKS:

DATE: 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 47 (411)

NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71236327M , T80053105 , PICT T80053140 SALUNKHE MITALI KISHOR BHARATI , т80053140 01. CONTROL SYSTEMS 100 40 63 P C 11. SIGNAL CODING & ESTIMATION THEORYPP 100 40 45 P C 02. DIGITAL COMMUNICATION 100 40 40 P C 12. SIGNAL CODING & ESTIMATION THEORYPR 50 20 37 P 03. DIGITAL COMMUNICATION 50 20 26 P C 13. SYSTEM PROGRA. & OPERATING SYS. PP 100 40 56 P C PR 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 55 P C 14. SYSTEM PROGRA. OPERATING SYS. TW 50 20 37 P C 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 40 P C 15. COMPUTER ORGANIZATION & ARCHITEC PP 100 40 60 P C 100 40 63 P C 16. INDUSTRIAL MANAGEMENT 100 40 06. MICROCONTROLLERS & APPLICATION PP PР 66 P C 100 40 65 P C 07. MICROCONTROLLERS & APPLICATION PR 50 20 40 P C 17. WAVE THEORY & ANTENNA PP 08. DIGITAL SIGNAL PROCESSING 100 40 46 P C 18. WAVE THEORY & ANTENNA 50 20 33 P C PR 09. DIGITAL SIGNAL PROCESSING 50 20 34 P C 50 20 37 P C OR 19. MINI PROJECT & SEMINAR OR 10. ELECTRONIC DESIGN PRACTICE 50 20 22 P C 20. TEST & MEASUREMENT TECHNIQUES 50 20 30 P C OR OR GRAND TOTAL = 895+05/1500, RESULT: FIRST CLASS [0.2] ORDN. 1 MARKS: T80053141 SALVE PRAJAKTA BHAUSAHEB MANGAL , 71201051D , , PICT , т80053141 01. CONTROL SYSTEMS 100 40 65 P 100 40 44 P 02. DIGITAL COMMUNICATION 03. DIGITAL COMMUNICATION 50 20 37 P PR 100 40 50 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 50 20 40 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 100 40 06. MICROCONTROLLERS & APPLICATION PP 65 P 50 20 07. MICROCONTROLLERS & APPLICATION PR 43 P 100 40 08. DIGITAL SIGNAL PROCESSING 48 P 50 20 29 P 09. DIGITAL SIGNAL PROCESSING OR 10. ELECTRONIC DESIGN PRACTICE 50 20 36 P FIRST TERM TOTAL = 457/750. ORDN. 1 MARKS: , T80053107 , PICT T80053142 SARWAR ABHIJIT ASHOK MEERA , 71100988м , T80053142 100 40 54 P 01. CONTROL SYSTEMS 100 40 51 P C 11. SIGNAL CODING & ESTIMATION THEORYPP 02. DIGITAL COMMUNICATION 100 40 40 P C 12. SIGNAL CODING & ESTIMATION THEORYPR 50 20 34 P C PΡ 03. DIGITAL COMMUNICATION 50 20 30 P C 13. SYSTEM PROGRA. & OPERATING SYS. PP 100 40 46 P C 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 40 P C 14. SYSTEM PROGRA. & OPERATING SYS. TW 50 20 37 P C 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 36 P C 15. COMPUTER ORGANIZATION & ARCHITEC PP 100 40 44 P C 06. MICROCONTROLLERS & APPLICATION PP 100 40 42 P C 16. INDUSTRIAL MANAGEMENT 100 40 58 P C 07. MICROCONTROLLERS & APPLICATION PR 50 20 30 P C 17. WAVE THEORY & ANTENNA 100 40 67 P C PP 100 40 50 20 32 P C 08. DIGITAL SIGNAL PROCESSING 42 P C 18. WAVE THEORY & ANTENNA 09. DIGITAL SIGNAL PROCESSING 50 20 36 P C 19. MINI PROJECT & SEMINAR 50 20 35 P C OR OR 10. ELECTRONIC DESIGN PRACTICE OR 50 20 28 P C 20. TEST & MEASUREMENT TECHNIQUES OR 50 20 31 P C GRAND TOTAL = 813/1500, RESULT: SECOND CLASS ORDN. 1 MARKS:

UNIVERSITY OF	PUNE	,T.E.	(200	8 PAT	Γ.)(ELE	ECTRON	NICS &	TELEC	.) EXA	AMINA	TION	NOV. 2	013			
DATE : 11 MAR. 2014	CENT	RE : P	UNE	INST	ITUTE (OF COM	MPUTER	TECHN	IOLOGY	, PUNI	E.			PAGE N	10. 48	8 (412)
NOTE: FIRST LINE : SEAT NO., NAME OF	F THE	CANDI	DATE	, MC	OTHER,	PERMA	ANENT F	REG. N	IO., I	PREVI	ous s	EAT NO	., COI	LEGE,	SEA	T NO.
OTHER LINES: HEAD OF PASSING,	MAX.	MARKS	, M	IN. F	PASS MA	ARKS,	MARKS	S OBTA	INED,	P/F	: PASS	/FAIL,	C:PRI	VIOUS	CARRY	OVER
T80053143 SATPUTE SHIVRAJ PRAKASH					IJAYA			, 7	12010!	55G	,		,	PICT		, т80053143
01. CONTROL SYSTEMS	PP	100		66												
02. DIGITAL COMMUNICATION	PP	100	40	51	Р											
03. DIGITAL COMMUNICATION	PR	50	20	43	Р											
04. NETWORK SYNTHESIS & FILTER DESIGN	NPP	100	40	64	Р											
05. NETWORK SYNTHESIS & FILTER DESIGN	NTW	50	20	38	Р											
06. MICROCONTROLLERS & APPLICATION	PP	100	40	63	Р											
07. MICROCONTROLLERS & APPLICATION	PR	50	20	32	Р											
08. DIGITAL SIGNAL PROCESSING	PP	100	40	51	Р											
09. DIGITAL SIGNAL PROCESSING	OR	50	20	38	Р											
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	32	Р											
FIRST TERM TOTAL = $478/750$.																
ORDN. 1 MARKS :																
T80053144 SAURABH NANGLIA				SA	ANDHYA			, 7	12010	56E	,		,	PICT		, т80053144
01. CONTROL SYSTEMS	PP	100	40	63	Р											
02. DIGITAL COMMUNICATION	PP	100	40	43	Р											
03. DIGITAL COMMUNICATION	PR	50	20	39	Р											
04. NETWORK SYNTHESIS & FILTER DESIG	NPP	100	40	55	Р											
05. NETWORK SYNTHESIS & FILTER DESIG	NTW	50	20	30	Р											
06. MICROCONTROLLERS & APPLICATION	PP	100	40	56	Р											
07. MICROCONTROLLERS & APPLICATION	PR	50	20	10	F											
08. DIGITAL SIGNAL PROCESSING	PP	100	40	52	Р											
09. DIGITAL SIGNAL PROCESSING	OR	50	20	33	Р											
10. ELECTRONIC DESIGN PRACTICE	OR			22												
FIRST TERM TOTAL = 403/750.																
ORDN. 1 MARKS :																
T80053145 SAURAV KUMAR					AUSHAN				12010					PICT		, т80053145
01. CONTROL SYSTEMS	PP	100	40	70				,			,		,			,
02. DIGITAL COMMUNICATION	PP	100		40												
03. DIGITAL COMMUNICATION	PR	50	20	42												
04. NETWORK SYNTHESIS & FILTER DESIGN		100		40												
05. NETWORK SYNTHESIS & FILTER DESIGN			20	41												
06. MICROCONTROLLERS & APPLICATION	PP	100	-	56												
07. MICROCONTROLLERS & APPLICATION	PR		20	23												
08. DIGITAL SIGNAL PROCESSING	PR PP	100		48												
			20	39												
	OR															
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	35	Р											
FIRST TERM TOTAL = 434/750.																
ORDN. 1 MARKS :																

DATE: 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 49 (413) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71201060C , , PICT , T80053146 T80053146 SHAH PARIN LITESH BHAVYA 01. CONTROL SYSTEMS 100 40 81 P 02. DIGITAL COMMUNICATION 100 40 43 P 03. DIGITAL COMMUNICATION 50 20 32 P PR 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 66 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 32 P 06. MICROCONTROLLERS & APPLICATION PP 100 40 65 P 07. MICROCONTROLLERS & APPLICATION PR 50 20 30 P 08. DIGITAL SIGNAL PROCESSING 100 40 56 P 09. DIGITAL SIGNAL PROCESSING 50 20 33 P OR 10. ELECTRONIC DESIGN PRACTICE 50 20 33 P OR FIRST TERM TOTAL = 471/750. ORDN. 1 MARKS: , т80053147 T80053147 SHAHA SANKET MANOJKUMAR NEETA , 71100997L , , PICT 01. CONTROL SYSTEMS 100 40 65 P 02. DIGITAL COMMUNICATION 100 40 43 P 03. DIGITAL COMMUNICATION 50 20 44 P 100 40 46 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 50 20 40 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 100 40 48 P 06. MICROCONTROLLERS & APPLICATION PP 50 20 32 P 07. MICROCONTROLLERS & APPLICATION 08. DIGITAL SIGNAL PROCESSING 100 40 47 P 50 20 35 P 09. DIGITAL SIGNAL PROCESSING OR 10. ELECTRONIC DESIGN PRACTICE OR 50 20 36 P FIRST TERM TOTAL = 436/750. ORDN. 1 MARKS: T80053148 SHELAR RAHUL BAPU **MEENA** , 71201065D , т80053148 , PICT 01. CONTROL SYSTEMS 100 40 66 P 100 40 53 P 02. DIGITAL COMMUNICATION PP 03. DIGITAL COMMUNICATION 50 20 38 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 55 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 37 P 06. MICROCONTROLLERS & APPLICATION 100 40 66 P 07. MICROCONTROLLERS & APPLICATION PR 50 20 33 P 100 40 48 P 08. DIGITAL SIGNAL PROCESSING 09. DIGITAL SIGNAL PROCESSING 50 20 36 P OR 10. ELECTRONIC DESIGN PRACTICE OR 50 20 36 P FIRST TERM TOTAL = 468/750. ORDN. 1 MARKS:

DATE: 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 50 (414) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71201066B , , PICT , T80053149 T80053149 SHELAR SAGAR PRAKASH REKHA 01. CONTROL SYSTEMS 100 40 65 P 02. DIGITAL COMMUNICATION 100 40 51 P 03. DIGITAL COMMUNICATION 50 20 37 P PR 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 62 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 41 P 06. MICROCONTROLLERS & APPLICATION PP 100 40 48 P 50 20 32 P 07. MICROCONTROLLERS & APPLICATION PR 08. DIGITAL SIGNAL PROCESSING 100 40 61 P 09. DIGITAL SIGNAL PROCESSING 50 20 41 P OR 10. ELECTRONIC DESIGN PRACTICE 50 20 34 P OR FIRST TERM TOTAL = 472/750. ORDN. 1 MARKS: , т80053150 T80053150 SHEP VISHWAJIT CHANDRAKANT URMILA , 71201068J , , PICT 01. CONTROL SYSTEMS 100 40 46 P 02. DIGITAL COMMUNICATION 100 40 27 F 03. DIGITAL COMMUNICATION 50 20 20 P 100 40 40 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 50 20 29 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 100 40 46 P 06. MICROCONTROLLERS & APPLICATION PP 50 20 22 P 07. MICROCONTROLLERS & APPLICATION PR 08. DIGITAL SIGNAL PROCESSING 100 40 46 P 09. DIGITAL SIGNAL PROCESSING 50 20 28 P OR 10. ELECTRONIC DESIGN PRACTICE OR 50 20 28 P FIRST TERM TOTAL = 332/750. ORDN. 1 MARKS: T80053151 SHINDE SANIKA HANMANT SANGEETA , 71201071J , т80053151 , PICT 100 40 56 P 01. CONTROL SYSTEMS 100 40 40 P 02. DIGITAL COMMUNICATION PP 03. DIGITAL COMMUNICATION 50 20 32 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 52 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 39 P 06. MICROCONTROLLERS & APPLICATION 100 40 60 P 07. MICROCONTROLLERS & APPLICATION PR 50 20 30 P 100 40 61 P 08. DIGITAL SIGNAL PROCESSING 09. DIGITAL SIGNAL PROCESSING 50 20 40 P OR 10. ELECTRONIC DESIGN PRACTICE OR 50 20 30 P FIRST TERM TOTAL = 440/750. ORDN. 1 MARKS:

UNIVERSITY OF PUNE ,T.E.(2008 PAT.)(ELECTRONICS & TELEC.) EXAMINATION NOV. 2013 DATE: 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 51 (415) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71201072G , , PICT , T80053152 T80053152 SHINDE SIDDESH BALASAHEB ASHA 01. CONTROL SYSTEMS 100 40 78 P PP 02. DIGITAL COMMUNICATION 100 40 51 P 03. DIGITAL COMMUNICATION 50 20 38 P PR 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 77 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 44 P 100 40 76 P 06. MICROCONTROLLERS & APPLICATION PP 07. MICROCONTROLLERS & APPLICATION PR 50 20 32 P 08. DIGITAL SIGNAL PROCESSING 100 40 73 P 50 20 42 P 09. DIGITAL SIGNAL PROCESSING OR 10. ELECTRONIC DESIGN PRACTICE 50 20 33 P OR FIRST TERM TOTAL = 544/750. ORDN. 1 MARKS: T80053153 SHINDE VAISHALI GULAB SHOBHA , т80053153 01. CONTROL SYSTEMS 100 40 84 P 02. DIGITAL COMMUNICATION 100 40 58 P 03. DIGITAL COMMUNICATION 50 20 33 P PR 100 40 58 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 50 20 41 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 100 40 06. MICROCONTROLLERS & APPLICATION PP 65 P 50 20 38 P 07. MICROCONTROLLERS & APPLICATION 08. DIGITAL SIGNAL PROCESSING 100 40 47 P 50 20 38 P 09. DIGITAL SIGNAL PROCESSING OR 50 20 22 P 10. ELECTRONIC DESIGN PRACTICE FIRST TERM TOTAL = 484/750. ORDN. 1 MARKS: , T80053111 , PICT T80053154 SHITRE SOURABH DNYANESHWAR SNEHA , 71101011M , т80053154 11. SIGNAL CODING & ESTIMATION THEORYPP 100 40 52 P 01. CONTROL SYSTEMS 100 40 47 P C 50 20 39 P C 02. DIGITAL COMMUNICATION 100 40 40 P C 12. SIGNAL CODING & ESTIMATION THEORYPR PP 03. DIGITAL COMMUNICATION 50 20 42 P C 13. SYSTEM PROGRA. & OPERATING SYS. PP 100 40 56 P C 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 46 P C 14. SYSTEM PROGRA. OPERATING SYS. TW 50 20 42 P C 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 43 P C 15. COMPUTER ORGANIZATION & ARCHITEC PP 100 40 59 P C 06. MICROCONTROLLERS & APPLICATION PP 100 40 48 P C 16. INDUSTRIAL MANAGEMENT 100 40 50 P C 07. MICROCONTROLLERS & APPLICATION PR 50 20 40 P C 17. WAVE THEORY & ANTENNA 100 40 40 P C PΡ 100 40 50 20 38 P C 08. DIGITAL SIGNAL PROCESSING 46 P C 18. WAVE THEORY & ANTENNA 09. DIGITAL SIGNAL PROCESSING 50 20 35 P C 19. MINI PROJECT & SEMINAR 50 20 44 P C OR OR 10. ELECTRONIC DESIGN PRACTICE OR 50 20 38 P C 20. TEST & MEASUREMENT TECHNIQUES OR 50 20 36 P C GRAND TOTAL = 881/1500, RESULT: HIGHER SECOND CLASS ORDN. 1 MARKS:

DATE: 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 52 (416)

NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71101016в SAROJ , T80053114 , PICT T80053155 SIDDHANT KHURANA , т80053155 01. CONTROL SYSTEMS 100 40 40 P C 11. SIGNAL CODING & ESTIMATION THEORYPP 100 40 42 P 02. DIGITAL COMMUNICATION 100 40 23 F 12. SIGNAL CODING & ESTIMATION THEORYPR 50 20 03. DIGITAL COMMUNICATION 50 20 20 P C 13. SYSTEM PROGRA. & OPERATING SYS. PP 100 40 45 P C PR 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 42 P C 14. SYSTEM PROGRA. OPERATING SYS. TW 50 20 34 P C 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 31 P C 15. COMPUTER ORGANIZATION & ARCHITEC PP 100 40 44 P C 100 40 49 P C 16. INDUSTRIAL MANAGEMENT 100 40 54 P C 06. MICROCONTROLLERS & APPLICATION PP 100 40 40 P C 07. MICROCONTROLLERS & APPLICATION PR 50 20 24 P C PΡ 17. WAVE THEORY & ANTENNA 08. DIGITAL SIGNAL PROCESSING 100 40 57 P C 18. WAVE THEORY & ANTENNA 50 20 21 P C PR 09. DIGITAL SIGNAL PROCESSING 50 20 24 P C 50 20 26 P C OR 19. MINI PROJECT & SEMINAR OR 50 20 35 P 10. ELECTRONIC DESIGN PRACTICE 50 20 32 P C 20. TEST & MEASUREMENT TECHNIQUES OR OR GRAND TOTAL = 718/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS: T80053156 SONAWANE YASHWANT DEEPAK SEEMA , 71201079D , , PICT , т80053156 01. CONTROL SYSTEMS 100 40 54 P 100 40 57 P 02. DIGITAL COMMUNICATION 03. DIGITAL COMMUNICATION 50 20 38 P PR 100 40 47 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 50 20 27 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 100 40 06. MICROCONTROLLERS & APPLICATION PP 58 P 50 20 26 P 07. MICROCONTROLLERS & APPLICATION PR 100 40 53 P 08. DIGITAL SIGNAL PROCESSING 50 20 35 P 09. DIGITAL SIGNAL PROCESSING OR 10. ELECTRONIC DESIGN PRACTICE OR 50 20 30 P FIRST TERM TOTAL = 425/750. ORDN. 1 MARKS: T80053157 SONU SINGH , T80053160 , PICT USHA , 71045633G , т80053157 60 P C 100 40 43 P 01. CONTROL SYSTEMS 100 40 11. SIGNAL CODING & ESTIMATION THEORYPP 100 40 02. DIGITAL COMMUNICATION 33# P 12. SIGNAL CODING & ESTIMATION THEORYPR 50 20 20 P PΡ 03. DIGITAL COMMUNICATION 50 20 29 P C 13. SYSTEM PROGRA. & OPERATING SYS. PP 100 40 40 P C 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 40 P C 14. SYSTEM PROGRA. OPERATING SYS. TW 50 20 27 P C 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 31 P C 15. COMPUTER ORGANIZATION & ARCHITEC PP 100 40 40 P C 06. MICROCONTROLLERS & APPLICATION PP 100 40 40 P C 16. INDUSTRIAL MANAGEMENT 100 40 55 P C 07. MICROCONTROLLERS & APPLICATION PR 50 20 20 P C 17. WAVE THEORY & ANTENNA 100 40 54 P C PP 100 40 50 20 35 P C 08. DIGITAL SIGNAL PROCESSING 41 P C 18. WAVE THEORY & ANTENNA 09. DIGITAL SIGNAL PROCESSING 50 20 35 P C 19. MINI PROJECT & SEMINAR 50 20 35 P C OR OR 10. ELECTRONIC DESIGN PRACTICE OR 50 20 25 P C 20. TEST & MEASUREMENT TECHNIQUES OR 50 20 30 P C GRAND TOTAL = 733/1500, RESULT: PASS CLASS # [0.4] ORDN. 1 MARKS:

DATE: 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 53 (417) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER NALINI , 71344374L , , , PICT , T80053158 T80053158 SORATE TEJASWI TANAJI 01. CONTROL SYSTEMS 100 40 66 P 02. DIGITAL COMMUNICATION 100 40 50 P 03. DIGITAL COMMUNICATION 50 20 38 P PR 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 74 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 38 P 06. MICROCONTROLLERS & APPLICATION PP 100 40 71 P 50 20 41 P 07. MICROCONTROLLERS & APPLICATION PR 08. DIGITAL SIGNAL PROCESSING 100 40 59 P 09. DIGITAL SIGNAL PROCESSING 50 20 31 P OR 10. ELECTRONIC DESIGN PRACTICE 50 20 33 P OR FIRST TERM TOTAL = 501/750. ORDN. 1 MARKS: , т80053159 T80053159 SRISHTI GANJOO NISHA , 71201080H , , PICT 01. CONTROL SYSTEMS 100 40 64 P 02. DIGITAL COMMUNICATION 100 40 63 P 03. DIGITAL COMMUNICATION 50 20 40 P PR 100 40 64 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 50 20 43 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 100 40 77 P 06. MICROCONTROLLERS & APPLICATION PP 50 20 24 P 07. MICROCONTROLLERS & APPLICATION PR 08. DIGITAL SIGNAL PROCESSING 100 40 70 P 09. DIGITAL SIGNAL PROCESSING 50 20 36 P OR 10. ELECTRONIC DESIGN PRACTICE OR 50 20 38 P FIRST TERM TOTAL = 519/750. ORDN. 1 MARKS: T80053160 SUSHIL KUMAR SHAKUNTALA DEVI , 71201085J , PICT , т80053160 100 40 60 P 01. CONTROL SYSTEMS 100 40 32 F 02. DIGITAL COMMUNICATION PΡ 03. DIGITAL COMMUNICATION 50 20 20 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 69 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 36 P 06. MICROCONTROLLERS & APPLICATION PP 100 40 43 P 07. MICROCONTROLLERS & APPLICATION PR 50 20 02 F 100 40 46 P 08. DIGITAL SIGNAL PROCESSING 09. DIGITAL SIGNAL PROCESSING 50 20 34 P OR 10. ELECTRONIC DESIGN PRACTICE OR 50 20 22 P FIRST TERM TOTAL = 364/750. ORDN. 1 MARKS:

UNIVERSITY OF PUNE ,T.E.(2008 PAT.)(ELECTRONICS & TELEC.) EXAMINATION NOV. 2013 DATE: 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 54 (418) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER MRUDULA , 71201090E , , PICT , T80053161 T80053161 TAMBE MAITREYEE MILIND 01. CONTROL SYSTEMS 100 40 66 P 02. DIGITAL COMMUNICATION 100 40 45 P 03. DIGITAL COMMUNICATION 50 20 35 P PR 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 80 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 47 P 100 40 65 P 06. MICROCONTROLLERS & APPLICATION PP 07. MICROCONTROLLERS & APPLICATION PR 50 20 35 P 08. DIGITAL SIGNAL PROCESSING 100 40 71 P 50 20 38 P 09. DIGITAL SIGNAL PROCESSING OR 10. ELECTRONIC DESIGN PRACTICE 50 20 38 P OR FIRST TERM TOTAL = 520/750. ORDN. 1 MARKS: T80053162 TANDEL PRASAD SADANAND SNEHA , 71201092M , , PICT , T80053162 01. CONTROL SYSTEMS 100 40 77 P 02. DIGITAL COMMUNICATION 100 40 55 P 03. DIGITAL COMMUNICATION 50 20 44 P PR 100 40 80 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 50 20 38 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 100 40 75 P 06. MICROCONTROLLERS & APPLICATION PP 50 20 43 P 07. MICROCONTROLLERS & APPLICATION 08. DIGITAL SIGNAL PROCESSING 100 40 64 P 50 20 37 P 09. DIGITAL SIGNAL PROCESSING OR 10. ELECTRONIC DESIGN PRACTICE OR 50 20 34 P FIRST TERM TOTAL = 547/750. ORDN. 1 MARKS: , т80053120 , T80053163 TANNA VATSAL AJAY VAISHALI , 71236329н , т80053163 PICT 100 40 62 P C 100 40 AA F 01. CONTROL SYSTEMS 11. SIGNAL CODING & ESTIMATION THEORYPP 50 20 29 P C 02. DIGITAL COMMUNICATION 100 40 AA F 12. SIGNAL CODING & ESTIMATION THEORYPR PP 03. DIGITAL COMMUNICATION 50 20 30 P C 13. SYSTEM PROGRA. © OPERATING SYS. PP 100 40 70 P C 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 43 P C 14. SYSTEM PROGRA. OPERATING SYS. TW 50 20 31 P C 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 34 P C 15. COMPUTER ORGANIZATION & ARCHITEC PP 100 40 54 P C 06. MICROCONTROLLERS & APPLICATION PP 100 40 43 P C 16. INDUSTRIAL MANAGEMENT 100 40 65 P C 07. MICROCONTROLLERS & APPLICATION PR 50 20 23 P C 17. WAVE THEORY & ANTENNA 100 40 40 P C PΡ 100 40 40 P C 50 20 35 P C 08. DIGITAL SIGNAL PROCESSING 18. WAVE THEORY & ANTENNA 09. DIGITAL SIGNAL PROCESSING 50 20 30 P C 19. MINI PROJECT & SEMINAR 50 20 34 P C OR OR 10. ELECTRONIC DESIGN PRACTICE OR 50 20 29 P C 20. TEST & MEASUREMENT TECHNIQUES OR 50 20 34 P C GRAND TOTAL = 726/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS:

UNIVERSITY OF	F PUNE	,T.E.	(200	8 PAT	Γ.)(El	LECTRONICS &	TELEC.) EX	XAMINAT	ION NOV. 20	13	
DATE : 11 MAR. 2014	CENT	RE : P	UNE	INST	TUTE	OF COMPUTER	TECHNOLOG	Y, PUNE	-	PAGE 1	NO. 55 (419)
NOTE: FIRST LINE : SEAT NO., NAME (OF THE	CANDI	DATE	, MC	THER.	, PERMANENT F	REG. NO.,	PREVIO	US SEAT NO.	, COLLEGE,	SEAT NO.
OTHER LINES: HEAD OF PASSING,	MAX.	MARKS	, M	IN. F	PASS N	MARKS, MARKS	S OBTAINED	, P/F:	PASS/FAIL,	C:PREVIOUS	CARRY OVER
T80053164 TATHE SAKET NANDKISHOR					EENA		, 712010	093к	,	, PICT	, т80053164
01. CONTROL SYSTEMS	PP	100		72							
02. DIGITAL COMMUNICATION	PP	100	40	50	Р						
03. DIGITAL COMMUNICATION	PR	50	20	37	Р						
04. NETWORK SYNTHESIS & FILTER DESIG	GNPP	100	40	56	Р						
05. NETWORK SYNTHESIS & FILTER DESIG	GNTW	50	20	33	Р						
06. MICROCONTROLLERS & APPLICATION	PP	100	40	69	Р						
07. MICROCONTROLLERS & APPLICATION	PR	50	20	26	Р						
08. DIGITAL SIGNAL PROCESSING	PP	100	40	45	Р						
09. DIGITAL SIGNAL PROCESSING	OR	50	20	35	Р						
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	35	Р						
FIRST TERM TOTAL = $458/750$.											
ORDN. 1 MARKS :											
T80053165 TERKAR PRASAD VITTHALRAG)			AS	SHA		, 711010	030н	,	, PICT	, т80053165
01. CONTROL SYSTEMS	PP	100	40	53	Р						
02. DIGITAL COMMUNICATION	PP	100	40	55	Р						
03. DIGITAL COMMUNICATION	PR	50	20	07	F						
04. NETWORK SYNTHESIS & FILTER DESIG	GNPP	100	40	47	Р						
05. NETWORK SYNTHESIS & FILTER DESIG	GNTW	50	20	33	Р						
06. MICROCONTROLLERS & APPLICATION	PP	100	40	42	Р						
07. MICROCONTROLLERS & APPLICATION	PR	50	20	08	F						
08. DIGITAL SIGNAL PROCESSING	PP	100	40	43	Р						
09. DIGITAL SIGNAL PROCESSING	OR	50	20	34	Р						
10. ELECTRONIC DESIGN PRACTICE	OR			14	F						
FIRST TERM TOTAL = 336/750.											
ORDN. 1 MARKS :											
T80053166 THAKARE APURV VIJAY					EENA		, 711010		,	, PICT	
01. CONTROL SYSTEMS	PP	100	40	58			,		,	,	,
02. DIGITAL COMMUNICATION	PP	100		33							
03. DIGITAL COMMUNICATION	PR	50	20	07							
04. NETWORK SYNTHESIS & FILTER DESIG		100		42							
05. NETWORK SYNTHESIS & FILTER DESIG			20	31							
06. MICROCONTROLLERS & APPLICATION	PP	100	-	52							
07. MICROCONTROLLERS & APPLICATION	PR		20	05							
08. DIGITAL SIGNAL PROCESSING	PP	100		50							
09. DIGITAL SIGNAL PROCESSING	OR		20	30							
10. ELECTRONIC DESIGN PRACTICE	OR OR		20	28							
	UK	30	20	20	۲						
FIRST TERM TOTAL = 336/750. ORDN. 1 MARKS :											
ONDIN. I IMANAS .											

DATE: 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 56 (420) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71201096D , , PICT , T80053167 T80053167 TIKHE SHIRIN ABHAY SWATI 01. CONTROL SYSTEMS 100 40 64 P 02. DIGITAL COMMUNICATION 100 40 49 P 03. DIGITAL COMMUNICATION 50 20 33 P PR 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 71 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 38 P 100 40 60 P 06. MICROCONTROLLERS & APPLICATION PP 07. MICROCONTROLLERS & APPLICATION PR 50 20 26 P 08. DIGITAL SIGNAL PROCESSING 100 40 56 P 50 20 29 P 09. DIGITAL SIGNAL PROCESSING OR 10. ELECTRONIC DESIGN PRACTICE 50 20 32 P OR FIRST TERM TOTAL = 458/750. ORDN. 1 MARKS: T80053168 TIWARI VIPUL TARUN PADMA , 71101038C , T80053125 , PICT , T80053168 01. CONTROL SYSTEMS 100 40 61 P C 11. SIGNAL CODING & ESTIMATION THEORYPP 100 40 40 P C 100 40 50 P C 12. SIGNAL CODING & ESTIMATION THEORYPR 50 20 22 P 02. DIGITAL COMMUNICATION 03. DIGITAL COMMUNICATION 50 20 20 P C 13. SYSTEM PROGRA. & OPERATING SYS. PP 100 40 70 P C PR 100 40 40 P C 50 20 04. NETWORK SYNTHESIS & FILTER DESIGNPP 14. SYSTEM PROGRA. & OPERATING SYS. TW 34 P C 50 20 31 P C 100 40 53 P C 05. NETWORK SYNTHESIS & FILTER DESIGNTW 15. COMPUTER ORGANIZATION & ARCHITEC PP 100 40 55 P C 100 40 06. MICROCONTROLLERS & APPLICATION PP 16. INDUSTRIAL MANAGEMENT 67 P.C 50 20 23 P C 100 40 63 P C 07. MICROCONTROLLERS & APPLICATION PR 17. WAVE THEORY & ANTENNA PP 08. DIGITAL SIGNAL PROCESSING 100 40 52 P C 50 20 28 P 18. WAVE THEORY & ANTENNA 50 20 20 P C 50 20 22 P 09. DIGITAL SIGNAL PROCESSING OR 19. MINI PROJECT & SEMINAR OR 10. ELECTRONIC DESIGN PRACTICE OR 50 20 20 P C 20. TEST & MEASUREMENT TECHNIQUES OR 50 20 22 P C GRAND TOTAL = 793/1500, RESULT: SECOND CLASS ORDN. 1 MARKS: T80053169 TUPE NITIN BABASAHEB SANJIVANI , 71201098L , т80053169 , PICT 01. CONTROL SYSTEMS 100 40 60 P 02. DIGITAL COMMUNICATION 100 40 33 F 03. DIGITAL COMMUNICATION 50 20 38 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 45 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 40 P 06. MICROCONTROLLERS & APPLICATION PP 100 40 57 P 07. MICROCONTROLLERS & APPLICATION PR 50 20 21 P 100 40 08. DIGITAL SIGNAL PROCESSING AA F 09. DIGITAL SIGNAL PROCESSING 50 20 30 P OR 10. ELECTRONIC DESIGN PRACTICE OR 50 20 35 P FIRST TERM TOTAL = 359/750. ORDN. 1 MARKS:

UNIVERSITY O	F PUNE	,T.E.	(200	8 PAT	.)(EI	LECTRONICS & TELEC.)	EXAMINAT	TION NOV. 20	13	
DATE : 11 MAR. 2014	CENT	TRE : P	UNE	INSTI	TUTE	OF COMPUTER TECHNOLO	OGY, PUNE		PAGE NO.	57 (421)
NOTE: ETECT LINE : CEAT NO NAME						DEDMANENT DEC. NO.				
NOTE: FIRST LINE : SEAT NO., NAME OTHER LINES: HEAD OF PASSING,				•		•		_	•	
T80053170 UNDE MADHURI BALASAHEB					XMI	, 7134			, PICT	, т80053170
01. CONTROL SYSTEMS	PP	100	40	62		, : =5		,	,	,
02. DIGITAL COMMUNICATION		100		28						
03. DIGITAL COMMUNICATION	PR	50	20	08						
04. NETWORK SYNTHESIS & FILTER DESI	GNPP	100	40	42	Р					
05. NETWORK SYNTHESIS & FILTER DESI	GNTW	50	20	37	Р					
06. MICROCONTROLLERS & APPLICATION	PP	100	40	58	Р					
07. MICROCONTROLLERS & APPLICATION	PR	50	20	30	Р					
08. DIGITAL SIGNAL PROCESSING	PP	100	40	53	Р					
09. DIGITAL SIGNAL PROCESSING	OR	50	20	34	Р					
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	30	Р					
FIRST TERM TOTAL = 382/750.										
ORDN. 1 MARKS :										
T80053171 UPADHYE VEDA UDAY				MΑ	NISHA	A , 7120	01101D	,	, PICT	, т80053171
01. CONTROL SYSTEMS	PP	100	40	79	Р					
02. DIGITAL COMMUNICATION	PP	100	40	59	Р					
03. DIGITAL COMMUNICATION	PR	50	20	42						
04. NETWORK SYNTHESIS & FILTER DESI	GNPP	100	40	63						
05. NETWORK SYNTHESIS & FILTER DESI	GNTW	50	20	42						
06. MICROCONTROLLERS & APPLICATION	PP	100	40	68						
07. MICROCONTROLLERS & APPLICATION	PR	50	20	24						
08. DIGITAL SIGNAL PROCESSING	PP	100		69						
09. DIGITAL SIGNAL PROCESSING	OR		20							
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	32	Р					
FIRST TERM TOTAL = 516/750.										
ORDN. 1 MARKS :										
T90052172 UCTAD CARA ARRUM CHAKOO										
T80053172 USTAD SABA ABDUL SHAKOO 01. CONTROL SYSTEMS		100	40	76	TYA	, /120	01102в	,	, PICT	, т80053172
02. DIGITAL COMMUNICATION	PP PP	100 100		65						
03. DIGITAL COMMUNICATION	PR	50	20	43						
04. NETWORK SYNTHESIS & FILTER DESI		100		78						
05. NETWORK SYNTHESIS & FILTER DESI		50		39						
06. MICROCONTROLLERS & APPLICATION	PP	100	_	77						
07. MICROCONTROLLERS & APPLICATION	PR		20	35						
08. DIGITAL SIGNAL PROCESSING	PP	100	40	66						
09. DIGITAL SIGNAL PROCESSING	OR		20	38						
10. ELECTRONIC DESIGN PRACTICE	OR		20	35						
FIRST TERM TOTAL = $552/750$.			_ •		-					
ORDN. 1 MARKS :										
-										

DATE: 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 58 (422)

NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71101044H , T80053127 , PICT , T80053173 T80053173 VAIDYA SANDIP MILIND SANGEETA 01. CONTROL SYSTEMS PP 100 40 65 P C 11. SIGNAL CODING & ESTIMATION THEORYPP 100 40 47 P 02. DIGITAL COMMUNICATION 100 40 42 P C 12. SIGNAL CODING & ESTIMATION THEORYPR 50 20 03. DIGITAL COMMUNICATION 50 20 35 P C 13. SYSTEM PROGRA. & OPERATING SYS. PP 100 40 56 P C PR 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 56 P C 14. SYSTEM PROGRA. OPERATING SYS. TW 50 20 40 P C 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 42 P C 15. COMPUTER ORGANIZATION & ARCHITEC PP 100 40 52 P C 100 40 53 P C 16. INDUSTRIAL MANAGEMENT 100 40 58 P C 06. MICROCONTROLLERS & APPLICATION PP PР 100 40 67 P C 07. MICROCONTROLLERS & APPLICATION PR 50 20 41 P C 17. WAVE THEORY & ANTENNA PP 08. DIGITAL SIGNAL PROCESSING 100 40 40 P C 18. WAVE THEORY & ANTENNA 50 20 35 P C PR 09. DIGITAL SIGNAL PROCESSING 50 20 35 P C 50 20 43 P C OR 19. MINI PROJECT & SEMINAR OR 50 20 40 P C 10. ELECTRONIC DESIGN PRACTICE 50 20 37 P C 20. TEST & MEASUREMENT TECHNIQUES OR OR GRAND TOTAL = 922/1500, RESULT: FIRST CLASS ORDN. 1 MARKS: T80053174 VARADE RUSHIKESH DILIP CHHAYA , 71101045F , T80053128 , PICT , T80053174 01. CONTROL SYSTEMS 100 40 46 P C 11. SIGNAL CODING & ESTIMATION THEORYPP 100 40 40 P C 100 40 27 F 50 20 36 P C 02. DIGITAL COMMUNICATION 12. SIGNAL CODING & ESTIMATION THEORYPR 03. DIGITAL COMMUNICATION 50 20 23 P C 13. SYSTEM PROGRA. & OPERATING SYS. PP 100 40 53 P C PR 100 40 40 P C 50 20 30 P C 04. NETWORK SYNTHESIS & FILTER DESIGNPP 14. SYSTEM PROGRA.& OPERATING SYS. TW 50 20 27 P C 100 40 53 P C 05. NETWORK SYNTHESIS & FILTER DESIGNTW 15. COMPUTER ORGANIZATION & ARCHITEC PP 100 40 40 P C 100 40 60 P C 06. MICROCONTROLLERS & APPLICATION PP 16. INDUSTRIAL MANAGEMENT 50 20 35 P C 100 40 49 P C 07. MICROCONTROLLERS & APPLICATION PR 17. WAVE THEORY & ANTENNA PP 100 40 47 P C 50 20 22 P C 08. DIGITAL SIGNAL PROCESSING 18. WAVE THEORY & ANTENNA 50 20 40 P C 50 20 36 P C 09. DIGITAL SIGNAL PROCESSING OR 19. MINI PROJECT & SEMINAR OR 50 20 28 P C 10. ELECTRONIC DESIGN PRACTICE OR 50 20 29 P C 20. TEST & MEASUREMENT TECHNIQUES OR GRAND TOTAL = 761/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS: T80053175 VIDHATE TRUPTI SOPAN , 71101048L , T80053130 , PICT , T80053175 VARSHA 11. SIGNAL CODING & ESTIMATION THEORYPP 100 40 40 P C 01. CONTROL SYSTEMS 100 40 52 P C 100 40 40 P 02. DIGITAL COMMUNICATION 12. SIGNAL CODING & ESTIMATION THEORYPR 50 20 33 P C PP 03. DIGITAL COMMUNICATION 50 20 36 P C 13. SYSTEM PROGRA. & OPERATING SYS. PP 100 40 47 P C 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 40 P C 14. SYSTEM PROGRA. & OPERATING SYS. TW 50 20 35 P C 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 38 P C 15. COMPUTER ORGANIZATION & ARCHITEC PP 100 40 40 P C 06. MICROCONTROLLERS & APPLICATION PP 100 40 41 P C 16. INDUSTRIAL MANAGEMENT 100 40 46 P C 07. MICROCONTROLLERS & APPLICATION PR 50 20 27 P C 100 40 47 P C 17. WAVE THEORY & ANTENNA PP 100 40 50 20 32 P C 08. DIGITAL SIGNAL PROCESSING 45 P C 18. WAVE THEORY & ANTENNA 09. DIGITAL SIGNAL PROCESSING 50 20 30 P C 19. MINI PROJECT & SEMINAR 50 20 36 P C OR OR 10. ELECTRONIC DESIGN PRACTICE OR 50 20 27 P C 20. TEST & MEASUREMENT TECHNIQUES OR 50 20 33 P C GRAND TOTAL = 765/1500, RESULT: SECOND CLASS ORDN. 1 MARKS:

DATE: 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 59 (423) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71201110C , , PICT , T80053176 T80053176 VIKRANT SINGH SUMITRA 01. CONTROL SYSTEMS 100 40 AA F 02. DIGITAL COMMUNICATION 100 40 31 F 03. DIGITAL COMMUNICATION 50 20 23 P PR 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 53 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 28 P 06. MICROCONTROLLERS & APPLICATION PP 100 40 15 F 07. MICROCONTROLLERS & APPLICATION PR 50 20 03 F 08. DIGITAL SIGNAL PROCESSING 100 40 48 P 09. DIGITAL SIGNAL PROCESSING 50 20 37 P OR 50 20 22 P 10. ELECTRONIC DESIGN PRACTICE OR FIRST TERM TOTAL = 260/750. ORDN. 1 MARKS: T80053177 VISHAL VATSA ASHA , 71201111M , , PICT , т80053177 01. CONTROL SYSTEMS 100 40 66 P 02. DIGITAL COMMUNICATION 100 40 40 P 03. DIGITAL COMMUNICATION 50 20 36 P PR 100 40 65 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 50 20 36 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 100 40 46 P 06. MICROCONTROLLERS & APPLICATION PP 50 20 30 P 07. MICROCONTROLLERS & APPLICATION PR 08. DIGITAL SIGNAL PROCESSING 100 40 50 P 50 20 35 P 09. DIGITAL SIGNAL PROCESSING OR 10. ELECTRONIC DESIGN PRACTICE OR 50 20 35 P FIRST TERM TOTAL = 439/750. ORDN. 1 MARKS: T80053178 WADIKAR MUGDHA RAVINDRA MEENAL , 71201113н , т80053178 , PICT 01. CONTROL SYSTEMS 100 40 72 P 02. DIGITAL COMMUNICATION 100 40 43 P PP 03. DIGITAL COMMUNICATION 50 20 41 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 58 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 37 P 06. MICROCONTROLLERS & APPLICATION PP 100 40 68 P 07. MICROCONTROLLERS & APPLICATION PR 50 20 30 P 100 40 55 P 08. DIGITAL SIGNAL PROCESSING 09. DIGITAL SIGNAL PROCESSING 50 20 35 P OR 10. ELECTRONIC DESIGN PRACTICE OR 50 20 37 P FIRST TERM TOTAL = 476/750. ORDN. 1 MARKS:

UNIVERSITY O	F PUNE	,T.E.	(200	8 PA	Γ.)(ELEC	TRONICS &	TELEC.) EX	AMINAT	ION NOV. 20	13		
DATE : 11 MAR. 2014	CENT	RE : P	PUNE	INST:	TUTE OF	COMPUTER	TECHNOLOGY	, PUNE		PAGE	NO. 60 (424)	
NOTE: FIRST LINE : SEAT NO., NAME OTHER LINES: HEAD OF PASSING,	OF THE	CANDI	DATE	, MC	OTHER, P	ERMANENT	REG. NO.,	PREVIO	US SEAT NO.	, COLLEGE,		
T80053179 WARPE HRUSHEEKESH SUNIL					 /ITA		, 712011		,			
01. CONTROL SYSTEMS	PP	100	40	66			,		,	,	,	
02. DIGITAL COMMUNICATION	PP	100	40	55	Р							
03. DIGITAL COMMUNICATION	PR	50	20	07	F							
04. NETWORK SYNTHESIS & FILTER DESI	GNPP	100	40	55	Р							
05. NETWORK SYNTHESIS & FILTER DESI	GNTW	50	20	36	Р							
06. MICROCONTROLLERS & APPLICATION	PP	100	40	56	Р							
07. MICROCONTROLLERS & APPLICATION	PR	50	20	30	Р							
08. DIGITAL SIGNAL PROCESSING	PP	100	40	60	Р							
09. DIGITAL SIGNAL PROCESSING	OR	50	20	35	Р							
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	36	Р							
FIRST TERM TOTAL = $436/750$.												
ORDN. 1 MARKS :												
T80053180 WAYKOS ABHIJIT JINCHAND	RA			N/	AYANA		, 712011	20L	,	, PICT	, т80053180	
01. CONTROL SYSTEMS	PP	100		71	Р							
02. DIGITAL COMMUNICATION	PP	100		67								
03. DIGITAL COMMUNICATION	PR	50		42								
04. NETWORK SYNTHESIS & FILTER DESI		100		70								
05. NETWORK SYNTHESIS & FILTER DESI	GNTW	50			Р							
06. MICROCONTROLLERS & APPLICATION	PP	100		72								
07. MICROCONTROLLERS & APPLICATION	PR	50		10								
08. DIGITAL SIGNAL PROCESSING	PP	100		59								
09. DIGITAL SIGNAL PROCESSING	OR		20		P _							
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	35	Р							
FIRST TERM TOTAL = 498/750.												
ORDN. 1 MARKS :												
T80053181 YEMULA AKHIL					 ANJULA		, 712011		,			
01. CONTROL SYSTEMS	PP	100	40	62			, /12011	232	,	, , , , , ,	, 100033101	
02. DIGITAL COMMUNICATION	PP	100			P							
03. DIGITAL COMMUNICATION	PR		20	35								
04. NETWORK SYNTHESIS & FILTER DESI		100		61								
05. NETWORK SYNTHESIS & FILTER DESI			20	34								
06. MICROCONTROLLERS & APPLICATION	PP	100		63								
07. MICROCONTROLLERS & APPLICATION	PR		20		Р							
08. DIGITAL SIGNAL PROCESSING	PP	100		71								
09. DIGITAL SIGNAL PROCESSING	OR		20	35								
10. ELECTRONIC DESIGN PRACTICE	OR		20	33								
FIRST TERM TOTAL = $479/750$.												
ORDN. 1 MARKS :												

	UNIVERSITY OF	PUNE ,	T.E.(2008	PAT.)(EL	ECTRONICS &	& TELEC.) I	EXAMINATION NO	ov. 2013	i
1 /		CENTRE	. DUNE T	NCTTTUTE	OF COMPUTE	TECHNOLO	CV DUNE		

DATE : 11 MAR. 2014	CENT	RE : P	UNE	INSTI	TUTE OF	COMPUTER TECH	NOLOGY, PUNE	Ξ.	PAGE NO	. 61 (425)
NOTE: FIRST LINE : SEAT NO., NAME OTHER LINES: HEAD OF PASSING	OF THE	CANDI	DATE	, MC	THER, PE		NO., PREVIO	OUS SEAT NO.,	COLLEGE,	SEAT NO.
T80053182 YEOLE PRAJAKT PRAMOD					OTI		 71236332н	 , т80053134		
01. CONTROL SYSTEMS	PP	100	40	AA	F					
02. DIGITAL COMMUNICATION	PP	100	40	AA	F					
03. DIGITAL COMMUNICATION	PR	50	20	32	Р					
04. NETWORK SYNTHESIS & FILTER DES	IGNPP	100	40	AA	F					
05. NETWORK SYNTHESIS & FILTER DES	IGNTW	50	20	20	РС					
06. MICROCONTROLLERS & APPLICATION	PP	100	40	AA	F					
07. MICROCONTROLLERS & APPLICATION	PR	50	20	22	РС					
08. DIGITAL SIGNAL PROCESSING	PP	100	40	AA	F					
09. DIGITAL SIGNAL PROCESSING	OR	50	20	35	РС					
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	27	РС					
FIRST TERM TOTAL = $136/750$.										
ORDN. 1 MARKS :										

DATE: 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 01 (426)

NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER DINAZ , 71200745J , , PICT , T80054201 T80054201 AADITYA RAMESH 01. DATABASE MANAGEMENT SYSTEMS 100 40 60 P 02. DATA COMMUNICATION 100 40 69 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 58 P 04. DIGITAL SIGNAL PROCESSING 100 40 57 P 05. THEORY OF COMPUTATION 100 40 66 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 20 35 P 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 39 P 08. SIGNAL PROCESSING LABORATORY 25 10 19 P TW 09. SIGNAL PROCESSING LABORATORY 50 20 40 P OR 10. HARDWARE LABORATORY 25 10 19 P TW 11. HARDWARE LABORATORY 50 20 38 P PR FIRST TERM TOTAL = 500/750. ORDN. 1 MARKS: T80054202 ABHINAV KAUL , 71200747E , , PICT , т80054202 SUREKHA 01. DATABASE MANAGEMENT SYSTEMS 50 P 100 40 100 40 47 P 02. DATA COMMUNICATION PP 100 40 22 F 03. MICROPROCESSORS & MICROCONTROLLERPP 04. DIGITAL SIGNAL PROCESSING 100 40 24 F 05. THEORY OF COMPUTATION 100 40 40 P 50 20 36 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 31 P 25 10 18 P 08. SIGNAL PROCESSING LABORATORY TW 09. SIGNAL PROCESSING LABORATORY 50 20 30 P OR 10. HARDWARE LABORATORY 25 10 15 P TW 11. HARDWARE LABORATORY 50 20 30 P PR FIRST TERM TOTAL = 343/750. ORDN. 1 MARKS: , 71200749м T80054203 ABHYANKAR RUCHA RHISHIKESH , PICT , т80054203 ARTI 01. DATABASE MANAGEMENT SYSTEMS 100 40 60 P 02. DATA COMMUNICATION 100 40 63 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 52 P 04. DIGITAL SIGNAL PROCESSING 100 40 62 P 05. THEORY OF COMPUTATION 100 40 64 P 50 20 40 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 43 P 08. SIGNAL PROCESSING LABORATORY 25 10 21 P TW 42 P 09. SIGNAL PROCESSING LABORATORY 50 20 10. HARDWARE LABORATORY 25 10 20 P TW 11. HARDWARE LABORATORY 50 20 40 P PR FIRST TERM TOTAL = 507/750. ORDN. 1 MARKS:

DATE: 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 02 (427)

NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71045358C , T80054202 , PICT , T80054204 T80054204 ABNAVE OMKAR RAJENDRA SHOBHA 01. DATABASE MANAGEMENT SYSTEMS 100 40 51 P C 02. DATA COMMUNICATION 100 40 48 P C 100 40 AA F 03. MICROPROCESSORS & MICROCONTROLLERPP 04. DIGITAL SIGNAL PROCESSING 100 40 AA F 05. THEORY OF COMPUTATION 100 40 AA F 50 20 21 P C 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 AA F 08. SIGNAL PROCESSING LABORATORY 25 10 10 P C TW 09. SIGNAL PROCESSING LABORATORY 50 20 AA F OR 10. HARDWARE LABORATORY 25 10 15 P C TW 11. HARDWARE LABORATORY 50 20 AA F PR FIRST TERM TOTAL = 145/750. ORDN. 1 MARKS: T80054205 AJINKYA DNYANESHWAR RAJPUT MEENAKSHI , 71200753K , , PICT , т80054205 01. DATABASE MANAGEMENT SYSTEMS 100 40 58 P 100 40 60 P 02. DATA COMMUNICATION 100 40 53 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 40 P 04. DIGITAL SIGNAL PROCESSING 05. THEORY OF COMPUTATION 100 40 40 P 50 20 36 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 38 P 25 10 16 P 08. SIGNAL PROCESSING LABORATORY TW 09. SIGNAL PROCESSING LABORATORY OR 50 20 30 P 10. HARDWARE LABORATORY 25 10 14 P TW 11. HARDWARE LABORATORY 50 20 40 P PR FIRST TERM TOTAL = 425/750. ORDN. 1 MARKS: T80054206 AKASH MAHAPATRA , т80054206 **SUJATA** 01. DATABASE MANAGEMENT SYSTEMS 100 40 55 P 02. DATA COMMUNICATION PP 100 40 56 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 40 P 04. DIGITAL SIGNAL PROCESSING 100 40 47 P 05. THEORY OF COMPUTATION 100 40 50 P 50 20 42 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 41 P 08. SIGNAL PROCESSING LABORATORY 25 10 22 P TW 09. SIGNAL PROCESSING LABORATORY 50 20 27 P 10. HARDWARE LABORATORY 25 10 21 P TW 11. HARDWARE LABORATORY 50 20 34 P PR FIRST TERM TOTAL = 435/750. ORDN. 1 MARKS:

DATE: 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 03 (428)

NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER T80054207 AMAN RATHI , 71200759J , , PICT , T80054207 GEETA 01. DATABASE MANAGEMENT SYSTEMS 100 40 55 P 02. DATA COMMUNICATION 100 40 60 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 46 P 04. DIGITAL SIGNAL PROCESSING 100 40 20 F 05. THEORY OF COMPUTATION 100 40 AA F 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 20 39 P 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 38 P 08. SIGNAL PROCESSING LABORATORY 25 10 19 P TW 09. SIGNAL PROCESSING LABORATORY 50 20 22 P OR 10. HARDWARE LABORATORY 25 10 20 P TW 11. HARDWARE LABORATORY 50 20 20 P PR FIRST TERM TOTAL = 339/750. ORDN. 1 MARKS: T80054208 AMAN SINGH , 71200760B , , PICT , т80054208 KIRAN 01. DATABASE MANAGEMENT SYSTEMS 53 P 100 40 100 40 66 P 02. DATA COMMUNICATION PP 100 40 50 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 22 F 04. DIGITAL SIGNAL PROCESSING 05. THEORY OF COMPUTATION 100 40 17 F 50 20 24 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 30 P 25 10 13 P 08. SIGNAL PROCESSING LABORATORY TW 09. SIGNAL PROCESSING LABORATORY 50 20 28 P OR 10. HARDWARE LABORATORY 25 10 13 P TW 11. HARDWARE LABORATORY 50 20 40 P PR FIRST TERM TOTAL = 356/750. ORDN. 1 MARKS: , 71200762J T80054209 ANIRUDH SUDARSHAN , PICT , т80054209 PADMAPRIYA 01. DATABASE MANAGEMENT SYSTEMS 100 40 58 P 02. DATA COMMUNICATION PP 100 40 64 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 53 P 04. DIGITAL SIGNAL PROCESSING 100 40 69 P 05. THEORY OF COMPUTATION 100 40 77 P 50 20 06. RDBMS & VISUAL PROGRAMMING LAB. TW 39 P 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 42 P 08. SIGNAL PROCESSING LABORATORY 25 10 17 P TW 09. SIGNAL PROCESSING LABORATORY 50 20 24 P 10. HARDWARE LABORATORY 25 10 16 P TW 11. HARDWARE LABORATORY 50 20 38 P PR FIRST TERM TOTAL = 497/750. ORDN. 1 MARKS:

UNIVERSITY OF PUNE ,T.E.(2008 PAT.)(COMPUTER) EXAMINATION NOV. 2013 DATE: 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 04 (429) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71347755F , , , PICT , T80054210 T80054210 ANTHONY ELIZABETH XAVIER ANJALI 01. DATABASE MANAGEMENT SYSTEMS 100 40 46 P 02. DATA COMMUNICATION 100 40 43 P 100 40 47 P 03. MICROPROCESSORS & MICROCONTROLLERPP 04. DIGITAL SIGNAL PROCESSING 100 40 47 P 05. THEORY OF COMPUTATION 100 40 54 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 20 45 P 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 45 P 08. SIGNAL PROCESSING LABORATORY 25 10 22 P TW 09. SIGNAL PROCESSING LABORATORY 50 20 37 P OR 10. HARDWARE LABORATORY 25 10 22 P TW 11. HARDWARE LABORATORY 50 20 36 P PR FIRST TERM TOTAL = 444/750. ORDN. 1 MARKS: T80054211 ANVEKAR ASHWINI KISHOR , 71200763G , , PICT , т80054211 KANCHANA 01. DATABASE MANAGEMENT SYSTEMS 100 40 60 P 100 40 46 P 02. DATA COMMUNICATION 100 40 47 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 57 P 04. DIGITAL SIGNAL PROCESSING 05. THEORY OF COMPUTATION 100 40 54 P 50 20 43 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 45 P 25 10 23 P 08. SIGNAL PROCESSING LABORATORY TW 09. SIGNAL PROCESSING LABORATORY OR 50 20 44 P 10. HARDWARE LABORATORY 25 10 21 P TW 11. HARDWARE LABORATORY 50 20 41 P PR FIRST TERM TOTAL = 481/750. ORDN. 1 MARKS: T80054212 BAGDIYA TUSHAR VIJAYCHAND , т80054212 VEENA 01. DATABASE MANAGEMENT SYSTEMS 100 40 51 P 02. DATA COMMUNICATION PP 100 40 44 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 58 P 04. DIGITAL SIGNAL PROCESSING 100 40 61 P 05. THEORY OF COMPUTATION 100 40 71 P 50 20 36 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 38 P 08. SIGNAL PROCESSING LABORATORY 25 10 17 P TW 09. SIGNAL PROCESSING LABORATORY 50 20 35 P 10. HARDWARE LABORATORY 25 10 16 P TW 11. HARDWARE LABORATORY 50 20 42 P PR FIRST TERM TOTAL = 469/750. ORDN. 1 MARKS:

UNIVERSITY OF PUNE ,T.E.(2008 PAT.)(COMPUTER) EXAMINATION NOV. 2013 DATE: 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 05 (430) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71200772F , , PICT , T80054213 T80054213 BAGUL PALLAVI DEEPAK MADHAVI 01. DATABASE MANAGEMENT SYSTEMS 100 40 52 P 02. DATA COMMUNICATION 100 40 42 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 46 P 04. DIGITAL SIGNAL PROCESSING 100 40 74 P 05. THEORY OF COMPUTATION 100 40 52 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 20 39 P 50 20 32 P 07. RDBMS & VISUAL PROGRAMMING LAB. PR 08. SIGNAL PROCESSING LABORATORY 25 10 21 P TW 09. SIGNAL PROCESSING LABORATORY 50 20 30 P OR 25 10 22 P 10. HARDWARE LABORATORY TW 11. HARDWARE LABORATORY 50 20 16 F PR FIRST TERM TOTAL = 426/750. ORDN. 1 MARKS: T80054214 BAHETI BHUSHAN RAJGOPAL KIRAN , 71200773D , , PICT , T80054214 01. DATABASE MANAGEMENT SYSTEMS 100 40 49 P 100 40 51 P 02. DATA COMMUNICATION 100 40 40 P 03. MICROPROCESSORS & MICROCONTROLLERPP 04. DIGITAL SIGNAL PROCESSING 100 40 59 P 05. THEORY OF COMPUTATION 100 40 54 P 50 20 36 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 40 P 25 10 08. SIGNAL PROCESSING LABORATORY TW 19 P 09. SIGNAL PROCESSING LABORATORY OR 50 20 41 P 10. HARDWARE LABORATORY 25 10 19 P TW 11. HARDWARE LABORATORY 50 20 42 P PR FIRST TERM TOTAL = 450/750. ORDN. 1 MARKS: , 71200775L T80054215 BAJAJ KRISHNA ANJANIKUMAR **VARSHA** , PICT , т80054215 01. DATABASE MANAGEMENT SYSTEMS 100 40 62 P 02. DATA COMMUNICATION PP 100 40 56 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 41 P 04. DIGITAL SIGNAL PROCESSING 100 40 63 P 05. THEORY OF COMPUTATION 100 40 60 P 50 20 39 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 38 P 08. SIGNAL PROCESSING LABORATORY 25 10 21 P TW 09. SIGNAL PROCESSING LABORATORY 50 20 43 P 10. HARDWARE LABORATORY 25 10 20 P TW 11. HARDWARE LABORATORY 50 20 37 P PR FIRST TERM TOTAL = 480/750.

DATE: 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 06 (431)

NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER KALPANA , 71100741B , T80054206 , PICT T80054216 BALIWANT SOURABH SANJAY , т80054216 12. PRINCIPLES OF PROGRAMMING LANG. PP 100 40 59 PC 01. DATABASE MANAGEMENT SYSTEMS 100 40 43 P C PP 13. COMPUTER NETWORKS 02. DATA COMMUNICATION 100 40 61 P C 100 40 43 P C 100 40 45 P C 14. FINANCE & MANAGEMENT INFORMA.SYS.PP 100 40 55 P C 03. MICROPROCESSORS & MICROCONTROLLERPP 04. DIGITAL SIGNAL PROCESSING 100 40 44 P C 15. SYSTEMS PROGRAMMING & OPERA.SYS. PP 100 40 54 P C 05. THEORY OF COMPUTATION 100 40 44 P C 16. SOFTWARE ENGINEERING 100 40 54 P.C 50 20 33 P C 25 10 19 P C 06. RDBMS & VISUAL PROGRAMMING LAB. TW 17. SOFTWARE LABORATORY TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 44 P C 50 20 28 P C 18. SOFTWARE LABORATORY PR08. SIGNAL PROCESSING LABORATORY 25 10 16 P C 19. COMPUTER NETWORK 25 10 20 P C TW TW 09. SIGNAL PROCESSING LABORATORY 50 20 22 P C 50 20 37 P 20. COMPUTER NETWORK OR 10. HARDWARE LABORATORY 25 10 18 P C 50 20 38 P C TW 21. SOFTWARE DEVELOPMENT TOOLS LAB. TW 11. HARDWARE LABORATORY 50 20 30 P C 22. SEMINAR AND TECHNICAL COMMUNI. TW 50 20 38 P C PR GRAND TOTAL = 845/1500, RESULT: HIGHER SECOND CLASS ORDN. 1 MARKS: T80054217 BANG VIJAY SANTOSHKUMAR , 71200780G , , PICT , т80054217 JYOTI 01. DATABASE MANAGEMENT SYSTEMS 100 40 62 P 100 40 57 P 02. DATA COMMUNICATION 100 40 48 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 64 P 04. DIGITAL SIGNAL PROCESSING 05. THEORY OF COMPUTATION 100 40 57 P 50 20 41 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 44 P 08. SIGNAL PROCESSING LABORATORY TW 25 10 22 P 09. SIGNAL PROCESSING LABORATORY OR 50 20 45 P 10. HARDWARE LABORATORY 25 10 22 P TW 11. HARDWARE LABORATORY 50 20 40 P PR FIRST TERM TOTAL = 502/750. ORDN. 1 MARKS: , 71200781E , , PICT T80054218 BANGAD ANKITA ISHWARPRASAD USHA , T80054218 01. DATABASE MANAGEMENT SYSTEMS 100 40 61 P 02. DATA COMMUNICATION PP 100 40 52 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 46 P 04. DIGITAL SIGNAL PROCESSING 100 40 73 P 05. THEORY OF COMPUTATION 100 40 52 P PP 50 20 35 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 40 P 08. SIGNAL PROCESSING LABORATORY 25 10 21 P TW 09. SIGNAL PROCESSING LABORATORY 50 20 40 P 10. HARDWARE LABORATORY 25 10 21 P TW 11. HARDWARE LABORATORY 50 20 41 P PR FIRST TERM TOTAL = 482/750. ORDN. 1 MARKS:

UNIVERSITY OF PUNE ,T.E.(2008 PAT.)(COMPUTER) EXAMINATION NOV. 2013 DATE: 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 07 (432) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71200786F , , PICT , T80054219 T80054219 BAT PANKAJ RAMSINGH PUSHPA 01. DATABASE MANAGEMENT SYSTEMS 100 40 49 P 02. DATA COMMUNICATION 100 40 41 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 40 P 04. DIGITAL SIGNAL PROCESSING 100 40 48 P 05. THEORY OF COMPUTATION 100 40 46 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 20 38 P 50 20 40 P 07. RDBMS & VISUAL PROGRAMMING LAB. PR 08. SIGNAL PROCESSING LABORATORY 25 10 20 P TW 09. SIGNAL PROCESSING LABORATORY 50 20 40 P OR 10. HARDWARE LABORATORY 25 10 19 P TW 11. HARDWARE LABORATORY 50 20 36 P PR FIRST TERM TOTAL = 417/750. ORDN. 1 MARKS: T80054220 BELE ANAND PURUSHOTTAM , 71347756D , , PICT , т80054220 UJJWALA 01. DATABASE MANAGEMENT SYSTEMS 100 40 52 P 02. DATA COMMUNICATION 100 40 58 P 100 40 47 P 03. MICROPROCESSORS & MICROCONTROLLERPP 04. DIGITAL SIGNAL PROCESSING 100 40 62 P 05. THEORY OF COMPUTATION 100 40 65 P 50 20 37 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 45 P 08. SIGNAL PROCESSING LABORATORY 25 10 21 P TW 09. SIGNAL PROCESSING LABORATORY OR 50 20 28 P 10. HARDWARE LABORATORY 25 10 19 P TW 11. HARDWARE LABORATORY 50 20 32 P PR FIRST TERM TOTAL = 466/750. ORDN. 1 MARKS:

T80054221 BHADKAMKAR SIDDHARTH CHA	ARUDAT	TA		SNE	ЕНА	, 71200789L	,	,	PICT	, т80054221
01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	58	Р					
02. DATA COMMUNICATION	PP	100	40	55	Р					
03. MICROPROCESSORS & MICROCONTROLLI	ERPP	100	40	40	Р					
04. DIGITAL SIGNAL PROCESSING	PP	100	40	75	Р					
05. THEORY OF COMPUTATION	PP	100	40	68	Р					
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	38	Р					
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	40	Р					
08. SIGNAL PROCESSING LABORATORY	TW	25	10	19	Р					
09. SIGNAL PROCESSING LABORATORY	OR	50	20	25	Р					
10. HARDWARE LABORATORY	TW	25	10	19	Р					
11. HARDWARE LABORATORY	PR	50	20	35	Р					
FIRST TERM TOTAL = $472/750$.										
ORDN. 1 MARKS :										

DATE: 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 08 (433)

NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER T80054222 BHALERAO SHRADDHA VINOD SUNITA , 71347757B , , PICT , T80054222 01. DATABASE MANAGEMENT SYSTEMS 100 40 52 P 02. DATA COMMUNICATION 100 40 40 P 100 40 29 F 03. MICROPROCESSORS & MICROCONTROLLERPP 04. DIGITAL SIGNAL PROCESSING 100 40 42 P 05. THEORY OF COMPUTATION 100 40 44 P 50 20 43 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 43 P 08. SIGNAL PROCESSING LABORATORY 25 10 22 P TW 09. SIGNAL PROCESSING LABORATORY 50 20 38 P OR 25 10 22 P 10. HARDWARE LABORATORY TW 11. HARDWARE LABORATORY 50 20 26 P PR FIRST TERM TOTAL = 401/750. ORDN. 1 MARKS: T80054223 BHAWSAR SHOBHANA JAYVANT , 71347758L , , PICT , T80054223 JAYSHREE 01. DATABASE MANAGEMENT SYSTEMS 100 40 57 P 100 40 50 P 02. DATA COMMUNICATION 100 40 51 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 66 P 04. DIGITAL SIGNAL PROCESSING 05. THEORY OF COMPUTATION 100 40 52 P 50 20 38 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 40 P 25 10 18 P 08. SIGNAL PROCESSING LABORATORY TW 09. SIGNAL PROCESSING LABORATORY 50 20 40 P OR 10. HARDWARE LABORATORY 25 10 18 P TW 11. HARDWARE LABORATORY 50 20 39 P PR FIRST TERM TOTAL = 469/750. ORDN. 1 MARKS: , 71045390G T80054224 BHOIR RUPALI LAXMAN , PICT , т80054224 VANITA 01. DATABASE MANAGEMENT SYSTEMS 100 40 20 F 02. DATA COMMUNICATION PP 100 40 27 F 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 18 F 04. DIGITAL SIGNAL PROCESSING 100 40 23 F 05. THEORY OF COMPUTATION 100 40 16 F 50 20 32 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 40 P 08. SIGNAL PROCESSING LABORATORY 25 10 18 P TW 09. SIGNAL PROCESSING LABORATORY 50 20 08 F 10. HARDWARE LABORATORY 25 10 18 P TW 11. HARDWARE LABORATORY 50 20 10 F PR FIRST TERM TOTAL = 230/750. ORDN. 1 MARKS:

DATE: 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 09 (434)

NOTE: FIRST LINE : SEAT NO., NAME													
OTHER LINES: HEAD OF PASSING,	MAX.	MARKS	, M	IN. F	PASS N	MARKS, M	ARK	(S OBTAINED, P/F:PASS/FAIL, C:P	REVIO	US CAR	RY C	VER	
T80054225 BHOMBE AMOL SONAJI					 OKILA		•	, 70701380B , T80054216 ,				 т8005	
01. DATABASE MANAGEMENT SYSTEMS	PP	100	40		P C		2.	PRINCIPLES OF PROGRAMMING LANG.		100	, 40		P C
02. DATA COMMUNICATION	PP	100	40		P C			COMPUTER NETWORKS	PP	100	_		P C
03. MICROPROCESSORS & MICROCONTROLL		100	40		P C		_	FINANCE & MANAGEMENT INFORMA.SYS		100			PС
04. DIGITAL SIGNAL PROCESSING	PP	100	40	45				SYSTEMS PROGRAMMING & OPERA.SYS.		100			PС
05. THEORY OF COMPUTATION	PP	100			P C		-	SOFTWARE ENGINEERING	PP	100			PС
06. RDBMS & VISUAL PROGRAMMING LAB.			20		PC		-	SOFTWARE LABORATORY		25			PC
07. RDBMS & VISUAL PROGRAMMING LAB.			20		PC			SOFTWARE LABORATORY	PR	50			PC
08. SIGNAL PROCESSING LABORATORY	TW		10	13				COMPUTER NETWORK	TW	25	10		РC
09. SIGNAL PROCESSING LABORATORY	OR	50	-		PC			COMPUTER NETWORK	OR		20	35	
10. HARDWARE LABORATORY			10	13			-	SOFTWARE DEVELOPMENT TOOLS LAB.	_		20		РC
11. HARDWARE LABORATORY	PR		20		P C			SEMINAR AND TECHNICAL COMMUNI.	TW		20		P C
GRAND TOTAL = 785/1500, RESULT: SECO												-	
ORDN. 1 MARKS :													
T80054226 BHOSALE SHITAL MANOHAR				JA	AYA			, 71132426D , Т80054347 ,	PI	СТ	,	т8005	4226
01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	45	РС	1	2.	PRINCIPLES OF PROGRAMMING LANG.		100	40	40	РС
02. DATA COMMUNICATION	PP	100	40	40	РС	1	3.	COMPUTER NETWORKS	PP	100	40	47	РС
03. MICROPROCESSORS & MICROCONTROLL	ERPP	100	40	40	РС	1	4.	FINANCE & MANAGEMENT INFORMA.SYS	.PP	100	40	46	РС
04. DIGITAL SIGNAL PROCESSING	PP	100	40	AA	F	1	5.	SYSTEMS PROGRAMMING & OPERA.SYS.	PP	100	40	40	РС
05. THEORY OF COMPUTATION	PP	100	40	40	РС	1	6.	SOFTWARE ENGINEERING	PP	100	40	40	РС
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	34	РС	1	7.	SOFTWARE LABORATORY	TW	25	10	15	РС
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	22	РС	1	8.	SOFTWARE LABORATORY	PR	50	20	25	РС
08. SIGNAL PROCESSING LABORATORY	TW	25	10	15	РС	1	9.	COMPUTER NETWORK	TW	25	10	18	РС
09. SIGNAL PROCESSING LABORATORY	OR	50	20	22	РС	2	0.	COMPUTER NETWORK	OR	50	20	21	РС
10. HARDWARE LABORATORY	TW	25	10	18	РС	2:	1.	SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	27	РС
11. HARDWARE LABORATORY	PR	50	20	23	РС	2	2.	SEMINAR AND TECHNICAL COMMUNI.	TW	50	20	34	РС
GRAND TOTAL = 652/1500, RESULT: FAIL	S A.T.	K.T.							RESU	LT RES	ERVE	D FOR	BKLC
ORDN. 1 MARKS :													
T80054227 BHUTADA RUTUSHA KAMALKI	SHOR			SU	JNITA			, 71200798к , , ,	PI	СТ	,	т8005	4227
01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	59	Р								
02. DATA COMMUNICATION	PP	100	40	60	Р								
03. MICROPROCESSORS & MICROCONTROLL	ERPP	100	40	43	Р								
04. DIGITAL SIGNAL PROCESSING	PP	100	40	80	Р								
05. THEORY OF COMPUTATION	PP	100	40	72	Р								
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	44	Р								
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	43	Р								
08. SIGNAL PROCESSING LABORATORY	TW	25	10	24	Р								
09. SIGNAL PROCESSING LABORATORY	OR	50	20	41	Р								
10. HARDWARE LABORATORY	TW	25	10	22	Р								
11. HARDWARE LABORATORY	PR	50	20	40	Р								
FIRST TERM TOTAL = 528/750.													
ORDN. 1 MARKS :													

UNIVERSITY OF PUNE ,T.E.(2008 PAT.)(COMPUTER) EXAMINATION NOV. 2013 DATE: 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 10 (435) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER JASMINE , 71200800E , , PICT , T80054228 T80054228 BINO JOSEPH 01. DATABASE MANAGEMENT SYSTEMS 100 40 61 P 02. DATA COMMUNICATION 100 40 63 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 52 P 04. DIGITAL SIGNAL PROCESSING 100 40 79 P 05. THEORY OF COMPUTATION 100 40 70 P 50 20 40 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 46 P 08. SIGNAL PROCESSING LABORATORY 25 10 21 P TW 09. SIGNAL PROCESSING LABORATORY 50 20 28 P OR 10. HARDWARE LABORATORY 25 10 21 P TW 11. HARDWARE LABORATORY 50 20 41 P PR FIRST TERM TOTAL = 522/750. ORDN. 1 MARKS: T80054229 BORSE AKSHAY RAJENDRA , 71200804H , , PICT , т80054229 SUREKHA 100 40 01. DATABASE MANAGEMENT SYSTEMS 54 P 100 40 44 P 02. DATA COMMUNICATION 100 40 42 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 74 P 04. DIGITAL SIGNAL PROCESSING 05. THEORY OF COMPUTATION 100 40 54 P 50 20 35 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 43 P 25 10 08. SIGNAL PROCESSING LABORATORY TW 20 P 09. SIGNAL PROCESSING LABORATORY OR 50 20 30 P 10. HARDWARE LABORATORY 25 10 19 P TW 11. HARDWARE LABORATORY 50 20 01 F PR FIRST TERM TOTAL = 416/750. ORDN. 1 MARKS: , 71200806D T80054230 BUDHAWANT MAYUR SHIVAJI , PICT , т80054230 HIRABAI 01. DATABASE MANAGEMENT SYSTEMS 100 40 59 P 02. DATA COMMUNICATION PP 100 40 53 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 50 P 04. DIGITAL SIGNAL PROCESSING 100 40 65 P 05. THEORY OF COMPUTATION 100 40 61 P 50 20 42 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 42 P 08. SIGNAL PROCESSING LABORATORY 25 10 21 P TW 09. SIGNAL PROCESSING LABORATORY 50 20 42 P 10. HARDWARE LABORATORY 25 10 21 P TW 11. HARDWARE LABORATORY 50 20 40 P PR FIRST TERM TOTAL = 496/750. ORDN. 1 MARKS:

DATE: 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 11 (436)

NOTE: FIRST LINE : SEAT NO., NAME O	F THE	CANDI	DATE	, MO	THER,	PERMANEN	Γ REG. NO.,	PREVIO	OUS SE	AT NO.,	COI	LLEGE,	, S	EAT	NO.	
T80054231 CHAUDHAR VISHAL SHESHRAC					VITA	12	-	0762E	-		-					54231
01. DATABASE MANAGEMENT SYSTEMS		100			P C		. PRINCIPLES			ING LAN			100			PC
02. DATA COMMUNICATION		100			PC		. COMPUTER N						100	_		P C
03. MICROPROCESSORS & MICROCONTROLLE		100	40		P C		. FINANCE &							40	53	P C
04. DIGITAL SIGNAL PROCESSING	PP 	100	_		P C		. SYSTEMS PF			OPERA.S			100	_	66	
05. THEORY OF COMPUTATION	PP	100			P C	_	. SOFTWARE E					PP 	100	_		P C
06. RDBMS & VISUAL PROGRAMMING LAB.			20		P C		. SOFTWARE L					ΓW 	_	10		P C
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50			РС		. SOFTWARE L					PR		20		РС
08. SIGNAL PROCESSING LABORATORY	TW	25			РС		. COMPUTER N				-	ΓW		10		РС
09. SIGNAL PROCESSING LABORATORY	OR		20		РС		. COMPUTER N					OR		20	30	
10. HARDWARE LABORATORY	TW	25	10	18	РС	21	. SOFTWARE [DEVELOP	MENT TO	OOLS LA	λB	ΓW		20		РС
11. HARDWARE LABORATORY	PR		20	24	РС	22	. SEMINAR AN	ND TECHN	NICAL (COMMUNI	Ι	ΓW	50	20	32	РС
GRAND TOTAL = $827/1500$, RESULT: HIGHE	R SEC	OND CL	.ASS													
ORDN. 1 MARKS :																
T80054232 CHAUDHARI KALYANI BABASA	HEB			NA	NDA		, 71347	7759յ	,		,	PICT	Γ	,	т8005	4232
01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	67	Р											
02. DATA COMMUNICATION	PP	100	40	61	Р											
03. MICROPROCESSORS & MICROCONTROLLE	RPP	100	40	59	Р											
04. DIGITAL SIGNAL PROCESSING	PP	100	40	77	Р											
05. THEORY OF COMPUTATION	PP	100	40	51	Р											
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	43	Р											
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	44	Р											
08. SIGNAL PROCESSING LABORATORY	TW	25	10	23	Р											
09. SIGNAL PROCESSING LABORATORY	OR	50	20	44	Р											
10. HARDWARE LABORATORY	TW	25	10	23	Р											
11. HARDWARE LABORATORY	PR	50	20	41	Р											
FIRST TERM TOTAL = 533/750.																
ORDN. 1 MARKS :																
T80054233 CHAUHAN KRUTI SANDEEP							 , 71200									 54233
01. DATABASE MANAGEMENT SYSTEMS	DD	100	40	49	ETA		, /1200	00130	,		,	PICT	l	,	10003	14233
	PP	100		_												
02. DATA COMMUNICATION	PP	100	40	49												
03. MICROPROCESSORS & MICROCONTROLLE		100		40												
04. DIGITAL SIGNAL PROCESSING	PP	100		60												
05. THEORY OF COMPUTATION	PP	100		58												
06. RDBMS & VISUAL PROGRAMMING LAB.			20	37												
07. RDBMS & VISUAL PROGRAMMING LAB.	PR		20	40												
08. SIGNAL PROCESSING LABORATORY	TW	25		20												
09. SIGNAL PROCESSING LABORATORY	OR		20	28												
10. HARDWARE LABORATORY	TW	25		19												
11. HARDWARE LABORATORY	PR	50	20	35	Р											
FIRST TERM TOTAL = $435/750$.																
ORDN. 1 MARKS :																

DATE: 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 12 (437)

NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71200814E , , PICT , T80054234 T80054234 CHAVAN GAURAV SANDIP MEENAKSHI 01. DATABASE MANAGEMENT SYSTEMS 100 40 51 P 02. DATA COMMUNICATION 100 40 40 P 100 40 40 P 03. MICROPROCESSORS & MICROCONTROLLERPP 04. DIGITAL SIGNAL PROCESSING 100 40 60 P 05. THEORY OF COMPUTATION 100 40 55 P 50 20 43 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 40 P 08. SIGNAL PROCESSING LABORATORY 25 10 21 P TW 09. SIGNAL PROCESSING LABORATORY 50 20 41 P OR 10. HARDWARE LABORATORY 25 10 21 P TW 11. HARDWARE LABORATORY 50 20 33 P PR FIRST TERM TOTAL = 445/750. ORDN. 1 MARKS: T80054235 CHAVAN PRACHI RAMKRISHNA , 71200815C , , PICT , т80054235 SADHANA 01. DATABASE MANAGEMENT SYSTEMS 100 40 51 P 100 40 53 P 02. DATA COMMUNICATION 100 40 54 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 61 P 04. DIGITAL SIGNAL PROCESSING 05. THEORY OF COMPUTATION 100 40 61 P 50 20 42 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 44 P 25 10 23 P 08. SIGNAL PROCESSING LABORATORY TW 09. SIGNAL PROCESSING LABORATORY OR 50 20 42 P 10. HARDWARE LABORATORY 25 10 22 P TW 11. HARDWARE LABORATORY 50 20 42 P PR FIRST TERM TOTAL = 495/750. ORDN. 1 MARKS: , 71200819F T80054236 CHAVHAN KUNAL RAVINDRA , PICT , т80054236 LALITA 01. DATABASE MANAGEMENT SYSTEMS 100 40 45 P 02. DATA COMMUNICATION PP 100 40 AA F 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 23 F 04. DIGITAL SIGNAL PROCESSING 100 40 42 P 05. THEORY OF COMPUTATION 100 40 45 P 50 20 34 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 38 P 08. SIGNAL PROCESSING LABORATORY 25 10 16 P TW 50 20 22 P 09. SIGNAL PROCESSING LABORATORY 10. HARDWARE LABORATORY 25 10 15 P TW 11. HARDWARE LABORATORY 50 20 33 P PR FIRST TERM TOTAL = 313/750. ORDN. 1 MARKS:

DATE: 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 13 (438)

NOTE: FIRST LINE : SEAT NO., NAME (OF THE	CANDI	DATE	, MO	THER, F	PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. RKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
T80054237 DAMSE SAYALI LAXMAN				LA	ATA	, 71100774J , T80054225 , PICT , T80054237
01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	40	PC	12. PRINCIPLES OF PROGRAMMING LANG. PP 100 40 54 P C
02. DATA COMMUNICATION	PP	100	40	40	PC	13. COMPUTER NETWORKS PP 100 40 47 P C
03. MICROPROCESSORS & MICROCONTROLL	ERPP	100	40	43	PC	14. FINANCE & MANAGEMENT INFORMA.SYS.PP 100 40 47 P C
04. DIGITAL SIGNAL PROCESSING	PP	100	40	40	PC	15. SYSTEMS PROGRAMMING & OPERA.SYS. PP 100 40 52 P C
05. THEORY OF COMPUTATION	PP	100	40	40	РС	16. SOFTWARE ENGINEERING PP 100 40 PC
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	31	РС	17. SOFTWARE LABORATORY TW 25 10 20 P C
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	42	РС	18. SOFTWARE LABORATORY PR 50 20 25 P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	18	PC	19. COMPUTER NETWORK TW 25 10 22 P C
09. SIGNAL PROCESSING LABORATORY	OR	50	20	28	PC	20. COMPUTER NETWORK OR 50 20 30 P
10. HARDWARE LABORATORY	TW	25	10	17	PC	21. SOFTWARE DEVELOPMENT TOOLS LAB. TW 50 20 36 P C
11. HARDWARE LABORATORY	PR	50	20	26	PC	22. SEMINAR AND TECHNICAL COMMUNI. TW 50 20 38 P C
GRAND TOTAL = 776/1500, RESULT: SECON	ND CLA	SS				
ORDN. 1 MARKS :						
T80054238 DESAI SHAILEE MIHIR				PA	LLAVI	, 71200830G , , ,
01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	55	Р	
02. DATA COMMUNICATION	PP	100	40	45	Р	
03. MICROPROCESSORS & MICROCONTROLL	ERPP	100	40	42	Р	
04. DIGITAL SIGNAL PROCESSING	PP	100	40	40	Р	
05. THEORY OF COMPUTATION	PP	100	40	49	Р	
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	39	Р	
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	32	Р	
08. SIGNAL PROCESSING LABORATORY	TW	25	10	20	Р	
09. SIGNAL PROCESSING LABORATORY	OR	50	20	24	Р	
10. HARDWARE LABORATORY	TW	25	10	21	Р	
11. HARDWARE LABORATORY	PR	50	20	39	Р	
FIRST TERM TOTAL = 406/750.						
ORDN. 1 MARKS :						
						71347760-
T80054239 DESHMUKH POOJA DEEPAK		100	40		NCHAN	, 71347760в , , , , рІСТ , т80054239
01. DATABASE MANAGEMENT SYSTEMS	PP	100		62		
02. DATA COMMUNICATION	PP	100	40	47		
03. MICROPROCESSORS & MICROCONTROLLI		100	40	56		
04. DIGITAL SIGNAL PROCESSING	PP	100		59		
05. THEORY OF COMPUTATION	PP	100		60		
06. RDBMS & VISUAL PROGRAMMING LAB.			20	43		
07. RDBMS & VISUAL PROGRAMMING LAB.	PR		20	44		
08. SIGNAL PROCESSING LABORATORY	TW		10	22		
09. SIGNAL PROCESSING LABORATORY	OR		20	32		
10. HARDWARE LABORATORY	TW	25		22		
11. HARDWARE LABORATORY	PR	50	20	34	Р	
FIRST TERM TOTAL = $481/750$.						
ORDN. 1 MARKS :						

DATE: 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 14 (439)

NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71200837D , , PICT , T80054240 T80054240 DHAGE SHRADDHA BHAUSAHEB SUMAN 01. DATABASE MANAGEMENT SYSTEMS 100 40 58 P PP 02. DATA COMMUNICATION 100 40 55 P 100 40 49 P 03. MICROPROCESSORS & MICROCONTROLLERPP 04. DIGITAL SIGNAL PROCESSING 100 40 50 P 05. THEORY OF COMPUTATION 100 40 57 P 50 20 41 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 42 P 08. SIGNAL PROCESSING LABORATORY 25 10 22 P TW 09. SIGNAL PROCESSING LABORATORY 50 20 31 P OR 10. HARDWARE LABORATORY 25 10 21 P TW 11. HARDWARE LABORATORY 50 20 36 P PR FIRST TERM TOTAL = 462/750. ORDN. 1 MARKS: T80054241 DHAPTE OMKAR HANUMANT , 71239295F , T80054231 , PICT , т80054241 MINAXI 56 P C 12. PRINCIPLES OF PROGRAMMING LANG. PP 100 40 64 P C 01. DATABASE MANAGEMENT SYSTEMS 100 40 100 40 62 P C 100 40 64 P C 02. DATA COMMUNICATION PP 13. COMPUTER NETWORKS 61 P C 100 40 100 40 03. MICROPROCESSORS & MICROCONTROLLERPP 14. FINANCE & MANAGEMENT INFORMA.SYS.PP 61 P C 100 40 53 P C 100 40 71 P C 04. DIGITAL SIGNAL PROCESSING 15. SYSTEMS PROGRAMMING & OPERA.SYS. PP 05. THEORY OF COMPUTATION 100 40 40 P C 100 40 16. SOFTWARE ENGINEERING 62 P C 50 20 33 P C 25 10 12 P C 06. RDBMS & VISUAL PROGRAMMING LAB. TW 17. SOFTWARE LABORATORY TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 39 P C 50 20 18. SOFTWARE LABORATORY PR 40 P C 25 10 15 P C 25 10 15 P C 08. SIGNAL PROCESSING LABORATORY TW 19. COMPUTER NETWORK TW 21 P C 09. SIGNAL PROCESSING LABORATORY 50 20 20. COMPUTER NETWORK 50 20 32 P OR 10. HARDWARE LABORATORY 25 10 18 P C 50 20 25 P C TW 21. SOFTWARE DEVELOPMENT TOOLS LAB. TW 11. HARDWARE LABORATORY 50 20 25 P C 50 20 31 P C PR 22. SEMINAR AND TECHNICAL COMMUNI. TW GRAND TOTAL = 900/1500, RESULT: FIRST CLASS ORDN. 1 MARKS: , PICT , 71200839L , т80054242 T80054242 DHAVALIKAR RAHUL DEEPAK SMITA 01. DATABASE MANAGEMENT SYSTEMS 100 40 57 P 02. DATA COMMUNICATION PP 100 40 44 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 55 P 04. DIGITAL SIGNAL PROCESSING 100 40 53 P 05. THEORY OF COMPUTATION 100 40 68 P PP 50 20 39 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 42 P 08. SIGNAL PROCESSING LABORATORY 25 10 19 P TW 09. SIGNAL PROCESSING LABORATORY 50 20 34 P 10. HARDWARE LABORATORY 25 10 18 P TW 11. HARDWARE LABORATORY 50 20 43 P PR FIRST TERM TOTAL = 472/750. ORDN. 1 MARKS:

UNIVERSITY OF PUNE ,T.E.(2008 PAT.)(COMPUTER) EXAMINATION NOV. 2013 DATE: 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 15 (440) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71200840D , , PICT , T80054243 T80054243 DHEKALE AMRUTA RAMBHAU KANTABAI 01. DATABASE MANAGEMENT SYSTEMS 100 40 72 P 02. DATA COMMUNICATION 100 40 46 P 100 40 49 P 03. MICROPROCESSORS & MICROCONTROLLERPP 04. DIGITAL SIGNAL PROCESSING 100 40 56 P 05. THEORY OF COMPUTATION 100 40 67 P 50 20 35 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 32 P 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 08. SIGNAL PROCESSING LABORATORY 25 10 TW 18 P 09. SIGNAL PROCESSING LABORATORY 50 20 22 P OR 10. HARDWARE LABORATORY 25 10 18 P TW 11. HARDWARE LABORATORY 50 20 28 P PR FIRST TERM TOTAL = 443/750. ORDN. 1 MARKS: T80054244 DHOLE ANIKET RAJENDRA , 71200841B , , PICT , т80054244 SUNITA 01. DATABASE MANAGEMENT SYSTEMS 100 40 68 P 100 40 54 P 02. DATA COMMUNICATION 100 40 50 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 66 P 04. DIGITAL SIGNAL PROCESSING 05. THEORY OF COMPUTATION 100 40 66 P 50 20 34 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 40 P 25 10 17 P 08. SIGNAL PROCESSING LABORATORY TW 09. SIGNAL PROCESSING LABORATORY 50 20 25 P OR 10. HARDWARE LABORATORY 25 10 15 P TW 11. HARDWARE LABORATORY 50 20 39 P PR FIRST TERM TOTAL = 474/750. ORDN. 1 MARKS: , 71200844G T80054245 DINGANE RASHMI SUNIL , PICT , т80054245 CHITRA 01. DATABASE MANAGEMENT SYSTEMS 100 40 75 P 02. DATA COMMUNICATION PP 100 40 66 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 62 P 04. DIGITAL SIGNAL PROCESSING 100 40 57 P 05. THEORY OF COMPUTATION 100 40 55 P 50 20 40 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 41 P 08. SIGNAL PROCESSING LABORATORY 25 10 20 P TW 09. SIGNAL PROCESSING LABORATORY 50 20 40 P 10. HARDWARE LABORATORY 25 10 20 P TW 11. HARDWARE LABORATORY 50 20 39 P PR FIRST TERM TOTAL = 515/750. ORDN. 1 MARKS:

DATE: 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 16 (441)

NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71100789G , T80054234 , PICT , T80054246 T80054246 DOIPHODE NISHIGANDHA SANJAY MADHUBALA 01. DATABASE MANAGEMENT SYSTEMS 100 40 49 P C 12. PRINCIPLES OF PROGRAMMING LANG. PP 100 40 58 PC 13. COMPUTER NETWORKS 02. DATA COMMUNICATION 100 40 44 P C 100 40 56 P C 100 40 45 P C 14. FINANCE & MANAGEMENT INFORMA.SYS.PP 100 40 51 P.C 03. MICROPROCESSORS & MICROCONTROLLERPP 04. DIGITAL SIGNAL PROCESSING 100 40 41 P C 15. SYSTEMS PROGRAMMING & OPERA.SYS. PP 100 40 58 P C 05. THEORY OF COMPUTATION 100 40 40 P C 100 40 50 P.C 16. SOFTWARE ENGINEERING 50 20 36 P C 25 10 19 P C 06. RDBMS & VISUAL PROGRAMMING LAB. TW 17. SOFTWARE LABORATORY TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 40 P C 50 20 42 P 18. SOFTWARE LABORATORY PR 08. SIGNAL PROCESSING LABORATORY 25 10 17 P C 19. COMPUTER NETWORK 25 10 23 P C TW TW 50 20 32 P C 50 20 30 P C 09. SIGNAL PROCESSING LABORATORY 20. COMPUTER NETWORK OR 10. HARDWARE LABORATORY 25 10 19 P C 50 20 39 P C TW 21. SOFTWARE DEVELOPMENT TOOLS LAB. TW PR 11. HARDWARE LABORATORY 50 20 30 P C 22. SEMINAR AND TECHNICAL COMMUNI. TW 50 20 40 P C GRAND TOTAL = 859/1500, RESULT: HIGHER SECOND CLASS ORDN. 1 MARKS: , 71045424E , T80054236 , PICT , T80054247 T80054247 DURUGKAR SURAJ ANIL NIRMALA 01. DATABASE MANAGEMENT SYSTEMS 100 40 48 P 100 40 44 P 02. DATA COMMUNICATION PP 100 40 30 F 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 23 F 04. DIGITAL SIGNAL PROCESSING 100 40 45 P 05. THEORY OF COMPUTATION 50 20 22 P C 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 36 P C 50 20 08. SIGNAL PROCESSING LABORATORY TW 25 10 12 P C 09. SIGNAL PROCESSING LABORATORY 50 20 21 P C OR 10. HARDWARE LABORATORY 25 10 12 P C TW 50 20 20 P C 11. HARDWARE LABORATORY PR FIRST TERM TOTAL = 313/750. ORDN. 1 MARKS: , 71045426M , T80054238 , PICT JANABAI , т80054248 T80054248 GABHALE VARSHA LIMBAJI 01. DATABASE MANAGEMENT SYSTEMS 100 40 42 P C 12. PRINCIPLES OF PROGRAMMING LANG. PP 100 40 46 PC 02. DATA COMMUNICATION PP 100 40 48 P C 13. COMPUTER NETWORKS PP 100 40 40 P C 40 P C 100 40 100 40 40 P 03. MICROPROCESSORS & MICROCONTROLLERPP 14. FINANCE & MANAGEMENT INFORMA.SYS.PP 04. DIGITAL SIGNAL PROCESSING 100 40 41 P C 15. SYSTEMS PROGRAMMING & OPERA.SYS. PP 100 40 45 P.C. 05. THEORY OF COMPUTATION 100 40 40 P C PP 100 40 40 P C PP 16. SOFTWARE ENGINEERING 50 20 33 P C TW 25 10 21 P C 06. RDBMS & VISUAL PROGRAMMING LAB. TW 17. SOFTWARE LABORATORY 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 22 P C 18. SOFTWARE LABORATORY 50 20 25 P C PR 22 P C 08. SIGNAL PROCESSING LABORATORY 25 10 18 P C 19. COMPUTER NETWORK TW 25 10 TW 09. SIGNAL PROCESSING LABORATORY 50 20 32 P C 50 20 26 P C 20. COMPUTER NETWORK 10. HARDWARE LABORATORY 25 10 20 P C 21. SOFTWARE DEVELOPMENT TOOLS LAB. TW 50 20 40 P C TW 11. HARDWARE LABORATORY 50 20 34 P C 22. SEMINAR AND TECHNICAL COMMUNI. TW 50 20 40 P C PR GRAND TOTAL = 755/1500, RESULT: SECOND CLASS ORDN. 1 MARKS:

DATE: 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 17 (442)

NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER SINDHU , 71200852H , , PICT , T80054250 T80054250 GAIKWAD KAJAL PANDURANG 01. DATABASE MANAGEMENT SYSTEMS 100 40 67 P 02. DATA COMMUNICATION 100 40 66 P 100 40 53 P 03. MICROPROCESSORS & MICROCONTROLLERPP 04. DIGITAL SIGNAL PROCESSING 100 40 55 P 05. THEORY OF COMPUTATION 100 40 53 P 50 20 41 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 34 P 08. SIGNAL PROCESSING LABORATORY 25 10 22 P TW 09. SIGNAL PROCESSING LABORATORY 50 20 32 P OR 25 10 22 P 10. HARDWARE LABORATORY TW 11. HARDWARE LABORATORY 50 20 42 P PR FIRST TERM TOTAL = 487/750. ORDN. 1 MARKS: , 71045431H , T80054350 , PICT , T80054251 T80054251 GAIKWAD MAHESH EKNATH LATA 40 P C 12. PRINCIPLES OF PROGRAMMING LANG. PP 100 40 PC 01. DATABASE MANAGEMENT SYSTEMS 100 40 100 40 42 P C 100 40 40 P C 02. DATA COMMUNICATION PP 13. COMPUTER NETWORKS PP 100 40 42 P C 100 40 03. MICROPROCESSORS & MICROCONTROLLERPP 14. FINANCE & MANAGEMENT INFORMA.SYS.PP 46 P C 100 40 45 P C 100 40 40 P C 04. DIGITAL SIGNAL PROCESSING 15. SYSTEMS PROGRAMMING & OPERA.SYS. PP 05. THEORY OF COMPUTATION 100 40 41 P C 100 40 16. SOFTWARE ENGINEERING 44 P C 50 20 34 P C 25 10 06. RDBMS & VISUAL PROGRAMMING LAB. TW 17. SOFTWARE LABORATORY TW 10 P C 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 25 P C 50 20 10 F 18. SOFTWARE LABORATORY PR 25 10 25 10 15 P C 08. SIGNAL PROCESSING LABORATORY TW 14 P C 19. COMPUTER NETWORK TW 09. SIGNAL PROCESSING LABORATORY OR 50 20 20 P C 20. COMPUTER NETWORK 50 20 35 P C 10. HARDWARE LABORATORY 25 10 13 P C 50 20 20 P C TW 21. SOFTWARE DEVELOPMENT TOOLS LAB. TW 11. HARDWARE LABORATORY 50 20 20 P C 50 20 27 P C PR 22. SEMINAR AND TECHNICAL COMMUNI. TW GRAND TOTAL = 663/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS: T80054252 GAIKWAD POOJA ANIL SWATI , T80054252 01. DATABASE MANAGEMENT SYSTEMS 100 40 68 P 02. DATA COMMUNICATION PP 100 40 58 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 54 P 04. DIGITAL SIGNAL PROCESSING 100 40 21 F 05. THEORY OF COMPUTATION 100 40 52 P PP 50 20 38 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 40 P 08. SIGNAL PROCESSING LABORATORY 25 10 21 P TW 09. SIGNAL PROCESSING LABORATORY 50 20 21 P 10. HARDWARE LABORATORY 25 10 21 P TW 11. HARDWARE LABORATORY 50 20 32 P PR FIRST TERM TOTAL = 426/750. ORDN. 1 MARKS:

DATE: 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 18 (443)

NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71200858G , , PICT , T80054253 T80054253 GAURAV MISHRA MAMATA 01. DATABASE MANAGEMENT SYSTEMS 100 40 66 P 02. DATA COMMUNICATION 100 40 56 P 100 40 53 P 03. MICROPROCESSORS & MICROCONTROLLERPP 04. DIGITAL SIGNAL PROCESSING 100 40 58 P 05. THEORY OF COMPUTATION 100 40 63 P 50 20 41 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 42 P 08. SIGNAL PROCESSING LABORATORY 25 10 19 P TW 09. SIGNAL PROCESSING LABORATORY 50 20 35 P OR 10. HARDWARE LABORATORY 25 10 20 P TW 11. HARDWARE LABORATORY 50 20 33 P PR FIRST TERM TOTAL = 486/750. ORDN. 1 MARKS: T80054254 GAURI SANDEEP DESHMUKH , 71200859E , , PICT , т80054254 AARTI 01. DATABASE MANAGEMENT SYSTEMS 100 40 71 P 100 40 69 P 02. DATA COMMUNICATION 100 40 46 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 48 P 04. DIGITAL SIGNAL PROCESSING 05. THEORY OF COMPUTATION 100 40 51 P 50 20 41 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 43 P 50 20 08. SIGNAL PROCESSING LABORATORY TW 25 10 23 P 09. SIGNAL PROCESSING LABORATORY 50 20 37 P OR 10. HARDWARE LABORATORY 25 10 23 P TW 11. HARDWARE LABORATORY 50 20 39 P PR FIRST TERM TOTAL = 491/750. ORDN. 1 MARKS: , 71100799D , T80054241 , PICT T80054255 GAVALI TANUJA KASHINATH **PARVATA** , T80054255 01. DATABASE MANAGEMENT SYSTEMS 100 40 52 P C 12. PRINCIPLES OF PROGRAMMING LANG. PP 100 40 58 PC PP 02. DATA COMMUNICATION PP 100 40 44 P C 13. COMPUTER NETWORKS PP 100 40 53 P.C 100 40 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 47 P C 14. FINANCE & MANAGEMENT INFORMA.SYS.PP 50 P C 04. DIGITAL SIGNAL PROCESSING 100 40 40 P C 15. SYSTEMS PROGRAMMING & OPERA.SYS. PP 100 40 49 P C 05. THEORY OF COMPUTATION 100 40 40 P C PP 100 40 53 P C PP 16. SOFTWARE ENGINEERING 50 20 32 P C 19 P C 06. RDBMS & VISUAL PROGRAMMING LAB. TW 17. SOFTWARE LABORATORY TW 25 10 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 25 P C 18. SOFTWARE LABORATORY 50 20 27 P PR 08. SIGNAL PROCESSING LABORATORY 25 10 16 P C 19. COMPUTER NETWORK TW 25 10 19 P C TW 32 P C 50 20 09. SIGNAL PROCESSING LABORATORY 50 20 21 P C 20. COMPUTER NETWORK 10. HARDWARE LABORATORY 25 10 15 P C 21. SOFTWARE DEVELOPMENT TOOLS LAB. TW 50 20 39 P C TW 11. HARDWARE LABORATORY 50 20 22 P C 22. SEMINAR AND TECHNICAL COMMUNI. TW 50 20 36 P C PR GRAND TOTAL = 789/1500, RESULT: SECOND CLASS ORDN. 1 MARKS:

UNIVERSITY OF PUNE ,T.E.(2008 PAT.)(COMPUTER) EXAMINATION NOV. 2013 DATE: 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 19 (444) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71200861G , , PICT , T80054256 T80054256 GAWALI PRAJAKTA PANDURANG SHOBHA 01. DATABASE MANAGEMENT SYSTEMS 100 40 44 P 02. DATA COMMUNICATION 100 40 45 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 22 F 04. DIGITAL SIGNAL PROCESSING 100 40 27 F 05. THEORY OF COMPUTATION 100 40 18 F 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 20 35 P 32 P 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 08. SIGNAL PROCESSING LABORATORY 25 10 15 P TW 09. SIGNAL PROCESSING LABORATORY 50 20 28 P OR 10. HARDWARE LABORATORY 25 10 15 P TW 11. HARDWARE LABORATORY 50 20 22 P PR FIRST TERM TOTAL = 303/750. ORDN. 1 MARKS: T80054257 GAWANDE KETKI NANDKISHORE , 71200862E , , PICT , т80054257 SUSHAMA 01. DATABASE MANAGEMENT SYSTEMS 100 40 68 P 02. DATA COMMUNICATION 100 40 52 P 100 40 43 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 46 P 04. DIGITAL SIGNAL PROCESSING 05. THEORY OF COMPUTATION 100 40 51 P 50 20 38 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 42 P 25 10 19 P 08. SIGNAL PROCESSING LABORATORY TW 09. SIGNAL PROCESSING LABORATORY OR 50 20 38 P 10. HARDWARE LABORATORY 25 10 20 P TW 11. HARDWARE LABORATORY 50 20 37 P PR FIRST TERM TOTAL = 454/750. ORDN. 1 MARKS: , 71200870F T80054258 GOVIND BAPU CHAUDHARY , PICT , т80054258 SUMAN 01. DATABASE MANAGEMENT SYSTEMS 100 40 74 P 02. DATA COMMUNICATION PP 100 40 58 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 49 P 04. DIGITAL SIGNAL PROCESSING 100 40 45 P 05. THEORY OF COMPUTATION 100 40 62 P 50 20 45 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 45 P 08. SIGNAL PROCESSING LABORATORY 25 10 20 P TW 09. SIGNAL PROCESSING LABORATORY 50 20 33 P 10. HARDWARE LABORATORY 25 10 21 P TW 11. HARDWARE LABORATORY 50 20 39 P PR FIRST TERM TOTAL = 491/750.

ORDN. 1 MARKS:

UNIVERSITY OF PUNE ,T.E.(2008 PAT.)(COMPUTER) EXAMINATION NOV. 2013 DATE: 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 20 (445) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71100812E , , PICT , T80054259 T80054259 GUNJAL DIPALI BHANUDAS SUREKHA 01. DATABASE MANAGEMENT SYSTEMS 100 40 55 P 02. DATA COMMUNICATION 100 40 53 P 100 40 29 F 03. MICROPROCESSORS & MICROCONTROLLERPP 04. DIGITAL SIGNAL PROCESSING 100 40 27 F 05. THEORY OF COMPUTATION 100 40 40 P 50 20 20 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 39 P 08. SIGNAL PROCESSING LABORATORY 25 10 12 P TW 09. SIGNAL PROCESSING LABORATORY 50 20 07 F OR 10. HARDWARE LABORATORY 25 10 13 P TW 11. HARDWARE LABORATORY 50 20 04 F PR FIRST TERM TOTAL = 299/750. ORDN. 1 MARKS: T80054260 HALKARE PRAJAKTA DNYANDEO , 71200876E , , PICT , т80054260 SANGITA 01. DATABASE MANAGEMENT SYSTEMS 100 40 64 P 100 40 68 P 02. DATA COMMUNICATION 100 40 40 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 63 P 04. DIGITAL SIGNAL PROCESSING 05. THEORY OF COMPUTATION 100 40 59 P 50 20 44 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 41 P 25 10 22 P 08. SIGNAL PROCESSING LABORATORY TW 09. SIGNAL PROCESSING LABORATORY OR 50 20 35 P 10. HARDWARE LABORATORY 25 10 21 P TW 11. HARDWARE LABORATORY 50 20 42 P PR FIRST TERM TOTAL = 499/750. ORDN. 1 MARKS: , 71200882K T80054261 HOOLI MAYURESH BASAVARAJ , PICT , T80054261 SHAILA 01. DATABASE MANAGEMENT SYSTEMS 100 40 61 P 02. DATA COMMUNICATION PP 100 40 57 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 40 P 04. DIGITAL SIGNAL PROCESSING 100 40 23 F 05. THEORY OF COMPUTATION 100 40 55 P 50 20 37 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 38 P 08. SIGNAL PROCESSING LABORATORY 25 10 19 P TW 50 20 22 P 09. SIGNAL PROCESSING LABORATORY 10. HARDWARE LABORATORY 25 10 19 P TW 11. HARDWARE LABORATORY 50 20 32 P PR FIRST TERM TOTAL = 403/750.

ORDN. 1 MARKS :

DATE: 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 21 (446)

DATE . II MAR. 2014	CENT	KE . P	UNE	TN211	TOTE OF	COMPUTER	IECHNOLOGY, PUN	L.	PAGE NO.	21 (440)
NOTE: ETPCT LINE : CEAT NO NAME (DMANENT DE		OUS SEAT NO		
NOTE: FIRST LINE: SEAT NO., NAME (-	•		•		•	
OTHER LINES: HEAD OF PASSING,										
T80054262 JADHAV AMOL JETA					 \TA		, 71045453J			
	PP	100	40		PC		, /10434333	, 10003423	2 , PICI	, 180034202
02. DATA COMMUNICATION	PP	100	40		P C					
03. MICROPROCESSORS & MICROCONTROLLE		100	40		P C					
04. DIGITAL SIGNAL PROCESSING	PP	100	40	45						
05. THEORY OF COMPUTATION	PP	100	40	AA						
06. RDBMS & VISUAL PROGRAMMING LAB.		50	20		P C					
07. RDBMS & VISUAL PROGRAMMING LAB.		50	20	12						
08. SIGNAL PROCESSING LABORATORY	TW		10		P C					
09. SIGNAL PROCESSING LABORATORY		50	20	05						
10. HARDWARE LABORATORY		25			P C					
11. HARDWARE LABORATORY		_	20	AA						
FIRST TERM TOTAL = 269/750.	FK	30	20	AA	Г					
ORDN. 1 MARKS :										
ORDIN. I MARKS .										
T80054263 JADHAV RUTUJA RAMESH				 NA	 NDA		, 71347762J	,		, т80054263
01. DATABASE MANAGEMENT SYSTEMS	DD	100	40	75			, 713477023	,	, FICT	, 100034203
02. DATA COMMUNICATION	PP	100	40	72						
03. MICROPROCESSORS & MICROCONTROLLE		100	40	65						
04. DIGITAL SIGNAL PROCESSING	PP	100	40	43						
05. THEORY OF COMPUTATION	PP	100	40	70						
06. RDBMS & VISUAL PROGRAMMING LAB.		50		35						
07. RDBMS & VISUAL PROGRAMMING LAB.			20	40						
08. SIGNAL PROCESSING LABORATORY	TW	25		18						
09. SIGNAL PROCESSING LABORATORY	OR	50	20	28						
10. HARDWARE LABORATORY	TW		10	18						
11. HARDWARE LABORATORY	PR		20	34						
FIRST TERM TOTAL = 498/750.	110	30	20	34	•					
ORDN. 1 MARKS :										
T80054264 JAGTAP AMRUTA SATISH			•		NJIVANI		71200005-	,		т80054264
	PP	100	40	80			, 12200000	,	,	, 100031201
02. DATA COMMUNICATION	PP	100	40	76						
03. MICROPROCESSORS & MICROCONTROLLE		100		56						
04. DIGITAL SIGNAL PROCESSING	PP	100		54						
05. THEORY OF COMPUTATION	PP	100		67						
06. RDBMS & VISUAL PROGRAMMING LAB.			20	45						
07. RDBMS & VISUAL PROGRAMMING LAB.			20	42						
08. SIGNAL PROCESSING LABORATORY	TW		10	23						
09. SIGNAL PROCESSING LABORATORY	OR		20	46						
	TW		10	23						
11. HARDWARE LABORATORY	PR		20	43						
FIRST TERM TOTAL = 555/750.		50	_0	.5	•					
ORDN. 1 MARKS :										

UNIVERSITY OF PUNE ,T.E.(2008 PAT.)(COMPUTER) EXAMINATION NOV. 2013 DATE: 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 22 (447) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71200892G , , PICT , T80054265 T80054265 JOSHI AKSHAY ANIL JYOTI 01. DATABASE MANAGEMENT SYSTEMS 100 40 68 P 02. DATA COMMUNICATION 100 40 63 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 54 P 04. DIGITAL SIGNAL PROCESSING 100 40 51 P 05. THEORY OF COMPUTATION 100 40 61 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 20 42 P 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 44 P 08. SIGNAL PROCESSING LABORATORY 25 10 22 P TW 09. SIGNAL PROCESSING LABORATORY 50 20 44 P OR 10. HARDWARE LABORATORY 25 10 20 P TW 11. HARDWARE LABORATORY 50 20 40 P PR FIRST TERM TOTAL = 509/750. ORDN. 1 MARKS: T80054266 JOSHI AVANI AVINASH , 71200893E , , PICT , т80054266 ASHWINI 01. DATABASE MANAGEMENT SYSTEMS 100 40 61 P 100 40 60 P 02. DATA COMMUNICATION 100 40 47 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 40 P 04. DIGITAL SIGNAL PROCESSING 05. THEORY OF COMPUTATION 100 40 42 P 50 20 32 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 15 F 25 10 19 P 08. SIGNAL PROCESSING LABORATORY TW 09. SIGNAL PROCESSING LABORATORY OR 50 20 40 P 10. HARDWARE LABORATORY 25 10 19 P TW 11. HARDWARE LABORATORY 50 20 02 F PR FIRST TERM TOTAL = 377/750. ORDN. 1 MARKS: , 71200897н T80054267 JOSHIPURA RUJUTA SHISHIR , PICT , т80054267 MEGHAL 01. DATABASE MANAGEMENT SYSTEMS 100 40 76 P 02. DATA COMMUNICATION PP 100 40 75 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 64 P 04. DIGITAL SIGNAL PROCESSING 100 40 50 P 05. THEORY OF COMPUTATION 100 40 71 P 50 20 39 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 43 P 08. SIGNAL PROCESSING LABORATORY 25 10 19 P TW 09. SIGNAL PROCESSING LABORATORY 50 20 43 P 10. HARDWARE LABORATORY 25 10 18 P TW 11. HARDWARE LABORATORY 50 20 40 P PR FIRST TERM TOTAL = 538/750. ORDN. 1 MARKS:

UNIVERSITY OF PUNE ,T.E. (2008 PAT.) (COMPUTER) EXAMINATION NOV. 2013 DATE: 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 23 (448) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER T80054268 KADAM VIJAY BALASAHEB SUREKHA , 71347763G , , PICT , T80054268 01. DATABASE MANAGEMENT SYSTEMS 100 40 60 P 02. DATA COMMUNICATION 100 40 64 P 100 40 54 P 03. MICROPROCESSORS & MICROCONTROLLERPP 04. DIGITAL SIGNAL PROCESSING 100 40 45 P 05. THEORY OF COMPUTATION 100 40 55 P 50 20 35 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 44 P 08. SIGNAL PROCESSING LABORATORY 25 10 22 P TW 09. SIGNAL PROCESSING LABORATORY 50 20 40 P OR 10. HARDWARE LABORATORY 25 10 20 P TW 11. HARDWARE LABORATORY 50 20 43 P PR FIRST TERM TOTAL = 482/750. ORDN. 1 MARKS: T80054269 KALBHOR KOMAL JALINDAR KALBHOR RAJASHREE , 71347764E , , PICT , т80054269 01. DATABASE MANAGEMENT SYSTEMS 100 40 64 P 100 40 52 P 02. DATA COMMUNICATION 100 40 49 P 03. MICROPROCESSORS & MICROCONTROLLERPP 04. DIGITAL SIGNAL PROCESSING 100 40 52 P 05. THEORY OF COMPUTATION 100 40 54 P 50 20 43 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 45 P 25 10 22 P 08. SIGNAL PROCESSING LABORATORY TW 09. SIGNAL PROCESSING LABORATORY OR 50 20 35 P 10. HARDWARE LABORATORY 25 10 22 P TW 11. HARDWARE LABORATORY 50 20 35 P PR FIRST TERM TOTAL = 473/750. ORDN. 1 MARKS: T80054270 KALBHOR SHILPA SOPAN KALBHOR ANJALI , т80054270 01. DATABASE MANAGEMENT SYSTEMS 100 40 68 P 02. DATA COMMUNICATION PP 100 40 61 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 51 P 04. DIGITAL SIGNAL PROCESSING 100 40 40 P 05. THEORY OF COMPUTATION 100 40 65 P 50 20 06. RDBMS & VISUAL PROGRAMMING LAB. TW 44 P 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 45 P 08. SIGNAL PROCESSING LABORATORY 25 10 22 P TW 09. SIGNAL PROCESSING LABORATORY 50 20 36 P 10. HARDWARE LABORATORY 25 10 22 P TW 11. HARDWARE LABORATORY 50 20 39 P PR FIRST TERM TOTAL = 493/750. ORDN. 1 MARKS:

UNIVERSITY OF PUNE ,T.E.(2008 PAT.)(COMPUTER) EXAMINATION NOV. 2013 DATE: 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 24 (449) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71200901K , , PICT , T80054271 T80054271 KALDATE SHASHANK RAMDAS JAYASHRI 01. DATABASE MANAGEMENT SYSTEMS 100 40 57 P 02. DATA COMMUNICATION 100 40 54 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 40 P 04. DIGITAL SIGNAL PROCESSING 100 40 40 P 05. THEORY OF COMPUTATION 100 40 55 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 20 32 P 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 39 P 08. SIGNAL PROCESSING LABORATORY 25 10 TW 18 P 09. SIGNAL PROCESSING LABORATORY 50 20 02 F OR 10. HARDWARE LABORATORY 25 10 17 P TW 11. HARDWARE LABORATORY 50 20 37 P PR FIRST TERM TOTAL = 391/750. ORDN. 1 MARKS: T80054272 KALUSKAR SANIYA VIKRAM , 71200904D , , PICT , т80054272 PALLAVEE 01. DATABASE MANAGEMENT SYSTEMS 100 40 63 P 100 40 65 P 02. DATA COMMUNICATION 100 40 52 P 03. MICROPROCESSORS & MICROCONTROLLERPP 52 P 100 40 04. DIGITAL SIGNAL PROCESSING 05. THEORY OF COMPUTATION 100 40 60 P 50 20 30 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 39 P 25 10 17 P 08. SIGNAL PROCESSING LABORATORY TW 09. SIGNAL PROCESSING LABORATORY 50 20 35 P OR 10. HARDWARE LABORATORY 25 10 18 P TW 11. HARDWARE LABORATORY 50 20 26 P PR FIRST TERM TOTAL = 457/750. ORDN. 1 MARKS: , 71347766M T80054273 KAMBALE NILESH VIJAY , PICT , т80054273 JAYMALA 01. DATABASE MANAGEMENT SYSTEMS 100 40 66 P 02. DATA COMMUNICATION PP 100 40 53 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 32 F 04. DIGITAL SIGNAL PROCESSING 100 40 17 F 05. THEORY OF COMPUTATION 100 40 21 F 50 20 35 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 36 P 08. SIGNAL PROCESSING LABORATORY 25 10 20 P TW 09. SIGNAL PROCESSING LABORATORY 50 20 09 F 10. HARDWARE LABORATORY 25 10 20 P TW 11. HARDWARE LABORATORY 50 20 10 F PR FIRST TERM TOTAL = 319/750.

ORDN. 1 MARKS :

UNIVERSITY OF PUNE ,T.E.(2008 PAT.)(COMPUTER) EXAMINATION NOV. 2013 DATE: 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 25 (450) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER LATA , 71200905B , , PICT , T80054274 T80054274 KAMBLE ASHAY UTTAM 01. DATABASE MANAGEMENT SYSTEMS 100 40 64 P 02. DATA COMMUNICATION 100 40 45 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 40 P 04. DIGITAL SIGNAL PROCESSING 100 40 40 P 05. THEORY OF COMPUTATION 100 40 52 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 20 24 P

10. HARDWARE LABORATORY TW 25 10 16 P 11. HARDWARE LABORATORY PR 50 20 30 P

FIRST TERM TOTAL = 388/750.

07. RDBMS & VISUAL PROGRAMMING LAB. PR

08. SIGNAL PROCESSING LABORATORY

09. SIGNAL PROCESSING LABORATORY

ORDN. 1 MARKS:

T80054275 KANTALE SACHIN WAMANRAO PRAMILA , 71100848F , T80054262 , PICT , T80054275

, 71100848F T80054275 KANTALE SACHIN WAMANRAO PRAMILA 01. DATABASE MANAGEMENT SYSTEMS 100 40 46 P 100 40 40 P 02. DATA COMMUNICATION 100 40 28 F 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 09 F 04. DIGITAL SIGNAL PROCESSING 05. THEORY OF COMPUTATION 100 40 30 F 50 20 06. RDBMS & VISUAL PROGRAMMING LAB. TW 24 P C 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 23 P C 25 10 11 P C 08. SIGNAL PROCESSING LABORATORY TW 09. SIGNAL PROCESSING LABORATORY OR 50 20 22 P 10. HARDWARE LABORATORY 25 10 13 P C TW 11. HARDWARE LABORATORY 50 20 04 F PR

TW

OR

50 20

25 10

50 20 24 P

38 P

15 P

FIRST TERM TOTAL = 250/750.

ORDN. 1 MARKS:

, 71200910J

, PICT

, т80054276

T80054276 KAPILESHWAR KAUSTUBH DATTATRAYA KETAKI 01. DATABASE MANAGEMENT SYSTEMS 100 40 68 P 02. DATA COMMUNICATION PP 100 40 46 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 50 P 04. DIGITAL SIGNAL PROCESSING 100 40 51 P 05. THEORY OF COMPUTATION 100 40 68 P 50 20 06. RDBMS & VISUAL PROGRAMMING LAB. TW 43 P 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 45 P 08. SIGNAL PROCESSING LABORATORY 25 10 23 P TW 09. SIGNAL PROCESSING LABORATORY 50 20 42 P 10. HARDWARE LABORATORY 25 10 21 P TW 11. HARDWARE LABORATORY 50 20 38 P PR FIRST TERM TOTAL = 495/750.

ORDN. 1 MARKS :

DATE: 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 26 (451)

NOTE: FIRST LINE : SEAT NO., NAME (OF THE	 CANDI	 DATE	MO			REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
							OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
T80054277 KARANDE RAHUL MADAN					 NITA		, 71045472E , T80054264 , PICT , T8005427
	PP	100	40		РС	12. P	PRINCIPLES OF PROGRAMMING LANG. PP 100 40 49 P
02. DATA COMMUNICATION	PP	100	40	49	РС	13. C	COMPUTER NETWORKS PP 100 40 40 P
03. MICROPROCESSORS & MICROCONTROLL	ERPP	100	40	40	РС	14. F	FINANCE & MANAGEMENT INFORMA.SYS.PP 100 40 41 P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	AA	F	15. S	SYSTEMS PROGRAMMING & OPERA.SYS. PP 100 40 50 P
05. THEORY OF COMPUTATION	PP	100	40	33	F	16. s	OFTWARE ENGINEERING PP 100 40 52 P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	23	РС	17. S	OFTWARE LABORATORY TW 25 10 12 P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	25	РC	18. S	OFTWARE LABORATORY PR 50 20 22 P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	11	РС	19. c	COMPUTER NETWORK TW 25 10 13 P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	22	РС	20. C	COMPUTER NETWORK OR 50 20 30 P
10. HARDWARE LABORATORY	TW	25	10	10	РС	21. S	SOFTWARE DEVELOPMENT TOOLS LAB. TW 50 20 24 P
11. HARDWARE LABORATORY	PR	50			P C		SEMINAR AND TECHNICAL COMMUNI. TW 50 20 22 P
GRAND TOTAL = 634/1500, RESULT: FAILS							
ORDN. 1 MARKS :	, , , , , ,						
T80054278 KARPE AKASH TUKARAM				ASI			, 71200912E , , PICT , T8005427
	PP	100	40	72			, ,, , ,, , ,, ,, ,, ,, ,, ,, ,, ,, ,, ,, ,
02. DATA COMMUNICATION	PP	100		61			
03. MICROPROCESSORS & MICROCONTROLL		100		46			
04. DIGITAL SIGNAL PROCESSING	PP	100		49			
05. THEORY OF COMPUTATION	PP	100		51			
06. RDBMS & VISUAL PROGRAMMING LAB.		50		35			
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50		40			
08. SIGNAL PROCESSING LABORATORY	TW	25	10	19			
09. SIGNAL PROCESSING LABORATORY	OR	50		23			
10. HARDWARE LABORATORY	TW	25		19	P		
11. HARDWARE LABORATORY	PR	_	20	35			
FIRST TERM TOTAL = 450/750.	FIX	30	20	33	Г		
ORDN. 1 MARKS :							
T80054279 KARTIK ANIL REDDY					MRATA		, 71200913C , , PICT , T8005427
	PP	100	40	60			, , , , , , , , , , , , , , , , , , , ,
02. DATA COMMUNICATION	PP	100		51			
03. MICROPROCESSORS & MICROCONTROLL		100		45			
04. DIGITAL SIGNAL PROCESSING	PP	100		40			
05. THEORY OF COMPUTATION	PP	100		55			
06. RDBMS & VISUAL PROGRAMMING LAB.			20	39			
07. RDBMS & VISUAL PROGRAMMING LAB.			20	45			
08. SIGNAL PROCESSING LABORATORY	TW		10	18			
09. SIGNAL PROCESSING LABORATORY	OR		20	35			
10. HARDWARE LABORATORY	TW	25		18			
11. HARDWARE LABORATORY	PR	50		30			
FIRST TERM TOTAL = 436/750.	۲K	30	20	30	r'		
•							
ORDN. 1 MARKS :							

DATE: 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 27 (452)

NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71200914M , , PICT , T80054280 T80054280 KASAT SARVESH BALKISAN SWARUPA 01. DATABASE MANAGEMENT SYSTEMS 100 40 72 P 02. DATA COMMUNICATION 100 40 72 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 63 P 04. DIGITAL SIGNAL PROCESSING 100 40 74 P 05. THEORY OF COMPUTATION 100 40 69 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 20 42 P 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 45 P 08. SIGNAL PROCESSING LABORATORY 25 10 23 P TW 09. SIGNAL PROCESSING LABORATORY 50 20 44 P OR 10. HARDWARE LABORATORY 25 10 22 P TW 11. HARDWARE LABORATORY 50 20 40 P PR FIRST TERM TOTAL = 566/750. ORDN. 1 MARKS: T80054281 KEDARE SWAPNIL PANDIT , 71045479B , , PICT , T80054281 NIRMALA 01. DATABASE MANAGEMENT SYSTEMS 100 40 43 P 100 40 41 P 02. DATA COMMUNICATION 100 40 11 F 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 11 F 04. DIGITAL SIGNAL PROCESSING 05. THEORY OF COMPUTATION 100 40 09 F 50 20 20 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 AA F 25 10 12 P 08. SIGNAL PROCESSING LABORATORY TW 09. SIGNAL PROCESSING LABORATORY OR 50 20 05 F 10. HARDWARE LABORATORY 25 10 11 P TW 11. HARDWARE LABORATORY 50 20 AA F PR FIRST TERM TOTAL = 163/750. ORDN. 1 MARKS: , 71200919в T80054282 KHADE SOURABH DURYODHAN , PICT , T80054282 SANGITA 01. DATABASE MANAGEMENT SYSTEMS 100 40 40 P 02. DATA COMMUNICATION PP 100 40 49 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 33 F 04. DIGITAL SIGNAL PROCESSING 100 40 41 P 05. THEORY OF COMPUTATION 100 40 41 P 50 20 31 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 40 P 08. SIGNAL PROCESSING LABORATORY 25 10 15 P TW 09. SIGNAL PROCESSING LABORATORY 50 20 28 P 10. HARDWARE LABORATORY 25 10 14 P TW 11. HARDWARE LABORATORY 50 20 35 P PR FIRST TERM TOTAL = 367/750. ORDN. 1 MARKS:

UNIVERSITY OF PUNE ,T.E.(2008 PAT.)(COMPUTER) EXAMINATION NOV. 2013 DATE : 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 28 (453) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER T80054283 KHADKE SRUSHTI SANJEEV . 71200920F PICT , T80054283

180034283 KHADKE SKUSHII SANJEEV				US	HA	,	7120092	20F	,	,	PICI
01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	56	Р						
02. DATA COMMUNICATION	PP	100	40	49	Р						
03. MICROPROCESSORS & MICROCONTROLL	ERPP	100	40	42	Р						
04. DIGITAL SIGNAL PROCESSING	PP	100	40	44	Р						
05. THEORY OF COMPUTATION	PP	100	40	43	Р						
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	39	Р						
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	40	Р						
08. SIGNAL PROCESSING LABORATORY	TW	25	10	19	Р						
09. SIGNAL PROCESSING LABORATORY	OR	50	20	32	Р						
10. HARDWARE LABORATORY	TW	25	10	21	Р						
11. HARDWARE LABORATORY	PR	50	20	35	Р						
FIRST TERM TOTAL = $420/750$.											
ORDN. 1 MARKS :											

, 70925473D , T80054269 , PICT , T80054284

, 71200923L , , PICT , T80054285

T80054284 KHALID RAZA KHAN				AY	ESHA	
01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	40	РС	
02. DATA COMMUNICATION	PP	100	40	AA	F	
03. MICROPROCESSORS & MICROCONTROLLE	RPP	100	40	27	F	
04. DIGITAL SIGNAL PROCESSING	PP	100	40	AA	F	
05. THEORY OF COMPUTATION	PP	100	40	26	F	
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	20	РС	
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	20	Р	
08. SIGNAL PROCESSING LABORATORY	TW	25	10	10	РС	
09. SIGNAL PROCESSING LABORATORY	OR	50	20	AA	F	
10. HARDWARE LABORATORY	TW	25	10	10	РС	
11 HADDWARE LABORATORY	DD	FΛ	20	۸ ۸	_	

11. HARDWARE LABORATORY PR 50 20 AA F

FIRST TERM TOTAL = 153/750.

ORDN. 1 MARKS:

T80054285 KHANDAVE MAYUR NARAYANRA	O			PU	SHPA	
01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	52	Р	
02. DATA COMMUNICATION	PP	100	40	47	Р	
03. MICROPROCESSORS & MICROCONTROLLE	RPP	100	40	59	Р	
04. DIGITAL SIGNAL PROCESSING	PP	100	40	61	Р	
05. THEORY OF COMPUTATION	PP	100	40	67	Р	
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	35	Р	
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	42	Р	
08. SIGNAL PROCESSING LABORATORY	TW	25	10	16	Р	
09. SIGNAL PROCESSING LABORATORY	OR	50	20	37	Р	
10. HARDWARE LABORATORY	TW	25	10	17	Р	
11. HARDWARE LABORATORY	PR	50	20	38	Р	
474 /750						

FIRST TERM TOTAL = 471/750.

ORDN. 1 MARKS:

UNIVERSITY OF PUNE ,T.E.(2008 PAT.)(COMPUTER) EXAMINATION NOV. 2013

DATE: 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 29 (454)

NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71200931M , , , PICT , T80054286 T80054286 KOTHARI APARNA ASHOK ARUNA 01. DATABASE MANAGEMENT SYSTEMS 100 40 54 P 02. DATA COMMUNICATION 100 40 51 P 100 40 59 P 03. MICROPROCESSORS & MICROCONTROLLERPP 04. DIGITAL SIGNAL PROCESSING 100 40 72 P 05. THEORY OF COMPUTATION 100 40 65 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 20 37 P 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 42 P 08. SIGNAL PROCESSING LABORATORY 25 10 21 P TW 09. SIGNAL PROCESSING LABORATORY 50 20 45 P OR 10. HARDWARE LABORATORY 25 10 20 P TW 11. HARDWARE LABORATORY 50 20 40 P PR FIRST TERM TOTAL = 506/750. ORDN. 1 MARKS: , 71200934F , , PICT , т80054287 T80054287 KULKARNI JANHAVI SUHASCHANDRA SAVANI 01. DATABASE MANAGEMENT SYSTEMS 100 40 59 P 100 40 52 P 02. DATA COMMUNICATION 100 40 60 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 56 P 04. DIGITAL SIGNAL PROCESSING 05. THEORY OF COMPUTATION 100 40 72 P 50 20 35 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 43 P 25 10 08. SIGNAL PROCESSING LABORATORY TW 18 P 09. SIGNAL PROCESSING LABORATORY OR 50 20 43 P 10. HARDWARE LABORATORY 25 10 19 P TW 11. HARDWARE LABORATORY 50 20 42 P PR FIRST TERM TOTAL = 499/750. ORDN. 1 MARKS: , 71200935D T80054288 KULKARNI MALHAR SUNIL , PICT , т80054288 VYJAYANTHI 01. DATABASE MANAGEMENT SYSTEMS 100 40 61 P 02. DATA COMMUNICATION PP 100 40 56 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 61 P 04. DIGITAL SIGNAL PROCESSING 100 40 58 P 05. THEORY OF COMPUTATION 100 40 67 P 50 20 06. RDBMS & VISUAL PROGRAMMING LAB. TW 38 P 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 45 P 08. SIGNAL PROCESSING LABORATORY 25 10 21 P TW 09. SIGNAL PROCESSING LABORATORY 50 20 45 P 10. HARDWARE LABORATORY 25 10 21 P TW 11. HARDWARE LABORATORY 50 20 42 P PR FIRST TERM TOTAL = 515/750. ORDN. 1 MARKS:

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DATE : II MAK! ZUIT	CLIVII		0.11	111311		T COM OTE	K TECHNOLOGI, TONE:	17102	10.	30		
NOTE: FIRST LINE : SEAT NO., NAME (PEG NO PREVIOUS SEAT NO C					
·				-	-		KS OBTAINED, P/F:PASS/FAIL, C:P	•				
T80054289 KULKARNI VARAD SURESH					LOTTAM		, 70818756м , т80054279 ,					
01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	55	РС	12.	PRINCIPLES OF PROGRAMMING LANG.		100	-		РС
02. DATA COMMUNICATION	PP	100	40	40	РС	13.	COMPUTER NETWORKS	PP :	100	40	45	РС
03. MICROPROCESSORS & MICROCONTROLLE	ERPP	100	40	40	РС	14.	FINANCE & MANAGEMENT INFORMA.SYS	.PP	100	40	50	РС
04. DIGITAL SIGNAL PROCESSING	PP	100	40	40	Р	15.	SYSTEMS PROGRAMMING & OPERA.SYS.	PP :	100	40	48	РС
05. THEORY OF COMPUTATION	PP	100	40	40	РС	16.	SOFTWARE ENGINEERING	PP :	100	40	40	РС
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	39	РС	17.	SOFTWARE LABORATORY	TW	25	10	16	РС
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	43	РС	18.	SOFTWARE LABORATORY	PR	50	20	39	РС
08. SIGNAL PROCESSING LABORATORY	TW	25	10	19	РС	19.	COMPUTER NETWORK	TW	25	10	16	РС
09. SIGNAL PROCESSING LABORATORY	OR	50	20	33	РС	20.	COMPUTER NETWORK	OR	50	20	38	РС
10. HARDWARE LABORATORY	TW	25	10	17	РС	21.	SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	35	РС
11. HARDWARE LABORATORY	PR	50	20	38	РС	22.	SEMINAR AND TECHNICAL COMMUNI.	TW	50	20	40	РС
GRAND TOTAL = 831/1500, RESULT: HIGHE	ER SEC	OND CL	ASS					RESULT	RES	ERVE) FOR	BKLG
ORDN. 1 MARKS :												
T80054290 LANKE PRASAD PRAVIN				SUN	NANDA		, 71200943E , , ,	PICT		, -	г8005	4290
01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	71	Р							
02. DATA COMMUNICATION	PP	100	40	73	Р							
03. MICROPROCESSORS & MICROCONTROLLE	ERPP	100	40	71	Р							
04. DIGITAL SIGNAL PROCESSING	PP	100	40	85	Р							
05. THEORY OF COMPUTATION	PP	100	40	89	Р							
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	48	Р							
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	47	Р							
08. SIGNAL PROCESSING LABORATORY	TW	25	10	24	Р							
09. SIGNAL PROCESSING LABORATORY	OR	50	20	46	Р							
10. HARDWARE LABORATORY	TW	25	10	23	Р							
11. HARDWARE LABORATORY	PR	50	20	45	Р							
FIRST TERM TOTAL = $622/750$.												
ORDN. 1 MARKS :												
T80054291 MAHAJAN ABHIJEET SHIVDAS	5			PRA	ABHAVA	TI	, 71100887G , т80054284 ,	PICT		, -	г8005	4291
01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	63	РС	12.	PRINCIPLES OF PROGRAMMING LANG.	PP :	100	40	50	PC
02. DATA COMMUNICATION	PP	100	40	65	РС	13.	COMPUTER NETWORKS	PP :	100	40	48	P C
03. MICROPROCESSORS & MICROCONTROLLE	ERPP	100	40	48	РС	14.	FINANCE & MANAGEMENT INFORMA.SYS	.PP	100	40	53	PC
04. DIGITAL SIGNAL PROCESSING	PP	100	40	51	РС	15.	SYSTEMS PROGRAMMING & OPERA.SYS.	PP :	100	40	51	PC
05. THEORY OF COMPUTATION	PP	100	40	45	РС	16.	SOFTWARE ENGINEERING	PP :	100	40	42	РС
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	27	РС	17.	SOFTWARE LABORATORY	TW	25	10	16	РС
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	42	РС	18.	SOFTWARE LABORATORY	PR	50	20	30	Р
08. SIGNAL PROCESSING LABORATORY	TW	25	10	15	P C	19.	COMPUTER NETWORK	TW	25	10	16	PC
09. SIGNAL PROCESSING LABORATORY	OR	50	20	35	PC	20.	COMPUTER NETWORK	OR	50	20	22	P C
10. HARDWARE LABORATORY	TW	25	10	16	PC	21.	SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	31	P C
11. HARDWARE LABORATORY	PR	50	20	30	P C	22.	SEMINAR AND TECHNICAL COMMUNI.	TW	50	20	33	P C
GRAND TOTAL = 829/1500, RESULT: HIGHE	ER SEC	OND CL	ASS									
ORDN. 1 MARKS :												

UNIVERSITY OF PUNE ,T.E.(2008 PAT.)(COMPUTER) EXAMINATION NOV. 2013 DATE: 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 31 (456) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER ARCHANA , 71200946к , , , РІСТ , Т80054292 T80054292 MAHAJAN ABOLI VIKRANT 01. DATABASE MANAGEMENT SYSTEMS 100 40 53 P 02. DATA COMMUNICATION 100 40 56 P 100 40 60 P 03. MICROPROCESSORS & MICROCONTROLLERPP 04. DIGITAL SIGNAL PROCESSING 100 40 76 P 05. THEORY OF COMPUTATION 100 40 72 P 50 20 44 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 45 P 08. SIGNAL PROCESSING LABORATORY 25 10 24 P TW 09. SIGNAL PROCESSING LABORATORY 50 20 47 P OR 10. HARDWARE LABORATORY 25 10 23 P TW 11. HARDWARE LABORATORY 50 20 43 P PR FIRST TERM TOTAL = 543/750. ORDN. 1 MARKS: T80054293 MAHALE DHANSHRI RAJENDRA , 71200948F , , , PICT , т80054293 MANGALA 01. DATABASE MANAGEMENT SYSTEMS 100 40 55 P 100 40 52 P 02. DATA COMMUNICATION 100 40 51 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 69 P 04. DIGITAL SIGNAL PROCESSING 05. THEORY OF COMPUTATION 100 40 76 P 50 20 38 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 42 P 25 10 08. SIGNAL PROCESSING LABORATORY TW 19 P 09. SIGNAL PROCESSING LABORATORY OR 50 20 41 P 10. HARDWARE LABORATORY 25 10 18 P TW 11. HARDWARE LABORATORY 50 20 36 P PR FIRST TERM TOTAL = 497/750. ORDN. 1 MARKS: , 71200950н T80054294 MAITHILI TAKALE , PICT , т80054294 MEENA 01. DATABASE MANAGEMENT SYSTEMS 100 40 60 P 02. DATA COMMUNICATION PP 100 40 60 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 56 P 04. DIGITAL SIGNAL PROCESSING 100 40 47 P 05. THEORY OF COMPUTATION 100 40 47 P 50 20 06. RDBMS & VISUAL PROGRAMMING LAB. TW 41 P 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 41 P 08. SIGNAL PROCESSING LABORATORY 25 10 21 P TW 09. SIGNAL PROCESSING LABORATORY 50 20 35 P 10. HARDWARE LABORATORY 25 10 21 P TW 11. HARDWARE LABORATORY 50 20 34 P PR FIRST TERM TOTAL = 463/750. ORDN. 1 MARKS:

DATE: 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 32 (457)

NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71200951F , , PICT , T80054295 T80054295 MALI PARAG SHRIKRISHNA SUNANDA 01. DATABASE MANAGEMENT SYSTEMS 100 40 68 P 02. DATA COMMUNICATION 100 40 70 P 100 40 63 P 03. MICROPROCESSORS & MICROCONTROLLERPP 04. DIGITAL SIGNAL PROCESSING 100 40 77 P 05. THEORY OF COMPUTATION 100 40 77 P 50 20 45 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 46 P 08. SIGNAL PROCESSING LABORATORY 25 10 24 P TW 09. SIGNAL PROCESSING LABORATORY 50 20 41 P OR 25 10 22 P 10. HARDWARE LABORATORY TW 11. HARDWARE LABORATORY 50 20 40 P PR FIRST TERM TOTAL = 573/750. ORDN. 1 MARKS: T80054296 MANE SAGAR ASHOK , т80054296 SANGITA 01. DATABASE MANAGEMENT SYSTEMS 100 40 58 P 100 40 53 P 02. DATA COMMUNICATION PP 100 40 43 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 52 P 04. DIGITAL SIGNAL PROCESSING 05. THEORY OF COMPUTATION 100 40 13 F 50 20 36 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 41 P 25 10 18 P 08. SIGNAL PROCESSING LABORATORY TW 09. SIGNAL PROCESSING LABORATORY 50 20 24 P OR 10. HARDWARE LABORATORY 25 10 18 P TW 11. HARDWARE LABORATORY 50 20 30 P PR FIRST TERM TOTAL = 386/750. ORDN. 1 MARKS: , 71200957E T80054297 MANTRI SHARAYU SHARAD , PICT , т80054297 PRIYA 01. DATABASE MANAGEMENT SYSTEMS 100 40 62 P 02. DATA COMMUNICATION PP 100 40 64 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 57 P 04. DIGITAL SIGNAL PROCESSING 100 40 76 P 05. THEORY OF COMPUTATION 100 40 61 P 50 20 35 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 36 P 08. SIGNAL PROCESSING LABORATORY 25 10 20 P TW 09. SIGNAL PROCESSING LABORATORY 50 20 39 P 10. HARDWARE LABORATORY 25 10 20 P TW 11. HARDWARE LABORATORY 50 20 39 P PR FIRST TERM TOTAL = 509/750. ORDN. 1 MARKS:

DATE: 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 33 (458)

NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER T80054298 MHASKE POOJA DAGDU MANISHA , 71347767K , , PICT , T80054298 01. DATABASE MANAGEMENT SYSTEMS 100 40 64 P 02. DATA COMMUNICATION 100 40 53 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 67 P 04. DIGITAL SIGNAL PROCESSING 100 40 67 P 05. THEORY OF COMPUTATION 100 40 51 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 20 36 P 50 20 37 P 07. RDBMS & VISUAL PROGRAMMING LAB. PR 08. SIGNAL PROCESSING LABORATORY 25 10 TW 18 P 09. SIGNAL PROCESSING LABORATORY 50 20 30 P OR 10. HARDWARE LABORATORY 25 10 18 P TW 11. HARDWARE LABORATORY 50 20 38 P PR FIRST TERM TOTAL = 479/750. ORDN. 1 MARKS: T80054299 MHASKULE YOGESH DATTATRAYA , 71347768H , , PICT , т80054299 LATA 01. DATABASE MANAGEMENT SYSTEMS 100 40 62 P 100 40 56 P 02. DATA COMMUNICATION 100 40 64 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 60 P 04. DIGITAL SIGNAL PROCESSING 05. THEORY OF COMPUTATION 100 40 45 P 50 20 40 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 40 P 25 10 21 P 08. SIGNAL PROCESSING LABORATORY TW 09. SIGNAL PROCESSING LABORATORY OR 50 20 26 P 10. HARDWARE LABORATORY 25 10 20 P TW 11. HARDWARE LABORATORY 50 20 26 P PR FIRST TERM TOTAL = 460/750. ORDN. 1 MARKS: , 71200962м T80054300 MISAL AKSHAYA ARJUN , PICT , т80054300 SANGITA 01. DATABASE MANAGEMENT SYSTEMS 100 40 66 P 02. DATA COMMUNICATION PP 100 40 60 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 68 P 04. DIGITAL SIGNAL PROCESSING 100 40 66 P 05. THEORY OF COMPUTATION 100 40 69 P 50 20 39 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 37 P 08. SIGNAL PROCESSING LABORATORY 25 10 17 P TW 09. SIGNAL PROCESSING LABORATORY 50 20 23 P 10. HARDWARE LABORATORY 25 10 18 P TW 11. HARDWARE LABORATORY 50 20 29 P PR FIRST TERM TOTAL = 492/750. ORDN. 1 MARKS:

DATE: 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 34 (459)

NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71045671к , Т80054289 , РІСТ T80054301 MOHIT RAJVARDHAN MEENA KUMARI , T80054301 100 40 44 P C 12. PRINCIPLES OF PROGRAMMING LANG. PP 100 40 47 PC 01. DATABASE MANAGEMENT SYSTEMS 13. COMPUTER NETWORKS 02. DATA COMMUNICATION 100 40 40 P C 100 40 100 40 49 P C 14. FINANCE & MANAGEMENT INFORMA.SYS.PP 100 40 40 P C 03. MICROPROCESSORS & MICROCONTROLLERPP 04. DIGITAL SIGNAL PROCESSING 100 40 40 P 15. SYSTEMS PROGRAMMING & OPERA.SYS. PP 100 40 40 P C 05. THEORY OF COMPUTATION 100 40 41 P C 100 40 40 P.C 16. SOFTWARE ENGINEERING 50 20 32 P C 25 10 18 P C 06. RDBMS & VISUAL PROGRAMMING LAB. TW 17. SOFTWARE LABORATORY TW 38 P C 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 50 20 35 P C 18. SOFTWARE LABORATORY PR08. SIGNAL PROCESSING LABORATORY 25 10 16 P C 19. COMPUTER NETWORK 25 10 18 P C TW TW 09. SIGNAL PROCESSING LABORATORY 50 20 20 P C 50 20 39 P 20. COMPUTER NETWORK OR 10. HARDWARE LABORATORY 25 10 15 P C 50 20 37 P C TW 21. SOFTWARE DEVELOPMENT TOOLS LAB. TW 11. HARDWARE LABORATORY 50 20 36 P C 22. SEMINAR AND TECHNICAL COMMUNI. TW 50 20 41 P C PR GRAND TOTAL = 766/1500, RESULT: SECOND CLASS ORDN. 1 MARKS: , т80054302 T80054302 MORDANI NAVIN BHAGWAN SUPARNA 01. DATABASE MANAGEMENT SYSTEMS 100 40 58 P 100 40 48 P 02. DATA COMMUNICATION 100 40 59 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 67 P 04. DIGITAL SIGNAL PROCESSING 100 40 56 P 05. THEORY OF COMPUTATION 50 20 06. RDBMS & VISUAL PROGRAMMING LAB. TW 41 P 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 42 P 08. SIGNAL PROCESSING LABORATORY TW 25 10 21 P 09. SIGNAL PROCESSING LABORATORY 50 20 39 P OR 10. HARDWARE LABORATORY 25 10 22 P TW 11. HARDWARE LABORATORY 50 20 40 P PR FIRST TERM TOTAL = 493/750. ORDN. 1 MARKS: MORE ANITA SHANKAR , 71347769F , , PICT T80054303 MORE SAYALI SHANKAR , т80054303 01. DATABASE MANAGEMENT SYSTEMS 100 40 65 P 02. DATA COMMUNICATION PP 100 40 68 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 67 P 04. DIGITAL SIGNAL PROCESSING 100 40 52 P 05. THEORY OF COMPUTATION 100 40 60 P PP 50 20 06. RDBMS & VISUAL PROGRAMMING LAB. TW 38 P 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 42 P 08. SIGNAL PROCESSING LABORATORY 25 10 18 P TW 09. SIGNAL PROCESSING LABORATORY 50 20 35 P 10. HARDWARE LABORATORY 25 10 18 P TW 11. HARDWARE LABORATORY 50 20 36 P PR FIRST TERM TOTAL = 499/750. ORDN. 1 MARKS:

DATE: 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 35 (460)

NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71200966D , , PICT , T80054304 T80054304 MORE SOHAM SAMEER HARSHA 01. DATABASE MANAGEMENT SYSTEMS 100 40 53 P 02. DATA COMMUNICATION 100 40 51 P 100 40 44 P 03. MICROPROCESSORS & MICROCONTROLLERPP 04. DIGITAL SIGNAL PROCESSING 100 40 40 P 05. THEORY OF COMPUTATION 100 40 44 P 50 20 21 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 38 P 08. SIGNAL PROCESSING LABORATORY 25 10 TW 13 P 09. SIGNAL PROCESSING LABORATORY 50 20 15 F OR 10. HARDWARE LABORATORY 25 10 12 P TW 11. HARDWARE LABORATORY 50 20 36 P PR FIRST TERM TOTAL = 367/750. ORDN. 1 MARKS: T80054305 MUDASSIR KHAN MASOOD KHAN , 71200967B , , PICT , T80054305 AMENA KHANUM 01. DATABASE MANAGEMENT SYSTEMS 100 40 58 P 100 40 52 P 02. DATA COMMUNICATION 100 40 58 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 67 P 04. DIGITAL SIGNAL PROCESSING 05. THEORY OF COMPUTATION 100 40 55 P 50 20 43 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 44 P 08. SIGNAL PROCESSING LABORATORY TW 25 10 19 P 09. SIGNAL PROCESSING LABORATORY OR 50 20 32 P 10. HARDWARE LABORATORY 25 10 18 P TW 11. HARDWARE LABORATORY 50 20 34 P PR FIRST TERM TOTAL = 480/750. ORDN. 1 MARKS: , 71100902D , T80054291 , PICT T80054306 MUNMOON GHOSH REKHA , т80054306 01. DATABASE MANAGEMENT SYSTEMS 100 40 59 P C 12. PRINCIPLES OF PROGRAMMING LANG. PP 100 40 69 PC 55 P C 02. DATA COMMUNICATION PP 100 40 53 P C 13. COMPUTER NETWORKS PP 100 40 100 40 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 62 P C 14. FINANCE & MANAGEMENT INFORMA.SYS.PP 55 P C 04. DIGITAL SIGNAL PROCESSING 100 40 62 P C 15. SYSTEMS PROGRAMMING & OPERA.SYS. PP 100 40 67 P.C 05. THEORY OF COMPUTATION 100 40 41 P C 100 40 49 P C PP 16. SOFTWARE ENGINEERING 50 20 29 P C 25 10 11 P C 06. RDBMS & VISUAL PROGRAMMING LAB. TW 17. SOFTWARE LABORATORY TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 37 P C 18. SOFTWARE LABORATORY 50 20 22 P PR 13 P C 08. SIGNAL PROCESSING LABORATORY 25 10 15 P C 19. COMPUTER NETWORK TW 25 10 TW 38 P 50 20 09. SIGNAL PROCESSING LABORATORY 50 20 34 P C 20. COMPUTER NETWORK 10. HARDWARE LABORATORY 25 10 14 P C 21. SOFTWARE DEVELOPMENT TOOLS LAB. TW 50 20 20 P C TW 11. HARDWARE LABORATORY 50 20 25 P C 22. SEMINAR AND TECHNICAL COMMUNI. TW 50 20 35 P C PR GRAND TOTAL = 865/1500, RESULT: HIGHER SECOND CLASS ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71200971L , , PICT , T80054307 T80054307 MUNOT BHAVESH RAJENDRA MANORAMA 01. DATABASE MANAGEMENT SYSTEMS 100 40 50 P 02. DATA COMMUNICATION 100 40 54 P 100 40 58 P 03. MICROPROCESSORS & MICROCONTROLLERPP 04. DIGITAL SIGNAL PROCESSING 100 40 69 P 05. THEORY OF COMPUTATION 100 40 64 P 50 20 40 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 43 P 08. SIGNAL PROCESSING LABORATORY 25 10 19 P TW 09. SIGNAL PROCESSING LABORATORY 50 20 29 P OR 10. HARDWARE LABORATORY 25 10 22 P TW 11. HARDWARE LABORATORY 50 20 40 P PR FIRST TERM TOTAL = 488/750. ORDN. 1 MARKS: T80054308 MURARKA UTKARSH VINAY , 71200972J , , PICT , т80054308 HEMLATA 100 40 48 P 01. DATABASE MANAGEMENT SYSTEMS 100 40 56 P 02. DATA COMMUNICATION 100 40 62 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 63 P 04. DIGITAL SIGNAL PROCESSING 05. THEORY OF COMPUTATION 100 40 40 P 50 20 26 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 36 P 25 10 08. SIGNAL PROCESSING LABORATORY TW 14 P 09. SIGNAL PROCESSING LABORATORY 50 20 21 P OR 10. HARDWARE LABORATORY 25 10 12 P TW 11. HARDWARE LABORATORY 50 20 16 F PR FIRST TERM TOTAL = 394/750. ORDN. 1 MARKS: , 71200973G T80054309 MUZUMDAR VARSHA JAYANT , PICT , т80054309 SUNITA 01. DATABASE MANAGEMENT SYSTEMS 100 40 72 P 02. DATA COMMUNICATION PP 100 40 63 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 65 P 04. DIGITAL SIGNAL PROCESSING 100 40 72 P 05. THEORY OF COMPUTATION 100 40 58 P 50 20 42 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 35 P 08. SIGNAL PROCESSING LABORATORY 25 10 19 P TW 09. SIGNAL PROCESSING LABORATORY 50 20 38 P 10. HARDWARE LABORATORY 25 10 19 P TW 11. HARDWARE LABORATORY 50 20 42 P PR FIRST TERM TOTAL = 525/750. ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER ARUNA , 71200974E , , PICT , T80054310 T80054310 NADE APPURWA ANIL 01. DATABASE MANAGEMENT SYSTEMS 100 40 40 P 02. DATA COMMUNICATION 100 40 44 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 40 P 04. DIGITAL SIGNAL PROCESSING 100 40 40 P 05. THEORY OF COMPUTATION 100 40 48 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 20 37 P 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 39 P 08. SIGNAL PROCESSING LABORATORY 25 10 17 P TW 09. SIGNAL PROCESSING LABORATORY 50 20 20 P OR 10. HARDWARE LABORATORY 25 10 17 P TW 11. HARDWARE LABORATORY 50 20 41 P PR FIRST TERM TOTAL = 383/750. ORDN. 1 MARKS: T80054311 NAHAR AKSHAY VIJAY , 71200976M , , PICT , т80054311 MANGAL 01. DATABASE MANAGEMENT SYSTEMS 100 40 60 P 100 40 62 P 02. DATA COMMUNICATION 100 40 53 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 74 P 04. DIGITAL SIGNAL PROCESSING 05. THEORY OF COMPUTATION 100 40 57 P 50 20 38 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 34 P 25 10 18 P 08. SIGNAL PROCESSING LABORATORY TW 09. SIGNAL PROCESSING LABORATORY 50 20 36 P OR 10. HARDWARE LABORATORY 25 10 19 P TW 11. HARDWARE LABORATORY 50 20 40 P PR FIRST TERM TOTAL = 491/750. ORDN. 1 MARKS: T80054312 NAIK GANESH NILKANTH , 71347770к , PICT , т80054312 SUREKHA 01. DATABASE MANAGEMENT SYSTEMS 100 40 64 P 02. DATA COMMUNICATION PP 100 40 57 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 48 P 04. DIGITAL SIGNAL PROCESSING 100 40 59 P 05. THEORY OF COMPUTATION 100 40 51 P 50 20 42 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 42 P 08. SIGNAL PROCESSING LABORATORY 25 10 20 P TW 50 20 20 P 09. SIGNAL PROCESSING LABORATORY 10. HARDWARE LABORATORY 25 10 19 P TW 11. HARDWARE LABORATORY 50 20 32 P PR FIRST TERM TOTAL = 454/750. ORDN. 1 MARKS:

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NOTE: FIRST LINE : SEAT NO., NAME (REG. NO., PREVIOUS SEAT NO., C					
							KS OBTAINED, P/F:PASS/FAIL, C:P					
T80054313 NAIK SANDESH TARSING					MAL		, 71100908C , T80054293 ,				T8005	
	PP	100	40	63	РС	12.	PRINCIPLES OF PROGRAMMING LANG.		100	40		РС
02. DATA COMMUNICATION	PP	100	40	66	РС	13.	COMPUTER NETWORKS	PP	100	40	43	РС
03. MICROPROCESSORS & MICROCONTROLLE	ERPP	100	40	48	РС	14.	FINANCE & MANAGEMENT INFORMA.SYS	.PP	100	40	59	РС
04. DIGITAL SIGNAL PROCESSING	PP	100	40	48	РС	15.	SYSTEMS PROGRAMMING & OPERA.SYS.	PP	100	40	40	РС
05. THEORY OF COMPUTATION	PP	100	40	51	РС	16.	SOFTWARE ENGINEERING	PP	100	40	48	РС
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	24	РС	17.	SOFTWARE LABORATORY	TW	25	10	10	РС
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	25	РС	18.	SOFTWARE LABORATORY	PR	50	20	35	Р
08. SIGNAL PROCESSING LABORATORY	TW	25	10	10	РС	19.	COMPUTER NETWORK	TW	25	10	10	РС
09. SIGNAL PROCESSING LABORATORY	OR	50	20	32	РС	20.	COMPUTER NETWORK	OR	50	20	32	Р
10. HARDWARE LABORATORY	TW	25	10	13	РС	21.	SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	20	РС
11. HARDWARE LABORATORY	PR	50	20	26	РС	22.	SEMINAR AND TECHNICAL COMMUNI.	TW	50	20	20	РС
GRAND TOTAL = 776/1500, RESULT: SECON	ND CLA	SS										
ORDN. 1 MARKS :												
T80054314 NAINA BALANA				KU	ISUM		, 71200979F , , ,	PIC	Т	,	T8005	4314
01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	62	Р							
02. DATA COMMUNICATION	PP	100	40	67	Р							
03. MICROPROCESSORS & MICROCONTROLLE	ERPP	100	40	66	Р							
04. DIGITAL SIGNAL PROCESSING	PP	100	40	65	Р							
05. THEORY OF COMPUTATION	PP	100	40	69	Р							
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	33	Р							
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	38	Р							
08. SIGNAL PROCESSING LABORATORY	TW	25	10	15	Р							
09. SIGNAL PROCESSING LABORATORY	OR	50	20	22	Р							
10. HARDWARE LABORATORY	TW	25	10	15	Р							
11. HARDWARE LABORATORY	PR	50	20	36	Р							
FIRST TERM TOTAL = 488/750.												
ORDN. 1 MARKS :												
T20054215 NAVESNI KUMAD CUDTA												
T80054315 NAVEEN KUMAR GUPTA 01. DATABASE MANAGEMENT SYSTEMS	DD	100	40		IITA	12	, 71100915F , T80054295 ,				T8005	
	PP	100	40		P C		PRINCIPLES OF PROGRAMMING LANG.		100	40		P C
02. DATA COMMUNICATION	PP	100			P C		COMPUTER NETWORKS	PP	100			PC
03. MICROPROCESSORS & MICROCONTROLLE		100	40		P C		FINANCE & MANAGEMENT INFORMA.SYS		100		63	PC
04. DIGITAL SIGNAL PROCESSING	PP	100	40		P C		SYSTEMS PROGRAMMING & OPERA.SYS.		100		65	
05. THEORY OF COMPUTATION	PP	100	40		P C	_	SOFTWARE ENGINEERING	PP	100			P C
06. RDBMS & VISUAL PROGRAMMING LAB.			20		P C		SOFTWARE LABORATORY	TW	25		15	
07. RDBMS & VISUAL PROGRAMMING LAB.			20		P C		SOFTWARE LABORATORY	PR		20	35 16	
08. SIGNAL PROCESSING LABORATORY	TW		10		P C		COMPUTER NETWORK	TW		10		P C
09. SIGNAL PROCESSING LABORATORY	OR Tu		20		P C	-	COMPUTER NETWORK	OR Tu		20	30	
10. HARDWARE LABORATORY	TW	_	10		P C		SOFTWARE DEVELOPMENT TOOLS LAB.			20		P C
11. HARDWARE LABORATORY	PR		20	30	PC	22.	SEMINAR AND TECHNICAL COMMUNI.	TW	50	20	41	РС
GRAND TOTAL = 947/1500, RESULT: FIRST	i CLAS	5										
ORDN. 1 MARKS:					, .							
	-	_	-		=	-			_	_		_

UNIVERSITY OF PUNE ,T.E. (2008 PAT.) (COMPUTER) EXAMINATION NOV. 2013 DATE: 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 39 (464) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71200983D , , PICT , T80054316 T80054316 NAZARKAR SHWETA SHRIPAD SMITA 01. DATABASE MANAGEMENT SYSTEMS 100 40 51 P 02. DATA COMMUNICATION 100 40 52 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 61 P 04. DIGITAL SIGNAL PROCESSING 100 40 73 P 05. THEORY OF COMPUTATION 100 40 63 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 20 39 P 50 20 37 P 07. RDBMS & VISUAL PROGRAMMING LAB. PR 08. SIGNAL PROCESSING LABORATORY 25 10 19 P TW 09. SIGNAL PROCESSING LABORATORY 50 20 25 P OR 10. HARDWARE LABORATORY 25 10 19 P TW 11. HARDWARE LABORATORY 50 20 35 P PR FIRST TERM TOTAL = 474/750. ORDN. 1 MARKS: T80054317 NIKHIL DWIVEDI , 71200988E , , PICT , т80054317 KUSUM 01. DATABASE MANAGEMENT SYSTEMS 100 40 51 P 100 40 46 P 02. DATA COMMUNICATION PP 100 40 52 P 03. MICROPROCESSORS & MICROCONTROLLERPP 04. DIGITAL SIGNAL PROCESSING 100 40 69 P 05. THEORY OF COMPUTATION 100 40 58 P 50 20 44 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 43 P 25 10 08. SIGNAL PROCESSING LABORATORY TW 23 P 09. SIGNAL PROCESSING LABORATORY OR 50 20 44 P 10. HARDWARE LABORATORY 25 10 22 P TW 11. HARDWARE LABORATORY 50 20 42 P PR FIRST TERM TOTAL = 494/750. ORDN. 1 MARKS: , 71200989C T80054318 NIMKAR HRUSHIKESH CHARUDATTA , PICT , T80054318 SHARMILA 01. DATABASE MANAGEMENT SYSTEMS 100 40 53 P 02. DATA COMMUNICATION PP 100 40 51 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 56 P 04. DIGITAL SIGNAL PROCESSING 100 40 74 P 05. THEORY OF COMPUTATION 100 40 57 P 50 20 40 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 41 P 08. SIGNAL PROCESSING LABORATORY 25 10 23 P TW 09. SIGNAL PROCESSING LABORATORY 50 20 40 P 10. HARDWARE LABORATORY 25 10 20 P TW 11. HARDWARE LABORATORY 50 20 42 P PR FIRST TERM TOTAL = 497/750. ORDN. 1 MARKS:

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UNIVERSITY OF PUNE ,T.E. (2008 PAT.) (COMPUTER) EXAMINATION NOV. 2013 DATE: 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 40 (465) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71200990G , , PICT , T80054319 T80054319 NISHTHA KALRA SANGEETA 01. DATABASE MANAGEMENT SYSTEMS 100 40 48 P 02. DATA COMMUNICATION 100 40 51 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 48 P 04. DIGITAL SIGNAL PROCESSING 100 40 54 P 05. THEORY OF COMPUTATION 100 40 52 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 20 35 P 50 20 37 P 07. RDBMS & VISUAL PROGRAMMING LAB. PR 08. SIGNAL PROCESSING LABORATORY 25 10 15 P TW 09. SIGNAL PROCESSING LABORATORY 50 20 24 P OR 10. HARDWARE LABORATORY 25 10 15 P TW 11. HARDWARE LABORATORY 50 20 02 F PR FIRST TERM TOTAL = 381/750. ORDN. 1 MARKS: T80054320 PAGAR PRITESH DILIP PAGAR PALLAVI DILIP , 71347771H , , PICT , т80054320 01. DATABASE MANAGEMENT SYSTEMS 100 40 63 P 02. DATA COMMUNICATION 100 40 50 P 100 40 57 P 03. MICROPROCESSORS & MICROCONTROLLERPP 04. DIGITAL SIGNAL PROCESSING 100 40 62 P 05. THEORY OF COMPUTATION 100 40 58 P 50 20 42 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 43 P 25 10 16 P 08. SIGNAL PROCESSING LABORATORY TW 09. SIGNAL PROCESSING LABORATORY OR 50 20 34 P 10. HARDWARE LABORATORY 25 10 17 P TW 11. HARDWARE LABORATORY 50 20 30 P PR FIRST TERM TOTAL = 472/750. ORDN. 1 MARKS: , 71200995H , , PICT T80054321 PAITHANKAR PRANETA VISHWANATH , т80054321 UTKARSHA 01. DATABASE MANAGEMENT SYSTEMS 100 40 63 P 02. DATA COMMUNICATION PP 100 40 59 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 62 P 04. DIGITAL SIGNAL PROCESSING 100 40 63 P 05. THEORY OF COMPUTATION 100 40 54 P 50 20 40 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 38 P 08. SIGNAL PROCESSING LABORATORY 25 10 19 P TW 09. SIGNAL PROCESSING LABORATORY 50 20 35 P 10. HARDWARE LABORATORY 25 10 20 P TW 11. HARDWARE LABORATORY 50 20 41 P PR FIRST TERM TOTAL = 494/750. ORDN. 1 MARKS:

ORDN. 1 MARKS :

DATE: 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 41 (466)

NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER SADHANA , 71100926M , T80054297 , PICT T80054322 PANDE SWAPNIL SURESH , т80054322 01. DATABASE MANAGEMENT SYSTEMS 100 40 64 P C 12. PRINCIPLES OF PROGRAMMING LANG. PP 100 40 68 PC 13. COMPUTER NETWORKS 02. DATA COMMUNICATION 100 40 69 P C 100 40 57 P C 100 40 56 P C 14. FINANCE & MANAGEMENT INFORMA.SYS.PP 100 40 59 P C 03. MICROPROCESSORS & MICROCONTROLLERPP 04. DIGITAL SIGNAL PROCESSING 100 40 62 P C 15. SYSTEMS PROGRAMMING & OPERA.SYS. PP 100 40 56 P C 05. THEORY OF COMPUTATION 100 40 48 P C 100 40 42 P.C 16. SOFTWARE ENGINEERING 50 20 38 P C 25 10 17 PC 06. RDBMS & VISUAL PROGRAMMING LAB. TW 17. SOFTWARE LABORATORY TW 28 P C 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 50 20 38 P C 18. SOFTWARE LABORATORY PR08. SIGNAL PROCESSING LABORATORY 25 10 16 P C 19. COMPUTER NETWORK 25 10 18 P C TW TW 09. SIGNAL PROCESSING LABORATORY 50 20 32 P C 50 20 38 P 20. COMPUTER NETWORK OR 10. HARDWARE LABORATORY 25 10 19 P C 50 20 37 P C TW 21. SOFTWARE DEVELOPMENT TOOLS LAB. TW 11. HARDWARE LABORATORY 50 20 28 P C 22. SEMINAR AND TECHNICAL COMMUNI. TW 50 20 41 P C PR GRAND TOTAL = 931/1500, RESULT: FIRST CLASS ORDN. 1 MARKS: , т80054323 T80054323 PANDHARE PRIYANKA RAVINDRA RAJANI 47 P 01. DATABASE MANAGEMENT SYSTEMS 100 40 100 40 40 P 02. DATA COMMUNICATION 100 40 47 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 42 P 04. DIGITAL SIGNAL PROCESSING 05. THEORY OF COMPUTATION 100 40 40 P 50 20 34 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 32 P 08. SIGNAL PROCESSING LABORATORY TW 25 10 17 P 50 20 20 P 09. SIGNAL PROCESSING LABORATORY OR 10. HARDWARE LABORATORY 25 10 17 P TW 50 20 05 F 11. HARDWARE LABORATORY PR FIRST TERM TOTAL = 341/750. ORDN. 1 MARKS: , 71201000K , PICT T80054324 PARAG MAHALA , т80054324 **PRAGYAN** 01. DATABASE MANAGEMENT SYSTEMS 100 40 42 P 02. DATA COMMUNICATION PP 100 40 46 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 46 P 04. DIGITAL SIGNAL PROCESSING 100 40 53 P 05. THEORY OF COMPUTATION 100 40 43 P PP 50 20 06. RDBMS & VISUAL PROGRAMMING LAB. TW 38 P 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 28 P 08. SIGNAL PROCESSING LABORATORY 25 10 17 P TW 09. SIGNAL PROCESSING LABORATORY 50 20 20 P 10. HARDWARE LABORATORY 25 10 17 P TW 11. HARDWARE LABORATORY 50 20 35 P PR FIRST TERM TOTAL = 385/750. ORDN. 1 MARKS:

UNIVERSITY OF PUNE ,T.E.(2008 PAT.)(COMPUTER) EXAMINATION NOV. 2013 DATE: 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 42 (467) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71201002F , , PICT , T80054325 T80054325 PARAKH RUSHABH GIRISH JYOTI 01. DATABASE MANAGEMENT SYSTEMS 100 40 61 P 02. DATA COMMUNICATION 100 40 55 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 47 P 04. DIGITAL SIGNAL PROCESSING 100 40 63 P 05. THEORY OF COMPUTATION 100 40 55 P 50 20 35 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 40 P 08. SIGNAL PROCESSING LABORATORY 25 10 TW 16 P 09. SIGNAL PROCESSING LABORATORY 50 20 20 P OR 10. HARDWARE LABORATORY 25 10 16 P TW 11. HARDWARE LABORATORY 50 20 40 P PR FIRST TERM TOTAL = 448/750. ORDN. 1 MARKS: T80054326 PATIL ABHILASHA VIJAY , 71201004B , , PICT , т80054326 SANDHYA 01. DATABASE MANAGEMENT SYSTEMS 100 40 72 P 100 40 64 P 02. DATA COMMUNICATION 100 40 40 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 77 P 04. DIGITAL SIGNAL PROCESSING 05. THEORY OF COMPUTATION 100 40 71 P 50 20 38 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 35 P 25 10 18 P 08. SIGNAL PROCESSING LABORATORY TW 09. SIGNAL PROCESSING LABORATORY 50 20 32 P OR 10. HARDWARE LABORATORY 25 10 20 P TW 11. HARDWARE LABORATORY 50 20 41 P PR FIRST TERM TOTAL = 508/750. ORDN. 1 MARKS: , 71201008E T80054327 PATIL PRATIK PRALHAD , PICT , т80054327 PRATIBHA 01. DATABASE MANAGEMENT SYSTEMS 100 40 65 P 02. DATA COMMUNICATION PP 100 40 58 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 43 P 04. DIGITAL SIGNAL PROCESSING 100 40 59 P 05. THEORY OF COMPUTATION 100 40 54 P 50 20 37 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 41 P 08. SIGNAL PROCESSING LABORATORY 25 10 16 P TW 09. SIGNAL PROCESSING LABORATORY 50 20 12 F 10. HARDWARE LABORATORY 25 10 16 P TW 11. HARDWARE LABORATORY 50 20 42 P PR FIRST TERM TOTAL = 443/750. ORDN. 1 MARKS:

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NOTE: FIRST LINE : SEAT NO., NAME (OTHER LINES: HEAD OF PASSING,	OF THE	CANDI	DATE	, MO	THER, PE	RMANENT		OLLEGE	E, S	EAT	NO.	
T80054328 PATIL VINEET JAYANT 01. DATABASE MANAGEMENT SYSTEMS	PP	100	40		AJAKTA	10	, 70601438D , T80054369 , PRINCIPLES OF PROGRAMMING LANG.		100	-	T8005	P C
02. DATA COMMUNICATION	PP PP	100 100		AA	P C		COMPUTER NETWORKS	PP PP	100			PC
03. MICROPROCESSORS & MICROCONTROLLI		100	40	AA			FINANCE & MANAGEMENT INFORMA.SYS		100	_	46	PC
04. DIGITAL SIGNAL PROCESSING	PP	100	40		г РС		SYSTEMS PROGRAMMING & OPERA.SYS.		100		62	PC
		100			_					_		PC
05. THEORY OF COMPUTATION	PP	50		20	P C P C	_	SOFTWARE LARORATORY	PP TW	100	10		
06. RDBMS & VISUAL PROGRAMMING LAB.		50	-	20			SOFTWARE LABORATORY		_	20	36	P C
07. RDBMS & VISUAL PROGRAMMING LAB. 08. SIGNAL PROCESSING LABORATORY		25	_	11	P		SOFTWARE LABORATORY COMPUTER NETWORK	PR		10		P C
	TW	_	-		_			TW		20		
09. SIGNAL PROCESSING LABORATORY	OR Tu	50	_		P C		COMPUTER NETWORK	OR Tw				PC
10. HARDWARE LABORATORY	TW	25	10	10	P C		SOFTWARE DEVELOPMENT TOOLS LAB.			20 20		PC
11. HARDWARE LABORATORY		50 v T	20	21	PC	22.	SEMINAR AND TECHNICAL COMMUNI.	TW	30	20	21	РС
GRAND TOTAL = 599/1500, RESULT: FAILS ORDN. 1 MARKS:	5 A.I.	K. I.										
T80054329 PATTHE KAUSTUBH TEJRAM					 ЮВНА		, 71347772F , , ,	PIC			т8005	
	PP	100	40	44			, , , , , , , , , , , , , , , , , , , ,	1 10	- 1	,	10003	4323
02. DATA COMMUNICATION		100	40	53								
03. MICROPROCESSORS & MICROCONTROLLI		100	40	33								
04. DIGITAL SIGNAL PROCESSING	PP	100	40	42								
05. THEORY OF COMPUTATION	PP	100	_	40								
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50		32								
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	-	27								
08. SIGNAL PROCESSING LABORATORY	TW	25	10	20								
09. SIGNAL PROCESSING LABORATORY	OR	50	20	27								
10. HARDWARE LABORATORY	TW	25	10	20								
11. HARDWARE LABORATORY	PR	50	_	20								
FIRST TERM TOTAL = 358/750.		30	20	20	•							
ORDN. 1 MARKS :												
T80054330 PAWAR NAYAN SUNIL					LINI		, 71239304ј , т80054305 ,				т8005	
01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	66	РС	12.	PRINCIPLES OF PROGRAMMING LANG.	PP	100	40	66	РС
02. DATA COMMUNICATION	PP	100	40	62	РС	13.	COMPUTER NETWORKS	PP	100	40	59	РС
03. MICROPROCESSORS & MICROCONTROLLI	ERPP	100	40	65	РС	14.	FINANCE & MANAGEMENT INFORMA.SYS	S.PP	100	40	65	РС
04. DIGITAL SIGNAL PROCESSING	PP	100	40	56	РС	15.	SYSTEMS PROGRAMMING & OPERA.SYS.	PP	100	40	66	РС
05. THEORY OF COMPUTATION	PP	100	40	57	РС	16.	SOFTWARE ENGINEERING	PP	100	40	66	РС
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	37	РС	17.	SOFTWARE LABORATORY	TW	25	10	22	РС
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	33	РС	18.	SOFTWARE LABORATORY	PR	50	20	40	Р
08. SIGNAL PROCESSING LABORATORY	TW	25	10	19	РС	19.	COMPUTER NETWORK	TW	25	10	20	РС
09. SIGNAL PROCESSING LABORATORY	OR	50	20	20	РС	20.	COMPUTER NETWORK	OR	50	20	22	РС
10. HARDWARE LABORATORY	TW	25	10	21	РС	21.	SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20		РС
11. HARDWARE LABORATORY	PR	50	20	34	РС	22.	SEMINAR AND TECHNICAL COMMUNI.	TW	50	20	41	РС
GRAND TOTAL = 979/1500, RESULT: FIRST	T CLAS	S										
ORDN. 1 MARKS :												

UNIVERSITY OF PUNE ,T.E.(2008 PAT.)(COMPUTER) EXAMINATION NOV. 2013 DATE: 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 44 (469) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71201015H , , PICT , T80054331 T80054331 PHARANDE ABHISHEK VIJAY RUKHMINEE 01. DATABASE MANAGEMENT SYSTEMS 100 40 57 P 02. DATA COMMUNICATION 100 40 62 P 100 40 44 P 03. MICROPROCESSORS & MICROCONTROLLERPP 04. DIGITAL SIGNAL PROCESSING 100 40 66 P 05. THEORY OF COMPUTATION 100 40 67 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 20 38 P 40 P 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 08. SIGNAL PROCESSING LABORATORY 25 10 19 P TW 09. SIGNAL PROCESSING LABORATORY 50 20 36 P OR 10. HARDWARE LABORATORY 25 10 18 P TW 11. HARDWARE LABORATORY 50 20 36 P PR FIRST TERM TOTAL = 483/750. ORDN. 1 MARKS: T80054332 RAJEEV SEBASTIAN , 71201031K , , PICT , т80054332 **THRESA** 01. DATABASE MANAGEMENT SYSTEMS 100 40 62 P 100 40 73 P 02. DATA COMMUNICATION PP 100 40 41 P 03. MICROPROCESSORS & MICROCONTROLLERPP 04. DIGITAL SIGNAL PROCESSING 100 40 80 P 05. THEORY OF COMPUTATION 100 40 71 P 50 20 42 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 43 P 25 10 23 P 08. SIGNAL PROCESSING LABORATORY TW 09. SIGNAL PROCESSING LABORATORY OR 50 20 38 P 10. HARDWARE LABORATORY 25 10 22 P TW 11. HARDWARE LABORATORY 50 20 40 P PR FIRST TERM TOTAL = 535/750. ORDN. 1 MARKS: T80054333 RATHI ADITYA PRADIPKUMAR , т80054333 **SNEHAL** 01. DATABASE MANAGEMENT SYSTEMS 100 40 40 P 02. DATA COMMUNICATION PP 100 40 52 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 40 P 04. DIGITAL SIGNAL PROCESSING 100 40 44 P 05. THEORY OF COMPUTATION 100 40 47 P 50 20 35 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 30 P 08. SIGNAL PROCESSING LABORATORY 25 10 18 P TW 09. SIGNAL PROCESSING LABORATORY 50 20 20 P 10. HARDWARE LABORATORY 25 10 17 P TW 11. HARDWARE LABORATORY 50 20 25 P PR FIRST TERM TOTAL = 368/750. ORDN. 1 MARKS:

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				-	•		REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. RKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER	
T80054334 RATHI ROSHANI DIPAK					 ΌΤΙ		, 71100969E , T80054311 , PICT , T80054	
01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	40	РС	12.		РС
02. DATA COMMUNICATION	PP	100	40		РС	13.		РС
03. MICROPROCESSORS & MICROCONTROLL	ERPP	100	40	50	РС	14.		РС
04. DIGITAL SIGNAL PROCESSING	PP	100	40	45	РС	15.	SYSTEMS PROGRAMMING & OPERA.SYS. PP 100 40 56	РС
05. THEORY OF COMPUTATION	PP	100	40	67				РС
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	29	РС	17.	SOFTWARE LABORATORY TW 25 10 20	РС
07. RDBMS & VISUAL PROGRAMMING LAB.		50	20	38	РС			РС
08. SIGNAL PROCESSING LABORATORY	TW	25	10	18	РС			РС
09. SIGNAL PROCESSING LABORATORY	OR	50	20	28	РC	20.		Р
10. HARDWARE LABORATORY	TW	25	10	19	_			P C
11. HARDWARE LABORATORY	PR	50	20	_	PC			PC
GRAND TOTAL = 844/1500, RESULT: HIGH							SELIZIVIK AND FEELINGENE COLLIGINET. III. SC 20 SS	
ORDN. 1 MARKS :		0.12 C2	, 100					
T80054335 RATHORE DHRUVESH JAYESH				 JY	 ′OTI		, 71201037J , PICT , T80054	 4335
01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	68			, 122020373 , , , , , , , , , , , , , , , , , ,	.555
02. DATA COMMUNICATION	PP	100	40	76				
03. MICROPROCESSORS & MICROCONTROLL		100	40	56				
04. DIGITAL SIGNAL PROCESSING	PP	100	40	81				
05. THEORY OF COMPUTATION	PP	100	40	76				
06. RDBMS & VISUAL PROGRAMMING LAB.		50	20	44				
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	44				
08. SIGNAL PROCESSING LABORATORY	TW	25	10	24				
09. SIGNAL PROCESSING LABORATORY	OR	50	20	43				
10. HARDWARE LABORATORY	TW	25	10	23				
11. HARDWARE LABORATORY	PR	50		41				
FIRST TERM TOTAL = 576/750.	FIX	30	20	71	r			
ORDN. 1 MARKS :								
T80054336 RATHORE VIPULKUMAR RAME					 IJANA		, 71201038G , , , PICT , T80054	
01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	74			, 122020300 , , 120. , 10003	.550
02. DATA COMMUNICATION	PP	100		63				
03. MICROPROCESSORS & MICROCONTROLL		100		45				
04. DIGITAL SIGNAL PROCESSING	PP	100	40	76				
05. THEORY OF COMPUTATION	PP	100	40	76				
06. RDBMS & VISUAL PROGRAMMING LAB.			20	42				
07. RDBMS & VISUAL PROGRAMMING LAB.			20	42				
08. SIGNAL PROCESSING LABORATORY	TW		10	21				
09. SIGNAL PROCESSING LABORATORY	OR		20	38				
10. HARDWARE LABORATORY	TW	25	10	21				
11. HARDWARE LABORATORY	PR	50		42				
FIRST TERM TOTAL = 540/750.	1 11	30	20	74	•			
ORDN. 1 MARKS :								
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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER RUKMINI , 71100973C , T80054312 , PICT , T80054337 T80054337 RAUT VAIBHAV SHARAD 12. PRINCIPLES OF PROGRAMMING LANG. PP 100 40 47 PC 01. DATABASE MANAGEMENT SYSTEMS 100 40 54 P C 13. COMPUTER NETWORKS PP 02. DATA COMMUNICATION 100 40 66 P C 100 40 51 P C 100 40 54 P C 14. FINANCE & MANAGEMENT INFORMA.SYS.PP 100 40 51 P.C 03. MICROPROCESSORS & MICROCONTROLLERPP 04. DIGITAL SIGNAL PROCESSING 100 40 73 P C 15. SYSTEMS PROGRAMMING & OPERA.SYS. PP 100 40 52 P C 05. THEORY OF COMPUTATION 100 40 65 P C 100 40 49 P.C 16. SOFTWARE ENGINEERING 50 20 23 P C 25 10 15 P C 06. RDBMS & VISUAL PROGRAMMING LAB. TW 17. SOFTWARE LABORATORY TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 39 P C 50 20 34 P C 18. SOFTWARE LABORATORY PR 08. SIGNAL PROCESSING LABORATORY 25 10 10 P C 19. COMPUTER NETWORK 25 10 15 P C TW TW 09. SIGNAL PROCESSING LABORATORY 50 20 35 P C 50 20 28 P 20. COMPUTER NETWORK OR 10. HARDWARE LABORATORY 25 10 13 P C 50 20 31 P C TW 21. SOFTWARE DEVELOPMENT TOOLS LAB. TW PR 11. HARDWARE LABORATORY 50 20 33 P C 22. SEMINAR AND TECHNICAL COMMUNI. TW 50 20 31 P C GRAND TOTAL = 869/1500, RESULT: HIGHER SECOND CLASS ORDN. 1 MARKS: , 71100974M , T80054313 , PICT , T80054338 T80054338 RENUKE JAGDISH SHIVAJIRAO VANAMALA 01. DATABASE MANAGEMENT SYSTEMS 40 P C 12. PRINCIPLES OF PROGRAMMING LANG. PP 100 40 52 P C 100 40 100 40 48 P C 13. COMPUTER NETWORKS PP 100 40 56 P C 02. DATA COMMUNICATION 100 40 50 P C 100 40 03. MICROPROCESSORS & MICROCONTROLLERPP 14. FINANCE & MANAGEMENT INFORMA.SYS.PP 55 P C 100 40 43 P C 100 40 47 P C 04. DIGITAL SIGNAL PROCESSING 15. SYSTEMS PROGRAMMING & OPERA.SYS. PP 100 40 50 P C 100 40 49 P C 05. THEORY OF COMPUTATION 16. SOFTWARE ENGINEERING 50 20 33 P C 25 10 18 P C 06. RDBMS & VISUAL PROGRAMMING LAB. TW 17. SOFTWARE LABORATORY TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 22 P C 50 20 22 P 18. SOFTWARE LABORATORY PR 25 10 08. SIGNAL PROCESSING LABORATORY TW 25 10 16 P C 19. COMPUTER NETWORK TW 18 P C 50 20 50 20 28 P 09. SIGNAL PROCESSING LABORATORY OR 20 P C 20. COMPUTER NETWORK 10. HARDWARE LABORATORY 25 10 19 P C 50 20 30 P C TW 21. SOFTWARE DEVELOPMENT TOOLS LAB. TW 11. HARDWARE LABORATORY 50 20 23 P C 22. SEMINAR AND TECHNICAL COMMUNI. TW 50 20 33 P C PR GRAND TOTAL = 772/1500, RESULT: SECOND CLASS ORDN. 1 MARKS: , 71201039E , , PICT T80054339 RISHI AGRAWAL MANJU , т80054339 01. DATABASE MANAGEMENT SYSTEMS 100 40 54 P 02. DATA COMMUNICATION PP 100 40 57 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 46 P 04. DIGITAL SIGNAL PROCESSING 100 40 64 P 05. THEORY OF COMPUTATION 100 40 57 P PP 50 20 06. RDBMS & VISUAL PROGRAMMING LAB. TW 37 P 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 38 P 08. SIGNAL PROCESSING LABORATORY 25 10 16 P TW 09. SIGNAL PROCESSING LABORATORY 50 20 22 P 10. HARDWARE LABORATORY 25 10 19 P TW 11. HARDWARE LABORATORY 50 20 35 P PR FIRST TERM TOTAL = 445/750. ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71201041G , , PICT , T80054340 T80054340 ROHERA SHWETA VINOD VEENA 01. DATABASE MANAGEMENT SYSTEMS 100 40 50 P 02. DATA COMMUNICATION 100 40 59 P 100 40 30 F 03. MICROPROCESSORS & MICROCONTROLLERPP 04. DIGITAL SIGNAL PROCESSING 100 40 41 P 05. THEORY OF COMPUTATION 100 40 54 P 50 20 31 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 26 P 08. SIGNAL PROCESSING LABORATORY 25 10 TW 15 P 09. SIGNAL PROCESSING LABORATORY 50 20 20 P OR 10. HARDWARE LABORATORY 25 10 15 P TW 11. HARDWARE LABORATORY 50 20 36 P PR FIRST TERM TOTAL = 377/750. ORDN. 1 MARKS: T80054341 RUIKAR AMEY SUHAS ARATI , 71201044M , , PICT , T80054341 67 P 01. DATABASE MANAGEMENT SYSTEMS 100 40 100 40 68 P 02. DATA COMMUNICATION PP 100 40 53 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 71 P 04. DIGITAL SIGNAL PROCESSING 05. THEORY OF COMPUTATION 100 40 74 P 50 20 45 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 40 P 08. SIGNAL PROCESSING LABORATORY TW 25 10 23 P 09. SIGNAL PROCESSING LABORATORY 50 20 39 P OR 10. HARDWARE LABORATORY 25 10 21 P TW 11. HARDWARE LABORATORY 50 20 38 P PR FIRST TERM TOTAL = 539/750. ORDN. 1 MARKS: , 71239305G , T80054314 , PICT T80054342 RUPNER GEETA MADHAV **GOKULA** , т80054342 01. DATABASE MANAGEMENT SYSTEMS 100 40 60 P C 12. PRINCIPLES OF PROGRAMMING LANG. PP 100 40 60 PC 63 P C 02. DATA COMMUNICATION PP 100 40 67 P C 13. COMPUTER NETWORKS PP 100 40 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 65 P C 14. FINANCE & MANAGEMENT INFORMA.SYS.PP 100 40 72 P C 04. DIGITAL SIGNAL PROCESSING 100 40 46 P C 15. SYSTEMS PROGRAMMING & OPERA.SYS. PP 100 40 55 P C 05. THEORY OF COMPUTATION 100 40 59 P C PP 100 40 59 P C PP 16. SOFTWARE ENGINEERING 50 20 40 P C 23 P C 06. RDBMS & VISUAL PROGRAMMING LAB. TW 17. SOFTWARE LABORATORY TW 25 10 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 45 P C 18. SOFTWARE LABORATORY 50 20 39 P PR 23 P C 08. SIGNAL PROCESSING LABORATORY TW 25 10 23 P C 19. COMPUTER NETWORK TW 25 10 50 20 22 P C 09. SIGNAL PROCESSING LABORATORY 50 20 29 P C 20. COMPUTER NETWORK OR 10. HARDWARE LABORATORY 25 10 23 P C 21. SOFTWARE DEVELOPMENT TOOLS LAB. TW 50 20 44 P C TW 11. HARDWARE LABORATORY 50 20 35 P C 22. SEMINAR AND TECHNICAL COMMUNI. TW 50 20 42 P C PR GRAND TOTAL = 994/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS:

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DATE . II MAR. 2014	CENTI	KE . P	UNE	TN211	IUIE	OF COMPOSI	EK LECHNOLC	JGY, PUNE.		PAGE	. NO.	40	(4	73)
NOTE: FIRST LINE: SEAT NO., NAME (•	•		•		•		•			
OTHER LINES: HEAD OF PASSING,														
T80054343 SAISH SURESH SALI	D.D.	100	40		ENA		, 7120)1U47F ,	,	PIC	. 1	,	т8005	4343
	PP	100		62										
02. DATA COMMUNICATION	PP 	100	40	62										
03. MICROPROCESSORS & MICROCONTROLLI		100	40	51										
04. DIGITAL SIGNAL PROCESSING	PP	100	40	75										
05. THEORY OF COMPUTATION	PP	100	40	71										
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	44										
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	38	Р									
08. SIGNAL PROCESSING LABORATORY	TW	25	10	23										
09. SIGNAL PROCESSING LABORATORY	OR	50	20	38	Р									
10. HARDWARE LABORATORY	TW	25	10	22	Р									
11. HARDWARE LABORATORY	PR	50	20	41	Р									
FIRST TERM TOTAL = $527/750$.														
ORDN. 1 MARKS :														
T80054344 SALECHA DIVYA SANJAY				NI	RMALA	A	, 7110)0983L ,	, т80054315 ,	PIC	Τ.	,	т8005	4344
01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	46	РС	12	. PRINCIPLE	S OF PROGRA	AMMING LANG.	PP	100	40	42	РС
02. DATA COMMUNICATION	PP	100	40	53	РС	13	. COMPUTER	NETWORKS		PP	100	40	41	РС
03. MICROPROCESSORS & MICROCONTROLLI	ERPP	100	40	43	РС	14	. FINANCE &	MANAGEMENT	Γ INFORMA.SYS	S.PP	100	40	40	РС
04. DIGITAL SIGNAL PROCESSING	PP	100	40	55	РС	15	. SYSTEMS F	PROGRAMMING	& OPERA.SYS.	PP	100	40	43	РС
05. THEORY OF COMPUTATION	PP	100	40	62	РС	16	. SOFTWARE	ENGINEERING	3	PP	100	40	40	РС
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	32	РС	17	. SOFTWARE	LABORATORY		TW	25	10	20	РС
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	37	РС	18	. SOFTWARE	LABORATORY		PR	50	20	12	F
08. SIGNAL PROCESSING LABORATORY	TW	25	10	18	РС		. COMPUTER			TW	25			РС
09. SIGNAL PROCESSING LABORATORY	OR	50	20	36	РС		. COMPUTER			OR	50	20	30	РС
10. HARDWARE LABORATORY	TW		10		РС				T TOOLS LAB.			20		РC
11. HARDWARE LABORATORY	PR		20		P C			AND TECHNICA		TW		20	_	P C
GRAND TOTAL = 799/1500, RESULT: FAILS				32			. 32.12.0.00		(2 00, 11, 10, 12, 1		50		33	
ORDN. 1 MARKS:	<i>5</i> /(1.11													
T80054346 SALVE HONEY SUNIL			• •		 NBINA					PIC			 т8005	
	PP	100	40	54			, 7120	,	,	110		,	10003	1310
02. DATA COMMUNICATION	PP	100	40	47										
03. MICROPROCESSORS & MICROCONTROLLI		100		41										
04. DIGITAL SIGNAL PROCESSING	PP	100		61										
05. THEORY OF COMPUTATION	PP	100		59										
06. RDBMS & VISUAL PROGRAMMING LAB.			20	35										
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50		39										
08. SIGNAL PROCESSING LABORATORY	TW		10	22										
09. SIGNAL PROCESSING LABORATORY	OR		20	35										
10. HARDWARE LABORATORY	TW		10	20										
11. HARDWARE LABORATORY	PR	50	20	10	F									
FIRST TERM TOTAL = $423/750$.														
ORDN. 1 MARKS :														

DATE: 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 49 (474)

NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER T80054347 SANAP VISHAL PRABHAKAR SUMATI , 71347773D , , PICT , T80054347 01. DATABASE MANAGEMENT SYSTEMS 100 40 57 P 02. DATA COMMUNICATION 100 40 64 P 100 40 49 P 03. MICROPROCESSORS & MICROCONTROLLERPP 04. DIGITAL SIGNAL PROCESSING 100 40 60 P 05. THEORY OF COMPUTATION 100 40 65 P 50 20 39 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 42 P 08. SIGNAL PROCESSING LABORATORY 25 10 22 P TW 50 20 22 P 09. SIGNAL PROCESSING LABORATORY OR 10. HARDWARE LABORATORY 25 10 20 P TW 11. HARDWARE LABORATORY 50 20 31 P PR FIRST TERM TOTAL = 471/750. ORDN. 1 MARKS: T80054348 SANDEEP AGARWAL , 70925587L , T80054375 , PICT , т80054348 RITA 100 40 47 P C 12. PRINCIPLES OF PROGRAMMING LANG. PP 100 40 PC 01. DATABASE MANAGEMENT SYSTEMS 100 40 40 P C 100 40 41 P C 02. DATA COMMUNICATION PP 13. COMPUTER NETWORKS PP 100 40 100 40 03. MICROPROCESSORS & MICROCONTROLLERPP AA F 14. FINANCE & MANAGEMENT INFORMA.SYS.PP 40 P C 100 40 50 P C 100 40 04. DIGITAL SIGNAL PROCESSING 15. SYSTEMS PROGRAMMING & OPERA.SYS. PP 40 P 100 40 50 P C 100 40 05. THEORY OF COMPUTATION 16. SOFTWARE ENGINEERING 44 P C 50 20 34 P C 25 10 06. RDBMS & VISUAL PROGRAMMING LAB. TW 17. SOFTWARE LABORATORY TW 10 P C 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 20 P C 50 20 22 P C 18. SOFTWARE LABORATORY PR 25 10 15 P C 25 10 08. SIGNAL PROCESSING LABORATORY TW 19. COMPUTER NETWORK TW 11 P C 09. SIGNAL PROCESSING LABORATORY OR 50 20 22 P C 20. COMPUTER NETWORK 50 20 21 P C 10. HARDWARE LABORATORY 25 10 17 P C 50 20 20 P C TW 21. SOFTWARE DEVELOPMENT TOOLS LAB. TW 11. HARDWARE LABORATORY 50 20 30 P C 50 20 24 P C PR 22. SEMINAR AND TECHNICAL COMMUNI. TW GRAND TOTAL = 638/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS: T80054349 SANDEEP JEEVRAJ PARMAR , т80054349 PUSHPA PARMAR 01. DATABASE MANAGEMENT SYSTEMS 100 40 57 P 02. DATA COMMUNICATION PP 100 40 60 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 40 P 04. DIGITAL SIGNAL PROCESSING 100 40 54 P 05. THEORY OF COMPUTATION 100 40 50 P PP 50 20 06. RDBMS & VISUAL PROGRAMMING LAB. TW 44 P 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 41 P 08. SIGNAL PROCESSING LABORATORY 25 10 22 P TW 09. SIGNAL PROCESSING LABORATORY 50 20 23 P 10. HARDWARE LABORATORY 25 10 21 P TW 11. HARDWARE LABORATORY 50 20 40 P PR FIRST TERM TOTAL = 452/750. ORDN. 1 MARKS:

DATE: 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 50 (475)

NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71201054J , , PICT , T80054350 T80054350 SATHE VYANKTESHPRASAD GANESH BHAGYASHRI 01. DATABASE MANAGEMENT SYSTEMS 100 40 57 P 02. DATA COMMUNICATION 100 40 64 P 100 40 42 P 03. MICROPROCESSORS & MICROCONTROLLERPP 04. DIGITAL SIGNAL PROCESSING 100 40 55 P 05. THEORY OF COMPUTATION 100 40 65 P 50 20 24 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 34 P 08. SIGNAL PROCESSING LABORATORY 25 10 TW 12 P 50 20 20 P 09. SIGNAL PROCESSING LABORATORY OR 10. HARDWARE LABORATORY 25 10 11 P TW 11. HARDWARE LABORATORY 50 20 34 P PR FIRST TERM TOTAL = 418/750. ORDN. 1 MARKS: SHRUTI , 71100991M , T80054320 , PICT , T80054352 T80054352 SAVAJI ADITYA JAYANT 47 P C 12. PRINCIPLES OF PROGRAMMING LANG. PP 100 40 48 PC 01. DATABASE MANAGEMENT SYSTEMS 100 40 100 40 72 P C 13. COMPUTER NETWORKS PP 100 40 02. DATA COMMUNICATION PP 47 P C 100 40 44 P C 100 40 03. MICROPROCESSORS & MICROCONTROLLERPP 14. FINANCE & MANAGEMENT INFORMA.SYS.PP 57 P C 100 40 54 P C 100 40 04. DIGITAL SIGNAL PROCESSING 15. SYSTEMS PROGRAMMING & OPERA.SYS. PP 49 P C 100 40 40 P C 100 40 05. THEORY OF COMPUTATION 16. SOFTWARE ENGINEERING 48 P C 50 20 35 P C 25 10 06. RDBMS & VISUAL PROGRAMMING LAB. TW 17. SOFTWARE LABORATORY TW 21 P C 07. RDBMS & VISUAL PROGRAMMING LAB. PR 39 P C 50 20 30 P 50 20 18. SOFTWARE LABORATORY PR 25 10 08. SIGNAL PROCESSING LABORATORY TW 25 10 20 P C 19. COMPUTER NETWORK TW 21 P C 09. SIGNAL PROCESSING LABORATORY OR 50 20 38 P C 20. COMPUTER NETWORK 50 20 25 P C 10. HARDWARE LABORATORY 25 10 18 P C 50 20 38 P C TW 21. SOFTWARE DEVELOPMENT TOOLS LAB. TW 50 20 26 P C 11. HARDWARE LABORATORY 50 20 38 P C PR 22. SEMINAR AND TECHNICAL COMMUNI. TW GRAND TOTAL = 855/1500, RESULT: HIGHER SECOND CLASS ORDN. 1 MARKS: , 71100992K , T80054321 , PICT , T80054353 SUNITA T80054353 SAWANT TEJASWINI NANASAHEB 01. DATABASE MANAGEMENT SYSTEMS 100 40 48 P C 12. PRINCIPLES OF PROGRAMMING LANG. PP 100 40 56 PC 02. DATA COMMUNICATION PP 100 40 58 P C 13. COMPUTER NETWORKS PP 100 40 43 P C 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 47 P C 14. FINANCE & MANAGEMENT INFORMA.SYS.PP 100 40 53 P C 04. DIGITAL SIGNAL PROCESSING 100 40 40 P C 15. SYSTEMS PROGRAMMING & OPERA.SYS. PP 100 40 46 P.C 05. THEORY OF COMPUTATION 100 40 47 P C PP 100 40 46 P C PP 16. SOFTWARE ENGINEERING 50 20 32 P C 25 10 06. RDBMS & VISUAL PROGRAMMING LAB. TW 17. SOFTWARE LABORATORY TW 19 P C 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 30 P C 18. SOFTWARE LABORATORY 50 20 29 P C PR 21 P C 08. SIGNAL PROCESSING LABORATORY 25 10 16 P C 19. COMPUTER NETWORK TW 25 10 TW 34 P 50 20 09. SIGNAL PROCESSING LABORATORY 50 20 34 P C 20. COMPUTER NETWORK 10. HARDWARE LABORATORY 25 10 20 P C 21. SOFTWARE DEVELOPMENT TOOLS LAB. TW 50 20 36 P C TW 11. HARDWARE LABORATORY 50 20 21 P C 22. SEMINAR AND TECHNICAL COMMUNI. TW 50 20 36 P C PR GRAND TOTAL = 812/1500, RESULT: SECOND CLASS ORDN. 1 MARKS:

UNIVERSITY OF PUNE ,T.E.(2008 PAT.)(COMPUTER) EXAMINATION NOV. 2013 DATE: 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 51 (476) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71201062к , , , РІСТ , Т80054354 T80054354 SHAHANE TANMAY SHIVKUMAR VASUNDHARA 01. DATABASE MANAGEMENT SYSTEMS 100 40 45 P 02. DATA COMMUNICATION 100 40 40 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 40 P 04. DIGITAL SIGNAL PROCESSING 100 40 40 P 05. THEORY OF COMPUTATION 100 40 44 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 20 20 P 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 36 P 08. SIGNAL PROCESSING LABORATORY 25 10 15 P TW 09. SIGNAL PROCESSING LABORATORY 50 20 13 F OR 10. HARDWARE LABORATORY 25 10 13 P TW 11. HARDWARE LABORATORY 50 20 31 P PR FIRST TERM TOTAL = 337/750. ORDN. 1 MARKS: T80054355 SHELKANDE AKASH DAGADU , 71101000F , , PICT , т80054355 SEETA 01. DATABASE MANAGEMENT SYSTEMS 42 P 100 40 100 40 32 F 02. DATA COMMUNICATION 100 40 19 F 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 26 F 04. DIGITAL SIGNAL PROCESSING 05. THEORY OF COMPUTATION 100 40 32 F 50 20 22 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 30 P 25 10 15 P 08. SIGNAL PROCESSING LABORATORY TW 09. SIGNAL PROCESSING LABORATORY 50 20 20 P OR 10. HARDWARE LABORATORY 25 10 15 P TW 11. HARDWARE LABORATORY 50 20 32 P PR FIRST TERM TOTAL = 285/750. ORDN. 1 MARKS: , 71201067L T80054356 SHENDKAR NISHIGANDHA NARSING , PICT , T80054356 SANGEETA 01. DATABASE MANAGEMENT SYSTEMS 100 40 51 P 02. DATA COMMUNICATION PP 100 40 57 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 48 P 04. DIGITAL SIGNAL PROCESSING 100 40 66 P 05. THEORY OF COMPUTATION 100 40 65 P 50 20 06. RDBMS & VISUAL PROGRAMMING LAB. TW 41 P 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 42 P 08. SIGNAL PROCESSING LABORATORY 25 10 22 P TW 09. SIGNAL PROCESSING LABORATORY 50 20 20 P 10. HARDWARE LABORATORY 25 10 21 P TW 11. HARDWARE LABORATORY 50 20 40 P PR FIRST TERM TOTAL = 473/750.

ORDN. 1 MARKS :

UNIVERSITY OF PUNE ,T.E.(2008 PAT.)(COMPUTER) EXAMINATION NOV. 2013 DATE: 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 52 (477) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71201069G , , PICT , T80054357 T80054357 SHETE PRASHANT SAYAJI MEERA 01. DATABASE MANAGEMENT SYSTEMS 100 40 66 P 02. DATA COMMUNICATION 100 40 56 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 57 P 04. DIGITAL SIGNAL PROCESSING 100 40 57 P 05. THEORY OF COMPUTATION 100 40 72 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 20 35 P 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 37 P 08. SIGNAL PROCESSING LABORATORY 25 10 21 P TW 09. SIGNAL PROCESSING LABORATORY 50 20 23 P OR 10. HARDWARE LABORATORY 25 10 20 P TW 11. HARDWARE LABORATORY 50 20 28 P PR FIRST TERM TOTAL = 472/750. ORDN. 1 MARKS: T80054358 SHETTY VIPLUV GANESH , 71201070L , , PICT , т80054358 PUSHPA 01. DATABASE MANAGEMENT SYSTEMS 100 40 49 P 100 40 69 P 02. DATA COMMUNICATION 100 40 43 P 03. MICROPROCESSORS & MICROCONTROLLERPP 04. DIGITAL SIGNAL PROCESSING 100 40 54 P 05. THEORY OF COMPUTATION 100 40 68 P 50 20 40 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 40 P 25 10 21 P 08. SIGNAL PROCESSING LABORATORY TW 09. SIGNAL PROCESSING LABORATORY OR 50 20 36 P 10. HARDWARE LABORATORY 25 10 20 P TW 11. HARDWARE LABORATORY 50 20 45 P PR FIRST TERM TOTAL = 485/750. ORDN. 1 MARKS: T80054359 SHEWALE NEHA SHAMRAO SHEWALE VAISHALI , 71347775L , PICT , т80054359 01. DATABASE MANAGEMENT SYSTEMS 100 40 45 P 02. DATA COMMUNICATION PP 100 40 59 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 32 F 04. DIGITAL SIGNAL PROCESSING 100 40 46 P 05. THEORY OF COMPUTATION 100 40 60 P 50 20 40 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 36 P 08. SIGNAL PROCESSING LABORATORY 25 10 21 P TW 09. SIGNAL PROCESSING LABORATORY 50 20 21 P 10. HARDWARE LABORATORY 25 10 18 P TW 11. HARDWARE LABORATORY 50 20 30 P PR FIRST TERM TOTAL = 408/750. ORDN. 1 MARKS:

UNIVERSITY OF PUNE ,T.E. (2008 PAT.) (COMPUTER) EXAMINATION NOV. 2013 DATE: 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 53 (478) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80054360 SHIKHA AGARWAL		RITA			TA	, 71101003L	,	,	PICT	, т80054360
01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	61	Р					
02. DATA COMMUNICATION	PP	100	40	63	Р					
03. MICROPROCESSORS & MICROCONTROLLE	ERPP	100	40	40	Р					
04. DIGITAL SIGNAL PROCESSING	PP	100	40	52	Р					
05. THEORY OF COMPUTATION	PP	100	40	52	Р					
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	38	Р					
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	34	Р					
08. SIGNAL PROCESSING LABORATORY	TW	25	10	20	Р					
09. SIGNAL PROCESSING LABORATORY	OR	50	20	21	Р					
10. HARDWARE LABORATORY	TW	25	10	19	Р					
11. HARDWARE LABORATORY	PR	50	20	36	Р					
FIRST TERM TOTAL = $436/750$.										
ORDN. 1 MARKS :										

ORDN. 1 MARKS:

, 70601498H , T80054379 , PICT , T80054361 T80054361 SHUBHAM SINGH JAISHREE 01. DATABASE MANAGEMENT SYSTEMS 100 40 AA F 02. DATA COMMUNICATION 100 40 AA F PP 100 40 AA F 03. MICROPROCESSORS & MICROCONTROLLERPP 04. DIGITAL SIGNAL PROCESSING 100 40 AA F 05. THEORY OF COMPUTATION 100 40 AA F 50 20 20 P C 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 AA F 08. SIGNAL PROCESSING LABORATORY 25 10 11 P C TW

09. SIGNAL PROCESSING LABORATORY 50 20 20 P C 10. HARDWARE LABORATORY 25 10 10 P C TW 11. HARDWARE LABORATORY 50 20 20 P C PR

FIRST TERM TOTAL = 81/750.

, 71201074C , PICT , т80054362 T80054362 SIDDHANT SURENDRA GADRE VIDULA 01. DATABASE MANAGEMENT SYSTEMS 100 40 57 P 02. DATA COMMUNICATION PP 100 40 63 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 41 P 04. DIGITAL SIGNAL PROCESSING 100 40 51 P 05. THEORY OF COMPUTATION 100 40 59 P 50 20 06. RDBMS & VISUAL PROGRAMMING LAB. TW 35 P 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 40 P 08. SIGNAL PROCESSING LABORATORY TW 25 10 20 P 09. SIGNAL PROCESSING LABORATORY 50 20 28 P 10. HARDWARE LABORATORY 25 10 17 P TW

11. HARDWARE LABORATORY 50 20 29 P PR

FIRST TERM TOTAL = 440/750.

ORDN. 1 MARKS:

DATE: 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 54 (479)

NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71201076к , , , ріст , т80054363 T80054363 SIDHANT AGGARWAL ALPANA 01. DATABASE MANAGEMENT SYSTEMS 100 40 58 P 02. DATA COMMUNICATION 100 40 58 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 40 P 04. DIGITAL SIGNAL PROCESSING 100 40 53 P 05. THEORY OF COMPUTATION 100 40 51 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 20 41 P 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 41 P 08. SIGNAL PROCESSING LABORATORY 25 10 21 P TW 09. SIGNAL PROCESSING LABORATORY 50 20 24 P OR 10. HARDWARE LABORATORY 25 10 20 P TW 11. HARDWARE LABORATORY 50 20 36 P PR FIRST TERM TOTAL = 443/750. ORDN. 1 MARKS: T80054364 SOMDEEP DEY , 71201078F , , PICT , т80054364 JHUMA 01. DATABASE MANAGEMENT SYSTEMS 67 P 100 40 100 40 65 P 02. DATA COMMUNICATION PP 100 40 46 P 03. MICROPROCESSORS & MICROCONTROLLERPP 04. DIGITAL SIGNAL PROCESSING 100 40 56 P 05. THEORY OF COMPUTATION 100 40 73 P 50 20 40 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 40 P 25 10 22 P 08. SIGNAL PROCESSING LABORATORY TW 09. SIGNAL PROCESSING LABORATORY 50 20 39 P OR 10. HARDWARE LABORATORY 25 10 22 P TW 11. HARDWARE LABORATORY 50 20 36 P PR FIRST TERM TOTAL = 506/750. ORDN. 1 MARKS: , 71201081F T80054365 SUKET SHARMA , PICT , т80054365 SEEMA 01. DATABASE MANAGEMENT SYSTEMS 100 40 68 P 02. DATA COMMUNICATION PP 100 40 68 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 45 P 04. DIGITAL SIGNAL PROCESSING 100 40 74 P 05. THEORY OF COMPUTATION 100 40 78 P 50 20 06. RDBMS & VISUAL PROGRAMMING LAB. TW 46 P 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 46 P 08. SIGNAL PROCESSING LABORATORY 25 10 24 P TW 09. SIGNAL PROCESSING LABORATORY 50 20 22 P 10. HARDWARE LABORATORY 25 10 23 P TW 11. HARDWARE LABORATORY 50 20 45 P PR FIRST TERM TOTAL = 539/750. ORDN. 1 MARKS:

UNIVERSITY OF PUNE ,T.E. (2008 PAT.) (COMPUTER) EXAMINATION NOV. 2013 DATE: 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 55 (480) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER DEEPALI , 71201083B , , PICT , T80054366 T80054366 SURADKAR VIVEK PRAKASH 01. DATABASE MANAGEMENT SYSTEMS 100 40 57 P 02. DATA COMMUNICATION 100 40 64 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 49 P 04. DIGITAL SIGNAL PROCESSING 100 40 59 P 05. THEORY OF COMPUTATION 100 40 61 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 20 35 P 42 P 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 08. SIGNAL PROCESSING LABORATORY 25 10 20 P TW 09. SIGNAL PROCESSING LABORATORY 50 20 20 P OR 10. HARDWARE LABORATORY 25 10 19 P TW 11. HARDWARE LABORATORY 50 20 21 P PR FIRST TERM TOTAL = 447/750. ORDN. 1 MARKS: T80054367 SURUCHI SINGH , 71201084L , , PICT , т80054367 RENU 01. DATABASE MANAGEMENT SYSTEMS 100 40 58 P 100 40 54 P 02. DATA COMMUNICATION PP 100 40 40 P 03. MICROPROCESSORS & MICROCONTROLLERPP 04. DIGITAL SIGNAL PROCESSING 100 40 51 P 05. THEORY OF COMPUTATION 100 40 29 F 50 20 36 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 37 P 25 10 19 P 08. SIGNAL PROCESSING LABORATORY TW 09. SIGNAL PROCESSING LABORATORY 50 20 20 P OR 10. HARDWARE LABORATORY 25 10 18 P TW 11. HARDWARE LABORATORY 50 20 21 P PR FIRST TERM TOTAL = 383/750. ORDN. 1 MARKS: SUTAR MANISHA SANJAY , 71347776J , PICT T80054368 SUTAR ASHUTOSH SANJAY , т80054368 01. DATABASE MANAGEMENT SYSTEMS 100 40 48 P 02. DATA COMMUNICATION PP 100 40 AA F 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 45 P 04. DIGITAL SIGNAL PROCESSING 100 40 44 P 05. THEORY OF COMPUTATION 100 40 50 P 50 20 35 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 36 P 08. SIGNAL PROCESSING LABORATORY 25 10 18 P TW 09. SIGNAL PROCESSING LABORATORY 50 20 20 P 10. HARDWARE LABORATORY 25 10 17 P TW 11. HARDWARE LABORATORY 50 20 36 P PR FIRST TERM TOTAL = 349/750. ORDN. 1 MARKS:

UNIVERSITY OF PUNE ,T.E.(2008 PAT.)(COMPUTER) EXAMINATION NOV. 2013 DATE: 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 56 (481) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71201086G , , PICT , T80054369 T80054369 SUYASH PRAMOD MULBAGAL SHAILAJA 01. DATABASE MANAGEMENT SYSTEMS 100 40 53 P 02. DATA COMMUNICATION 100 40 41 P 100 40 40 P 03. MICROPROCESSORS & MICROCONTROLLERPP 04. DIGITAL SIGNAL PROCESSING 100 40 46 P 05. THEORY OF COMPUTATION 100 40 31 F 50 20 35 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 34 P 08. SIGNAL PROCESSING LABORATORY 25 10 TW 18 P 09. SIGNAL PROCESSING LABORATORY 50 20 30 P OR 10. HARDWARE LABORATORY 25 10 18 P TW 11. HARDWARE LABORATORY 50 20 28 P PR FIRST TERM TOTAL = 374/750. ORDN. 1 MARKS: T80054370 SWAPNIL KUMAR LANKE , 71201087E , , PICT , т80054370 SUNITA 01. DATABASE MANAGEMENT SYSTEMS 100 40 56 P 100 40 55 P 02. DATA COMMUNICATION 100 40 43 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 42 P 04. DIGITAL SIGNAL PROCESSING 05. THEORY OF COMPUTATION 100 40 43 P 50 20 20 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 30 P 25 10 12 P 08. SIGNAL PROCESSING LABORATORY TW 09. SIGNAL PROCESSING LABORATORY 50 20 10 F OR 10. HARDWARE LABORATORY 25 10 12 P TW 11. HARDWARE LABORATORY 50 20 29 P PR FIRST TERM TOTAL = 352/750. ORDN. 1 MARKS: , 71201091C , PICT T80054371 TAMHANE NEIL AJIT , т80054371 VAIBHAVI 01. DATABASE MANAGEMENT SYSTEMS 100 40 43 P 02. DATA COMMUNICATION PP 100 40 41 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 33 F 04. DIGITAL SIGNAL PROCESSING 100 40 49 P 05. THEORY OF COMPUTATION 100 40 25 F 50 20 06. RDBMS & VISUAL PROGRAMMING LAB. TW 38 P 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 30 P 08. SIGNAL PROCESSING LABORATORY 25 10 19 P TW 09. SIGNAL PROCESSING LABORATORY 50 20 12 F 10. HARDWARE LABORATORY 25 10 19 P TW 11. HARDWARE LABORATORY 50 20 36 P PR FIRST TERM TOTAL = 345/750.

ORDN. 1 MARKS :

DATE: 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 57 (482)

NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER NANDINI , 71201094H , , , РІСТ , Т80054372 T80054372 TAYADE SUSHANT RAMESH 01. DATABASE MANAGEMENT SYSTEMS 100 40 AA F 02. DATA COMMUNICATION 100 40 32 F 100 40 AA F 03. MICROPROCESSORS & MICROCONTROLLERPP 04. DIGITAL SIGNAL PROCESSING 100 40 AA F 05. THEORY OF COMPUTATION 100 40 AA F 50 20 20 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 31 P 08. SIGNAL PROCESSING LABORATORY 25 10 TW 12 P 50 20 10 F 09. SIGNAL PROCESSING LABORATORY OR 10. HARDWARE LABORATORY 25 10 12 P TW 11. HARDWARE LABORATORY 50 20 AA F PR FIRST TERM TOTAL = 117/750. ORDN. 1 MARKS: JAYASHREE , 70925632K , T80054381 , PICT , T80054373 T80054373 THAKARE SAGAR SATISH 100 40 42 P C 12. PRINCIPLES OF PROGRAMMING LANG. PP 100 40 45 PC 01. DATABASE MANAGEMENT SYSTEMS 100 40 47 P C 13. COMPUTER NETWORKS PP 100 40 40 P C 02. DATA COMMUNICATION PP 100 40 40 P C 100 40 03. MICROPROCESSORS & MICROCONTROLLERPP 14. FINANCE & MANAGEMENT INFORMA.SYS.PP 48 P C 100 40 40 P 100 40 59 P C 04. DIGITAL SIGNAL PROCESSING 15. SYSTEMS PROGRAMMING & OPERA.SYS. PP 100 40 40 P C 100 40 05. THEORY OF COMPUTATION 16. SOFTWARE ENGINEERING 47 P C 50 20 30 P C 25 10 06. RDBMS & VISUAL PROGRAMMING LAB. TW 17. SOFTWARE LABORATORY TW 10 P C 07. RDBMS & VISUAL PROGRAMMING LAB. PR 37 P C 50 20 20\$ P C 50 20 18. SOFTWARE LABORATORY PR 25 10 08. SIGNAL PROCESSING LABORATORY TW 25 10 12 P C 19. COMPUTER NETWORK TW 11 P C 50 20 09. SIGNAL PROCESSING LABORATORY OR 25 P C 20. COMPUTER NETWORK 50 20 21 P C 10. HARDWARE LABORATORY 25 10 15 P C 50 20 20 P C TW 21. SOFTWARE DEVELOPMENT TOOLS LAB. TW 11. HARDWARE LABORATORY 50 20 21 P C 22. SEMINAR AND TECHNICAL COMMUNI. TW 50 20 33 P C PR GRAND TOTAL = 703/1500, RESULT: PASS CLASS [\$ 0.1] ORDN. 1 MARKS : (18)2, SAROJADEVI , 71045648E , T80054335 , PICT , T80054374 T80054374 THAKUR DHIRAJSING UDAYSING 01. DATABASE MANAGEMENT SYSTEMS 100 40 44 P 12. PRINCIPLES OF PROGRAMMING LANG. PP 100 40 41 P C 02. DATA COMMUNICATION PP 100 40 28 F 13. COMPUTER NETWORKS PP 100 40 40 P 40 P C 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 14. FINANCE & MANAGEMENT INFORMA.SYS.PP 100 40 42 P C 04. DIGITAL SIGNAL PROCESSING 100 40 AA F 15. SYSTEMS PROGRAMMING & OPERA.SYS. PP 100 40 11 F 05. THEORY OF COMPUTATION 100 40 40 P C PP 100 40 41 P PP 16. SOFTWARE ENGINEERING 50 20 20 P C 25 10 06. RDBMS & VISUAL PROGRAMMING LAB. TW 17. SOFTWARE LABORATORY TW 12 P C 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 27 P C 18. SOFTWARE LABORATORY 50 20 36 P C PR 12 P C 08. SIGNAL PROCESSING LABORATORY 25 10 10 P C 19. COMPUTER NETWORK TW 25 10 TW 50 20 35 P 09. SIGNAL PROCESSING LABORATORY 50 20 22 P C 20. COMPUTER NETWORK 10. HARDWARE LABORATORY 25 10 10 P C 21. SOFTWARE DEVELOPMENT TOOLS LAB. TW 50 20 24 P C TW 11. HARDWARE LABORATORY 50 20 29 P C 22. SEMINAR AND TECHNICAL COMMUNI. TW 50 20 20 P C PR GRAND TOTAL = 584/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS:

UNIVERSITY OF PUNE ,T.E.(2008 PAT.)(COMPUTER) EXAMINATION NOV. 2013 DATE: 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 58 (483) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER KALPANA , 71201097B , , PICT , T80054375 T80054375 TIPARE ROHAN RAVINDRA 01. DATABASE MANAGEMENT SYSTEMS 100 40 54 P 02. DATA COMMUNICATION 100 40 57 P 100 40 50 P 03. MICROPROCESSORS & MICROCONTROLLERPP 04. DIGITAL SIGNAL PROCESSING 100 40 68 P 05. THEORY OF COMPUTATION 100 40 58 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 20 42 P 50 20 37 P 07. RDBMS & VISUAL PROGRAMMING LAB. PR 08. SIGNAL PROCESSING LABORATORY 25 10 21 P TW 09. SIGNAL PROCESSING LABORATORY 50 20 10 F OR 10. HARDWARE LABORATORY 25 10 21 P TW 11. HARDWARE LABORATORY 50 20 39 P PR FIRST TERM TOTAL = 457/750. ORDN. 1 MARKS: T80054376 UMATE PIYUSH NAGANATH , 71201099J , , PICT , т80054376 NAINA 01. DATABASE MANAGEMENT SYSTEMS 100 40 60 P 100 40 61 P 02. DATA COMMUNICATION 100 40 53 P 03. MICROPROCESSORS & MICROCONTROLLERPP 75 P 100 40 04. DIGITAL SIGNAL PROCESSING 05. THEORY OF COMPUTATION 100 40 55 P 50 20 39 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 38 P 25 10 22 P 08. SIGNAL PROCESSING LABORATORY TW 09. SIGNAL PROCESSING LABORATORY 50 20 36 P OR 10. HARDWARE LABORATORY 25 10 19 P TW 11. HARDWARE LABORATORY 50 20 39 P PR FIRST TERM TOTAL = 497/750. ORDN. 1 MARKS: , 71201100F T80054377 UNAVANE RUTUJA RAJESH , PICT , т80054377 ANJALI 01. DATABASE MANAGEMENT SYSTEMS 100 40 72 P 02. DATA COMMUNICATION PP 100 40 61 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 58 P 04. DIGITAL SIGNAL PROCESSING 100 40 77 P 05. THEORY OF COMPUTATION 100 40 53 P 50 20 06. RDBMS & VISUAL PROGRAMMING LAB. TW 43 P 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 44 P 08. SIGNAL PROCESSING LABORATORY 25 10 23 P TW 09. SIGNAL PROCESSING LABORATORY 50 20 20 P 10. HARDWARE LABORATORY 25 10 23 P TW 11. HARDWARE LABORATORY 50 20 40 P PR FIRST TERM TOTAL = 514/750. ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER RACHANA , 71201104J , , PICT , T80054378 T80054378 VALLARI ANAND 01. DATABASE MANAGEMENT SYSTEMS 100 40 46 P 02. DATA COMMUNICATION 100 40 40 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 26 F 04. DIGITAL SIGNAL PROCESSING 100 40 40 P 05. THEORY OF COMPUTATION 100 40 28 F 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 20 34 P 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 37 P 08. SIGNAL PROCESSING LABORATORY 25 10 17 P TW 09. SIGNAL PROCESSING LABORATORY 50 20 30 P OR 10. HARDWARE LABORATORY 25 10 16 P TW 11. HARDWARE LABORATORY 50 20 10 F PR FIRST TERM TOTAL = 324/750. ORDN. 1 MARKS: T80054379 VARADE POOJA VIJAY , 71201105G , , PICT , т80054379 CHANDA 01. DATABASE MANAGEMENT SYSTEMS 100 40 78 P 100 40 50 P 02. DATA COMMUNICATION 100 40 53 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 77 P 04. DIGITAL SIGNAL PROCESSING 05. THEORY OF COMPUTATION 100 40 64 P 50 20 38 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 36 P 25 10 21 P 08. SIGNAL PROCESSING LABORATORY TW 09. SIGNAL PROCESSING LABORATORY 50 20 39 P OR 10. HARDWARE LABORATORY 25 10 21 P TW 11. HARDWARE LABORATORY 50 20 38 P PR FIRST TERM TOTAL = 515/750. ORDN. 1 MARKS: , 71201106E T80054380 VARMA RASHMI RAVI , PICT , т80054380 LEELA 01. DATABASE MANAGEMENT SYSTEMS 100 40 53 P 02. DATA COMMUNICATION PP 100 40 53 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 43 P 04. DIGITAL SIGNAL PROCESSING 100 40 40 P 05. THEORY OF COMPUTATION 100 40 40 P 50 20 28 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 25 P 08. SIGNAL PROCESSING LABORATORY 25 10 17 P TW 09. SIGNAL PROCESSING LABORATORY 50 20 20 P 10. HARDWARE LABORATORY 25 10 18 P TW 11. HARDWARE LABORATORY 50 20 36 P PR FIRST TERM TOTAL = 373/750. ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71201107C , , PICT , T80054381 T80054381 VASAVE SUSMITA MANUVEL INDU 01. DATABASE MANAGEMENT SYSTEMS 100 40 61 P 02. DATA COMMUNICATION 100 40 49 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 48 P 04. DIGITAL SIGNAL PROCESSING 100 40 69 P 05. THEORY OF COMPUTATION 100 40 54 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 20 38 P 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 30 P 08. SIGNAL PROCESSING LABORATORY 25 10 20 P TW 09. SIGNAL PROCESSING LABORATORY 50 20 13 F OR 10. HARDWARE LABORATORY 25 10 18 P TW 11. HARDWARE LABORATORY 50 20 38 P PR FIRST TERM TOTAL = 438/750. ORDN. 1 MARKS: , 71201108M , , PICT , T80054382 T80054382 VAZIRABADKAR KETAN MADHAVRAO ANITA 01. DATABASE MANAGEMENT SYSTEMS 100 40 63 P 100 40 55 P 02. DATA COMMUNICATION 100 40 49 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 63 P 04. DIGITAL SIGNAL PROCESSING 05. THEORY OF COMPUTATION 100 40 43 P 50 20 35 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 30 P 25 10 20 P 08. SIGNAL PROCESSING LABORATORY TW 09. SIGNAL PROCESSING LABORATORY 50 20 20 P OR 10. HARDWARE LABORATORY 25 10 19 P TW 11. HARDWARE LABORATORY 50 20 32 P PR FIRST TERM TOTAL = 429/750. ORDN. 1 MARKS: , 71201109к T80054383 VIKRAM SUNIL PATIL , PICT , T80054383 JAYMALA 01. DATABASE MANAGEMENT SYSTEMS 100 40 61 P 02. DATA COMMUNICATION PP 100 40 52 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 55 P 04. DIGITAL SIGNAL PROCESSING 100 40 55 P 05. THEORY OF COMPUTATION 100 40 43 P 50 20 39 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 41 P 08. SIGNAL PROCESSING LABORATORY 25 10 23 P TW 09. SIGNAL PROCESSING LABORATORY 50 20 36 P 10. HARDWARE LABORATORY 25 10 19 P TW 11. HARDWARE LABORATORY 50 20 35 P PR FIRST TERM TOTAL = 459/750. ORDN. 1 MARKS:

UNIVERSITY OF PUNE ,T.E.(2008 PAT.)(COMPUTER) EXAMINATION NOV. 2013 DATE: 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 61 (486) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71201112K , , PICT , T80054384 T80054384 VISPUTE PRIYANKA SUNIL ANITA 01. DATABASE MANAGEMENT SYSTEMS 100 40 59 P 02. DATA COMMUNICATION 100 40 45 P 100 40 33 F 03. MICROPROCESSORS & MICROCONTROLLERPP 04. DIGITAL SIGNAL PROCESSING 100 40 57 P 05. THEORY OF COMPUTATION 100 40 40 P 50 20 35 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 37 P 08. SIGNAL PROCESSING LABORATORY 25 10 21 P TW 09. SIGNAL PROCESSING LABORATORY 50 20 38 P OR 10. HARDWARE LABORATORY 25 10 20 P TW 11. HARDWARE LABORATORY 50 20 22 P PR FIRST TERM TOTAL = 407/750. ORDN. 1 MARKS: T80054385 WAGH SHARAD DADASAHEB , 71347778E , , PICT , T80054385 DAYABAI 01. DATABASE MANAGEMENT SYSTEMS 100 40 71 P 100 40 58 P 02. DATA COMMUNICATION 100 40 49 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 40 P 04. DIGITAL SIGNAL PROCESSING 05. THEORY OF COMPUTATION 100 40 31 F 50 20 34 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 23 P 25 10 15 P 08. SIGNAL PROCESSING LABORATORY TW 09. SIGNAL PROCESSING LABORATORY 50 20 11 F OR 10. HARDWARE LABORATORY 25 10 17 P TW 11. HARDWARE LABORATORY 50 20 05 F PR FIRST TERM TOTAL = 354/750. ORDN. 1 MARKS: , 71201116в T80054386 WALVE SUWAS NANDAKUMAR , PICT , т80054386 SAMPADA 01. DATABASE MANAGEMENT SYSTEMS 100 40 47 P 02. DATA COMMUNICATION PP 100 40 54 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 52 P 04. DIGITAL SIGNAL PROCESSING 100 40 69 P 05. THEORY OF COMPUTATION 100 40 46 P 50 20 34 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 34 P 08. SIGNAL PROCESSING LABORATORY 25 10 20 P TW 09. SIGNAL PROCESSING LABORATORY 50 20 32 P 10. HARDWARE LABORATORY 25 10 16 P TW 11. HARDWARE LABORATORY 50 20 32 P PR FIRST TERM TOTAL = 436/750. ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER RANJANA , 71101064B , T80054342 , PICT T80054390 YEREKAR KARISHMA TUKARAM , т80054390 12. PRINCIPLES OF PROGRAMMING LANG. PP 100 40 57 PC 01. DATABASE MANAGEMENT SYSTEMS 100 40 44 P C PP 13. COMPUTER NETWORKS 02. DATA COMMUNICATION 100 40 67 P C 100 40 100 40 58 P C 14. FINANCE & MANAGEMENT INFORMA.SYS.PP 100 40 60 P C 03. MICROPROCESSORS & MICROCONTROLLERPP 04. DIGITAL SIGNAL PROCESSING 100 40 53 P C 15. SYSTEMS PROGRAMMING & OPERA.SYS. PP 100 40 54 P C 05. THEORY OF COMPUTATION 100 40 52 P C 100 40 56 P C 16. SOFTWARE ENGINEERING 50 20 34 P C 25 10 20 P C 06. RDBMS & VISUAL PROGRAMMING LAB. TW 17. SOFTWARE LABORATORY TW 32 P C 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 50 20 43 P 18. SOFTWARE LABORATORY PR08. SIGNAL PROCESSING LABORATORY 25 10 18 P C 19. COMPUTER NETWORK 25 10 21 P C TW TW 09. SIGNAL PROCESSING LABORATORY 50 20 30 P C 50 20 25 P C 20. COMPUTER NETWORK OR 10. HARDWARE LABORATORY 25 10 21 P C 50 20 35 P C TW 21. SOFTWARE DEVELOPMENT TOOLS LAB. TW 11. HARDWARE LABORATORY 50 20 30 P C 50 20 36 P C PR 22. SEMINAR AND TECHNICAL COMMUNI. TW GRAND TOTAL = 906/1500, RESULT: FIRST CLASS ORDN. 1 MARKS: T80054391 YEWALE SHUBHANGI KAILAS MANISHA , т80054391 01. DATABASE MANAGEMENT SYSTEMS 100 40 52 P 100 40 44 P 02. DATA COMMUNICATION 100 40 40 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 49 P 04. DIGITAL SIGNAL PROCESSING 100 40 26 F 05. THEORY OF COMPUTATION 50 20 30 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 30 P 08. SIGNAL PROCESSING LABORATORY TW 25 10 18 P 09. SIGNAL PROCESSING LABORATORY 50 20 10 F OR 10. HARDWARE LABORATORY 25 10 18 P TW 50 20 25 P 11. HARDWARE LABORATORY PR FIRST TERM TOTAL = 342/750. ORDN. 1 MARKS: , 71201124C , , PICT T80054392 ZAGADE VISHAL RAJU , т80054392 SARALA 01. DATABASE MANAGEMENT SYSTEMS 100 40 55 P 02. DATA COMMUNICATION PP 100 40 45 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 49 P 04. DIGITAL SIGNAL PROCESSING 100 40 54 P 05. THEORY OF COMPUTATION 100 40 40 P PP 50 20 30 P 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 34 P 08. SIGNAL PROCESSING LABORATORY 25 10 17 P TW 09. SIGNAL PROCESSING LABORATORY 50 20 20 P 10. HARDWARE LABORATORY 25 10 16 P TW 11. HARDWARE LABORATORY 50 20 36 P PR FIRST TERM TOTAL = 396/750. ORDN. 1 MARKS:

DATE : 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 01 (489) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71100711L , T80058501 , PICT T80058501 AAYUSH FUSKELAY SHASHI , T80058501 01. OPERATING SYSTEM 100 40 72 P C 13. SYSTEM SOFTWARE PROGRAMMING 100 40 52 P C 02. THEORY OF COMPUTATION 100 40 54 P C 14. MANAGEMENT INFORMATION SYSTEMS PP 100 40 54 P C 100 40 57 P C 100 40 57 P C 03. COMPUTER NETWORK TECHNOLOGY PP 15. PROGRAMMING PARADIGMS 04. DATBASE MANAGEMENT SYSTEMS 100 40 71 P C 100 40 63 P C PP 16. DESIGN & ANALYSIS OF ALGORITHMS PP 05. SOFTWARE ENGINEERING 100 40 53 P C 17. HUMAN COMPU.INTERACTION & USABI. PP 100 40 57 P C 25 10 21 P C 50 20 21 P C 06. OPERATING SYSTEM DESIGN LAB. 18. SOFTWARE DESIGN LABORATORY TW TW 07. OPERATING SYSTEM DESIGN LAB. 50 20 35 P C 50 20 36 P PR19. SOFTWARE DESIGN LABORATORY 25 10 18 P C 50 20 30 P C 08. INFORMATION SYSTEMS DESIGN LAB. TW 20. SOFTWARE DEVELOPMENT TOOLS LAB. TW 50 20 41 P C 50 20 38 P C 09. INFORMATION SYSTEMS DESIGN LAB. OR 21. SOFTWARE DEVELOPMENT TOOLS LAB. OR 25 10 18 P C 22. SEMINAR AND TECHNICAL COMMUN. TW 50 20 36 P C 10. NETWORK LABORATORY TW 11. NETWORK LABORATORY 50 20 39 P C OR 25 10 18 P C 12. SOFT SKILLS LABORATORY TW GRAND TOTAL = 941/1500, RESULT: FIRST CLASS ORDN. 1 MARKS: T80058502 ABHALE SAURABH BHANUDAS SUNITA , 71200746G , T80058502 , PICT 01. OPERATING SYSTEM 100 40 51 P 100 40 64 P 02. THEORY OF COMPUTATION PP 100 40 71 P 03. COMPUTER NETWORK TECHNOLOGY 100 40 60 P 04. DATBASE MANAGEMENT SYSTEMS PP 100 40 05. SOFTWARE ENGINEERING 68 P PP 25 10 21 P 06. OPERATING SYSTEM DESIGN LAB. TW 50 20 42 P 07. OPERATING SYSTEM DESIGN LAB. PR 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 22 P 50 20 09. INFORMATION SYSTEMS DESIGN LAB. OR 44 P 25 10 10. NETWORK LABORATORY TW 22 P 11. NETWORK LABORATORY 50 20 36 P 25 10 21 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 522/750. ORDN. 1 MARKS: T80058503 ADARSH KUMAR KIRAN , 71200750E , , PICT , T80058503 01. OPERATING SYSTEM 100 40 12 F 02. THEORY OF COMPUTATION 100 40 PP AA F 03. COMPUTER NETWORK TECHNOLOGY 100 40 64 P PP 04. DATBASE MANAGEMENT SYSTEMS 100 40 47 P 05. SOFTWARE ENGINEERING 100 40 PP AA F 25 10 06. OPERATING SYSTEM DESIGN LAB. 10 P TW 50 20 07. OPERATING SYSTEM DESIGN LAB. AA F 25 10 10 P 08. INFORMATION SYSTEMS DESIGN LAB. TW 50 20 09. INFORMATION SYSTEMS DESIGN LAB. OR AA F 10. NETWORK LABORATORY 25 10 10 P 11. NETWORK LABORATORY OR 50 20 AA F 12. SOFT SKILLS LABORATORY TW 25 10 10 P FIRST TERM TOTAL = 163/750. ORDN. 1 MARKS:

DATE: 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 02 (490) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , T80058504 , PICT DIMPLE , 71100719F T80058504 ADITYA SINGH SOLANKI , т80058504 01. OPERATING SYSTEM 100 40 40 P C 13. SYSTEM SOFTWARE PROGRAMMING 100 40 40 P C PP 02. THEORY OF COMPUTATION 100 40 42 P 14. MANAGEMENT INFORMATION SYSTEMS PP 100 40 40 P C 46 P C 100 40 100 40 45 P C 03. COMPUTER NETWORK TECHNOLOGY PP 15. PROGRAMMING PARADIGMS 04. DATBASE MANAGEMENT SYSTEMS 100 40 40 P C 16. DESIGN & ANALYSIS OF ALGORITHMS PP 100 40 40 P C PP 05. SOFTWARE ENGINEERING 100 40 43 P C 17. HUMAN COMPU.INTERACTION & USABI. PP 100 40 47 P C 25 10 15 P C 50 20 20 P C 06. OPERATING SYSTEM DESIGN LAB. 18. SOFTWARE DESIGN LABORATORY TW TW 32 P 07. OPERATING SYSTEM DESIGN LAB. 50 20 28 P C 50 20 PR19. SOFTWARE DESIGN LABORATORY 25 10 13 P C 50 20 28 P C 08. INFORMATION SYSTEMS DESIGN LAB. TW 20. SOFTWARE DEVELOPMENT TOOLS LAB. TW 50 20 26 P 50 20 20 P C 09. INFORMATION SYSTEMS DESIGN LAB. OR 21. SOFTWARE DEVELOPMENT TOOLS LAB. OR 25 10 15 P C 22. SEMINAR AND TECHNICAL COMMUN. TW 50 20 26 P C 10. NETWORK LABORATORY TW 11. NETWORK LABORATORY 50 20 20 P OR 25 10 14 P C 12. SOFT SKILLS LABORATORY TW GRAND TOTAL = 680/1500, RESULT: PASS CLASS ORDN. 1 MARKS: T80058505 AHIRE AKSHATA RAJENDRA MEENAKSHI , 71200752M , T80058505 , PICT 01. OPERATING SYSTEM 100 40 56 P 100 40 59 P 02. THEORY OF COMPUTATION PP 100 40 50 P 03. COMPUTER NETWORK TECHNOLOGY 100 40 49 P 04. DATBASE MANAGEMENT SYSTEMS PP 100 40 05. SOFTWARE ENGINEERING 45 P PP 25 10 22 P 06. OPERATING SYSTEM DESIGN LAB. TW 50 20 07. OPERATING SYSTEM DESIGN LAB. PR 38 P 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 20 P 50 20 20 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 25 10 10. NETWORK LABORATORY TW 22 P 11. NETWORK LABORATORY 50 20 25 P 25 10 22 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 428/750. ORDN. 1 MARKS: T80058506 AKSHAY A ARLIKATTI SHANTHI , 71200755F , , PICT , T80058506 01. OPERATING SYSTEM 100 40 52 P 02. THEORY OF COMPUTATION 100 40 45 P PP 03. COMPUTER NETWORK TECHNOLOGY 100 40 58 P PP 04. DATBASE MANAGEMENT SYSTEMS 100 40 54 P 05. SOFTWARE ENGINEERING 100 40 44 P PP 25 10 06. OPERATING SYSTEM DESIGN LAB. 20 P TW 50 20 40 P 07. OPERATING SYSTEM DESIGN LAB. 25 10 20 P 08. INFORMATION SYSTEMS DESIGN LAB. TW 50 20 45 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 10. NETWORK LABORATORY 25 10 20 P 11. NETWORK LABORATORY OR 50 20 35 P 12. SOFT SKILLS LABORATORY TW 25 10 16 P FIRST TERM TOTAL = 449/750. ORDN. 1 MARKS:

DATE: 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 03 (491) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71200758L T80058507 AMAN KUMAR NIGAM ROOPALI NIGAM , , PICT , T80058507 01. OPERATING SYSTEM 100 40 51 P 02. THEORY OF COMPUTATION 100 40 48 P 03. COMPUTER NETWORK TECHNOLOGY 100 40 56 P PP 04. DATBASE MANAGEMENT SYSTEMS 100 40 52 P 05. SOFTWARE ENGINEERING 100 40 55 P 25 10 22 P 06. OPERATING SYSTEM DESIGN LAB. TW 07. OPERATING SYSTEM DESIGN LAB. 50 20 45 P 25 10 20 P 08. INFORMATION SYSTEMS DESIGN LAB. TW 50 20 39 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 10. NETWORK LABORATORY 25 10 20 P TW 11. NETWORK LABORATORY 50 20 32 P OR 25 10 17 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 457/750. ORDN. 1 MARKS: T80058508 ANKIT V BANSAL URMILA , 71045371L , PICT , T80058508 01. OPERATING SYSTEM 100 40 16 F 100 40 22 F 02. THEORY OF COMPUTATION PP 100 40 46 P 03. COMPUTER NETWORK TECHNOLOGY 100 40 43 P 04. DATBASE MANAGEMENT SYSTEMS PP 100 40 50 P 05. SOFTWARE ENGINEERING PP 25 10 18 P 06. OPERATING SYSTEM DESIGN LAB. TW 50 20 07. OPERATING SYSTEM DESIGN LAB. PR AA F 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 13 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 03 F 25 10 10. NETWORK LABORATORY TW 13 P 50 20 11. NETWORK LABORATORY 25 P 25 10 12 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 261/750. ORDN. 1 MARKS: T80058509 ANUSHKA GHOGALE NIVEDITA , 70925337M , T80058512 , PICT , T80058509 01. OPERATING SYSTEM 100 40 55 P 02. THEORY OF COMPUTATION 100 40 58 P PP 03. COMPUTER NETWORK TECHNOLOGY PP 100 40 69 P 04. DATBASE MANAGEMENT SYSTEMS 100 40 59 P 05. SOFTWARE ENGINEERING 100 40 59 P PP 06. OPERATING SYSTEM DESIGN LAB. 25 10 20 P C TW 50 20 20 P 07. OPERATING SYSTEM DESIGN LAB. 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 11 P C 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 35 P 10. NETWORK LABORATORY 25 10 17 P C 11. NETWORK LABORATORY OR 50 20 28 P C 12. SOFT SKILLS LABORATORY TW 25 10 12 P C FIRST TERM TOTAL = 443/750. ORDN. 1 MARKS:

UNIVERSITY OF PUNE ,T.E. (2008 PAT) (INFORMATION TECHNOLOGY) EXAMINATION NOV. 2013 DATE: 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 04 (492) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71200764E , , PICT , T80058510 T80058510 ARIJIT PANDE ANNAPURNA 01. OPERATING SYSTEM 100 40 56 P 02. THEORY OF COMPUTATION 100 40 45 P 100 40 63 P 03. COMPUTER NETWORK TECHNOLOGY PP 04. DATBASE MANAGEMENT SYSTEMS 100 40 63 P PP 05. SOFTWARE ENGINEERING 100 40 61 P 25 10 23 P 06. OPERATING SYSTEM DESIGN LAB. TW 07. OPERATING SYSTEM DESIGN LAB. 50 20 40 P PR25 10 08. INFORMATION SYSTEMS DESIGN LAB. TW 23 P 50 20 09. INFORMATION SYSTEMS DESIGN LAB. OR 41 P 25 10 23 P 10. NETWORK LABORATORY TW 50 20 40 P 11. NETWORK LABORATORY OR 25 10 22 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 500/750. ORDN. 1 MARKS: T80058511 ASHISH GUPTA MEENA , 71200765C , T80058511 , PICT 100 40 54 P 01. OPERATING SYSTEM 100 40 02. THEORY OF COMPUTATION PP 71 P 100 40 70 P 03. COMPUTER NETWORK TECHNOLOGY 100 40 59 P 04. DATBASE MANAGEMENT SYSTEMS PP 100 40 05. SOFTWARE ENGINEERING 61 P PP 25 10 20 P 06. OPERATING SYSTEM DESIGN LAB. TW 50 20 40 P 07. OPERATING SYSTEM DESIGN LAB. PR 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 20 P 50 20 42 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 25 10 10. NETWORK LABORATORY TW 21 P 11. NETWORK LABORATORY 50 20 44 P OR 25 10 12. SOFT SKILLS LABORATORY TW 16 P FIRST TERM TOTAL = 518/750. ORDN. 1 MARKS: T80058512 AUTADE SAGAR ASHOK INDIRA , 71045376M , T80058515 , PICT , т80058512 01. OPERATING SYSTEM 100 40 45 P C 100 40 40 P C 13. SYSTEM SOFTWARE PROGRAMMING 02. THEORY OF COMPUTATION 100 40 40 P C 14. MANAGEMENT INFORMATION SYSTEMS PP 100 40 40 P C PP 03. COMPUTER NETWORK TECHNOLOGY 100 40 46 P C 100 40 40 P C PP 15. PROGRAMMING PARADIGMS 100 40 60 P 04. DATBASE MANAGEMENT SYSTEMS 40 P C 100 40 16. DESIGN & ANALYSIS OF ALGORITHMS PP 05. SOFTWARE ENGINEERING 100 40 42 P C 17. HUMAN COMPU.INTERACTION & USABI. PP 100 40 42 P C PP 06. OPERATING SYSTEM DESIGN LAB. 25 10 21 P C 18. SOFTWARE DESIGN LABORATORY TW 50 20 29 P C TW 50 20 39 P C 50 20 32 P C 07. OPERATING SYSTEM DESIGN LAB. 19. SOFTWARE DESIGN LABORATORY 25 10 22 P C 20. SOFTWARE DEVELOPMENT TOOLS LAB. TW 50 20 33 P C 08. INFORMATION SYSTEMS DESIGN LAB. TW 30 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 41 P C 50 20 21. SOFTWARE DEVELOPMENT TOOLS LAB. OR 10. NETWORK LABORATORY 25 10 21 P C 22. SEMINAR AND TECHNICAL COMMUN. TW 50 20 31 P C 11. NETWORK LABORATORY OR 50 20 28 P C 12. SOFT SKILLS LABORATORY 25 10 22 P C TW GRAND TOTAL = 784/1500, RESULT: SECOND CLASS

ORDN. 1 MARKS :

DATE: 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 05 (493) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71200778E T80058513 BALDAWA KOMAL DWARKADAS USHA , , PICT , T80058513 01. OPERATING SYSTEM 100 40 40 P PP 02. THEORY OF COMPUTATION 100 40 60 P 03. COMPUTER NETWORK TECHNOLOGY 100 40 43 P PP 04. DATBASE MANAGEMENT SYSTEMS 100 40 60 P 05. SOFTWARE ENGINEERING 100 40 42 P 25 10 17 P 06. OPERATING SYSTEM DESIGN LAB. TW 25 P 07. OPERATING SYSTEM DESIGN LAB. 50 20 25 10 08. INFORMATION SYSTEMS DESIGN LAB. TW 14 P 50 20 09. INFORMATION SYSTEMS DESIGN LAB. OR 08 F 10. NETWORK LABORATORY 25 10 17 P TW 11. NETWORK LABORATORY 50 20 38 P OR 25 10 17 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 381/750. ORDN. 1 MARKS: T80058514 BANSODE ASHLESHA ANKUSH SUREKHA , 71200782C , PICT , T80058514 100 40 51 P 01. OPERATING SYSTEM 100 40 56 P 02. THEORY OF COMPUTATION PP 100 40 59 P 03. COMPUTER NETWORK TECHNOLOGY 100 40 59 P 04. DATBASE MANAGEMENT SYSTEMS PP 100 40 05. SOFTWARE ENGINEERING PP 61 P 25 10 21 P 06. OPERATING SYSTEM DESIGN LAB. TW 50 20 35 P 07. OPERATING SYSTEM DESIGN LAB. PR 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 21 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 35 P 25 10 10. NETWORK LABORATORY TW 22 P 11. NETWORK LABORATORY 50 20 37 P 25 10 23 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 480/750. ORDN. 1 MARKS: T80058515 BARASKAR ADITI NARENDRA LEENA , 71200783м , PICT , T80058515 01. OPERATING SYSTEM 100 40 43 P 02. THEORY OF COMPUTATION 100 40 50 P PP 03. COMPUTER NETWORK TECHNOLOGY 100 40 69 P PP 69 P 04. DATBASE MANAGEMENT SYSTEMS 100 40 05. SOFTWARE ENGINEERING 100 40 65 P PP 06. OPERATING SYSTEM DESIGN LAB. 25 10 16 P TW 50 20 30 P 07. OPERATING SYSTEM DESIGN LAB. 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 19 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 44 P 10. NETWORK LABORATORY 25 10 21 P 11. NETWORK LABORATORY OR 50 20 43 P 12. SOFT SKILLS LABORATORY TW 25 10 16 P FIRST TERM TOTAL = 485/750. ORDN. 1 MARKS:

DATE: 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 06 (494) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71200784K T80058516 BARDE REEMA ANIL JYOTI , , PICT , T80058516 01. OPERATING SYSTEM 100 40 22 F 02. THEORY OF COMPUTATION 100 40 46 P 03. COMPUTER NETWORK TECHNOLOGY 100 40 49 P PP 04. DATBASE MANAGEMENT SYSTEMS 100 40 52 P 05. SOFTWARE ENGINEERING 100 40 46 P 25 10 23 P 06. OPERATING SYSTEM DESIGN LAB. TW 07. OPERATING SYSTEM DESIGN LAB. 50 20 30 P 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 22 P 50 20 43 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 10. NETWORK LABORATORY 25 10 22 P TW 11. NETWORK LABORATORY 50 20 34 P OR 25 10 23 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 412/750. ORDN. 1 MARKS: T80058517 BENDHALE AJAY MARUTI KALPANA , 71350916D , PICT , T80058517 100 40 49 P 01. OPERATING SYSTEM 100 40 59 P 02. THEORY OF COMPUTATION PP 03. COMPUTER NETWORK TECHNOLOGY 100 40 77 P 100 40 58 P 04. DATBASE MANAGEMENT SYSTEMS PP 100 40 56 P 05. SOFTWARE ENGINEERING PP 25 10 24 P 06. OPERATING SYSTEM DESIGN LAB. TW 50 20 07. OPERATING SYSTEM DESIGN LAB. PR 38 P 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 24 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 33 P 25 10 10. NETWORK LABORATORY TW 24 P 32 P 11. NETWORK LABORATORY 50 20 OR 25 10 24 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 498/750. ORDN. 1 MARKS: T80058518 BHALERAO DHAIRYASHEEL RATNAKAR RENUKA , 71200790D , PICT , T80058518 01. OPERATING SYSTEM 100 40 14 F 02. THEORY OF COMPUTATION 100 40 40 P 03. COMPUTER NETWORK TECHNOLOGY 100 40 49 P PP 51 P 04. DATBASE MANAGEMENT SYSTEMS 100 40 05. SOFTWARE ENGINEERING 100 40 56 P PP 06. OPERATING SYSTEM DESIGN LAB. 25 10 20 P TW 50 20 07 F 07. OPERATING SYSTEM DESIGN LAB. 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 20 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 35 P 10. NETWORK LABORATORY 25 10 19 P 11. NETWORK LABORATORY OR 50 20 32 P 12. SOFT SKILLS LABORATORY TW 25 10 17 P FIRST TERM TOTAL = 360/750. ORDN. 1 MARKS:

UNIVERSITY OF PUNE ,T.E. (2008 PAT) (INFORMATION TECHNOLOGY) EXAMINATION NOV. 2013 DATE : 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 07 (495) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , T80058642 , PICT , 70925361D T80058519 BHALERAO MANGESH GANESH NANDA , т80058519 01. OPERATING SYSTEM 100 40 40 P C 13. SYSTEM SOFTWARE PROGRAMMING 100 40 53 P C PP PP 02. THEORY OF COMPUTATION 100 40 40 P C 14. MANAGEMENT INFORMATION SYSTEMS PP 100 40 46 P C 100 40 40 P C 100 40 40 P C 03. COMPUTER NETWORK TECHNOLOGY PP 15. PROGRAMMING PARADIGMS 04. DATBASE MANAGEMENT SYSTEMS 100 40 40 P C 100 40 48 P PP 16. DESIGN & ANALYSIS OF ALGORITHMS PP 05. SOFTWARE ENGINEERING 100 40 40 P C 17. HUMAN COMPU.INTERACTION & USABI. PP 100 40 44 P C 06. OPERATING SYSTEM DESIGN LAB. 25 10 15 P C 50 20 20 P C 18. SOFTWARE DESIGN LABORATORY TW TW 07. OPERATING SYSTEM DESIGN LAB. 50 20 24 P C 50 20 23 P C PR19. SOFTWARE DESIGN LABORATORY 25 10 15 P C 50 20 30 P C 08. INFORMATION SYSTEMS DESIGN LAB. TW 20. SOFTWARE DEVELOPMENT TOOLS LAB. TW 50 20 37 P 50 20 41 P C 09. INFORMATION SYSTEMS DESIGN LAB. OR 21. SOFTWARE DEVELOPMENT TOOLS LAB. OR 25 10 22. SEMINAR AND TECHNICAL COMMUN. TW 50 20 37 P C 10. NETWORK LABORATORY TW 16 P C 11. NETWORK LABORATORY 50 20 32 P C OR 25 10 21 P C 12. SOFT SKILLS LABORATORY TW GRAND TOTAL = 742+08/1500, RESULT: SECOND CLASS [0.2] ORDN. 1 MARKS: T80058520 BHANDARKUMTHE MADHUR MADHAVRAO UMA , 71200792L , T80058520 , PICT 100 40 55 P 01. OPERATING SYSTEM 100 40 02. THEORY OF COMPUTATION 60 P 100 40 03. COMPUTER NETWORK TECHNOLOGY 66 P 100 40 04. DATBASE MANAGEMENT SYSTEMS PP 64 P 100 40 05. SOFTWARE ENGINEERING 65 P PP 25 10 24 P 06. OPERATING SYSTEM DESIGN LAB. TW 50 20 07. OPERATING SYSTEM DESIGN LAB. PR 40 P 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 23 P 50 20 09. INFORMATION SYSTEMS DESIGN LAB. OR 38 P 25 10 10. NETWORK LABORATORY TW 24 P 11. NETWORK LABORATORY 50 20 42 P 25 10 24 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 525/750. ORDN. 1 MARKS: T80058521 BHANDE KRISHNA SHRIDHAR SHILABAI , T80058521 01. OPERATING SYSTEM 100 40 49 P 02. THEORY OF COMPUTATION 100 40 65 P PP 03. COMPUTER NETWORK TECHNOLOGY 100 40 64 P PP 04. DATBASE MANAGEMENT SYSTEMS 100 40 58 P 05. SOFTWARE ENGINEERING 100 40 64 P PP 06. OPERATING SYSTEM DESIGN LAB. 25 10 20 P TW 50 20 21 P 07. OPERATING SYSTEM DESIGN LAB. 25 10 19 P 08. INFORMATION SYSTEMS DESIGN LAB. TW 50 20 38 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 10. NETWORK LABORATORY 25 10 20 P 11. NETWORK LABORATORY OR 50 20 32 P 12. SOFT SKILLS LABORATORY TW 25 10 17 P FIRST TERM TOTAL = 467/750. ORDN. 1 MARKS:

DATE: 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 08 (496) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71200796C T80058522 BHOIR KALPESH SHIVRAM SOMABAI , , PICT , T80058522 01. OPERATING SYSTEM 100 40 14 F 02. THEORY OF COMPUTATION 100 40 40 P 03. COMPUTER NETWORK TECHNOLOGY 100 40 40 P PP 04. DATBASE MANAGEMENT SYSTEMS 100 40 45 P 05. SOFTWARE ENGINEERING 100 40 44 P 25 10 23 P 06. OPERATING SYSTEM DESIGN LAB. TW 07. OPERATING SYSTEM DESIGN LAB. 50 20 18 F 25 10 23 P 08. INFORMATION SYSTEMS DESIGN LAB. TW 50 20 30 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 10. NETWORK LABORATORY 25 10 23 P TW 11. NETWORK LABORATORY 50 20 25 P OR 25 10 23 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 348/750. ORDN. 1 MARKS: T80058523 BHOSURE YOGESH KASHINATH SHEVANTA , 71350918L , PICT , T80058523 01. OPERATING SYSTEM 100 40 61 P 02. THEORY OF COMPUTATION 100 40 51 P PP 100 40 62 P 03. COMPUTER NETWORK TECHNOLOGY 100 40 63 P 04. DATBASE MANAGEMENT SYSTEMS PP 100 40 56 P 05. SOFTWARE ENGINEERING PP 25 10 22 P 06. OPERATING SYSTEM DESIGN LAB. TW 50 20 35 P 07. OPERATING SYSTEM DESIGN LAB. PR 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 21 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 34 P 25 10 10. NETWORK LABORATORY TW 22 P 11. NETWORK LABORATORY 50 20 37 P 25 10 19 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 483/750. ORDN. 1 MARKS: T80058524 BIRHADE NIKETAN SHASHIKANT VANDANA , 71200801C , , PICT , т80058524 01. OPERATING SYSTEM 100 40 44 P 02. THEORY OF COMPUTATION 100 40 40 P 03. COMPUTER NETWORK TECHNOLOGY 100 40 47 P PP 04. DATBASE MANAGEMENT SYSTEMS 100 40 47 P 05. SOFTWARE ENGINEERING 100 40 40 P PP 06. OPERATING SYSTEM DESIGN LAB. 25 10 18 P TW 50 20 35 P 07. OPERATING SYSTEM DESIGN LAB. 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 20 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 41 P 10. NETWORK LABORATORY 25 10 21 P 11. NETWORK LABORATORY OR 50 20 40 P 12. SOFT SKILLS LABORATORY TW 25 10 19 P FIRST TERM TOTAL = 412/750. ORDN. 1 MARKS:

DATE: 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 09 (497) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71350919J T80058525 BORATE SHRADDHA RAJARAM KAMAL , , PICT , T80058525 01. OPERATING SYSTEM 100 40 56 P PP 02. THEORY OF COMPUTATION 100 40 57 P 03. COMPUTER NETWORK TECHNOLOGY 100 40 68 P PP 04. DATBASE MANAGEMENT SYSTEMS 100 40 55 P 05. SOFTWARE ENGINEERING 100 40 63 P 25 10 22 P 06. OPERATING SYSTEM DESIGN LAB. TW 07. OPERATING SYSTEM DESIGN LAB. 50 20 35 P 25 10 21 P 08. INFORMATION SYSTEMS DESIGN LAB. TW 50 20 15 F 09. INFORMATION SYSTEMS DESIGN LAB. OR 10. NETWORK LABORATORY 25 10 23 P TW 11. NETWORK LABORATORY 50 20 35 P OR 25 10 20 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 470/750. ORDN. 1 MARKS: T80058526 CHANDRATRE ABHISHEK SURENDRA ARCHANA , 71200807в , PICT , T80058526 100 40 53 P 01. OPERATING SYSTEM 02. THEORY OF COMPUTATION 100 40 51 P PΡ 100 40 62 P 03. COMPUTER NETWORK TECHNOLOGY 100 40 60 P 04. DATBASE MANAGEMENT SYSTEMS PP 100 40 52 P 05. SOFTWARE ENGINEERING PP 25 10 24 P 06. OPERATING SYSTEM DESIGN LAB. TW 50 20 07. OPERATING SYSTEM DESIGN LAB. PR 43 P 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 24 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 43 P 25 10 10. NETWORK LABORATORY TW 24 P 11. NETWORK LABORATORY 50 20 45 P 25 10 24 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 505/750. ORDN. 1 MARKS: T80058527 CHANDRIKA PARIMOO ANITA , 71200808L , PICT , T80058527 01. OPERATING SYSTEM 100 40 66 P 02. THEORY OF COMPUTATION 100 40 73 P PP 03. COMPUTER NETWORK TECHNOLOGY 100 40 72 P PP 69 P 04. DATBASE MANAGEMENT SYSTEMS 100 40 05. SOFTWARE ENGINEERING 100 40 65 P PP 06. OPERATING SYSTEM DESIGN LAB. 25 10 22 P TW 50 20 44 P 07. OPERATING SYSTEM DESIGN LAB. 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 22 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 47 P 10. NETWORK LABORATORY 25 10 22 P 11. NETWORK LABORATORY OR 50 20 40 P 12. SOFT SKILLS LABORATORY TW 25 10 23 P FIRST TERM TOTAL = 565/750. ORDN. 1 MARKS:

DATE: 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 10 (498) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71200809J T80058528 CHARKHA BHUSHAN HEMANT SEEMA , , PICT , T80058528 01. OPERATING SYSTEM 100 40 30 F 02. THEORY OF COMPUTATION 100 40 40 P 03. COMPUTER NETWORK TECHNOLOGY 100 40 66 P PP 04. DATBASE MANAGEMENT SYSTEMS 100 40 59 P 05. SOFTWARE ENGINEERING 100 40 46 P 25 10 16 P 06. OPERATING SYSTEM DESIGN LAB. TW 07. OPERATING SYSTEM DESIGN LAB. 50 20 30 P 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 16 P 50 20 40 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 10. NETWORK LABORATORY 25 10 10 P TW 11. NETWORK LABORATORY 50 20 29 P OR 25 10 13 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 395/750. ORDN. 1 MARKS: T80058529 CHASKAR VIPUL VINAYAK PO0JA , 71200811L , PICT , T80058529 100 40 58 P 01. OPERATING SYSTEM 100 40 74 P 02. THEORY OF COMPUTATION PP 03. COMPUTER NETWORK TECHNOLOGY 100 40 74 P 100 40 57 P 04. DATBASE MANAGEMENT SYSTEMS PP 100 40 56 P 05. SOFTWARE ENGINEERING PP 25 10 24 P 06. OPERATING SYSTEM DESIGN LAB. TW 50 20 40 P 07. OPERATING SYSTEM DESIGN LAB. PR 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 24 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 42 P 25 10 10. NETWORK LABORATORY TW 24 P 11. NETWORK LABORATORY 50 20 42 P 25 10 23 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 538/750. ORDN. 1 MARKS: T80058530 CHATTERJEE SUDIPTO ANJAN MADHUCHHANDA , 71200812J , PICT , T80058530 01. OPERATING SYSTEM 100 40 46 P 02. THEORY OF COMPUTATION 100 40 40 P PP 03. COMPUTER NETWORK TECHNOLOGY 100 40 61 P PP 04. DATBASE MANAGEMENT SYSTEMS 100 40 40 P 05. SOFTWARE ENGINEERING 100 40 51 P PP 06. OPERATING SYSTEM DESIGN LAB. 25 10 21 P TW 50 20 39 P 07. OPERATING SYSTEM DESIGN LAB. 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 20 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 41 P 10. NETWORK LABORATORY 25 10 23 P 11. NETWORK LABORATORY OR 50 20 43 P 12. SOFT SKILLS LABORATORY TW 25 10 17 P FIRST TERM TOTAL = 442/750. ORDN. 1 MARKS:

DATE : 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 11 (499) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , T80058527 , PICT T80058531 CHAUDHARI PRANITA RAVISHANKAR MEENA , 71100764M , т80058531 01. OPERATING SYSTEM 100 40 40 P C 13. SYSTEM SOFTWARE PROGRAMMING 100 40 40 P C PP 02. THEORY OF COMPUTATION 100 40 40 P C 14. MANAGEMENT INFORMATION SYSTEMS PP 100 40 AA F 100 40 40 P C 100 40 40 P C 03. COMPUTER NETWORK TECHNOLOGY PP 15. PROGRAMMING PARADIGMS 04. DATBASE MANAGEMENT SYSTEMS 100 40 16. DESIGN & ANALYSIS OF ALGORITHMS PP 100 40 61 P AA F 05. SOFTWARE ENGINEERING 100 40 43 P 17. HUMAN COMPU.INTERACTION & USABI. PP 100 40 44 P C 25 10 21 P C 50 20 22 P C 06. OPERATING SYSTEM DESIGN LAB. 18. SOFTWARE DESIGN LABORATORY TW TW 07. OPERATING SYSTEM DESIGN LAB. 50 20 30 P C 50 20 35 P PR 19. SOFTWARE DESIGN LABORATORY 25 10 18 P C 50 20 38 P C 08. INFORMATION SYSTEMS DESIGN LAB. TW 20. SOFTWARE DEVELOPMENT TOOLS LAB. TW 50 20 50 20 33 P C 30 P C 09. INFORMATION SYSTEMS DESIGN LAB. OR 21. SOFTWARE DEVELOPMENT TOOLS LAB. OR 10. NETWORK LABORATORY 25 10 18 P C 22. SEMINAR AND TECHNICAL COMMUN. TW 50 20 23 P C TW 11. NETWORK LABORATORY 50 20 28 P C OR 25 10 19 P C 12. SOFT SKILLS LABORATORY TW GRAND TOTAL = 663/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS: T80058532 CHAVAN KAVITA DHANAJI **SHOBHA** , 71350920в , T80058532 , PICT 01. OPERATING SYSTEM 100 40 41 P 100 40 40 P 02. THEORY OF COMPUTATION PP 100 40 48 P 03. COMPUTER NETWORK TECHNOLOGY 100 40 04. DATBASE MANAGEMENT SYSTEMS PP 54 P 100 40 49 P 05. SOFTWARE ENGINEERING PP 25 10 21 P 06. OPERATING SYSTEM DESIGN LAB. TW 50 20 20 P 07. OPERATING SYSTEM DESIGN LAB. PR 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 22 P 50 20 22 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 25 10 10. NETWORK LABORATORY TW 22 P 11. NETWORK LABORATORY 50 20 38 P 25 10 22 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 399/750. ORDN. 1 MARKS: SAROJ T80058533 CHAVAN SONAL NILKANTH , 71200817к , T80058533 , PICT 01. OPERATING SYSTEM 100 40 51 P 02. THEORY OF COMPUTATION 100 40 53 P PP 03. COMPUTER NETWORK TECHNOLOGY 100 40 66 P PP 04. DATBASE MANAGEMENT SYSTEMS 100 40 59 P 05. SOFTWARE ENGINEERING 100 40 57 P PP 25 10 06. OPERATING SYSTEM DESIGN LAB. 18 P TW 50 20 30 P 07. OPERATING SYSTEM DESIGN LAB. 25 10 22 P 08. INFORMATION SYSTEMS DESIGN LAB. TW 50 20 38 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 10. NETWORK LABORATORY 25 10 22 P 11. NETWORK LABORATORY OR 50 20 40 P 12. SOFT SKILLS LABORATORY TW 25 10 20 P FIRST TERM TOTAL = 476/750. ORDN. 1 MARKS:

DATE: 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 12 (500) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71200820к T80058534 CHAWARE PURUSHOTTAM GAJANAN MANJUSHA , , PICT , T80058534 01. OPERATING SYSTEM 100 40 45 P 02. THEORY OF COMPUTATION 100 40 55 P 03. COMPUTER NETWORK TECHNOLOGY 100 40 71 P PP 04. DATBASE MANAGEMENT SYSTEMS 100 40 64 P 05. SOFTWARE ENGINEERING 100 40 72 P 25 10 23 P 06. OPERATING SYSTEM DESIGN LAB. TW 07. OPERATING SYSTEM DESIGN LAB. 50 20 40 P 25 10 22 P 08. INFORMATION SYSTEMS DESIGN LAB. TW 50 20 36 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 10. NETWORK LABORATORY 25 10 22 P TW 11. NETWORK LABORATORY 50 20 42 P OR 25 10 24 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 516/750. ORDN. 1 MARKS: T80058535 CHOUDHARI PRIYANKA SANJAY **POONAM** , 71200822F , PICT , T80058535 100 40 31 F 01. OPERATING SYSTEM 100 40 26 F 02. THEORY OF COMPUTATION PP 100 40 48 P 03. COMPUTER NETWORK TECHNOLOGY 100 40 40 P 04. DATBASE MANAGEMENT SYSTEMS PP 100 40 40 P 05. SOFTWARE ENGINEERING PP 25 10 19 P 06. OPERATING SYSTEM DESIGN LAB. TW 50 20 22 P 07. OPERATING SYSTEM DESIGN LAB. PR 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 18 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 14 F 25 10 20 P 10. NETWORK LABORATORY TW 11. NETWORK LABORATORY 50 20 22 P 25 10 16 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 316/750. ORDN. 1 MARKS: T80058536 D CUNHA JOANNE JOHN ANNIE , 71200824в , PICT , T80058536 01. OPERATING SYSTEM 100 40 40 P 02. THEORY OF COMPUTATION 100 40 51 P PP 03. COMPUTER NETWORK TECHNOLOGY 100 40 58 P PP 04. DATBASE MANAGEMENT SYSTEMS 100 40 46 P 05. SOFTWARE ENGINEERING 100 40 52 P PP 06. OPERATING SYSTEM DESIGN LAB. 25 10 19 P TW 22 P 50 20 07. OPERATING SYSTEM DESIGN LAB. 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 18 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 28 P 10. NETWORK LABORATORY 25 10 22 P 11. NETWORK LABORATORY OR 50 20 38 P 12. SOFT SKILLS LABORATORY TW 25 10 20 P FIRST TERM TOTAL = 414/750. ORDN. 1 MARKS:

DATE: 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 13 (501) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71200825L T80058537 DABARE DEEP RAJESH RASHMI , , PICT , T80058537 01. OPERATING SYSTEM 100 40 42 P 02. THEORY OF COMPUTATION 100 40 53 P 03. COMPUTER NETWORK TECHNOLOGY 100 40 67 P PP 04. DATBASE MANAGEMENT SYSTEMS 100 40 52 P 05. SOFTWARE ENGINEERING 100 40 58 P 25 10 18 P 06. OPERATING SYSTEM DESIGN LAB. TW 07. OPERATING SYSTEM DESIGN LAB. 50 20 33 P 25 10 17 P 08. INFORMATION SYSTEMS DESIGN LAB. TW 50 20 33 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 10. NETWORK LABORATORY 25 10 19 P TW 11. NETWORK LABORATORY 50 20 37 P OR 25 10 14 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 443/750. ORDN. 1 MARKS: T80058538 DAHAWAD SUNIL NAMDEO , T80058529 , PICT VANITA , 71241757F 01. OPERATING SYSTEM 100 40 43 P C 100 40 02. THEORY OF COMPUTATION PP 46 P C 100 40 57 P C 03. COMPUTER NETWORK TECHNOLOGY 100 40 04. DATBASE MANAGEMENT SYSTEMS PP 40 P C 100 40 40 P C 05. SOFTWARE ENGINEERING PP 25 10 18 P C 06. OPERATING SYSTEM DESIGN LAB. TW 50 20 29 P C 07. OPERATING SYSTEM DESIGN LAB. PR 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 18 P C 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 25 P C 25 10 10. NETWORK LABORATORY TW 17 P C 11. NETWORK LABORATORY 50 20 AA F 25 10 19 P C 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 352/750. ORDN. 1 MARKS: T80058539 DARAK SHREYA TEJRAJ JYOTI , 71200827G , PICT , T80058539 01. OPERATING SYSTEM 100 40 59 P 02. THEORY OF COMPUTATION 100 40 62 P PP 03. COMPUTER NETWORK TECHNOLOGY 100 40 65 P PP 04. DATBASE MANAGEMENT SYSTEMS 100 40 53 P 05. SOFTWARE ENGINEERING 100 40 58 P PP 06. OPERATING SYSTEM DESIGN LAB. 25 10 22 P TW 50 20 34 P 07. OPERATING SYSTEM DESIGN LAB. 25 10 22 P 08. INFORMATION SYSTEMS DESIGN LAB. TW 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 40 P 10. NETWORK LABORATORY 25 10 22 P 11. NETWORK LABORATORY OR 50 20 39 P 12. SOFT SKILLS LABORATORY TW 25 10 22 P FIRST TERM TOTAL = 498/750. ORDN. 1 MARKS:

DATE: 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 14 (502) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , T80058530 , PICT T80058540 DARDA MAYOOR MONISH KALPANA , 71045410E , т80058540 01. OPERATING SYSTEM 100 40 52 P C 13. SYSTEM SOFTWARE PROGRAMMING 100 40 43 P C 02. THEORY OF COMPUTATION 100 40 14. MANAGEMENT INFORMATION SYSTEMS PP 100 40 41 P C AA F 100 40 60 P C 100 40 55 P C 03. COMPUTER NETWORK TECHNOLOGY PP 15. PROGRAMMING PARADIGMS 04. DATBASE MANAGEMENT SYSTEMS 100 40 40 P C 100 40 PP 16. DESIGN & ANALYSIS OF ALGORITHMS PP AA F 05. SOFTWARE ENGINEERING 100 40 53 P C 17. HUMAN COMPU.INTERACTION & USABI. PP 100 40 52 P C 25 10 10 P C 50 20 20 P C 06. OPERATING SYSTEM DESIGN LAB. 18. SOFTWARE DESIGN LABORATORY TW TW 07. OPERATING SYSTEM DESIGN LAB. 50 20 50 20 AA F PR AA F 19. SOFTWARE DESIGN LABORATORY 25 10 11 P C 50 20 20 P C 08. INFORMATION SYSTEMS DESIGN LAB. TW 20. SOFTWARE DEVELOPMENT TOOLS LAB. TW 50 20 AA F 50 20 30 P C 09. INFORMATION SYSTEMS DESIGN LAB. OR 21. SOFTWARE DEVELOPMENT TOOLS LAB. OR 25 10 13 P C 22. SEMINAR AND TECHNICAL COMMUN. TW 50 20 20 P C 10. NETWORK LABORATORY TW 50 20 AA F 11. NETWORK LABORATORY OR 25 10 13 P C 12. SOFT SKILLS LABORATORY TW GRAND TOTAL = 533/1500, RESULT: FAILS ORDN. 1 MARKS: T80058541 DEOGHATKAR BRAMHESH NAMDEO MAYALI , 71200829C , T80058541 , PICT 01. OPERATING SYSTEM 100 40 43 P 100 40 02. THEORY OF COMPUTATION 47 P PP 100 40 03. COMPUTER NETWORK TECHNOLOGY 64 P 100 40 04. DATBASE MANAGEMENT SYSTEMS PP 52 P 100 40 05. SOFTWARE ENGINEERING 48 P PP 25 10 17 P 06. OPERATING SYSTEM DESIGN LAB. TW 50 20 07. OPERATING SYSTEM DESIGN LAB. PR 38 P 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 18 P 50 20 09. INFORMATION SYSTEMS DESIGN LAB. OR 39 P 25 10 10. NETWORK LABORATORY 18 P TW 11. NETWORK LABORATORY 50 20 35 P 25 10 14 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 433/750. ORDN. 1 MARKS: T80058542 DESHMUKH MONIKA JAYANT JYOTI , 71200831E , , PICT , T80058542 01. OPERATING SYSTEM 100 40 60 P 02. THEORY OF COMPUTATION 100 40 61 P PP 03. COMPUTER NETWORK TECHNOLOGY 100 40 71 P PP 60 P 100 40 04. DATBASE MANAGEMENT SYSTEMS 05. SOFTWARE ENGINEERING 100 40 64 P PP 06. OPERATING SYSTEM DESIGN LAB. 25 10 19 P TW 50 20 37 P 07. OPERATING SYSTEM DESIGN LAB. 25 10 21 P 08. INFORMATION SYSTEMS DESIGN LAB. TW 50 20 35 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 10. NETWORK LABORATORY 25 10 22 P 11. NETWORK LABORATORY OR 50 20 37 P 12. SOFT SKILLS LABORATORY TW 25 10 19 P FIRST TERM TOTAL = 506/750. ORDN. 1 MARKS:

DATE: 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 15 (503) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71200836F T80058543 DEVASHISH SINGH SUSHMA , , PICT , T80058543 01. OPERATING SYSTEM 100 40 41 P 02. THEORY OF COMPUTATION 100 40 67 P 03. COMPUTER NETWORK TECHNOLOGY 100 40 56 P PP 04. DATBASE MANAGEMENT SYSTEMS 100 40 58 P 05. SOFTWARE ENGINEERING 100 40 58 P 25 10 20 P 06. OPERATING SYSTEM DESIGN LAB. TW 07. OPERATING SYSTEM DESIGN LAB. 50 20 41 P 25 10 17 P 08. INFORMATION SYSTEMS DESIGN LAB. TW 50 20 33 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 10. NETWORK LABORATORY 25 10 20 P TW 11. NETWORK LABORATORY 50 20 35 P OR 25 10 21 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 467/750. ORDN. 1 MARKS: T80058544 DHAMANE AKANKSHA ARUN **KANCHAN** , 71200838B , PICT , T80058544 100 40 48 P 01. OPERATING SYSTEM 100 40 53 P 02. THEORY OF COMPUTATION PP 100 40 51 P 03. COMPUTER NETWORK TECHNOLOGY 100 40 04. DATBASE MANAGEMENT SYSTEMS PP 53 P 100 40 57 P 05. SOFTWARE ENGINEERING PP 25 10 06. OPERATING SYSTEM DESIGN LAB. TW 18 P 50 20 35 P 07. OPERATING SYSTEM DESIGN LAB. PR 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 21 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 33 P 25 10 10. NETWORK LABORATORY TW 21 P 11. NETWORK LABORATORY 50 20 30 P OR 25 10 19 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 439/750. ORDN. 1 MARKS: T80058545 DHANAVE PRATIK BALASAHEB UJJWALLA , 71100783H , T80058531 , PICT , T80058545 01. OPERATING SYSTEM 100 40 42 P C 02. THEORY OF COMPUTATION 100 40 AA F PP 03. COMPUTER NETWORK TECHNOLOGY PP 100 40 51 P C 100 40 04. DATBASE MANAGEMENT SYSTEMS 43 P C 05. SOFTWARE ENGINEERING 100 40 48 P C PP 06. OPERATING SYSTEM DESIGN LAB. 25 10 12 P C TW 50 20 22 P 07. OPERATING SYSTEM DESIGN LAB. 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 16 P C 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 35 P C 10. NETWORK LABORATORY 25 10 13 P C 11. NETWORK LABORATORY OR 50 20 40 P 12. SOFT SKILLS LABORATORY TW 25 10 13 P C FIRST TERM TOTAL = 335/750. ORDN. 1 MARKS:

DATE: 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 16 (504) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71200845E T80058546 DISALE GITA BIBHISHAN RADHA , , PICT , T80058546 01. OPERATING SYSTEM 100 40 40 P 02. THEORY OF COMPUTATION 100 40 43 P 03. COMPUTER NETWORK TECHNOLOGY 100 40 49 P PP 04. DATBASE MANAGEMENT SYSTEMS 100 40 45 P 05. SOFTWARE ENGINEERING 100 40 43 P 25 10 21 P 06. OPERATING SYSTEM DESIGN LAB. TW 07. OPERATING SYSTEM DESIGN LAB. 50 20 33 P 25 10 21 P 08. INFORMATION SYSTEMS DESIGN LAB. TW 50 20 22 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 10. NETWORK LABORATORY 25 10 21 P TW 11. NETWORK LABORATORY 50 20 32 P OR 25 10 22 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 392/750. ORDN. 1 MARKS: T80058547 DIXIT PRANAV SUDHIR SHUBHANGI , 71200846C , PICT , T80058547 100 40 40 P 01. OPERATING SYSTEM 100 40 62 P 02. THEORY OF COMPUTATION PP 100 40 62 P 03. COMPUTER NETWORK TECHNOLOGY 100 40 50 P 04. DATBASE MANAGEMENT SYSTEMS PP 100 40 59 P 05. SOFTWARE ENGINEERING PP 25 10 19 P 06. OPERATING SYSTEM DESIGN LAB. TW 50 20 40 P 07. OPERATING SYSTEM DESIGN LAB. PR 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 19 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 33 P 25 10 10. NETWORK LABORATORY TW 21 P 11. NETWORK LABORATORY 50 20 39 P 25 10 19 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 463/750. ORDN. 1 MARKS: T80058548 DURGE PRAPTI ANIL BABITA , 71200850M , PICT , T80058548 01. OPERATING SYSTEM 100 40 26 F 02. THEORY OF COMPUTATION 100 40 40 P PP 03. COMPUTER NETWORK TECHNOLOGY 100 40 50 P PP 52 P 04. DATBASE MANAGEMENT SYSTEMS 100 40 05. SOFTWARE ENGINEERING 100 40 43 P PP 06. OPERATING SYSTEM DESIGN LAB. 25 10 18 P TW 50 20 18 F 07. OPERATING SYSTEM DESIGN LAB. 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 23 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 35 P 10. NETWORK LABORATORY 25 10 23 P 11. NETWORK LABORATORY OR 50 20 30 P 12. SOFT SKILLS LABORATORY TW 25 10 22 P FIRST TERM TOTAL = 380/750. ORDN. 1 MARKS:

UNIVERSITY OF PUNE ,T.E. (2008 PAT) (INFORMATION TECHNOLOGY) EXAMINATION NOV. 2013 DATE : 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 17 (505) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , T80058535 , PICT , 71100793E T80058549 GAIKWAD PIYUSH GHANASHYAM **VASUDHA** , т80058549 01. OPERATING SYSTEM 100 40 40 P C 13. SYSTEM SOFTWARE PROGRAMMING 100 40 AA F PP PΡ 02. THEORY OF COMPUTATION 100 40 40 P C 14. MANAGEMENT INFORMATION SYSTEMS PP 100 40 48 P C 100 40 41 P C 100 40 43 P C 03. COMPUTER NETWORK TECHNOLOGY PP 15. PROGRAMMING PARADIGMS 04. DATBASE MANAGEMENT SYSTEMS 100 40 40 P C 16. DESIGN & ANALYSIS OF ALGORITHMS PP 100 40 45 P C PP 05. SOFTWARE ENGINEERING 100 40 48 P C 17. HUMAN COMPU.INTERACTION & USABI. PP 100 40 47 P.C. 06. OPERATING SYSTEM DESIGN LAB. 25 10 10 P C 50 20 20 P C 18. SOFTWARE DESIGN LABORATORY TW 07. OPERATING SYSTEM DESIGN LAB. 50 20 31 P C 50 20 30 P C PR 19. SOFTWARE DESIGN LABORATORY 25 10 15 P C 50 20 20 P C 08. INFORMATION SYSTEMS DESIGN LAB. TW 20. SOFTWARE DEVELOPMENT TOOLS LAB. TW 50 20 31 P 50 20 20 P C 09. INFORMATION SYSTEMS DESIGN LAB. OR 21. SOFTWARE DEVELOPMENT TOOLS LAB. OR 10. NETWORK LABORATORY 25 10 22. SEMINAR AND TECHNICAL COMMUN. TW 50 20 30 P C TW 14 P C 11. NETWORK LABORATORY 50 20 30 P C OR 25 10 16 P C 12. SOFT SKILLS LABORATORY TW GRAND TOTAL = 659/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS: T80058550 GAIKWAD PRINCE VIJAYKUMAR **PRABHA** , 71200853F , T80058550 , PICT 100 40 27 F 01. OPERATING SYSTEM 100 40 40 P 02. THEORY OF COMPUTATION PP 100 40 57 P 03. COMPUTER NETWORK TECHNOLOGY 100 40 04. DATBASE MANAGEMENT SYSTEMS PP 33 F 100 40 50 P 05. SOFTWARE ENGINEERING PP 25 10 06. OPERATING SYSTEM DESIGN LAB. TW 16 P 50 20 07. OPERATING SYSTEM DESIGN LAB. PR 34 P 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 18 P 50 20 20 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 25 10 10. NETWORK LABORATORY TW 19 P 11. NETWORK LABORATORY 50 20 28 P 25 10 17 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 359/750. ORDN. 1 MARKS: T80058551 GAME AJAY BALASAHEB , 71200855B , T80058551 ANITA , PICT 01. OPERATING SYSTEM 100 40 66 P 02. THEORY OF COMPUTATION 100 40 74 P PP 03. COMPUTER NETWORK TECHNOLOGY 100 40 70 P PP 04. DATBASE MANAGEMENT SYSTEMS 100 40 63 P 05. SOFTWARE ENGINEERING 100 40 69 P PP 06. OPERATING SYSTEM DESIGN LAB. 25 10 20 P TW 50 20 38 P 07. OPERATING SYSTEM DESIGN LAB. 25 10 20 P 08. INFORMATION SYSTEMS DESIGN LAB. TW 50 20 40 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 10. NETWORK LABORATORY 25 10 23 P 11. NETWORK LABORATORY OR 50 20 38 P 12. SOFT SKILLS LABORATORY TW 25 10 21 P FIRST TERM TOTAL = 542/750. ORDN. 1 MARKS:

DATE: 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 18 (506) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER SUVARNA , 71200856L T80058552 GANDHI KAMLESH ISHWARLAL , , PICT , T80058552 01. OPERATING SYSTEM 100 40 57 P PP 02. THEORY OF COMPUTATION 100 40 62 P 03. COMPUTER NETWORK TECHNOLOGY 100 40 54 P PP 04. DATBASE MANAGEMENT SYSTEMS 100 40 56 P 05. SOFTWARE ENGINEERING 100 40 63 P 25 10 21 P 06. OPERATING SYSTEM DESIGN LAB. TW 07. OPERATING SYSTEM DESIGN LAB. 50 20 40 P 25 10 24 P 08. INFORMATION SYSTEMS DESIGN LAB. TW 50 20 37 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 10. NETWORK LABORATORY 25 10 24 P TW 11. NETWORK LABORATORY 50 20 36 P OR 25 10 24 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 498/750. ORDN. 1 MARKS: T80058553 GAVALI ANKITA SUNIL **SUDHA** , 71200860J , PICT , T80058553 01. OPERATING SYSTEM 100 40 40 P 100 40 20 F 02. THEORY OF COMPUTATION PP 100 40 40 P 03. COMPUTER NETWORK TECHNOLOGY 100 40 42 P 04. DATBASE MANAGEMENT SYSTEMS PP 100 40 47 P 05. SOFTWARE ENGINEERING PP 25 10 14 P 06. OPERATING SYSTEM DESIGN LAB. TW 50 20 25 P 07. OPERATING SYSTEM DESIGN LAB. PR 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 16 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 22 P 25 10 10. NETWORK LABORATORY TW 17 P 11. NETWORK LABORATORY 50 20 22 P 25 10 15 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 320/750. ORDN. 1 MARKS: T80058554 GHATE ABHIJIT SURESHRAO KUNDA , 71200864M , PICT , T80058554 01. OPERATING SYSTEM 100 40 47 P 02. THEORY OF COMPUTATION 100 40 40 P PP 03. COMPUTER NETWORK TECHNOLOGY 100 40 53 P PP 51 P 04. DATBASE MANAGEMENT SYSTEMS 100 40 05. SOFTWARE ENGINEERING 100 40 56 P PP 06. OPERATING SYSTEM DESIGN LAB. 25 10 17 P TW 50 20 37 P 07. OPERATING SYSTEM DESIGN LAB. 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 18 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 33 P 10. NETWORK LABORATORY 25 10 16 P 11. NETWORK LABORATORY OR 50 20 29 P 12. SOFT SKILLS LABORATORY TW 25 10 18 P FIRST TERM TOTAL = 415/750. ORDN. 1 MARKS:

DATE : 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 19 (507)

NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , T80058538 , PICT SWATI , 71100802н T80058555 GHODAKE PRAJAKTA DHANANJAY , T80058555 01. OPERATING SYSTEM 100 40 51 P C 13. SYSTEM SOFTWARE PROGRAMMING 100 40 45 P C PΡ 02. THEORY OF COMPUTATION 100 40 47 P C 14. MANAGEMENT INFORMATION SYSTEMS PP 100 40 48 P 100 40 40 P C 100 40 40 P C 03. COMPUTER NETWORK TECHNOLOGY PP 15. PROGRAMMING PARADIGMS 04. DATBASE MANAGEMENT SYSTEMS 100 40 40 P C 100 40 49 P C PP 16. DESIGN & ANALYSIS OF ALGORITHMS PP 05. SOFTWARE ENGINEERING 100 40 42 P C 17. HUMAN COMPU.INTERACTION & USABI. PP 100 40 40 P C PP 25 10 18 P C 50 20 30 P C 06. OPERATING SYSTEM DESIGN LAB. 18. SOFTWARE DESIGN LABORATORY TW TW 07. OPERATING SYSTEM DESIGN LAB. 50 20 38 P C 50 20 33 P C PR 19. SOFTWARE DESIGN LABORATORY 25 10 18 P C 50 20 30 P C 08. INFORMATION SYSTEMS DESIGN LAB. TW 20. SOFTWARE DEVELOPMENT TOOLS LAB. TW 50 20 50 20 30 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 24 P C 21. SOFTWARE DEVELOPMENT TOOLS LAB. OR 25 10 22. SEMINAR AND TECHNICAL COMMUN. TW 50 20 35 P C 10. NETWORK LABORATORY TW 16 P C 50 20 20 P C 11. NETWORK LABORATORY OR 25 10 18 P C 12. SOFT SKILLS LABORATORY TW GRAND TOTAL = 752/1500, RESULT: SECOND CLASS ORDN. 1 MARKS: T80058556 GHODE AMAR PRADEEPKUMAR , т80058539 , SINDHU , 71045436J , т80058556 100 40 46 P C 01. OPERATING SYSTEM 100 40 53 P C 13. SYSTEM SOFTWARE PROGRAMMING 02. THEORY OF COMPUTATION 100 40 40 P C 100 40 PP 14. MANAGEMENT INFORMATION SYSTEMS PP AA F 100 40 100 40 03. COMPUTER NETWORK TECHNOLOGY 48 P C 15. PROGRAMMING PARADIGMS 50 P C 100 40 04. DATBASE MANAGEMENT SYSTEMS PP 40 P C 16. DESIGN & ANALYSIS OF ALGORITHMS PP 100 40 AA F 100 40 05. SOFTWARE ENGINEERING 52 P C 17. HUMAN COMPU.INTERACTION & USABI. PP 100 40 42 P C PP 25 10 17 P C 06. OPERATING SYSTEM DESIGN LAB. TW 18. SOFTWARE DESIGN LABORATORY 50 20 20 P C 50 20 50 20 07. OPERATING SYSTEM DESIGN LAB. PR AA F 19. SOFTWARE DESIGN LABORATORY AA F 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 17 P C 20. SOFTWARE DEVELOPMENT TOOLS LAB. TW 50 20 20 P C 22 P C 50 20 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 21. SOFTWARE DEVELOPMENT TOOLS LAB. OR AA F 50 20 21 P C 10. NETWORK LABORATORY TW 25 10 12 P C 22. SEMINAR AND TECHNICAL COMMUN. TW 11. NETWORK LABORATORY 50 20 28 P C 12. SOFT SKILLS LABORATORY TW 25 10 15 P C GRAND TOTAL = 543/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS: T80058557 GHODKE NIRANJAN DATTATRAYA **PRADNYA** , 71200865к , T80058557 , PICT 100 40 62 P 01. OPERATING SYSTEM 02. THEORY OF COMPUTATION 100 40 62 P PP 03. COMPUTER NETWORK TECHNOLOGY 100 40 56 P PP 100 40 61 P 04. DATBASE MANAGEMENT SYSTEMS 05. SOFTWARE ENGINEERING 100 40 59 P PP 25 10 06. OPERATING SYSTEM DESIGN LAB. 21 P TW 50 20 38 P 07. OPERATING SYSTEM DESIGN LAB. 25 10 21 P 08. INFORMATION SYSTEMS DESIGN LAB. TW 50 20 42 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 10. NETWORK LABORATORY 25 10 22 P 11. NETWORK LABORATORY OR 50 20 38 P 12. SOFT SKILLS LABORATORY TW 25 10 21 P FIRST TERM TOTAL = 503/750. ORDN. 1 MARKS:

DATE: 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 20 (508) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71350921L T80058558 GOHOKAR KIRAN DADAJI REKHA , , PICT , T80058558 47 P 01. OPERATING SYSTEM 100 40 02. THEORY OF COMPUTATION 100 40 48 P 03. COMPUTER NETWORK TECHNOLOGY 100 40 56 P PP 04. DATBASE MANAGEMENT SYSTEMS 100 40 48 P 05. SOFTWARE ENGINEERING 100 40 52 P 25 10 18 P 06. OPERATING SYSTEM DESIGN LAB. TW 07. OPERATING SYSTEM DESIGN LAB. 50 20 20 P 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 21 P 50 20 29 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 10. NETWORK LABORATORY 25 10 21 P TW 11. NETWORK LABORATORY 50 20 38 P OR 25 10 23 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 421/750. ORDN. 1 MARKS: T80058559 GOKHALE VALLARI SHRIKANT PADMAJA , 71200867F , PICT , T80058559 01. OPERATING SYSTEM 100 40 56 P 02. THEORY OF COMPUTATION 100 40 64 P PP 03. COMPUTER NETWORK TECHNOLOGY 100 40 62 P 100 40 54 P 04. DATBASE MANAGEMENT SYSTEMS PP 100 40 60 P 05. SOFTWARE ENGINEERING PP 25 10 20 P 06. OPERATING SYSTEM DESIGN LAB. TW 50 20 30 P 07. OPERATING SYSTEM DESIGN LAB. PR 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 22 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 30 P 25 10 10. NETWORK LABORATORY TW 22 P 11. NETWORK LABORATORY 50 20 33 P 25 10 23 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 476/750. ORDN. 1 MARKS: T80058560 GOLAPKAR PIYUSH ANIL , 71200868D , PICT , T80058560 APARNA 01. OPERATING SYSTEM 100 40 41 P 02. THEORY OF COMPUTATION 100 40 53 P PP 03. COMPUTER NETWORK TECHNOLOGY 100 40 57 P PP 04. DATBASE MANAGEMENT SYSTEMS 100 40 51 P 05. SOFTWARE ENGINEERING 100 40 61 P PP 06. OPERATING SYSTEM DESIGN LAB. 25 10 17 P TW 50 20 42 P 07. OPERATING SYSTEM DESIGN LAB. 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 19 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 43 P 10. NETWORK LABORATORY 25 10 19 P 11. NETWORK LABORATORY OR 50 20 34 P 12. SOFT SKILLS LABORATORY TW 25 10 17 P FIRST TERM TOTAL = 454/750. ORDN. 1 MARKS:

DATE: 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 21 (509) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER KAMINI , 71200869в T80058562 GOSAVI KALYANI RAJGIR , , PICT , T80058562 01. OPERATING SYSTEM 100 40 54 P 02. THEORY OF COMPUTATION 100 40 59 P 03. COMPUTER NETWORK TECHNOLOGY 100 40 62 P PP 04. DATBASE MANAGEMENT SYSTEMS 100 40 62 P 05. SOFTWARE ENGINEERING 100 40 45 P 25 10 19 P 06. OPERATING SYSTEM DESIGN LAB. TW 07. OPERATING SYSTEM DESIGN LAB. 50 20 35 P 25 10 22 P 08. INFORMATION SYSTEMS DESIGN LAB. TW 50 20 33 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 10. NETWORK LABORATORY 25 10 22 P TW 11. NETWORK LABORATORY 50 20 33 P OR 25 10 23 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 469/750. ORDN. 1 MARKS: T80058563 GUPTA SHRADHA RAVINDRA **AMITA** , 71200873L , PICT , T80058563 01. OPERATING SYSTEM 100 40 71 P 02. THEORY OF COMPUTATION 100 40 81 P PP 03. COMPUTER NETWORK TECHNOLOGY 100 40 61 P 100 40 04. DATBASE MANAGEMENT SYSTEMS PP 53 P 100 40 57 P 05. SOFTWARE ENGINEERING PP 25 10 24 P 06. OPERATING SYSTEM DESIGN LAB. TW 50 20 07. OPERATING SYSTEM DESIGN LAB. PR 42 P 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 24 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 38 P 25 10 10. NETWORK LABORATORY TW 23 P 11. NETWORK LABORATORY 50 20 40 P 25 10 23 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 537/750. ORDN. 1 MARKS: T80058564 HADKE ANUP BANDA MANIK , 71200875G , PICT , T80058564 01. OPERATING SYSTEM 100 40 40 P 02. THEORY OF COMPUTATION 100 40 40 P PP 03. COMPUTER NETWORK TECHNOLOGY 100 40 42 P PP 32 F 04. DATBASE MANAGEMENT SYSTEMS 100 40 05. SOFTWARE ENGINEERING 100 40 58 P PP 06. OPERATING SYSTEM DESIGN LAB. 25 10 22 P TW 22 P 50 20 07. OPERATING SYSTEM DESIGN LAB. 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 20 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 39 P 10. NETWORK LABORATORY 25 10 23 P 11. NETWORK LABORATORY OR 50 20 37 P 12. SOFT SKILLS LABORATORY TW 25 10 21 P FIRST TERM TOTAL = 396/750. ORDN. 1 MARKS:

DATE: 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 22 (510) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71200877C T80058566 HARSH BAHETI ANJANA , , PICT , T80058566 01. OPERATING SYSTEM 100 40 40 P 02. THEORY OF COMPUTATION 100 40 60 P 03. COMPUTER NETWORK TECHNOLOGY 100 40 50 P PP 04. DATBASE MANAGEMENT SYSTEMS 100 40 56 P 05. SOFTWARE ENGINEERING 100 40 41 P 25 10 10 P 06. OPERATING SYSTEM DESIGN LAB. TW 07. OPERATING SYSTEM DESIGN LAB. 50 20 20 P 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 17 P 50 20 33 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 10. NETWORK LABORATORY 25 10 18 P TW 11. NETWORK LABORATORY 50 20 38 P OR 25 10 16 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 399/750. ORDN. 1 MARKS: T80058567 HATOLKAR ABHIRAM PRASANNA MANJUSHA , 71200878M , PICT , T80058567 01. OPERATING SYSTEM 100 40 54 P 02. THEORY OF COMPUTATION 100 40 45 P PP 100 40 61 P 03. COMPUTER NETWORK TECHNOLOGY 100 40 57 P 04. DATBASE MANAGEMENT SYSTEMS PP 100 40 05. SOFTWARE ENGINEERING PP 54 P 25 10 17 P 06. OPERATING SYSTEM DESIGN LAB. TW 50 20 07. OPERATING SYSTEM DESIGN LAB. PR 44 P 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 20 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 43 P 25 10 10. NETWORK LABORATORY TW 22 P 11. NETWORK LABORATORY 50 20 40 P 25 10 18 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 475/750. ORDN. 1 MARKS: T80058568 IYER PRIYANKA SUBRAMANIAN USHA , 71200884F , PICT , T80058568 01. OPERATING SYSTEM 100 40 47 P 02. THEORY OF COMPUTATION 100 40 60 P PP 03. COMPUTER NETWORK TECHNOLOGY 100 40 62 P PP 62 P 04. DATBASE MANAGEMENT SYSTEMS 100 40 05. SOFTWARE ENGINEERING 100 40 57 P PP 06. OPERATING SYSTEM DESIGN LAB. 25 10 22 P TW 50 20 38 P 07. OPERATING SYSTEM DESIGN LAB. 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 23 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 37 P 10. NETWORK LABORATORY 25 10 24 P 11. NETWORK LABORATORY OR 50 20 42 P 12. SOFT SKILLS LABORATORY TW 25 10 21 P FIRST TERM TOTAL = 495/750. ORDN. 1 MARKS:

DATE: 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 23 (511) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71350923G , т80058569 T80058569 JAGTAP MANISH KISHOR BHARTI , PICT 01. OPERATING SYSTEM 100 40 52 P 02. THEORY OF COMPUTATION 100 40 44 P PP 100 40 56 P 03. COMPUTER NETWORK TECHNOLOGY 04. DATBASE MANAGEMENT SYSTEMS 100 40 45 P 05. SOFTWARE ENGINEERING 100 40 54 P 25 10 23 P 06. OPERATING SYSTEM DESIGN LAB. TW 07. OPERATING SYSTEM DESIGN LAB. 50 20 46 P 25 10 08. INFORMATION SYSTEMS DESIGN LAB. TW 23 P 50 20 09. INFORMATION SYSTEMS DESIGN LAB. OR 43 P 25 10 24 P 10. NETWORK LABORATORY TW 50 20 39 P 11. NETWORK LABORATORY OR 25 10 21 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 470/750. ORDN. 1 MARKS: T80058570 JAIPURIA ROHIT SHARADKUMAR , т80058548 , AMITA , 71100828M PICT , т80058570 100 40 40 P C 100 40 41 P C 01. OPERATING SYSTEM 13. SYSTEM SOFTWARE PROGRAMMING 41 P C 100 40 100 40 02. THEORY OF COMPUTATION PΡ 14. MANAGEMENT INFORMATION SYSTEMS PP 40 P C 100 40 100 40 03. COMPUTER NETWORK TECHNOLOGY 45 P C 15. PROGRAMMING PARADIGMS 40 P C 100 40 100 40 04. DATBASE MANAGEMENT SYSTEMS PP 43 P C 16. DESIGN & ANALYSIS OF ALGORITHMS PP 42 P C 100 40 100 40 05. SOFTWARE ENGINEERING PP 42 P C 17. HUMAN COMPU.INTERACTION & USABI. PP 40 P C 25 10 19 P C 35 P C 06. OPERATING SYSTEM DESIGN LAB. TW 18. SOFTWARE DESIGN LABORATORY 50 20 50 20 20\$ P 50 20 22 P C 07. OPERATING SYSTEM DESIGN LAB. PR 19. SOFTWARE DESIGN LABORATORY 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 18 P C 20. SOFTWARE DEVELOPMENT TOOLS LAB. TW 50 20 35 P C 50 20 50 20 09. INFORMATION SYSTEMS DESIGN LAB. OR 30 P C 21. SOFTWARE DEVELOPMENT TOOLS LAB. OR 35 P C 50 20 37 P C 10. NETWORK LABORATORY TW 25 10 19 P C 22. SEMINAR AND TECHNICAL COMMUN. TW 50 20 11. NETWORK LABORATORY 28 P C OR 12. SOFT SKILLS LABORATORY TW 25 10 20 P C GRAND TOTAL = 732/1500, RESULT: PASS CLASS [\$ 0.1] ORDN. 1 MARKS: (07)2, T80058571 JALAMKAR ABHILASH RAJENDRA SUNITA , 71200889G , PICT , T80058571 01. OPERATING SYSTEM 100 40 49 P 02. THEORY OF COMPUTATION 100 40 46 P 03. COMPUTER NETWORK TECHNOLOGY 100 40 58 P PP 04. DATBASE MANAGEMENT SYSTEMS 100 40 53 P 05. SOFTWARE ENGINEERING 100 40 45 P PP 06. OPERATING SYSTEM DESIGN LAB. 25 10 15 P TW 50 20 30 P 07. OPERATING SYSTEM DESIGN LAB. 25 10 19 P 08. INFORMATION SYSTEMS DESIGN LAB. TW 50 20 35 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 10. NETWORK LABORATORY 25 10 20 P 11. NETWORK LABORATORY OR 50 20 37 P 12. SOFT SKILLS LABORATORY TW 25 10 16 P FIRST TERM TOTAL = 423/750. ORDN. 1 MARKS:

UNIVERSITY OF DATE: 11 MAR. 2014						ORMATION TECHN OF COMPUTER TE				24 (512)
NOTE: FIRST LINE : SEAT NO., NAME (SEAT NO.
OTHER LINES: HEAD OF PASSING,			-			•				
T80058573 JATAB NIKHIL HARIDWARIL					 ASHODA		, 71350924E	, , , , , ,		
01. OPERATING SYSTEM	PP	100	40	67		`	, 713303212	,	, 1101	, 100030373
02. THEORY OF COMPUTATION	PP	100	40	48						
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	56						
04. DATBASE MANAGEMENT SYSTEMS	PP	100	40	64						
05. SOFTWARE ENGINEERING	PP	100	40	61						
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	20						
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	37						
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	22	Р					
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	44						
10. NETWORK LABORATORY	TW	25	10	24						
11. NETWORK LABORATORY	OR	50	20	40						
12. SOFT SKILLS LABORATORY	TW	25	10	21						
FIRST TERM TOTAL = 504/750.										
ORDN. 1 MARKS :										
T80058574 JEEVJYOT SUKHDEV SINGH						KAUR CHHABDA				, т80058574
01. OPERATING SYSTEM	PP	100	40	47			•	,	,	•
02. THEORY OF COMPUTATION	PP	100	40	40	Р					
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	46						
04. DATBASE MANAGEMENT SYSTEMS	PP	100	40	63	Р					
05. SOFTWARE ENGINEERING	PP	100	40	54	Р					
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	21	Р					
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	42	Р					
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	23						
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	45	Р					
10. NETWORK LABORATORY	TW	25	10	23	Р					
11. NETWORK LABORATORY	OR	50	20	40	Р					
12. SOFT SKILLS LABORATORY	TW	25	10	20	Р					
FIRST TERM TOTAL = 464/750.										
ORDN. 1 MARKS :										
T80058575 JOSHI SWANAND ARVIND				AF	RUNA		, 71200896к	,	, PICT	, т80058575
01. OPERATING SYSTEM	PP	100	40	67	Р					
02. THEORY OF COMPUTATION	PP	100	40	68	Р					
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	62	Р					
04. DATBASE MANAGEMENT SYSTEMS	PP	100	40	66	Р					
05. SOFTWARE ENGINEERING	PP	100	40	55	Р					
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	22	Р					
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	42	Р					
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	22	Р					
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	42	Р					
10. NETWORK LABORATORY	TW	25	10	21	Р					
11. NETWORK LABORATORY	OR	50	20	42	Р					
12. SOFT SKILLS LABORATORY	TW	25	10	19	Р					
FIRST TERM TOTAL = 528/750.										
ORDN. 1 MARKS :										

DATE: 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 25 (513) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71200899D T80058576 KADAM YASHASWINI VISHNU , , PICT , T80058576 ALKA 01. OPERATING SYSTEM 100 40 49 P PP 02. THEORY OF COMPUTATION 100 40 49 P 03. COMPUTER NETWORK TECHNOLOGY 100 40 58 P PP 04. DATBASE MANAGEMENT SYSTEMS 100 40 65 P 05. SOFTWARE ENGINEERING 100 40 66 P 25 10 17 P 06. OPERATING SYSTEM DESIGN LAB. TW 07. OPERATING SYSTEM DESIGN LAB. 50 20 12 F 25 10 20 P 08. INFORMATION SYSTEMS DESIGN LAB. TW 50 20 42 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 10. NETWORK LABORATORY 25 10 20 P TW 11. NETWORK LABORATORY 50 20 35 P OR 25 10 18 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 451/750. ORDN. 1 MARKS: T80058577 KADU SHRADDHA AJIT MANJIRI , 71200900M , PICT , T80058577 01. OPERATING SYSTEM 100 40 56 P 100 40 72 P 02. THEORY OF COMPUTATION PP 100 40 64 P 03. COMPUTER NETWORK TECHNOLOGY 100 40 04. DATBASE MANAGEMENT SYSTEMS PP 58 P 100 40 63 P 05. SOFTWARE ENGINEERING PP 25 10 22 P 06. OPERATING SYSTEM DESIGN LAB. TW 50 20 35 P 07. OPERATING SYSTEM DESIGN LAB. PR 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 22 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 37 P 25 10 10. NETWORK LABORATORY TW 24 P 11. NETWORK LABORATORY 50 20 42 P 25 10 22 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 517/750. ORDN. 1 MARKS: T80058578 KAILAS JAHANGIR PAWARA , 71100842G , PICT , T80058578 THAWALI 01. OPERATING SYSTEM 100 40 33 F 02. THEORY OF COMPUTATION 100 40 26 F PP 03. COMPUTER NETWORK TECHNOLOGY 100 40 40 P PP 04. DATBASE MANAGEMENT SYSTEMS 100 40 AA F 05. SOFTWARE ENGINEERING 100 40 PP AA F 06. OPERATING SYSTEM DESIGN LAB. 25 10 13 P TW 32 P 50 20 07. OPERATING SYSTEM DESIGN LAB. 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 17 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 35 P 10. NETWORK LABORATORY 25 10 16 P 11. NETWORK LABORATORY OR 50 20 22 P 12. SOFT SKILLS LABORATORY TW 25 10 15 P FIRST TERM TOTAL = 249/750. ORDN. 1 MARKS:

DATE: 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 26 (514) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71200907J T80058579 KAMBLE NIKITA MAHADEV VANDANA , , PICT , T80058579 01. OPERATING SYSTEM 100 40 46 P 02. THEORY OF COMPUTATION 100 40 46 P 03. COMPUTER NETWORK TECHNOLOGY 100 40 62 P PP 04. DATBASE MANAGEMENT SYSTEMS 100 40 50 P 05. SOFTWARE ENGINEERING 100 40 56 P 25 10 22 P 06. OPERATING SYSTEM DESIGN LAB. TW 07. OPERATING SYSTEM DESIGN LAB. 50 20 33 P 25 10 23 P 08. INFORMATION SYSTEMS DESIGN LAB. TW 50 20 44 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 10. NETWORK LABORATORY 25 10 23 P TW 11. NETWORK LABORATORY 50 20 30 P OR 25 10 23 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 458/750. ORDN. 1 MARKS: T80058580 KATKE AMRUTA SURESH MANISHA , 71200916н , PICT , T80058580 01. OPERATING SYSTEM 100 40 46 P 100 40 47 P 02. THEORY OF COMPUTATION PP 100 40 54 P 03. COMPUTER NETWORK TECHNOLOGY 100 40 57 P 04. DATBASE MANAGEMENT SYSTEMS PP 100 40 57 P 05. SOFTWARE ENGINEERING PP 25 10 20 P 06. OPERATING SYSTEM DESIGN LAB. TW 50 20 07. OPERATING SYSTEM DESIGN LAB. PR 36 P 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 20 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 34 P 25 10 10. NETWORK LABORATORY TW 24 P 11. NETWORK LABORATORY 50 20 34 P 25 10 21 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 450/750. ORDN. 1 MARKS: T80058581 KAVADE PRIYANKA DHANANJAY RAJASHREE , 71200917F , PICT , T80058581 01. OPERATING SYSTEM 100 40 58 P 02. THEORY OF COMPUTATION 100 40 64 P PP 03. COMPUTER NETWORK TECHNOLOGY 100 40 49 P PP 04. DATBASE MANAGEMENT SYSTEMS 100 40 61 P 05. SOFTWARE ENGINEERING 100 40 60 P PP 06. OPERATING SYSTEM DESIGN LAB. 25 10 21 P TW 50 20 38 P 07. OPERATING SYSTEM DESIGN LAB. 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 22 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 35 P 10. NETWORK LABORATORY 25 10 22 P 11. NETWORK LABORATORY OR 50 20 31 P 12. SOFT SKILLS LABORATORY TW 25 10 23 P FIRST TERM TOTAL = 484/750. ORDN. 1 MARKS:

DATE: 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 27 (515) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER ANITA , 71200918D T80058582 KAVITKAR JAYASHREE GOPAL , , PICT , T80058582 01. OPERATING SYSTEM 100 40 51 P PP 02. THEORY OF COMPUTATION 100 40 64 P 03. COMPUTER NETWORK TECHNOLOGY 100 40 49 P PP 04. DATBASE MANAGEMENT SYSTEMS 100 40 54 P 05. SOFTWARE ENGINEERING 100 40 42 P 25 10 20 P 06. OPERATING SYSTEM DESIGN LAB. TW 07. OPERATING SYSTEM DESIGN LAB. 50 20 38 P 25 10 20 P 08. INFORMATION SYSTEMS DESIGN LAB. TW 50 20 37 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 10. NETWORK LABORATORY 25 10 23 P TW 11. NETWORK LABORATORY 50 20 35 P OR 25 10 20 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 453/750. ORDN. 1 MARKS: T80058583 KHABIYA ASHWINI ASHOK SUREKHA , 71350926M , PICT , T80058583 01. OPERATING SYSTEM 100 40 45 P 100 40 60 P 02. THEORY OF COMPUTATION PP 100 40 62 P 03. COMPUTER NETWORK TECHNOLOGY 100 40 63 P 04. DATBASE MANAGEMENT SYSTEMS PP 100 40 05. SOFTWARE ENGINEERING PP 65 P 25 10 21 P 06. OPERATING SYSTEM DESIGN LAB. TW 50 20 25 P 07. OPERATING SYSTEM DESIGN LAB. PR 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 22 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 31 P 25 10 22 P 10. NETWORK LABORATORY TW 11. NETWORK LABORATORY 50 20 36 P 25 10 23 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 475/750. ORDN. 1 MARKS: T80058584 KHAN SHAHBAZ AHMAD **FATIMA** , 71200922в , PICT , T80058584 01. OPERATING SYSTEM 100 40 49 P 02. THEORY OF COMPUTATION 100 40 50 P PP 03. COMPUTER NETWORK TECHNOLOGY 100 40 57 P PP 62 P 04. DATBASE MANAGEMENT SYSTEMS 100 40 05. SOFTWARE ENGINEERING 100 40 48 P PP 06. OPERATING SYSTEM DESIGN LAB. 25 10 19 P TW 50 20 12 F 07. OPERATING SYSTEM DESIGN LAB. 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 20 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 15 F 10. NETWORK LABORATORY 25 10 20 P 11. NETWORK LABORATORY OR 50 20 42 P 12. SOFT SKILLS LABORATORY TW 25 10 21 P FIRST TERM TOTAL = 415/750. ORDN. 1 MARKS:

DATE: 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 28 (516) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER SANTOSHI , 71200924〕 T80058585 KHANDELWAL ABHISHEK GOURISHANKAR , , PICT , T80058585 01. OPERATING SYSTEM 100 40 61 P 02. THEORY OF COMPUTATION 100 40 64 P 03. COMPUTER NETWORK TECHNOLOGY 100 40 63 P PP 04. DATBASE MANAGEMENT SYSTEMS 100 40 67 P 05. SOFTWARE ENGINEERING 100 40 67 P 25 10 23 P 06. OPERATING SYSTEM DESIGN LAB. TW 07. OPERATING SYSTEM DESIGN LAB. 50 20 45 P 25 10 23 P 08. INFORMATION SYSTEMS DESIGN LAB. TW 50 20 42 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 10. NETWORK LABORATORY 25 10 23 P TW 11. NETWORK LABORATORY 50 20 39 P OR 25 10 23 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 540/750. ORDN. 1 MARKS: T80058586 KHANDELWAL ARPIT NARENDRA MAMTA , 71200925G , PICT , T80058586 01. OPERATING SYSTEM 100 40 24 F 100 40 02. THEORY OF COMPUTATION PP 41 P 100 40 29 F 03. COMPUTER NETWORK TECHNOLOGY 100 40 42 P 04. DATBASE MANAGEMENT SYSTEMS PP 100 40 05. SOFTWARE ENGINEERING PP 41 P 25 10 12 P 06. OPERATING SYSTEM DESIGN LAB. TW 50 20 24 P 07. OPERATING SYSTEM DESIGN LAB. PR 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 19 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 40 P 25 10 10. NETWORK LABORATORY TW 19 P 11. NETWORK LABORATORY 50 20 36 P 25 10 17 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 344/750. ORDN. 1 MARKS: T80058587 KHAPLI TEJAS RAVINDRA RANJANA , 71200926E , , PICT , T80058587 01. OPERATING SYSTEM 100 40 32 F 02. THEORY OF COMPUTATION 100 40 53 P PP 03. COMPUTER NETWORK TECHNOLOGY 100 40 53 P PP 51 P 04. DATBASE MANAGEMENT SYSTEMS 100 40 05. SOFTWARE ENGINEERING 100 40 50 P PP 06. OPERATING SYSTEM DESIGN LAB. 25 10 20 P TW 50 20 30 P 07. OPERATING SYSTEM DESIGN LAB. 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 20 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 40 P 10. NETWORK LABORATORY 25 10 22 P 11. NETWORK LABORATORY OR 50 20 38 P 12. SOFT SKILLS LABORATORY TW 25 10 18 P FIRST TERM TOTAL = 427/750. ORDN. 1 MARKS:

DATE: 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 29 (517) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71045482в T80058588 KHARAT GANESH MAHADEO SHARDHA , , PICT , T80058588 01. OPERATING SYSTEM 100 40 24 F 02. THEORY OF COMPUTATION 100 40 40 P 03. COMPUTER NETWORK TECHNOLOGY 100 40 30 F PP 04. DATBASE MANAGEMENT SYSTEMS 100 40 40 P 05. SOFTWARE ENGINEERING 100 40 43 P 25 10 17 P 06. OPERATING SYSTEM DESIGN LAB. TW 07. OPERATING SYSTEM DESIGN LAB. 50 20 31 P 25 10 08. INFORMATION SYSTEMS DESIGN LAB. TW 18 P 50 20 35 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 10. NETWORK LABORATORY 25 10 21 P TW 11. NETWORK LABORATORY 50 20 10 F OR 25 10 19 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 328/750. ORDN. 1 MARKS: T80058589 KHARCHE KAUSTUBH PRAMOD NEHA , 71200927C , PICT , T80058589 01. OPERATING SYSTEM 100 40 55 P 02. THEORY OF COMPUTATION 100 40 53 P PP 100 40 62 P 03. COMPUTER NETWORK TECHNOLOGY 100 40 50 P 04. DATBASE MANAGEMENT SYSTEMS PP 100 40 55 P 05. SOFTWARE ENGINEERING PP 25 10 21 P 06. OPERATING SYSTEM DESIGN LAB. TW 50 20 42 P 07. OPERATING SYSTEM DESIGN LAB. PR 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 24 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 38 P 25 10 10. NETWORK LABORATORY TW 23 P 11. NETWORK LABORATORY 50 20 38 P 25 10 23 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 484/750. ORDN. 1 MARKS: SUNITA T80058590 KHEDKAR NITIN AJINATH , 71200928м , PICT , T80058590 01. OPERATING SYSTEM 100 40 31 F 02. THEORY OF COMPUTATION 100 40 40 P PP 03. COMPUTER NETWORK TECHNOLOGY 100 40 49 P PP 50 P 04. DATBASE MANAGEMENT SYSTEMS 100 40 05. SOFTWARE ENGINEERING 100 40 40 P PP 06. OPERATING SYSTEM DESIGN LAB. 25 10 18 P TW 50 20 28 P 07. OPERATING SYSTEM DESIGN LAB. 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 18 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 43 P 10. NETWORK LABORATORY 25 10 20 P 11. NETWORK LABORATORY OR 50 20 26 P 12. SOFT SKILLS LABORATORY TW 25 10 17 P FIRST TERM TOTAL = 380/750. ORDN. 1 MARKS:

UNIVERSITY OF PUNE ,T.E. (2008 PAT) (INFORMATION TECHNOLOGY) EXAMINATION NOV. 2013 DATE: 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 30 (518) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , T80058559 , PICT , 71045485G T80058591 KOSHTI ANUJA RAMESH MANGAL , т80058591 46 P C 01. OPERATING SYSTEM 100 40 13. SYSTEM SOFTWARE PROGRAMMING 100 40 61 P C PΡ 02. THEORY OF COMPUTATION 100 40 40 P C 14. MANAGEMENT INFORMATION SYSTEMS PP 100 40 54 P C 100 40 45 P C 100 40 61 P C 03. COMPUTER NETWORK TECHNOLOGY PP 15. PROGRAMMING PARADIGMS 04. DATBASE MANAGEMENT SYSTEMS 100 40 43 P C 16. DESIGN & ANALYSIS OF ALGORITHMS PP 100 40 55 P C PP 05. SOFTWARE ENGINEERING 100 40 52 P C 17. HUMAN COMPU.INTERACTION & USABI. PP 100 40 50 P C 25 10 20 P C 50 20 34 P C 06. OPERATING SYSTEM DESIGN LAB. 18. SOFTWARE DESIGN LABORATORY TW 07. OPERATING SYSTEM DESIGN LAB. 50 20 08 F 50 20 20 P C PR 19. SOFTWARE DESIGN LABORATORY 25 10 18 P C 50 20 31 P C 08. INFORMATION SYSTEMS DESIGN LAB. TW 20. SOFTWARE DEVELOPMENT TOOLS LAB. TW 50 20 50 20 32 P C 30 P C 09. INFORMATION SYSTEMS DESIGN LAB. OR 21. SOFTWARE DEVELOPMENT TOOLS LAB. OR 25 10 20 P C 22. SEMINAR AND TECHNICAL COMMUN. TW 50 20 32 P C 10. NETWORK LABORATORY TW 11. NETWORK LABORATORY 50 20 30 P C OR 25 10 20 P C 12. SOFT SKILLS LABORATORY TW GRAND TOTAL = 802/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS: T80058592 KOTHADIA RAMANI BHUPENDRA **ANAGHA** , 71200930C , T80058592 , PICT 01. OPERATING SYSTEM 100 40 43 P 100 40 40 P 02. THEORY OF COMPUTATION PΡ 100 40 40 P 03. COMPUTER NETWORK TECHNOLOGY 100 40 04. DATBASE MANAGEMENT SYSTEMS PP 62 P 100 40 50 P 05. SOFTWARE ENGINEERING PP 25 10 18 P 06. OPERATING SYSTEM DESIGN LAB. TW 50 20 07. OPERATING SYSTEM DESIGN LAB. PR 36 P 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 21 P 50 20 25 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 25 10 10. NETWORK LABORATORY TW 20 P 11. NETWORK LABORATORY 50 20 42 P 25 10 19 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 416/750. ORDN. 1 MARKS: T80058593 KULKARNI PALLAVI RAJENDRA REKHA , 71350927к , T80058593 , PICT 01. OPERATING SYSTEM 100 40 63 P 02. THEORY OF COMPUTATION 100 40 50 P PP 03. COMPUTER NETWORK TECHNOLOGY 100 40 64 P PP 04. DATBASE MANAGEMENT SYSTEMS 100 40 63 P 05. SOFTWARE ENGINEERING 100 40 54 P PP 06. OPERATING SYSTEM DESIGN LAB. 25 10 20 P TW 50 20 42 P 07. OPERATING SYSTEM DESIGN LAB. 25 10 23 P 08. INFORMATION SYSTEMS DESIGN LAB. TW 50 20 33 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 10. NETWORK LABORATORY 25 10 23 P 11. NETWORK LABORATORY OR 50 20 32 P 12. SOFT SKILLS LABORATORY TW 25 10 22 P FIRST TERM TOTAL = 489/750. ORDN. 1 MARKS:

UNIVERSITY O DATE : 11 MAR. 2014		-	-					CHNOLOG` TECHNOI	-		NOV. 2		PAGE N	1O -	31 (519)
NOTE: FIRST LINE : SEAT NO., NAME (OF THE	CANDI	DATE	, MO	THER, F	PERMA	NENT	REG. NO	, PRE	/IOUS SE	EAT NO.	., COL	LEGE,	SE	AT NO.
OTHER LINES: HEAD OF PASSING,	MAX.	MARKS	, M	IN. P	ASS MAF	RKS,	MARK	S OBTAIN	NED, P	/F:PASS	/FAIL,	C:PRE	VIOUS	CARR	Y OVER
				 LA					 200937L						 , т80058594
01. OPERATING SYSTEM	PP	100	40	46				, , , _ ,	-00337 L	,		,	1101		, 100030331
02. THEORY OF COMPUTATION		100	40	41											
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	49											
04. DATBASE MANAGEMENT SYSTEMS	PP	100	40	62											
05. SOFTWARE ENGINEERING	PP	100	40	44											
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	19											
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	10											
08. INFORMATION SYSTEMS DESIGN LAB.			10	21											
09. INFORMATION SYSTEMS DESIGN LAB.			20	40											
10. NETWORK LABORATORY	TW		10	20											
11. NETWORK LABORATORY	OR	50	20	21											
12. SOFT SKILLS LABORATORY			10	20											
FIRST TERM TOTAL = 393/750.					•										
ORDN. 1 MARKS :															
T80058595 KULKARNI TANMAY VIJAY				AL					200938J	,					, т80058595
01. OPERATING SYSTEM	PP	100	40	41				,		,		,			,
02. THEORY OF COMPUTATION	PP	100	40	54											
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	54											
	PP	100	40	62											
05. SOFTWARE ENGINEERING	PP	100	40	55											
06. OPERATING SYSTEM DESIGN LAB.	TW	25	_	17											
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	25	Р										
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	19											
09. INFORMATION SYSTEMS DESIGN LAB.		50	20	42											
10. NETWORK LABORATORY	TW		10	20											
11. NETWORK LABORATORY	OR	50	20	27											
12. SOFT SKILLS LABORATORY	TW		10	18											
FIRST TERM TOTAL = 434/750.			_,												
ORDN. 1 MARKS :															
T80058596 KUMARGAURAV SINGH					MATA				200940L	,		,	PICT		, т80058596
01. OPERATING SYSTEM	PP	100	40	54	Р			·		ŕ		•			,
02. THEORY OF COMPUTATION	PP	100	40	50	Р										
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	41	Р										
04. DATBASE MANAGEMENT SYSTEMS	PP	100	40	51	Р										
05. SOFTWARE ENGINEERING	PP	100	40	55	Р										
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	23	Р										
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	20	Р										
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	23	Р										
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	40	Р										
10. NETWORK LABORATORY	TW	25	10	23	Р										
11. NETWORK LABORATORY	OR	50	20	31	Р										
12. SOFT SKILLS LABORATORY	TW	25	10	24	Р										
FIRST TERM TOTAL = 435/750.															
ORDN. 1 MARKS :															

DATE: 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 32 (520) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71200941J T80058597 KUNJI RAHUL SRINIVASAN USHA , , PICT , T80058597 01. OPERATING SYSTEM 100 40 73 P 02. THEORY OF COMPUTATION 100 40 77 P 03. COMPUTER NETWORK TECHNOLOGY 100 40 71 P PP 04. DATBASE MANAGEMENT SYSTEMS 100 40 64 P 05. SOFTWARE ENGINEERING 100 40 70 P 25 10 24 P 06. OPERATING SYSTEM DESIGN LAB. TW 07. OPERATING SYSTEM DESIGN LAB. 50 20 45 P 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 24 P 50 20 44 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 10. NETWORK LABORATORY 25 10 24 P 11. NETWORK LABORATORY 50 20 44 P OR 25 10 24 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 584/750. ORDN. 1 MARKS: T80058598 LUNKAD JAYESH PRAKASH UJJWALA , 71350928н , PICT , T80058598 01. OPERATING SYSTEM 100 40 67 P 02. THEORY OF COMPUTATION 100 40 PP 71 P 03. COMPUTER NETWORK TECHNOLOGY 100 40 78 P 100 40 04. DATBASE MANAGEMENT SYSTEMS PP 68 P 100 40 70 P 05. SOFTWARE ENGINEERING PP 25 10 24 P 06. OPERATING SYSTEM DESIGN LAB. TW 50 20 45 P 07. OPERATING SYSTEM DESIGN LAB. PR 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 24 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 47 P 25 10 10. NETWORK LABORATORY TW 24 P 11. NETWORK LABORATORY 50 20 46 P OR 25 10 24 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 588/750. ORDN. 1 MARKS: T80058599 MAHENDRA TANVEERSINGH TEJENDRASINGH SURINDERKAUR , 71200949D , PICT , T80058599 01. OPERATING SYSTEM 100 40 31 F 02. THEORY OF COMPUTATION 100 40 49 P 03. COMPUTER NETWORK TECHNOLOGY 100 40 64 P PP 100 40 52 P 04. DATBASE MANAGEMENT SYSTEMS 05. SOFTWARE ENGINEERING 100 40 47 P PP 06. OPERATING SYSTEM DESIGN LAB. 25 10 20 P TW 50 20 38 P 07. OPERATING SYSTEM DESIGN LAB. 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 20 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 44 P 10. NETWORK LABORATORY 25 10 18 P 11. NETWORK LABORATORY OR 50 20 30 P 12. SOFT SKILLS LABORATORY TW 25 10 17 P FIRST TERM TOTAL = 430/750. ORDN. 1 MARKS:

DATE: 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 33 (521) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71200952D T80058600 MANALI DESAI MONA , , PICT , T80058600 01. OPERATING SYSTEM 100 40 49 P 02. THEORY OF COMPUTATION 100 40 40 P 03. COMPUTER NETWORK TECHNOLOGY 100 40 54 P PP 04. DATBASE MANAGEMENT SYSTEMS 100 40 40 P 05. SOFTWARE ENGINEERING 100 40 40 P 25 10 14 P 06. OPERATING SYSTEM DESIGN LAB. TW 07. OPERATING SYSTEM DESIGN LAB. 50 20 32 P 25 10 18 P 08. INFORMATION SYSTEMS DESIGN LAB. TW 50 20 37 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 10. NETWORK LABORATORY 25 10 15 P TW 11. NETWORK LABORATORY 50 20 36 P OR 25 10 12 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 387/750. ORDN. 1 MARKS: T80058601 MANDGE SHIVSHANKAR SHIVKUMAR USHA , 71200953B , PICT , T80058601 01. OPERATING SYSTEM 100 40 31 F 100 40 41 P 02. THEORY OF COMPUTATION PΡ 100 40 60 P 03. COMPUTER NETWORK TECHNOLOGY 100 40 04. DATBASE MANAGEMENT SYSTEMS PP 46 P 100 40 40 P 05. SOFTWARE ENGINEERING PP 25 10 22 P 06. OPERATING SYSTEM DESIGN LAB. TW 50 20 31 P 07. OPERATING SYSTEM DESIGN LAB. PR 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 24 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 38 P 25 10 10. NETWORK LABORATORY TW 23 P 11. NETWORK LABORATORY 50 20 21 P 25 10 23 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 400/750. ORDN. 1 MARKS: T80058602 MANIK AGARWAL REENA , 71200956G , PICT , T80058602 01. OPERATING SYSTEM 100 40 40 P 02. THEORY OF COMPUTATION 100 40 40 P PP 03. COMPUTER NETWORK TECHNOLOGY PP 100 40 70 P 04. DATBASE MANAGEMENT SYSTEMS 100 40 46 P 05. SOFTWARE ENGINEERING 100 40 51 P PP 06. OPERATING SYSTEM DESIGN LAB. 25 10 17 P TW 50 20 25 P 07. OPERATING SYSTEM DESIGN LAB. 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 17 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 35 P 10. NETWORK LABORATORY 25 10 18 P 11. NETWORK LABORATORY OR 50 20 30 P 12. SOFT SKILLS LABORATORY TW 25 10 18 P FIRST TERM TOTAL = 407/750. ORDN. 1 MARKS:

DATE: 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 34 (522) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71200959м T80058603 MARADKAR VISHAL PRABHAKAR SUMATI , , PICT , T80058603 01. OPERATING SYSTEM 100 40 60 P 02. THEORY OF COMPUTATION 100 40 56 P 03. COMPUTER NETWORK TECHNOLOGY 100 40 59 P PP 04. DATBASE MANAGEMENT SYSTEMS 100 40 65 P 05. SOFTWARE ENGINEERING 100 40 48 P 25 10 20 P 06. OPERATING SYSTEM DESIGN LAB. TW 07. OPERATING SYSTEM DESIGN LAB. 50 20 38 P 25 10 19 P 08. INFORMATION SYSTEMS DESIGN LAB. TW 50 20 30 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 10. NETWORK LABORATORY 25 10 21 P TW 11. NETWORK LABORATORY 50 20 25 P OR 25 10 21 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 462/750. ORDN. 1 MARKS: T80058604 MOGRA ISHITA HEMANT SUNITA , 71200963K , T80058604 , PICT 01. OPERATING SYSTEM 100 40 55 P 100 40 66 P 02. THEORY OF COMPUTATION PP 100 40 55 P 03. COMPUTER NETWORK TECHNOLOGY 100 40 60 P 04. DATBASE MANAGEMENT SYSTEMS PP 100 40 05. SOFTWARE ENGINEERING PP 47 P 25 10 18 P 06. OPERATING SYSTEM DESIGN LAB. TW 50 20 42 P 07. OPERATING SYSTEM DESIGN LAB. PR 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 18 P 50 20 39 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 25 10 10. NETWORK LABORATORY TW 21 P 11. NETWORK LABORATORY 50 20 22 P 25 10 18 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 461/750. ORDN. 1 MARKS: T80058605 MOHAMMED AAQUIB ANSARI MOHAMMED YUSUF SUFIYA , 71045526н , T8058574 , PICT , т80058605 01. OPERATING SYSTEM 100 40 45 P C 02. THEORY OF COMPUTATION 100 40 40 P C 03. COMPUTER NETWORK TECHNOLOGY 100 40 45 P C PP 04. DATBASE MANAGEMENT SYSTEMS 100 40 40 P C 05. SOFTWARE ENGINEERING 100 40 40 P C PP 06. OPERATING SYSTEM DESIGN LAB. 25 10 10 P C TW 50 20 20 P 07. OPERATING SYSTEM DESIGN LAB. 25 10 10 P C 08. INFORMATION SYSTEMS DESIGN LAB. TW 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 AA F 10. NETWORK LABORATORY 25 10 10 P C 11. NETWORK LABORATORY OR 50 20 11 F 12. SOFT SKILLS LABORATORY TW 25 10 12 P C FIRST TERM TOTAL = 283/750. ORDN. 1 MARKS:

DATE: 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 35 (523) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71200964н , т80058606 T80058606 MOKASHI SUPRIYA MANIK SHARADA , PICT 01. OPERATING SYSTEM 100 40 41 P 02. THEORY OF COMPUTATION 100 40 54 P PP 100 40 57 P 03. COMPUTER NETWORK TECHNOLOGY 04. DATBASE MANAGEMENT SYSTEMS 100 40 53 P 05. SOFTWARE ENGINEERING 100 40 48 P 25 10 18 P 06. OPERATING SYSTEM DESIGN LAB. TW 07. OPERATING SYSTEM DESIGN LAB. 50 20 05 F 25 10 22 P 08. INFORMATION SYSTEMS DESIGN LAB. TW 50 20 38 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 25 10 21 P 10. NETWORK LABORATORY TW 50 20 12 F 11. NETWORK LABORATORY OR 25 10 18 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 387/750. ORDN. 1 MARKS: T80058607 MULAY AMIT YESHWANT , т80058654 , **SNEHAL** , 71045530F PICT , т80058607 100 40 40 P C 100 40 40 P C 01. OPERATING SYSTEM 13. SYSTEM SOFTWARE PROGRAMMING 100 40 52 P C 100 40 02. THEORY OF COMPUTATION PP 14. MANAGEMENT INFORMATION SYSTEMS PP 67 P C 100 40 58 P C 100 40 50 P C 03. COMPUTER NETWORK TECHNOLOGY 15. PROGRAMMING PARADIGMS 100 40 100 40 50 P C 04. DATBASE MANAGEMENT SYSTEMS PP 58 P C 16. DESIGN & ANALYSIS OF ALGORITHMS PP 100 40 05. SOFTWARE ENGINEERING 46 P C 17. HUMAN COMPU.INTERACTION & USABI. PP 100 40 46 P C PP 25 10 15 P C 06. OPERATING SYSTEM DESIGN LAB. TW 18. SOFTWARE DESIGN LABORATORY 50 20 20 P C 50 20 23 P C 50 20 07. OPERATING SYSTEM DESIGN LAB. PR 19. SOFTWARE DESIGN LABORATORY AA F 50 20 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 15 P C 20. SOFTWARE DEVELOPMENT TOOLS LAB. TW 21 P C 50 20 20 P C 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 20 P C 21. SOFTWARE DEVELOPMENT TOOLS LAB. OR 50 20 20 P C 10. NETWORK LABORATORY TW 25 10 14 P C 22. SEMINAR AND TECHNICAL COMMUN. TW 11. NETWORK LABORATORY 50 20 23 P C 25 10 10 P C 12. SOFT SKILLS LABORATORY TW GRAND TOTAL = 708/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS: T80058608 MULE MANJUSHA ASHOK **SUVARNA** , 71200968L , T80058608 , PICT 01. OPERATING SYSTEM 100 40 56 P 02. THEORY OF COMPUTATION 100 40 67 P PP 03. COMPUTER NETWORK TECHNOLOGY 100 40 72 P PP 04. DATBASE MANAGEMENT SYSTEMS 100 40 63 P 05. SOFTWARE ENGINEERING 100 40 50 P PP 06. OPERATING SYSTEM DESIGN LAB. 25 10 20 P TW 50 20 38 P 07. OPERATING SYSTEM DESIGN LAB. 25 10 23 P 08. INFORMATION SYSTEMS DESIGN LAB. TW 50 20 30 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 10. NETWORK LABORATORY 25 10 22 P 11. NETWORK LABORATORY OR 50 20 39 P 12. SOFT SKILLS LABORATORY TW 25 10 20 P FIRST TERM TOTAL = 500/750. ORDN. 1 MARKS:

DATE: 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 36 (524) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER SADHANA , 71100903в T80058609 MUNOT SHRENIK SUMTILAL , , PICT , T80058609 01. OPERATING SYSTEM 100 40 43 P PP 02. THEORY OF COMPUTATION 100 40 26 F 03. COMPUTER NETWORK TECHNOLOGY 100 40 44 P PP 04. DATBASE MANAGEMENT SYSTEMS 100 40 41 P 05. SOFTWARE ENGINEERING 100 40 24 F 25 10 21 P 06. OPERATING SYSTEM DESIGN LAB. TW 25 P 07. OPERATING SYSTEM DESIGN LAB. 50 20 25 10 22 P 08. INFORMATION SYSTEMS DESIGN LAB. TW 50 20 42 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 22 P 10. NETWORK LABORATORY 25 10 TW 11. NETWORK LABORATORY 50 20 25 P OR 25 10 20 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 355/750. ORDN. 1 MARKS: T80058610 NAIK PRIYANKA MANOJ KAVITA , 71200977K , PICT , T80058610 01. OPERATING SYSTEM 100 40 55 P 100 40 54 P 02. THEORY OF COMPUTATION PP 100 40 62 P 03. COMPUTER NETWORK TECHNOLOGY 100 40 60 P 04. DATBASE MANAGEMENT SYSTEMS PP 100 40 49 P 05. SOFTWARE ENGINEERING PP 25 10 22 P 06. OPERATING SYSTEM DESIGN LAB. TW 50 20 07. OPERATING SYSTEM DESIGN LAB. PR 43 P 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 23 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 30 P 25 10 10. NETWORK LABORATORY TW 24 P 11. NETWORK LABORATORY 50 20 44 P 25 10 23 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 489/750. ORDN. 1 MARKS: T80058611 NAIK SHIVANI SHRIKANT **RADHIKA** , 71200978н , PICT , T80058611 01. OPERATING SYSTEM 100 40 47 P 02. THEORY OF COMPUTATION 100 40 77 P PP 03. COMPUTER NETWORK TECHNOLOGY 100 40 68 P PP 62 P 04. DATBASE MANAGEMENT SYSTEMS 100 40 05. SOFTWARE ENGINEERING 100 40 53 P PP 06. OPERATING SYSTEM DESIGN LAB. 25 10 18 P TW 42 P 50 20 07. OPERATING SYSTEM DESIGN LAB. 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 19 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 44 P 10. NETWORK LABORATORY 25 10 20 P 11. NETWORK LABORATORY OR 50 20 42 P 12. SOFT SKILLS LABORATORY TW 25 10 16 P FIRST TERM TOTAL = 508/750. ORDN. 1 MARKS:

DATE: 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 37 (525) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71200980к T80058612 NATU SWAPNIL RAVINDRA , , PICT , T80058612 ASHA 01. OPERATING SYSTEM 100 40 60 P 02. THEORY OF COMPUTATION 100 40 65 P 03. COMPUTER NETWORK TECHNOLOGY 100 40 78 P PP 04. DATBASE MANAGEMENT SYSTEMS 100 40 68 P 05. SOFTWARE ENGINEERING 100 40 52 P 25 10 24 P 06. OPERATING SYSTEM DESIGN LAB. TW 07. OPERATING SYSTEM DESIGN LAB. 50 20 44 P 25 10 23 P 08. INFORMATION SYSTEMS DESIGN LAB. TW 50 20 45 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 10. NETWORK LABORATORY 25 10 22 P TW 11. NETWORK LABORATORY 50 20 24 P OR 25 10 23 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 528/750. ORDN. 1 MARKS: T80058613 NAVGIRE SAGAR PRADEEP SUNETRA , 71200981H , PICT , T80058613 01. OPERATING SYSTEM 100 40 65 P 100 40 02. THEORY OF COMPUTATION PP 43 P 03. COMPUTER NETWORK TECHNOLOGY 100 40 79 P 100 40 04. DATBASE MANAGEMENT SYSTEMS PP 58 P 100 40 50 P 05. SOFTWARE ENGINEERING PP 25 10 19 P 06. OPERATING SYSTEM DESIGN LAB. TW 50 20 40 P 07. OPERATING SYSTEM DESIGN LAB. PR 25 10 08. INFORMATION SYSTEMS DESIGN LAB. TW 20 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 42 P 25 10 21 P 10. NETWORK LABORATORY TW 50 20 11. NETWORK LABORATORY 36 P 25 10 20 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 493/750. ORDN. 1 MARKS: SUNITA T80058614 NEHE ABHISHEK ASHOK , 71350929F , PICT , T80058614 01. OPERATING SYSTEM 100 40 70 P 02. THEORY OF COMPUTATION 100 40 56 P PP 03. COMPUTER NETWORK TECHNOLOGY 100 40 87 P PP 04. DATBASE MANAGEMENT SYSTEMS 100 40 68 P 05. SOFTWARE ENGINEERING 100 40 43 P PP 06. OPERATING SYSTEM DESIGN LAB. 25 10 23 P TW 50 20 28 P 07. OPERATING SYSTEM DESIGN LAB. 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 24 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 44 P 10. NETWORK LABORATORY 25 10 24 P 11. NETWORK LABORATORY OR 50 20 39 P 12. SOFT SKILLS LABORATORY TW 25 10 22 P FIRST TERM TOTAL = 528/750. ORDN. 1 MARKS:

DATE: 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 38 (526) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71200986J T80058615 NEWALKAR REVA VIVEK SMITA , , PICT , T80058615 01. OPERATING SYSTEM 100 40 53 P 02. THEORY OF COMPUTATION 100 40 54 P 03. COMPUTER NETWORK TECHNOLOGY 100 40 70 P PP 04. DATBASE MANAGEMENT SYSTEMS 100 40 70 P 05. SOFTWARE ENGINEERING 100 40 53 P 25 10 22 P 06. OPERATING SYSTEM DESIGN LAB. TW 07. OPERATING SYSTEM DESIGN LAB. 50 20 40 P 25 10 22 P 08. INFORMATION SYSTEMS DESIGN LAB. TW 50 20 25 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 10. NETWORK LABORATORY 25 10 22 P TW 11. NETWORK LABORATORY 50 20 23 P OR 25 10 23 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 477/750. ORDN. 1 MARKS: T80058616 OSWAL SAHIT SURESH SUMITRA , 71200992C , PICT , T80058616 100 40 01. OPERATING SYSTEM 61 P 100 40 57 P 02. THEORY OF COMPUTATION PP 100 40 85 P 03. COMPUTER NETWORK TECHNOLOGY 100 40 04. DATBASE MANAGEMENT SYSTEMS PP 65 P 100 40 57 P 05. SOFTWARE ENGINEERING PP 25 10 21 P 06. OPERATING SYSTEM DESIGN LAB. TW 50 20 07. OPERATING SYSTEM DESIGN LAB. PR 38 P 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 22 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 41 P 25 10 10. NETWORK LABORATORY TW 23 P 11. NETWORK LABORATORY 50 20 25 P 25 10 21 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 516/750. ORDN. 1 MARKS: T80058617 PABALKAR SARANG SHIVNATH VIDYA , 71200993м , PICT , T80058617 01. OPERATING SYSTEM 100 40 40 P 02. THEORY OF COMPUTATION 100 40 50 P PP 03. COMPUTER NETWORK TECHNOLOGY 100 40 65 P PP 57 P 04. DATBASE MANAGEMENT SYSTEMS 100 40 05. SOFTWARE ENGINEERING 100 40 40 P PP 06. OPERATING SYSTEM DESIGN LAB. 25 10 20 P TW 50 20 38 P 07. OPERATING SYSTEM DESIGN LAB. 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 21 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 36 P 10. NETWORK LABORATORY 25 10 21 P 11. NETWORK LABORATORY OR 50 20 28 P 12. SOFT SKILLS LABORATORY TW 25 10 20 P FIRST TERM TOTAL = 436/750. ORDN. 1 MARKS:

DATE: 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 39 (527) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71200994к T80058618 PADIA SARVESH LALIT SEEMA , , PICT , T80058618 01. OPERATING SYSTEM 100 40 46 P 02. THEORY OF COMPUTATION 100 40 68 P 03. COMPUTER NETWORK TECHNOLOGY 100 40 83 P PP 04. DATBASE MANAGEMENT SYSTEMS 100 40 60 P 05. SOFTWARE ENGINEERING 100 40 53 P 25 10 22 P 06. OPERATING SYSTEM DESIGN LAB. TW 07. OPERATING SYSTEM DESIGN LAB. 50 20 45 P 25 10 22 P 08. INFORMATION SYSTEMS DESIGN LAB. TW 50 20 45 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 10. NETWORK LABORATORY 25 10 23 P TW 11. NETWORK LABORATORY 50 20 32 P OR 25 10 22 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 521/750. ORDN. 1 MARKS: T80058619 PALLOD HRISHIKESH RAMNARAYAN VANDANA , 71200996F , PICT , T80058619 100 40 59 P 01. OPERATING SYSTEM 100 40 71 P 02. THEORY OF COMPUTATION PΡ 100 40 84 P 03. COMPUTER NETWORK TECHNOLOGY 100 40 04. DATBASE MANAGEMENT SYSTEMS PP 68 P 100 40 53 P 05. SOFTWARE ENGINEERING PP 25 10 22 P 06. OPERATING SYSTEM DESIGN LAB. TW 50 20 07. OPERATING SYSTEM DESIGN LAB. PR 45 P 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 24 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 47 P 25 10 10. NETWORK LABORATORY TW 24 P 11. NETWORK LABORATORY 50 20 40 P 25 10 22 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 559/750. ORDN. 1 MARKS: T80058620 PANCHARIYA ANIKET SANJAY LEENA , 71200997D , PICT , T80058620 01. OPERATING SYSTEM 100 40 61 P 02. THEORY OF COMPUTATION 100 40 62 P PP 03. COMPUTER NETWORK TECHNOLOGY 100 40 86 P PP 62 P 04. DATBASE MANAGEMENT SYSTEMS 100 40 05. SOFTWARE ENGINEERING 100 40 61 P PP 06. OPERATING SYSTEM DESIGN LAB. 25 10 19 P TW 50 20 43 P 07. OPERATING SYSTEM DESIGN LAB. 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 20 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 44 P 10. NETWORK LABORATORY 25 10 20 P 11. NETWORK LABORATORY OR 50 20 34 P 12. SOFT SKILLS LABORATORY TW 25 10 18 P FIRST TERM TOTAL = 530/750. ORDN. 1 MARKS:

DATE: 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 40 (528) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71201001H , , PICT , T80058621 T80058621 PARAKH NAMAN RAJENDRA SANGEETA 01. OPERATING SYSTEM 100 40 52 P 02. THEORY OF COMPUTATION 100 40 56 P 03. COMPUTER NETWORK TECHNOLOGY 100 40 54 P PP 04. DATBASE MANAGEMENT SYSTEMS 100 40 56 P 05. SOFTWARE ENGINEERING 100 40 67 P 25 10 20 P 06. OPERATING SYSTEM DESIGN LAB. TW 07. OPERATING SYSTEM DESIGN LAB. 50 20 38 P 25 10 22 P 08. INFORMATION SYSTEMS DESIGN LAB. TW 50 20 09. INFORMATION SYSTEMS DESIGN LAB. OR 43 P 10. NETWORK LABORATORY 25 10 21 P TW 11. NETWORK LABORATORY 50 20 33 P OR 25 10 20 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 482/750. ORDN. 1 MARKS: T80058622 PARINITA MATHARU RENU , 71201003D , PICT , T80058622 100 40 55 P 01. OPERATING SYSTEM 100 40 55 P 02. THEORY OF COMPUTATION PP 100 40 58 P 03. COMPUTER NETWORK TECHNOLOGY 100 40 54 P 04. DATBASE MANAGEMENT SYSTEMS PP 100 40 49 P 05. SOFTWARE ENGINEERING PP 25 10 16 P 06. OPERATING SYSTEM DESIGN LAB. TW 50 20 28 P 07. OPERATING SYSTEM DESIGN LAB. PR 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 18 P 50 20 38 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 25 10 10. NETWORK LABORATORY TW 16 P 11. NETWORK LABORATORY 50 20 33 P 25 10 16 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 436/750. ORDN. 1 MARKS: T80058623 PATADE PRATIK RAMDAS SEEMA , 71350930к , PICT , T80058623 01. OPERATING SYSTEM 100 40 62 P 02. THEORY OF COMPUTATION 100 40 47 P PP 03. COMPUTER NETWORK TECHNOLOGY 100 40 66 P PP 59 P 04. DATBASE MANAGEMENT SYSTEMS 100 40 05. SOFTWARE ENGINEERING 100 40 50 P PP 06. OPERATING SYSTEM DESIGN LAB. 25 10 18 P TW 32 P 50 20 07. OPERATING SYSTEM DESIGN LAB. 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 21 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 35 P 10. NETWORK LABORATORY 25 10 21 P 11. NETWORK LABORATORY OR 50 20 27 P 12. SOFT SKILLS LABORATORY TW 25 10 19 P FIRST TERM TOTAL = 457/750. ORDN. 1 MARKS:

DATE : 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 41 (529) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , T80058588 , PICT JYOTI , 71100938E T80058624 PATIL AMOL MADHUKAR , т80058624 01. OPERATING SYSTEM 100 40 54 P C 13. SYSTEM SOFTWARE PROGRAMMING 100 40 51 P C PΡ 02. THEORY OF COMPUTATION 100 40 44 P C 14. MANAGEMENT INFORMATION SYSTEMS PP 100 40 43 P C 100 40 45 P C 100 40 42 P C 03. COMPUTER NETWORK TECHNOLOGY PP 15. PROGRAMMING PARADIGMS 04. DATBASE MANAGEMENT SYSTEMS 100 40 45 P C 100 40 44 P C PP 16. DESIGN & ANALYSIS OF ALGORITHMS PP 05. SOFTWARE ENGINEERING 100 40 52 P C 17. HUMAN COMPU.INTERACTION & USABI. PP 100 40 52 P C 25 10 14 P C 50 20 29 P C 06. OPERATING SYSTEM DESIGN LAB. 18. SOFTWARE DESIGN LABORATORY TW 07. OPERATING SYSTEM DESIGN LAB. 50 20 32 P C 50 20 36 P C PR19. SOFTWARE DESIGN LABORATORY 25 10 18 P C 50 20 30 P C 08. INFORMATION SYSTEMS DESIGN LAB. TW 20. SOFTWARE DEVELOPMENT TOOLS LAB. TW 50 20 32 P 50 20 38 P C 09. INFORMATION SYSTEMS DESIGN LAB. OR 21. SOFTWARE DEVELOPMENT TOOLS LAB. OR 25 10 12 P C 22. SEMINAR AND TECHNICAL COMMUN. TW 50 20 23 P C 10. NETWORK LABORATORY TW 11. NETWORK LABORATORY 50 20 29 P C OR 25 10 12 P C 12. SOFT SKILLS LABORATORY TW GRAND TOTAL = 777/1500, RESULT: SECOND CLASS ORDN. 1 MARKS: T80058625 PATIL DEEPAK RAMESH MINAKSHEE , 71201005L , T80058625 , PICT 01. OPERATING SYSTEM 100 40 49 P 100 40 54 P 02. THEORY OF COMPUTATION PP 100 40 65 P 03. COMPUTER NETWORK TECHNOLOGY 100 40 04. DATBASE MANAGEMENT SYSTEMS PP 67 P 100 40 05. SOFTWARE ENGINEERING 46 P PP 25 10 21 P 06. OPERATING SYSTEM DESIGN LAB. TW 50 20 40 P 07. OPERATING SYSTEM DESIGN LAB. PR 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 22 P 50 20 42 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 25 10 10. NETWORK LABORATORY TW 22 P 11. NETWORK LABORATORY 50 20 32 P 25 10 23 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 483/750. ORDN. 1 MARKS: T80058626 PATIL INDRAJEET ANIL PRIYA , 71201006J , T80058626 , PICT 01. OPERATING SYSTEM 100 40 52 P 02. THEORY OF COMPUTATION 100 40 40 P PP 03. COMPUTER NETWORK TECHNOLOGY 100 40 49 P PP 04. DATBASE MANAGEMENT SYSTEMS 100 40 49 P 05. SOFTWARE ENGINEERING 100 40 46 P PP 25 10 06. OPERATING SYSTEM DESIGN LAB. 21 P TW 50 20 20 P 07. OPERATING SYSTEM DESIGN LAB. 25 10 23 P 08. INFORMATION SYSTEMS DESIGN LAB. TW 50 20 45 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 10. NETWORK LABORATORY 25 10 22 P 11. NETWORK LABORATORY OR 50 20 41 P 12. SOFT SKILLS LABORATORY TW 25 10 21 P FIRST TERM TOTAL = 429/750. ORDN. 1 MARKS:

UNIVERSITY OF PUNE ,T.E. (2008 PAT) (INFORMATION TECHNOLOGY) EXAMINATION NOV. 2013 DATE: 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 42 (530) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71350931H , , , PICT , T80058627 T80058627 PATIL MAYUR PAVANKUMAR MANDAKINI 01. OPERATING SYSTEM 100 40 60 P 02. THEORY OF COMPUTATION 100 40 52 P 100 40 69 P 03. COMPUTER NETWORK TECHNOLOGY PP 04. DATBASE MANAGEMENT SYSTEMS 100 40 61 P 05. SOFTWARE ENGINEERING 100 40 53 P 25 10 16 P 06. OPERATING SYSTEM DESIGN LAB. TW 07. OPERATING SYSTEM DESIGN LAB. 50 20 35 P PR 25 10 19 P 08. INFORMATION SYSTEMS DESIGN LAB. TW 50 20 42 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 25 10 18 P 10. NETWORK LABORATORY TW 50 20 37 P 11. NETWORK LABORATORY OR 25 10 16 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 478/750. ORDN. 1 MARKS: T80058628 PATKE SWATI SANJAY **SUNITA** , 71201011E , PICT , T80058628 01. OPERATING SYSTEM 100 40 40 P 100 40 40 P 02. THEORY OF COMPUTATION PP 100 40 45 P 03. COMPUTER NETWORK TECHNOLOGY 100 40 51 P 04. DATBASE MANAGEMENT SYSTEMS PP 100 40 40 P 05. SOFTWARE ENGINEERING PP 25 10 19 P 06. OPERATING SYSTEM DESIGN LAB. TW 50 20 07. OPERATING SYSTEM DESIGN LAB. PR 42 P 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 19 P 50 20 37 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 25 10 10. NETWORK LABORATORY TW 21 P 11. NETWORK LABORATORY 50 20 34 P OR 25 10 19 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 407/750. ORDN. 1 MARKS: T80058629 PAWAR CHANDAN DILIP REKHA , 70925555B , T80058592 , PICT , т80058629 01. OPERATING SYSTEM 100 40 72 P C 100 40 68 P C 13. SYSTEM SOFTWARE PROGRAMMING 02. THEORY OF COMPUTATION 100 40 43 P C 14. MANAGEMENT INFORMATION SYSTEMS PP 100 40 63 P C PP 03. COMPUTER NETWORK TECHNOLOGY PP 100 40 61 P C 100 40 57 P C 15. PROGRAMMING PARADIGMS 52 P C 04. DATBASE MANAGEMENT SYSTEMS 100 40 100 40 54 P C 16. DESIGN & ANALYSIS OF ALGORITHMS PP 05. SOFTWARE ENGINEERING 100 40 57 P C 17. HUMAN COMPU.INTERACTION & USABI. PP 100 40 69 P C PP 06. OPERATING SYSTEM DESIGN LAB. 25 10 16 P C 18. SOFTWARE DESIGN LABORATORY TW 50 20 25 P C TW 36 P 50 20 25 P C 50 20 07. OPERATING SYSTEM DESIGN LAB. 19. SOFTWARE DESIGN LABORATORY 25 10 17 P C 50 20 35 P C 08. INFORMATION SYSTEMS DESIGN LAB. TW 20. SOFTWARE DEVELOPMENT TOOLS LAB. TW 39 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 25 P C 50 20 21. SOFTWARE DEVELOPMENT TOOLS LAB. OR 10. NETWORK LABORATORY 25 10 14 P C 22. SEMINAR AND TECHNICAL COMMUN. TW 50 20 27 P C 11. NETWORK LABORATORY OR 50 20 37 P C 12. SOFT SKILLS LABORATORY 25 10 13 P C TW GRAND TOTAL = 905/1500, RESULT: FIRST CLASS ORDN. 1 MARKS:

DATE: 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 43 (531) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71350932F , т80058630 T80058630 PAWAR KAVITA SANDIPAN SHEVANTA , PICT 01. OPERATING SYSTEM 100 40 31 F 02. THEORY OF COMPUTATION 100 40 AA F PP 100 40 40 P 03. COMPUTER NETWORK TECHNOLOGY 04. DATBASE MANAGEMENT SYSTEMS 100 40 41 P 05. SOFTWARE ENGINEERING 100 40 33 F 25 10 15 P 06. OPERATING SYSTEM DESIGN LAB. TW 07. OPERATING SYSTEM DESIGN LAB. 50 20 05 F PR 25 10 17 P 08. INFORMATION SYSTEMS DESIGN LAB. TW 50 20 09. INFORMATION SYSTEMS DESIGN LAB. OR 12 F 25 10 17 P 10. NETWORK LABORATORY TW 50 20 28 P 11. NETWORK LABORATORY OR 25 10 13 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 252/750. ORDN. 1 MARKS: T80058631 PAWAR PRASAD GORAKSH , т80058593 , MEENAKSHI , 71100951B PICT , т80058631 42 P C 100 40 40 P C 01. OPERATING SYSTEM 100 40 13. SYSTEM SOFTWARE PROGRAMMING 40 P C 100 40 100 40 42 P 02. THEORY OF COMPUTATION PP 14. MANAGEMENT INFORMATION SYSTEMS PP 100 40 100 40 03. COMPUTER NETWORK TECHNOLOGY 40 P C 15. PROGRAMMING PARADIGMS 47 P 100 40 100 40 04. DATBASE MANAGEMENT SYSTEMS PP 40 P C 16. DESIGN & ANALYSIS OF ALGORITHMS PP 47 P C 100 40 40 P C 100 40 05. SOFTWARE ENGINEERING 17. HUMAN COMPU.INTERACTION & USABI. PP 40 P C PP 25 10 27 P C 06. OPERATING SYSTEM DESIGN LAB. TW 18 P C 18. SOFTWARE DESIGN LABORATORY 50 20 50 20 33 P C 50 20 25 P C 07. OPERATING SYSTEM DESIGN LAB. PR 19. SOFTWARE DESIGN LABORATORY 50 20 37 P C 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 15 P C 20. SOFTWARE DEVELOPMENT TOOLS LAB. TW 50 20 50 20 09. INFORMATION SYSTEMS DESIGN LAB. OR 37 P C 21. SOFTWARE DEVELOPMENT TOOLS LAB. OR 28 P C 50 20 36 P C 10. NETWORK LABORATORY TW 25 10 12 P C 22. SEMINAR AND TECHNICAL COMMUN. TW 11. NETWORK LABORATORY 50 20 38 P C 25 10 12 P C 12. SOFT SKILLS LABORATORY GRAND TOTAL = 736/1500, RESULT: PASS CLASS ORDN. 1 MARKS: T80058632 PHADNIS SHARWARI SADANAND SUPRIYA , 71201014K , PICT , T80058632 01. OPERATING SYSTEM 100 40 48 P 02. THEORY OF COMPUTATION 100 40 60 P PP 03. COMPUTER NETWORK TECHNOLOGY 100 40 72 P PP 04. DATBASE MANAGEMENT SYSTEMS 100 40 53 P 05. SOFTWARE ENGINEERING 100 40 56 P PP 06. OPERATING SYSTEM DESIGN LAB. 25 10 19 P TW 50 20 30 P 07. OPERATING SYSTEM DESIGN LAB. 25 10 20 P 08. INFORMATION SYSTEMS DESIGN LAB. TW 50 20 35 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 10. NETWORK LABORATORY 25 10 21 P 11. NETWORK LABORATORY OR 50 20 38 P 12. SOFT SKILLS LABORATORY TW 25 10 19 P FIRST TERM TOTAL = 471/750. ORDN. 1 MARKS:

DATE: 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 44 (532) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71201016F T80058633 PILAJI EKTA BALAJI AUUJA , , PICT , T80058633 01. OPERATING SYSTEM 100 40 44 P 02. THEORY OF COMPUTATION 100 40 59 P 03. COMPUTER NETWORK TECHNOLOGY 100 40 53 P PP 04. DATBASE MANAGEMENT SYSTEMS 100 40 53 P 05. SOFTWARE ENGINEERING 100 40 50 P 25 10 21 P 06. OPERATING SYSTEM DESIGN LAB. TW 07. OPERATING SYSTEM DESIGN LAB. 50 20 40 P 25 10 21 P 08. INFORMATION SYSTEMS DESIGN LAB. TW 50 20 30 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 10. NETWORK LABORATORY 25 10 23 P TW 11. NETWORK LABORATORY 50 20 38 P OR 25 10 22 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 454/750. ORDN. 1 MARKS: T80058634 POHANKAR RASIKA AVINASH ANJALI , 71201017D , PICT , T80058634 100 40 63 P 01. OPERATING SYSTEM 100 40 02. THEORY OF COMPUTATION PP 65 P 100 40 71 P 03. COMPUTER NETWORK TECHNOLOGY 100 40 71 P 04. DATBASE MANAGEMENT SYSTEMS PP 100 40 67 P 05. SOFTWARE ENGINEERING PP 25 10 06. OPERATING SYSTEM DESIGN LAB. TW 24 P 50 20 07. OPERATING SYSTEM DESIGN LAB. PR 45 P 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 24 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 42 P 25 10 10. NETWORK LABORATORY TW 24 P 11. NETWORK LABORATORY 50 20 47 P OR 25 10 24 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 567/750. ORDN. 1 MARKS: T80058635 POTDUKHE VAISHNAVI NILKANTH SANGEETA , 71201018в , PICT , T80058635 01. OPERATING SYSTEM 100 40 45 P 02. THEORY OF COMPUTATION 100 40 40 P PP 03. COMPUTER NETWORK TECHNOLOGY 100 40 40 P PP 100 40 40 P 04. DATBASE MANAGEMENT SYSTEMS 05. SOFTWARE ENGINEERING 100 40 32 F PP 06. OPERATING SYSTEM DESIGN LAB. 25 10 22 P TW 32 P 50 20 07. OPERATING SYSTEM DESIGN LAB. 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 22 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 34 P 10. NETWORK LABORATORY 25 10 22 P 11. NETWORK LABORATORY OR 50 20 21 P 12. SOFT SKILLS LABORATORY TW 25 10 23 P FIRST TERM TOTAL = 373/750. ORDN. 1 MARKS:

DATE: 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 45 (533) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71201019L T80058636 POTNIS ISHA RAHUL SUVARNA , , PICT , T80058636 01. OPERATING SYSTEM 100 40 59 P 02. THEORY OF COMPUTATION 100 40 70 P 03. COMPUTER NETWORK TECHNOLOGY 100 40 64 P PP 04. DATBASE MANAGEMENT SYSTEMS 100 40 63 P 05. SOFTWARE ENGINEERING 100 40 58 P 25 10 22 P 06. OPERATING SYSTEM DESIGN LAB. TW 07. OPERATING SYSTEM DESIGN LAB. 50 20 40 P 25 10 23 P 08. INFORMATION SYSTEMS DESIGN LAB. TW 50 20 09. INFORMATION SYSTEMS DESIGN LAB. OR 41 P 10. NETWORK LABORATORY 25 10 24 P TW 11. NETWORK LABORATORY 50 20 41 P OR 25 10 23 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 528/750. ORDN. 1 MARKS: T80058637 PRASHUL SINGH ARCHANA , 71201022L , PICT , T80058637 100 40 56 P 01. OPERATING SYSTEM 100 40 58 P 02. THEORY OF COMPUTATION PP 100 40 63 P 03. COMPUTER NETWORK TECHNOLOGY 100 40 52 P 04. DATBASE MANAGEMENT SYSTEMS PP 100 40 46 P 05. SOFTWARE ENGINEERING PP 25 10 21 P 06. OPERATING SYSTEM DESIGN LAB. TW 50 20 42 P 07. OPERATING SYSTEM DESIGN LAB. PR 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 21 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 32 P 25 10 10. NETWORK LABORATORY TW 23 P 11. NETWORK LABORATORY 50 20 41 P 25 10 22 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 477/750. ORDN. 1 MARKS: T80058638 PRERIT VILAS AUTI INDU , 71201023J , PICT , T80058638 01. OPERATING SYSTEM 100 40 46 P 02. THEORY OF COMPUTATION 100 40 62 P PP 03. COMPUTER NETWORK TECHNOLOGY 100 40 63 P PP 04. DATBASE MANAGEMENT SYSTEMS 100 40 68 P 05. SOFTWARE ENGINEERING 100 40 55 P PP 06. OPERATING SYSTEM DESIGN LAB. 25 10 20 P TW 32 P 50 20 07. OPERATING SYSTEM DESIGN LAB. 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 21 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 38 P 10. NETWORK LABORATORY 25 10 23 P 11. NETWORK LABORATORY OR 50 20 35 P 12. SOFT SKILLS LABORATORY TW 25 10 19 P FIRST TERM TOTAL = 482/750. ORDN. 1 MARKS:

DATE: 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 46 (534)

NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71201029H , , PICT , T80058639 T80058639 RAHUL KUMAR USHA 01. OPERATING SYSTEM 100 40 44 P 02. THEORY OF COMPUTATION 100 40 40 P 100 40 60 P 03. COMPUTER NETWORK TECHNOLOGY PР 04. DATBASE MANAGEMENT SYSTEMS 100 40 53 P PP 05. SOFTWARE ENGINEERING 100 40 29 F 25 10 19 P 06. OPERATING SYSTEM DESIGN LAB. TW 07. OPERATING SYSTEM DESIGN LAB. 50 20 35 P PR 25 10 20 P 08. INFORMATION SYSTEMS DESIGN LAB. TW 50 20 32 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 25 10 20 P 10. NETWORK LABORATORY TW 50 20 13 F 11. NETWORK LABORATORY OR 25 10 21 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 386/750. ORDN. 1 MARKS: T80058640 RAHUL RANJAN , т80058678 , , т80058640 KUMKUM , 70701594E PICT 46 P C 100 40 40 P C 01. OPERATING SYSTEM 100 40 13. SYSTEM SOFTWARE PROGRAMMING 02. THEORY OF COMPUTATION 40 P C 100 40 100 40 PP 14. MANAGEMENT INFORMATION SYSTEMS PP AA F 100 40 100 40 03. COMPUTER NETWORK TECHNOLOGY 42 P C 15. PROGRAMMING PARADIGMS 40 P C 100 40 100 40 40 P C 04. DATBASE MANAGEMENT SYSTEMS PP 41 P C 16. DESIGN & ANALYSIS OF ALGORITHMS PP 100 40 100 40 05. SOFTWARE ENGINEERING AA F 17. HUMAN COMPU.INTERACTION & USABI. PP 44 P C PP 25 10 06. OPERATING SYSTEM DESIGN LAB. TW 10 P C 18. SOFTWARE DESIGN LABORATORY 50 20 20 P C 50 20 29 P C 50 20 22 P C 07. OPERATING SYSTEM DESIGN LAB. PR 19. SOFTWARE DESIGN LABORATORY 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 10 P C 20. SOFTWARE DEVELOPMENT TOOLS LAB. TW 50 20 20 P C 23 P C 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 21. SOFTWARE DEVELOPMENT TOOLS LAB. OR 50 20 20 P C 50 20 20 P C 10. NETWORK LABORATORY TW 25 10 10 P C 22. SEMINAR AND TECHNICAL COMMUN. TW 11. NETWORK LABORATORY 50 20 20 P C 25 10 10 P C 12. SOFT SKILLS LABORATORY TW GRAND TOTAL = 547/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS: T80058641 RAHUL SHARMA **PROMILA** , 71100963F , T80058599 , PICT , т80058641 01. OPERATING SYSTEM 100 40 59 P C 100 40 57 P C 13. SYSTEM SOFTWARE PROGRAMMING 02. THEORY OF COMPUTATION 100 40 66 P C 14. MANAGEMENT INFORMATION SYSTEMS PP 100 40 62 P C PP PP 100 40 46 P C 100 40 49 P C 03. COMPUTER NETWORK TECHNOLOGY 15. PROGRAMMING PARADIGMS 100 40 51 P C 100 40 50 P C 04. DATBASE MANAGEMENT SYSTEMS 16. DESIGN & ANALYSIS OF ALGORITHMS PP 05. SOFTWARE ENGINEERING 100 40 43 P C 17. HUMAN COMPU.INTERACTION & USABI. PP 100 40 60 P C PP 06. OPERATING SYSTEM DESIGN LAB. 25 10 12 P C 18. SOFTWARE DESIGN LABORATORY TW 50 20 20 P C TW 50 20 36 P C 50 20 25 P C 07. OPERATING SYSTEM DESIGN LAB. 19. SOFTWARE DESIGN LABORATORY 25 10 11 P C 50 20 31 P C 08. INFORMATION SYSTEMS DESIGN LAB. TW 20. SOFTWARE DEVELOPMENT TOOLS LAB. TW 50 20 28 P C 50 20 22 P C 09. INFORMATION SYSTEMS DESIGN LAB. OR 21. SOFTWARE DEVELOPMENT TOOLS LAB. OR 10. NETWORK LABORATORY 25 10 13 P C 22. SEMINAR AND TECHNICAL COMMUN. TW 50 20 22 P C 11. NETWORK LABORATORY OR 50 20 14 F 12. SOFT SKILLS LABORATORY 25 10 12 P C TW GRAND TOTAL = 789/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS:

DATE: 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 47 (535) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71350933D T80058642 RANSHEVRE ROHIT YASHWANT USHA , , PICT , T80058642 01. OPERATING SYSTEM 100 40 40 P PP 02. THEORY OF COMPUTATION 100 40 40 P 03. COMPUTER NETWORK TECHNOLOGY 100 40 57 P PP 04. DATBASE MANAGEMENT SYSTEMS 100 40 63 P 05. SOFTWARE ENGINEERING 100 40 44 P 25 10 16 P 06. OPERATING SYSTEM DESIGN LAB. TW 07. OPERATING SYSTEM DESIGN LAB. 50 20 AA F 25 10 11 P 08. INFORMATION SYSTEMS DESIGN LAB. TW 50 20 39 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 10. NETWORK LABORATORY 25 10 20 P TW 11. NETWORK LABORATORY 50 20 30 P OR 25 10 15 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 375/750. ORDN. 1 MARKS: T80058643 RASKAR SUNITA ASHOK SUSHILA , 71350934в , PICT , T80058643 01. OPERATING SYSTEM 100 40 49 P 100 40 47 P 02. THEORY OF COMPUTATION PP 100 40 49 P 03. COMPUTER NETWORK TECHNOLOGY 100 40 04. DATBASE MANAGEMENT SYSTEMS PP 53 P 100 40 49 P 05. SOFTWARE ENGINEERING PP 25 10 21 P 06. OPERATING SYSTEM DESIGN LAB. TW 50 20 30 P 07. OPERATING SYSTEM DESIGN LAB. PR 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 22 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 38 P 25 10 10. NETWORK LABORATORY TW 22 P 11. NETWORK LABORATORY 50 20 42 P 25 10 18 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 440/750. ORDN. 1 MARKS: T80058644 RATHI TUSHAR SANDEEP SANGITA , 71201033F , PICT , T80058644 01. OPERATING SYSTEM 100 40 50 P 02. THEORY OF COMPUTATION 100 40 46 P PP 03. COMPUTER NETWORK TECHNOLOGY 100 40 50 P PP 04. DATBASE MANAGEMENT SYSTEMS 100 40 47 P 05. SOFTWARE ENGINEERING 100 40 40 P PP 06. OPERATING SYSTEM DESIGN LAB. 25 10 18 P TW 50 20 38 P 07. OPERATING SYSTEM DESIGN LAB. 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 17 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 33 P 10. NETWORK LABORATORY 25 10 23 P 11. NETWORK LABORATORY OR 50 20 28 P 12. SOFT SKILLS LABORATORY TW 25 10 17 P FIRST TERM TOTAL = 407/750. ORDN. 1 MARKS:

DATE: 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 48 (536) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71201035в T80058645 RATHOD NEHA SANJAY NALINI , , PICT , T80058645 01. OPERATING SYSTEM 100 40 54 P 02. THEORY OF COMPUTATION 100 40 48 P 03. COMPUTER NETWORK TECHNOLOGY 100 40 63 P PP 04. DATBASE MANAGEMENT SYSTEMS 100 40 51 P 05. SOFTWARE ENGINEERING 100 40 48 P 25 10 21 P 06. OPERATING SYSTEM DESIGN LAB. TW 07. OPERATING SYSTEM DESIGN LAB. 50 20 30 P 25 10 23 P 08. INFORMATION SYSTEMS DESIGN LAB. TW 50 20 32 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 10. NETWORK LABORATORY 25 10 23 P TW 11. NETWORK LABORATORY 50 20 38 P OR 25 10 22 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 453/750. ORDN. 1 MARKS: T80058646 RITESH RAJIV POREY **AMRUTA** , 71201040J , PICT , T80058646 01. OPERATING SYSTEM 100 40 57 P 100 40 02. THEORY OF COMPUTATION PP 61 P 100 40 68 P 03. COMPUTER NETWORK TECHNOLOGY 100 40 04. DATBASE MANAGEMENT SYSTEMS PP 58 P 100 40 57 P 05. SOFTWARE ENGINEERING PP 25 10 19 P 06. OPERATING SYSTEM DESIGN LAB. TW 50 20 40 P 07. OPERATING SYSTEM DESIGN LAB. PR 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 22 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 44 P 25 10 21 P 10. NETWORK LABORATORY TW 11. NETWORK LABORATORY 50 20 28 P 25 10 19 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 494/750. ORDN. 1 MARKS: T80058647 SAKSHI SIRPAL **ANUPAMA** , 71201048D , PICT , т80058647 01. OPERATING SYSTEM 100 40 59 P 02. THEORY OF COMPUTATION 100 40 68 P PP 03. COMPUTER NETWORK TECHNOLOGY PP 100 40 67 P 04. DATBASE MANAGEMENT SYSTEMS 100 40 65 P 05. SOFTWARE ENGINEERING 100 40 45 P PP 06. OPERATING SYSTEM DESIGN LAB. 25 10 20 P TW 50 20 30 P 07. OPERATING SYSTEM DESIGN LAB. 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 21 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 32 P 10. NETWORK LABORATORY 25 10 23 P 11. NETWORK LABORATORY OR 50 20 29 P 12. SOFT SKILLS LABORATORY TW 25 10 21 P FIRST TERM TOTAL = 480/750. ORDN. 1 MARKS:

DATE: 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 49 (537) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER ANITA , 71201049в T80058648 SALUNKE GAURI RANGNATH , , PICT , T80058648 01. OPERATING SYSTEM 100 40 68 P 02. THEORY OF COMPUTATION 100 40 59 P 03. COMPUTER NETWORK TECHNOLOGY 100 40 68 P PP 04. DATBASE MANAGEMENT SYSTEMS 100 40 68 P 05. SOFTWARE ENGINEERING 100 40 55 P 25 10 24 P 06. OPERATING SYSTEM DESIGN LAB. TW 07. OPERATING SYSTEM DESIGN LAB. 50 20 45 P 25 10 23 P 08. INFORMATION SYSTEMS DESIGN LAB. TW 50 20 42 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 10. NETWORK LABORATORY 25 10 22 P TW 11. NETWORK LABORATORY 50 20 32 P OR 25 10 24 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 530/750. ORDN. 1 MARKS: T80058649 SAPKALE SNEHAL SURESH **USHA** , 71201052B , PICT , T80058649 100 40 40 P 01. OPERATING SYSTEM 100 40 22 F 02. THEORY OF COMPUTATION PP 100 40 49 P 03. COMPUTER NETWORK TECHNOLOGY 100 40 51 P 04. DATBASE MANAGEMENT SYSTEMS PP 100 40 16 F 05. SOFTWARE ENGINEERING PP 25 10 15 P 06. OPERATING SYSTEM DESIGN LAB. TW 50 20 07. OPERATING SYSTEM DESIGN LAB. PR 05 F 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 15 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 25 P 25 10 10. NETWORK LABORATORY TW 16 P 11. NETWORK LABORATORY 50 20 28 P 25 10 12 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 294/750. ORDN. 1 MARKS: T80058650 SARODE ASHWINI RAJENDRA SANGITA , 71201053L , PICT , T80058650 01. OPERATING SYSTEM 100 40 65 P 02. THEORY OF COMPUTATION 100 40 61 P PP 03. COMPUTER NETWORK TECHNOLOGY 100 40 75 P PP 04. DATBASE MANAGEMENT SYSTEMS 100 40 61 P 05. SOFTWARE ENGINEERING 100 40 59 P PP 06. OPERATING SYSTEM DESIGN LAB. 25 10 24 P TW 50 20 41 P 07. OPERATING SYSTEM DESIGN LAB. 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 23 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 36 P 10. NETWORK LABORATORY 25 10 24 P 11. NETWORK LABORATORY OR 50 20 26 P 12. SOFT SKILLS LABORATORY TW 25 10 23 P FIRST TERM TOTAL = 518/750. ORDN. 1 MARKS:

UNIVERSITY OF PUNE ,T.E. (2008 PAT) (INFORMATION TECHNOLOGY) EXAMINATION NOV. 2013 DATE : 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 50 (538) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , T80058666 , PICT , 71045598E T80058651 SARTHAK MAJITHIA POOJA , т80058651 40 P C 01. OPERATING SYSTEM 100 40 13. SYSTEM SOFTWARE PROGRAMMING 100 40 AA F PΡ 02. THEORY OF COMPUTATION 100 40 45 P C 14. MANAGEMENT INFORMATION SYSTEMS PP 100 40 61 P C 100 40 40 P C 100 40 47 P C 03. COMPUTER NETWORK TECHNOLOGY PP 15. PROGRAMMING PARADIGMS 04. DATBASE MANAGEMENT SYSTEMS 100 40 51 P C 16. DESIGN & ANALYSIS OF ALGORITHMS PP 100 40 55 P C PP 05. SOFTWARE ENGINEERING 100 40 40 P 17. HUMAN COMPU.INTERACTION & USABI. PP 100 40 50 P C 25 10 11 P C 50 20 20 P C 06. OPERATING SYSTEM DESIGN LAB. 18. SOFTWARE DESIGN LABORATORY TW TW 22 P C 07. OPERATING SYSTEM DESIGN LAB. 50 20 50 20 AA F PR19. SOFTWARE DESIGN LABORATORY 25 10 12 P C 50 20 22 P C 08. INFORMATION SYSTEMS DESIGN LAB. TW 20. SOFTWARE DEVELOPMENT TOOLS LAB. TW 50 20 50 20 25 P 32 P C 09. INFORMATION SYSTEMS DESIGN LAB. OR 21. SOFTWARE DEVELOPMENT TOOLS LAB. OR 50 20 22 P C 25 10 22. SEMINAR AND TECHNICAL COMMUN. TW 10. NETWORK LABORATORY TW 10 P C 11. NETWORK LABORATORY 50 20 24 P C OR 25 10 18 P C 12. SOFT SKILLS LABORATORY TW GRAND TOTAL = 647/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS: T80058652 SAWANT ARUNDHATI ULHAS MADHAVI , 71201058M , T80058652 , PICT 01. OPERATING SYSTEM 100 40 51 P 100 40 40 P 02. THEORY OF COMPUTATION PP 100 40 63 P 03. COMPUTER NETWORK TECHNOLOGY 100 40 70 P 04. DATBASE MANAGEMENT SYSTEMS PP 100 40 40 P 05. SOFTWARE ENGINEERING PP 25 10 19 P 06. OPERATING SYSTEM DESIGN LAB. TW 50 20 07. OPERATING SYSTEM DESIGN LAB. PR 38 P 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 21 P 50 20 42 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 10. NETWORK LABORATORY TW 25 10 20 P 11. NETWORK LABORATORY 50 20 30 P 25 10 19 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 453/750. ORDN. 1 MARKS: SHILPA T80058653 SHAH DISHANT UMESH , 71201059к , T80058653 , PICT 01. OPERATING SYSTEM 100 40 56 P 02. THEORY OF COMPUTATION 100 40 59 P PP 03. COMPUTER NETWORK TECHNOLOGY 100 40 65 P PP 51 P 04. DATBASE MANAGEMENT SYSTEMS 100 40 05. SOFTWARE ENGINEERING 100 40 59 P PP 06. OPERATING SYSTEM DESIGN LAB. 25 10 10 P TW 20 P 50 20 07. OPERATING SYSTEM DESIGN LAB. 25 10 15 P 08. INFORMATION SYSTEMS DESIGN LAB. TW 50 20 30 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 10. NETWORK LABORATORY 25 10 21 P 11. NETWORK LABORATORY OR 50 20 14 F 12. SOFT SKILLS LABORATORY TW 25 10 16 P FIRST TERM TOTAL = 416/750. ORDN. 1 MARKS:

DATE: 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 51 (539) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71201061M T80058654 SHAH RUTUJA RAJESH BEENA , , PICT , T80058654 01. OPERATING SYSTEM 100 40 57 P 02. THEORY OF COMPUTATION 100 40 59 P 03. COMPUTER NETWORK TECHNOLOGY 100 40 65 P PP 04. DATBASE MANAGEMENT SYSTEMS 100 40 78 P 05. SOFTWARE ENGINEERING 100 40 62 P 25 10 23 P 06. OPERATING SYSTEM DESIGN LAB. TW 07. OPERATING SYSTEM DESIGN LAB. 50 20 43 P 25 10 24 P 08. INFORMATION SYSTEMS DESIGN LAB. TW 50 20 45 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 10. NETWORK LABORATORY 25 10 24 P TW 11. NETWORK LABORATORY 50 20 43 P OR 25 10 23 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 546/750. ORDN. 1 MARKS: T80058655 SHARIKA KHURANA **RASHMI** , 71201064F , PICT , T80058655 100 40 56 P 01. OPERATING SYSTEM 100 40 57 P 02. THEORY OF COMPUTATION PP 100 40 53 P 03. COMPUTER NETWORK TECHNOLOGY 100 40 59 P 04. DATBASE MANAGEMENT SYSTEMS PP 100 40 58 P 05. SOFTWARE ENGINEERING PP 25 10 18 P 06. OPERATING SYSTEM DESIGN LAB. TW 50 20 06 F 07. OPERATING SYSTEM DESIGN LAB. PR 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 18 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 39 P 25 10 10. NETWORK LABORATORY TW 21 P 11. NETWORK LABORATORY 50 20 23 P 25 10 17 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 425/750. ORDN. 1 MARKS: T80058656 SHINDE ANIKET NAVNATH BABYNANDA , 71350935L , PICT , T80058656 01. OPERATING SYSTEM 100 40 66 P 02. THEORY OF COMPUTATION 100 40 67 P PP 03. COMPUTER NETWORK TECHNOLOGY 100 40 73 P PP 72 P 04. DATBASE MANAGEMENT SYSTEMS 100 40 05. SOFTWARE ENGINEERING 100 40 55 P PP 06. OPERATING SYSTEM DESIGN LAB. 25 10 18 P TW 50 20 30 P 07. OPERATING SYSTEM DESIGN LAB. 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 17 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 42 P 10. NETWORK LABORATORY 25 10 20 P 11. NETWORK LABORATORY OR 50 20 40 P 12. SOFT SKILLS LABORATORY TW 25 10 16 P FIRST TERM TOTAL = 516/750. ORDN. 1 MARKS:

DATE: 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 52 (540)

						T REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT RKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY C	
T80058657 SHIRBHATE ABHILASH DNYAN					 JSHRI		т80058657
01. OPERATING SYSTEM	PP	100			P C	SYSTEM SOFTWARE PROGRAMMING PP 100 40	49 P C
02. THEORY OF COMPUTATION		100			PC	. MANAGEMENT INFORMATION SYSTEMS PP 100 40	45 P C
03. COMPUTER NETWORK TECHNOLOGY		100	40		PC	PROGRAMMING PARADIGMS PP 100 40	43 P C
04. DATBASE MANAGEMENT SYSTEMS	PP	100	40	_	PC	DESIGN & ANALYSIS OF ALGORITHMS PP 100 40	51 P
05. SOFTWARE ENGINEERING	PP	100	_		PC	. HUMAN COMPU.INTERACTION & USABI. PP 100 40	48 P C
	TW	25			PC	S. SOFTWARE DESIGN LABORATORY TW 50 20	21 P C
07. OPERATING SYSTEM DESIGN LAB.	PR	50			PC	. SOFTWARE DESIGN LABORATORY PR 50 20	35 P C
08. INFORMATION SYSTEMS DESIGN LAB.		25	_		PC	. SOFTWARE DEVELOPMENT TOOLS LAB. TW 50 20	32 P C
09. INFORMATION SYSTEMS DESIGN LAB.		50	-		PC	SOFTWARE DEVELOPMENT TOOLS LAB. OR 50 20	38 P
10. NETWORK LABORATORY	TW	25			PC	. SEMINAR AND TECHNICAL COMMUN. TW 50 20	32 P C
11. NETWORK LABORATORY	OR	50	20		P C	TO SEMERATE AND PERMITTERS COMMON!	32 . C
12. SOFT SKILLS LABORATORY	TW		10		P C		
GRAND TOTAL = 789/1500, RESULT: SECON		_	10				
ORDN. 1 MARKS:	ID CLAS	33					
						71201072F	т80058658
T80058658 SHUBHAM BOHRA	PP	100	40	МО 29		, 71201073E , , PICT ,	180038038
01. OPERATING SYSTEM							
02. THEORY OF COMPUTATION	PP	100	40	23			
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	40			
04. DATBASE MANAGEMENT SYSTEMS	PP	100	40	20			
05. SOFTWARE ENGINEERING	PP	100	40	40			
06. OPERATING SYSTEM DESIGN LAB	TW	25	-	15			
07. OPERATING SYSTEM DESIGN LAB	PR	50		30			
08. INFORMATION SYSTEMS DESIGN LAB		25		15			
09. INFORMATION SYSTEMS DESIGN LAB.	OR Tu	50	_	42			
10. NETWORK LABORATORY	TW	25	_	22 12			
11. NETWORK LABORATORY	OR Tu	50		16			
12. SOFT SKILLS LABORATORY	TW	25	10	10	Р		
FIRST TERM TOTAL = 304/750. ORDN. 1 MARKS :							
T80058659 SHUKLA MANISH NITYANAND					DHURI		Т80058659
01. OPERATING SYSTEM	PP	100			PC	. SYSTEM SOFTWARE PROGRAMMING PP 100 40	40 P C
02. THEORY OF COMPUTATION	PP	100	40		P C	. MANAGEMENT INFORMATION SYSTEMS PP 100 40	56 P C
03. COMPUTER NETWORK TECHNOLOGY	PP	100			P C	. PROGRAMMING PARADIGMS PP 100 40	58 P C
04. DATBASE MANAGEMENT SYSTEMS	PP	100			P C	DESIGN & ANALYSIS OF ALGORITHMS PP 100 40	AA F
05. SOFTWARE ENGINEERING	PP	100			PC	. HUMAN COMPU.INTERACTION & USABI. PP 100 40	44 P
06. OPERATING SYSTEM DESIGN LAB.	TW	25		17	PC	. SOFTWARE DESIGN LABORATORY TW 50 20	27 P C
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20		P C	. SOFTWARE DESIGN LABORATORY PR 50 20	38 P C
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25			PC	O. SOFTWARE DEVELOPMENT TOOLS LAB. TW 50 20	30 P C
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50			PC	SOFTWARE DEVELOPMENT TOOLS LAB. OR 50 20	21 P C
10. NETWORK LABORATORY	TW	25	_		PC	. SEMINAR AND TECHNICAL COMMUN. TW 50 20	20 P C
11. NETWORK LABORATORY	OR	50	20	_	P C		
12. SOFT SKILLS LABORATORY	TW	25	10	17	PC		
GRAND TOTAL = 779/1500, RESULT: FAILS	S A.T.	(.T.				RESULT RESERVE	.D FOR BKLG

ORDN. 1 MARKS:

UNIVERSITY OF PUNE ,T.E. (2008 PAT) (INFORMATION TECHNOLOGY) EXAMINATION NOV. 2013 DATE: 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 53 (541) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71201075M , , PICT , T80058660 T80058660 SIDDHARTH DALAL REKHA PP 100 40 40 P 01. OPERATING SYSTEM 02. THEORY OF COMPUTATION 100 40 50 P 03. COMPUTER NETWORK TECHNOLOGY 100 40 59 P PP 04. DATBASE MANAGEMENT SYSTEMS 100 40 60 P PP 05. SOFTWARE ENGINEERING 100 40 57 P 25 10 21 P 06. OPERATING SYSTEM DESIGN LAB. TW 07. OPERATING SYSTEM DESIGN LAB. 50 20 38 P PR08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 23 P 50 20 36 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 10. NETWORK LABORATORY 25 10 23 P TW 11. NETWORK LABORATORY 50 20 40 P OR 25 10 22 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 469/750. ORDN. 1 MARKS: T80058661 SIDHESH BADRINARAYAN KAUSALYA , 71201077н , PICT , T80058661 01. OPERATING SYSTEM 100 40 49 P 02. THEORY OF COMPUTATION 100 40 63 P PP 72 P 03. COMPUTER NETWORK TECHNOLOGY 100 40 100 40 70 P 04. DATBASE MANAGEMENT SYSTEMS PP 100 40 60 P 05. SOFTWARE ENGINEERING PP 06. OPERATING SYSTEM DESIGN LAB. 25 10 20 P TW 50 20 42 P 07. OPERATING SYSTEM DESIGN LAB. PR 25 10 08. INFORMATION SYSTEMS DESIGN LAB. TW 18 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 41 P 25 10 21 P 10. NETWORK LABORATORY TW 50 20 40 P 11. NETWORK LABORATORY OR 12. SOFT SKILLS LABORATORY 25 10 16 P TW FIRST TERM TOTAL = 512/750. ORDN. 1 MARKS:

T80058662 SOLANKE BHUSHAN NARENDRA	١			RE	KHA	, 71101017L , т80058620 , ріст , т80058662
01. OPERATING SYSTEM	PP	100	40	42	РС	13. SYSTEM SOFTWARE PROGRAMMING PP 100 40 PC
02. THEORY OF COMPUTATION	PP	100	40	48	РС	14. MANAGEMENT INFORMATION SYSTEMS PP 100 40 42 P C
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	42	РС	15. PROGRAMMING PARADIGMS PP 100 40 P C
04. DATBASE MANAGEMENT SYSTEMS	PP	100	40	40	РС	16. DESIGN & ANALYSIS OF ALGORITHMS PP 100 40 AA F
05. SOFTWARE ENGINEERING	PP	100	40	56	РС	17. HUMAN COMPU.INTERACTION & USABI. PP 100 40 45 P C
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	22	РС	18. SOFTWARE DESIGN LABORATORY TW 50 20 29 P C
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	27	РС	19. SOFTWARE DESIGN LABORATORY PR 50 20 36 P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	16	РС	20. SOFTWARE DEVELOPMENT TOOLS LAB. TW 50 20 34 P C
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	27	РС	21. SOFTWARE DEVELOPMENT TOOLS LAB. OR 50 20 AA F
10. NETWORK LABORATORY	TW	25	10	15	РС	22. SEMINAR AND TECHNICAL COMMUN. TW 50 20 29 P C
11. NETWORK LABORATORY	OR	50	20	26	РС	
12. SOFT SKILLS LABORATORY	TW	25	10	16	РС	

GRAND TOTAL = 672/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS:

RESULT RESERVED FOR BKLG

DATE: 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 54 (542) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , T80058670 , PICT , 70925625G T80058663 SUYASH PANDEY SHAILA , т80058663 01. OPERATING SYSTEM 100 40 55 P C 13. SYSTEM SOFTWARE PROGRAMMING 100 40 46 P C 02. THEORY OF COMPUTATION 100 40 40 P C 14. MANAGEMENT INFORMATION SYSTEMS PP 100 40 47 P C 100 40 46 P C 100 40 44 P C 03. COMPUTER NETWORK TECHNOLOGY PP 15. PROGRAMMING PARADIGMS 04. DATBASE MANAGEMENT SYSTEMS 100 40 40 P C 16. DESIGN & ANALYSIS OF ALGORITHMS PP 100 40 40 P PP 05. SOFTWARE ENGINEERING 100 40 40 P C 17. HUMAN COMPU.INTERACTION & USABI. PP 100 40 40 P C 25 10 11 P C 50 20 25 P C 06. OPERATING SYSTEM DESIGN LAB. 18. SOFTWARE DESIGN LABORATORY TW TW 07. OPERATING SYSTEM DESIGN LAB. 50 20 30 P C 50 20 35 P C PR19. SOFTWARE DESIGN LABORATORY 25 10 14 P C 50 20 25 P C 08. INFORMATION SYSTEMS DESIGN LAB. TW 20. SOFTWARE DEVELOPMENT TOOLS LAB. TW 50 20 28 P C 50 20 24 P C 09. INFORMATION SYSTEMS DESIGN LAB. OR 21. SOFTWARE DEVELOPMENT TOOLS LAB. OR 25 10 10 P C 22. SEMINAR AND TECHNICAL COMMUN. TW 50 20 30 P C 10. NETWORK LABORATORY TW 11. NETWORK LABORATORY 50 20 32 P C OR 25 10 15 P C 12. SOFT SKILLS LABORATORY TW GRAND TOTAL = 717/1500, RESULT: PASS CLASS ORDN. 1 MARKS: T80058665 TAKALKAR TANMAYEE SUHAS JYOTI , 71201089M , T80058665 , PICT 100 40 49 P 01. OPERATING SYSTEM 100 40 02. THEORY OF COMPUTATION PP 61 P 100 40 03. COMPUTER NETWORK TECHNOLOGY 66 P 100 40 70 P 04. DATBASE MANAGEMENT SYSTEMS PP 100 40 67 P 05. SOFTWARE ENGINEERING PP 25 10 19 P 06. OPERATING SYSTEM DESIGN LAB. TW 50 20 07. OPERATING SYSTEM DESIGN LAB. PR 40 P 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 19 P 50 20 38 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 25 10 10. NETWORK LABORATORY TW 23 P 11. NETWORK LABORATORY 50 20 30 P 25 10 20 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 502/750. ORDN. 1 MARKS: T80058666 TEJASH KUMAR **RITA** , 71045642F , PICT , T80058666 01. OPERATING SYSTEM 100 40 22 F 02. THEORY OF COMPUTATION 100 40 PP AA F 03. COMPUTER NETWORK TECHNOLOGY 100 40 44 P PP 04. DATBASE MANAGEMENT SYSTEMS 100 40 40 P 05. SOFTWARE ENGINEERING 100 40 PP AA F 25 10 06. OPERATING SYSTEM DESIGN LAB. 17 P TW 50 20 07. OPERATING SYSTEM DESIGN LAB. AA F 25 10 10 P 08. INFORMATION SYSTEMS DESIGN LAB. TW 50 20 08 F 09. INFORMATION SYSTEMS DESIGN LAB. OR 10. NETWORK LABORATORY 25 10 10 P 11. NETWORK LABORATORY OR 50 20 AA F 12. SOFT SKILLS LABORATORY TW 25 10 10 P FIRST TERM TOTAL = 161/750. ORDN. 1 MARKS:

DATE : 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 55 (543) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , T80058624 , PICT T80058667 TEKE AMRUTA PRAKASH SHALINEE , 71101028F , т80058667 46 P C 01. OPERATING SYSTEM 100 40 13. SYSTEM SOFTWARE PROGRAMMING 100 40 55 P C PP 02. THEORY OF COMPUTATION 100 40 40 P C 14. MANAGEMENT INFORMATION SYSTEMS PP 100 40 41 P C 100 40 66 P C 100 40 50 P C 03. COMPUTER NETWORK TECHNOLOGY PP 15. PROGRAMMING PARADIGMS 04. DATBASE MANAGEMENT SYSTEMS 100 40 60 P C 16. DESIGN & ANALYSIS OF ALGORITHMS PP 100 40 55 P PP 05. SOFTWARE ENGINEERING 100 40 61 P C 17. HUMAN COMPU.INTERACTION & USABI. PP 100 40 52 P C 25 10 19 P C 50 20 27 P C 06. OPERATING SYSTEM DESIGN LAB. 18. SOFTWARE DESIGN LABORATORY TW TW 07. OPERATING SYSTEM DESIGN LAB. 50 20 21 P C 50 20 36 P C PR19. SOFTWARE DESIGN LABORATORY 25 10 19 P C 50 20 34 P C 08. INFORMATION SYSTEMS DESIGN LAB. TW 20. SOFTWARE DEVELOPMENT TOOLS LAB. TW 50 20 22 P C 50 20 38 P C 09. INFORMATION SYSTEMS DESIGN LAB. OR 21. SOFTWARE DEVELOPMENT TOOLS LAB. OR 25 10 22. SEMINAR AND TECHNICAL COMMUN. TW 50 20 30 P C 10. NETWORK LABORATORY TW 16 P C 11. NETWORK LABORATORY 50 20 35 P C OR 19 P C 25 10 12. SOFT SKILLS LABORATORY TW GRAND TOTAL = 842/1500, RESULT: HIGHER SECOND CLASS ORDN. 1 MARKS: T80058668 THORAT SAYALI SURESH SULABHA , 71201095F , T80058668 , PICT 01. OPERATING SYSTEM 100 40 10 F 100 40 40 P 02. THEORY OF COMPUTATION PP 100 40 03. COMPUTER NETWORK TECHNOLOGY 40 P 100 40 04. DATBASE MANAGEMENT SYSTEMS PP 33 F 100 40 32 F 05. SOFTWARE ENGINEERING PP 25 10 19 P 06. OPERATING SYSTEM DESIGN LAB. TW 50 20 07. OPERATING SYSTEM DESIGN LAB. PR 25 P 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 21 P 50 20 33 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 25 10 10. NETWORK LABORATORY TW 21 P 11. NETWORK LABORATORY 50 20 30 P 25 10 15 P 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 319/750. ORDN. 1 MARKS: T80058669 TIKONE SNEHAL SHAM SUNITA , 71350936J , T80058669 , PICT 01. OPERATING SYSTEM 100 40 33 F 02. THEORY OF COMPUTATION 100 40 40 P PP 03. COMPUTER NETWORK TECHNOLOGY 100 40 59 P PP 04. DATBASE MANAGEMENT SYSTEMS 100 40 56 P 05. SOFTWARE ENGINEERING 100 40 68 P PP 25 10 06. OPERATING SYSTEM DESIGN LAB. 21 P TW 50 20 25 P 07. OPERATING SYSTEM DESIGN LAB. 25 10 22 P 08. INFORMATION SYSTEMS DESIGN LAB. TW 50 20 35 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 10. NETWORK LABORATORY 25 10 22 P 11. NETWORK LABORATORY OR 50 20 38 P 12. SOFT SKILLS LABORATORY TW 25 10 20 P FIRST TERM TOTAL = 439/750. ORDN. 1 MARKS:

CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.

PAGE NO. 56 (544)

UNIVERSITY OF PUNE , I.E. (2006 PAT) (INFORMATION TECHNOLOGY) EXAMINATION NOV. 2015

DATE : 11 MAR. 2014

NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , T80058627 , PICT T80058670 TOSHNIWAL VIVEK ANILKUMAR SANJAYA , 71101039м , т80058670 13. SYSTEM SOFTWARE PROGRAMMING 01. OPERATING SYSTEM 100 40 59 P C PP 100 40 63 P C PP 100 40 55 P C 14. MANAGEMENT INFORMATION SYSTEMS PP 100 40 48 P C 02. THEORY OF COMPUTATION 100 40 62 P C 100 40 03. COMPUTER NETWORK TECHNOLOGY PP 15. PROGRAMMING PARADIGMS PР 61 P C 04. DATBASE MANAGEMENT SYSTEMS 100 40 40 P C 100 40 54 P PP 16. DESIGN & ANALYSIS OF ALGORITHMS PP 05. SOFTWARE ENGINEERING 100 40 46 P C 17. HUMAN COMPU.INTERACTION & USABI. PP 100 40 47 P C PP 06. OPERATING SYSTEM DESIGN LAB. 25 10 19 P C 50 20 38 P C 18. SOFTWARE DESIGN LABORATORY TW TW 07. OPERATING SYSTEM DESIGN LAB. 50 20 23 P C 50 20 38 P C PR 19. SOFTWARE DESIGN LABORATORY 25 10 20 P C 50 20 42 P C 08. INFORMATION SYSTEMS DESIGN LAB. TW 20. SOFTWARE DEVELOPMENT TOOLS LAB. TW 50 20 50 20 33 P C 09. INFORMATION SYSTEMS DESIGN LAB. OR 43 P C 21. SOFTWARE DEVELOPMENT TOOLS LAB. OR 25 10 22. SEMINAR AND TECHNICAL COMMUN. TW 50 20 38 P C 10. NETWORK LABORATORY TW 18 P C 50 20 29 P C 11. NETWORK LABORATORY OR 25 10 18 P C 12. SOFT SKILLS LABORATORY TW GRAND TOTAL = 894+06/1500, RESULT: FIRST CLASS [0.2] ORDN. 1 MARKS: T80058671 VARAT KAUSTUBH SOMKANT , T80058630 , PICT TILOTTAMA , 71101046D , т80058671 40 P C 100 40 57 P C 01. OPERATING SYSTEM 100 40 13. SYSTEM SOFTWARE PROGRAMMING 02. THEORY OF COMPUTATION 45 P C 100 40 100 40 PP 14. MANAGEMENT INFORMATION SYSTEMS PP 49 P C 100 40 100 40 03. COMPUTER NETWORK TECHNOLOGY 40 P C 15. PROGRAMMING PARADIGMS 47 P C 100 40 49 P 04. DATBASE MANAGEMENT SYSTEMS PP 50 P C 16. DESIGN & ANALYSIS OF ALGORITHMS PP 100 40 100 40 17. HUMAN COMPU.INTERACTION & USABI. PP 05. SOFTWARE ENGINEERING 45 P C 100 40 42 P C PP 25 10 06. OPERATING SYSTEM DESIGN LAB. TW 10 P C 18. SOFTWARE DESIGN LABORATORY 50 20 20 P C 50 20 20 P C 07. OPERATING SYSTEM DESIGN LAB. PR AA F 19. SOFTWARE DESIGN LABORATORY 50 20 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 12 P C 20. SOFTWARE DEVELOPMENT TOOLS LAB. TW 50 20 25 P C 50 20 27 P C 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 AA F 21. SOFTWARE DEVELOPMENT TOOLS LAB. OR 50 20 20 P C 10. NETWORK LABORATORY TW 25 10 10 P C 22. SEMINAR AND TECHNICAL COMMUN. TW 11. NETWORK LABORATORY 50 20 AA F 25 10 12 P C 12. SOFT SKILLS LABORATORY TW GRAND TOTAL = 620/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS: T80058672 VICHARE GAURAV GIRISH VINA , 71101047B , T80058631 , PICT , т80058672 100 40 43 P C 100 40 49 P C 01. OPERATING SYSTEM 13. SYSTEM SOFTWARE PROGRAMMING 02. THEORY OF COMPUTATION 100 40 44 P C 14. MANAGEMENT INFORMATION SYSTEMS PP 100 40 40 P PP 100 40 52 P C 15. PROGRAMMING PARADIGMS 100 40 47 P C 03. COMPUTER NETWORK TECHNOLOGY PP 52 P 100 40 40 P C 100 40 04. DATBASE MANAGEMENT SYSTEMS 16. DESIGN & ANALYSIS OF ALGORITHMS PP 100 40 40 P C 17. HUMAN COMPU.INTERACTION & USABI. PP 100 40 40 P C 05. SOFTWARE ENGINEERING PP 06. OPERATING SYSTEM DESIGN LAB. 25 10 21 P C 18. SOFTWARE DESIGN LABORATORY TW 50 20 37 P C TW 50 20 23 P C 50 20 36 P C 07. OPERATING SYSTEM DESIGN LAB. 19. SOFTWARE DESIGN LABORATORY 25 10 50 20 37 P C 08. INFORMATION SYSTEMS DESIGN LAB. TW 24 P C 20. SOFTWARE DEVELOPMENT TOOLS LAB. TW 22 P C 50 20 50 20 09. INFORMATION SYSTEMS DESIGN LAB. OR 41 P C 21. SOFTWARE DEVELOPMENT TOOLS LAB. OR 10. NETWORK LABORATORY 25 10 20 P C 22. SEMINAR AND TECHNICAL COMMUN. TW 50 20 40 P C 11. NETWORK LABORATORY OR 50 20 31 P C 12. SOFT SKILLS LABORATORY 25 10 21 P C TW GRAND TOTAL = 800/1500, RESULT: SECOND CLASS ORDN. 1 MARKS:

DATE: 11 MAR. 2014 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 57 (545) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71101051L , T80058633 , PICT , T80058673 T80058673 VIRWANI SUNNY JAMANLAL KIRTI 01. OPERATING SYSTEM 100 40 44 P C PP 02. THEORY OF COMPUTATION 100 40 40 P 100 40 51 P C 03. COMPUTER NETWORK TECHNOLOGY PP 04. DATBASE MANAGEMENT SYSTEMS 100 40 40 P C 05. SOFTWARE ENGINEERING 100 40 40 P C 25 10 15 P C 06. OPERATING SYSTEM DESIGN LAB. TW 07. OPERATING SYSTEM DESIGN LAB. 50 20 25 P C 25 10 15 P C 08. INFORMATION SYSTEMS DESIGN LAB. TW 50 20 37 P C 09. INFORMATION SYSTEMS DESIGN LAB. OR 25 10 10. NETWORK LABORATORY TW 15 P C 50 20 11. NETWORK LABORATORY OR 43 P C 25 10 15 P C 12. SOFT SKILLS LABORATORY TW FIRST TERM TOTAL = 380/750. ORDN. 1 MARKS: T80058674 WADILE NITIN ADHAR , т80058634 , SUMAN , 71101052J PICT , т80058674 100 40 40 P C 100 40 40 P C 01. OPERATING SYSTEM 13. SYSTEM SOFTWARE PROGRAMMING 100 40 100 40 02. THEORY OF COMPUTATION PP 43 P C 14. MANAGEMENT INFORMATION SYSTEMS PP 40 P C 100 40 100 40 03. COMPUTER NETWORK TECHNOLOGY 48 P C 15. PROGRAMMING PARADIGMS 40 P C 100 40 100 40 47 P 04. DATBASE MANAGEMENT SYSTEMS PP 40 P C 16. DESIGN & ANALYSIS OF ALGORITHMS PP 100 40 100 40 05. SOFTWARE ENGINEERING 51 P C 17. HUMAN COMPU.INTERACTION & USABI. PP 43 P C PP 25 10 10 P C 21 P C 06. OPERATING SYSTEM DESIGN LAB. TW 18. SOFTWARE DESIGN LABORATORY 50 20 50 20 50 20 30 P C 07. OPERATING SYSTEM DESIGN LAB. PR 27 P C 19. SOFTWARE DESIGN LABORATORY 50 20 25 P C 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 12 P C 20. SOFTWARE DEVELOPMENT TOOLS LAB. TW 20 P C 50 20 27 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 21. SOFTWARE DEVELOPMENT TOOLS LAB. OR 50 20 22 P C 10. NETWORK LABORATORY TW 25 10 10 P C 22. SEMINAR AND TECHNICAL COMMUN. TW 11. NETWORK LABORATORY 50 20 AA F 25 10 13 P C 12. SOFT SKILLS LABORATORY TW GRAND TOTAL = 649/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS: T80058676 WALZADE ROHAN SUNIL MANISHA , 71201118J , , PICT , T80058676 01. OPERATING SYSTEM 100 40 40 P 02. THEORY OF COMPUTATION 100 40 50 P PP 03. COMPUTER NETWORK TECHNOLOGY 100 40 49 P PP 04. DATBASE MANAGEMENT SYSTEMS 100 40 57 P 05. SOFTWARE ENGINEERING 100 40 56 P PP 06. OPERATING SYSTEM DESIGN LAB. 25 10 17 P TW 50 20 25 P 07. OPERATING SYSTEM DESIGN LAB. 25 10 16 P 08. INFORMATION SYSTEMS DESIGN LAB. TW 50 20 32 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 10. NETWORK LABORATORY 25 10 23 P 11. NETWORK LABORATORY OR 50 20 23 P 12. SOFT SKILLS LABORATORY TW 25 10 18 P FIRST TERM TOTAL = 406/750. ORDN. 1 MARKS:

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