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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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B120053001 ABHISHEK USHEKWAR PREETI ,71400687E ,PICT ,B120053001

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	12	25	--	F
404182	COMPUTER NETWORKS	PP	100	40	06	19	--	F
404183	MICROWAVE ENGINEERING	PP	100	40	05	28	--	F
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	02	30	--	F
404185C	PLCS AND AUTOMATION	PP	100	40	13	34	47	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			36	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			24	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			25	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			12	F
404188	PROJECT PHASE I	OR	50	20			36	P

FIRST SEM TOTAL = --/750

B120053002 ADKE MEGHNEEL CHANDRASHEKHAR ROOPALI ,71500137L ,PICT ,B120053002

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	19	57	76	P
404182	COMPUTER NETWORKS	PP	100	40	26	47	73	P
404183	MICROWAVE ENGINEERING	PP	100	40	24	52	76	P
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	21	49	70	P
404185C	PLCS AND AUTOMATION	PP	100	40	21	50	71	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			44	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			38	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			38	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			38	P
404188	PROJECT PHASE I	OR	50	20			40	P

FIRST SEM TOTAL = 564/750

B120053003 AGARWAL DEVANSHU DINESH RADHA ,71400693K ,PICT ,B120053003

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	12	45	57	P
404182	COMPUTER NETWORKS	PP	100	40	20	45	65	P
404183	MICROWAVE ENGINEERING	PP	100	40	16	35	51	P
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	24	53	77	P
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	AA	39	--	F
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			37	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			34	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			36	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			36	P
404188	PROJECT PHASE I	OR	50	20			44	P

FIRST SEM TOTAL = --/750

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B120053004 AGRAWAL VIDHI SURESH SUNITA ,71400696D ,PICT ,B120053004

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	20	42	62	P
404182	COMPUTER NETWORKS	PP	100	40	21	41	62	P
404183	MICROWAVE ENGINEERING	PP	100	40	21	44	65	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	19	42	61	P
404185C	PLCS AND AUTOMATION	PP	100	40	20	47	67	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			43	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			35	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			39	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			30	P
404188	PROJECT PHASE I	OR	50	20			46	P

FIRST SEM TOTAL = 510/750

B120053005 AHIRE ANKITA ASHOK SANGITA ,71500138J ,PICT ,B120053005

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	23	54	77	P
404182	COMPUTER NETWORKS	PP	100	40	17	50	67	P
404183	MICROWAVE ENGINEERING	PP	100	40	18	56	74	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	9	51	60	P
404185C	PLCS AND AUTOMATION	PP	100	40	19	50	69	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			44	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			29	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			44	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			23	P
404188	PROJECT PHASE I	OR	50	20			39	P

FIRST SEM TOTAL = 526/750

B120053006 AISHWARYA AMBILWADE SWATI ,71400698L ,PICT ,B120053006

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	19	44	63	P
404182	COMPUTER NETWORKS	PP	100	40	24	44	68	P
404183	MICROWAVE ENGINEERING	PP	100	40	23	55	78	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	24	45	69	P
404185C	PLCS AND AUTOMATION	PP	100	40	19	49	68	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			42	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			28	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			43	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			32	P
404188	PROJECT PHASE I	OR	50	20			46	P

FIRST SEM TOTAL = 537/750

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B120053007 AJAY KUMAR JANKI ,71500139G ,PICT ,B120053007

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	12	33	45	P
404182	COMPUTER NETWORKS	PP	100	40	15	41	56	P
404183	MICROWAVE ENGINEERING	PP	100	40	13	47	60	P
404184C	SOFTWARE DEFINED RADIO	PP	100	40	15	41	56	P
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	AA	37	--	F
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			37	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			26	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			35	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			24	P
404188	PROJECT PHASE I	OR	50	20			41	P

FIRST SEM TOTAL = --/750

B120053008 AKHADE ANUJA RAMDAS SUREKHA ,71500140L ,PICT ,B120053008

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	16	45	61	P
404182	COMPUTER NETWORKS	PP	100	40	19	43	62	P
404183	MICROWAVE ENGINEERING	PP	100	40	21	43	64	P
404184C	SOFTWARE DEFINED RADIO	PP	100	40	25	43	68	P
404185C	PLCS AND AUTOMATION	PP	100	40	21	45	66	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			40	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			31	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			36	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			10	F
404188	PROJECT PHASE I	OR	50	20			37	P

FIRST SEM TOTAL = --/750

B120053009 AKOLAWALA JAWWAD SAJID FATIMA ,71400703L ,PICT ,B120053009

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	14	43	57	P
404182	COMPUTER NETWORKS	PP	100	40	15	40	55	P
404183	MICROWAVE ENGINEERING	PP	100	40	20	45	65	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	15	44	59	P
404185A	MULTI RATE & ADAP SIGN. PROC	PP	100	40	12	28	40	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			37	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			25	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			38	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			33	P
404188	PROJECT PHASE I	OR	50	20			45	P

FIRST SEM TOTAL = 454/750

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B120053010 ALBAL SUDEEP WAMAN ANURADHA ,71400708M ,PICT ,B120053010

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	15	50	65	P
404182	COMPUTER NETWORKS	PP	100	40	14	43	57	P
404183	MICROWAVE ENGINEERING	PP	100	40	17	43	60	P
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	15	45	60	P
404185C	PLCS AND AUTOMATION	PP	100	40	8	39	47	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			37	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			30	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			35	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			35	P
404188	PROJECT PHASE I	OR	50	20			43	P

FIRST SEM TOTAL = 469/750

B120053011 AMAR GOUTAM JAGTAP CHAYA ,71500141J ,PICT ,B120053011

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	20	42	62	P
404182	COMPUTER NETWORKS	PP	100	40	24	36	60	P
404183	MICROWAVE ENGINEERING	PP	100	40	18	41	59	P
404184C	SOFTWARE DEFINED RADIO	PP	100	40	24	39	63	P
404185C	PLCS AND AUTOMATION	PP	100	40	21	39	60	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			40	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			24	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			36	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			30	P
404188	PROJECT PHASE I	OR	50	20			39	P

FIRST SEM TOTAL = 473/750

B120053012 ANUJ BHADAURIA SUMAN BHADAURIA ,71400720L ,PICT ,B120053012

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	12	49	61	P
404182	COMPUTER NETWORKS	PP	100	40	13	45	58	P
404183	MICROWAVE ENGINEERING	PP	100	40	20	45	65	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	20	37	57	P
404185C	PLCS AND AUTOMATION	PP	100	40	20	38	58	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			35	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			30	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			32	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			31	P
404188	PROJECT PHASE I	OR	50	20			36	P

FIRST SEM TOTAL = 463/750

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B120053013 ASHISH ROY ANJANA ,71400724C ,PICT ,B120053013

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	19	58	77	P
404182	COMPUTER NETWORKS	PP	100	40	26	49	75	P
404183	MICROWAVE ENGINEERING	PP	100	40	20	53	73	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	23	51	74	P
404185C	PLCS AND AUTOMATION	PP	100	40	20	48	68	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			40	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			40	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			38	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			40	P
404188	PROJECT PHASE I	OR	50	20			40	P

FIRST SEM TOTAL = 565/750

B120053014 ASHISH SUBHASH KAMBLE NILANDA ,71400725M ,PICT ,B120053014

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	13	52	65	P
404182	COMPUTER NETWORKS	PP	100	40	19	45	64	P
404183	MICROWAVE ENGINEERING	PP	100	40	19	47	66	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	14	41	55	P
404185A	MULTI RATE & ADAP SIGN. PROC	PP	100	40	7	40	47	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			37	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			33	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			39	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			34	P
404188	PROJECT PHASE I	OR	50	20			42	P

FIRST SEM TOTAL = 482/750

B120053015 ASHTEKAR GAJANAN RAJENDRA SUNITA ,71400726K ,PICT ,B120053015

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	12	46	58	P
404182	COMPUTER NETWORKS	PP	100	40	4	36	40	P
404183	MICROWAVE ENGINEERING	PP	100	40	16	44	60	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	11	44	55	P
404185C	PLCS AND AUTOMATION	PP	100	40	16	34	50	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			35	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			21	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			35	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			32	P
404188	PROJECT PHASE I	OR	50	20			42	P

FIRST SEM TOTAL = 428/750

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B120053016 AUTI MAYUR DASHARATH SHAKUNTALA ,71400729D ,PICT ,B120053016

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	15	32	47	P
404182	COMPUTER NETWORKS	PP	100	40	23	39	62	P
404183	MICROWAVE ENGINEERING	PP	100	40	18	35	53	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	9	35	44	P
404185C	PLCS AND AUTOMATION	PP	100	40	18	30	48	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			39	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			22	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			39	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			36	P
404188	PROJECT PHASE I	OR	50	20			30	P

FIRST SEM TOTAL = 420/750

B120053017 BABLE AMIT UTTAMRAO PARVATIBAI ,71400731F ,PICT ,B120053017

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	12	54	66	P
404182	COMPUTER NETWORKS	PP	100	40	10	49	59	P
404183	MICROWAVE ENGINEERING	PP	100	40	16	39	55	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	16	48	64	P
404185C	PLCS AND AUTOMATION	PP	100	40	19	41	60	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			39	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			21	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			37	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			30	P
404188	PROJECT PHASE I	OR	50	20			42	P

FIRST SEM TOTAL = 473/750

B120053018 BAGAL NISHA NAVNATH VANDANA ,71400735J ,PICT ,B120053018

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	13	57	70	P
404182	COMPUTER NETWORKS	PP	100	40	12	48	60	P
404183	MICROWAVE ENGINEERING	PP	100	40	22	56	78	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	19	41	60	P
404185C	PLCS AND AUTOMATION	PP	100	40	18	52	70	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			39	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			40	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			39	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			40	P
404188	PROJECT PHASE I	OR	50	20			44	P

FIRST SEM TOTAL = 540/750

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B120053019 BAHIRAT MRUTYUNJAY SUBHASH VANITA ,71400737E ,PICT ,B120053019

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	12	29	41	P
404182	COMPUTER NETWORKS	PP	100	40	19	38	57	P
404183	MICROWAVE ENGINEERING	PP	100	40	19	36	55	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	07	28	--	F
404185C	PLCS AND AUTOMATION	PP	100	40	11	39	50	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			38	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			25	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			33	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			32	P
404188	PROJECT PHASE I	OR	50	20			42	P

FIRST SEM TOTAL = --/750

B120053020 BAIS AKASH UDHAVRAO LATA BAIS ,71400740E ,PICT ,B120053020

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	16	43	59	P
404182	COMPUTER NETWORKS	PP	100	40	21	41	62	P
404183	MICROWAVE ENGINEERING	PP	100	40	18	44	62	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	7	40	47	P
404185C	PLCS AND AUTOMATION	PP	100	40	20	34	54	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			41	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			24	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			38	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			26	P
404188	PROJECT PHASE I	OR	50	20			43	P

FIRST SEM TOTAL = 456/750

B120053021 BAJAJ POONAM KAILASH BABITA ,71400742M ,PICT ,B120053021

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	12	53	65	P
404182	COMPUTER NETWORKS	PP	100	40	12	41	53	P
404183	MICROWAVE ENGINEERING	PP	100	40	17	47	64	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	16	44	60	P
404185C	PLCS AND AUTOMATION	PP	100	40	12	43	55	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			38	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			27	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			36	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			33	P
404188	PROJECT PHASE I	OR	50	20			43	P

FIRST SEM TOTAL = 474/750

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B120053022 BALLAL SUMUKH DEEPAK SHARMILA ,71400744H ,PICT ,B120053022

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	14	51	65	P
404182	COMPUTER NETWORKS	PP	100	40	11	45	56	P
404183	MICROWAVE ENGINEERING	PP	100	40	15	39	54	P
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	15	47	62	P
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	AA	37	--	F
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			36	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			40	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			34	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			40	P
404188	PROJECT PHASE I	OR	50	20			46	P

FIRST SEM TOTAL = --/750

B120053023 BANAIT POOJA BHANUDAS KALPANA ,71500142G ,PICT ,B120053023

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	20	60	80	P
404182	COMPUTER NETWORKS	PP	100	40	24	52	76	P
404183	MICROWAVE ENGINEERING	PP	100	40	25	55	80	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	19	47	66	P
404185C	PLCS AND AUTOMATION	PP	100	40	25	51	76	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			46	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			31	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			46	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			34	P
404188	PROJECT PHASE I	OR	50	20			42	P

FIRST SEM TOTAL = 577/750

B120053024 BANGAD LALIT LAXMINARAYAN JAMUNA ,71400748L ,PICT ,B120053024

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	16	50	66	P
404182	COMPUTER NETWORKS	PP	100	40	20	45	65	P
404183	MICROWAVE ENGINEERING	PP	100	40	12	40	52	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	18	36	54	P
404185C	PLCS AND AUTOMATION	PP	100	40	21	37	58	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			42	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			33	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			44	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			38	P
404188	PROJECT PHASE I	OR	50	20			44	P

FIRST SEM TOTAL = 496/750

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120053025 BANSARI M CHAUHAN LEENA ,71400749J ,PICT ,B120053025

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	20	43	63	P
404182	COMPUTER NETWORKS	PP	100	40	26	51	77	P
404183	MICROWAVE ENGINEERING	PP	100	40	25	54	79	P
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	27	42	69	P
404185C	PLCS AND AUTOMATION	PP	100	40	22	50	72	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			41	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			33	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			38	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			37	P
404188	PROJECT PHASE I	OR	50	20			36	P

FIRST SEM TOTAL = 545/750

B120053026 BAPAT SARTHAK SUNIL MANJIRI ,71400750B ,PICT ,B120053026

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	12	54	66	P
404182	COMPUTER NETWORKS	PP	100	40	25	49	74	P
404183	MICROWAVE ENGINEERING	PP	100	40	20	48	68	P
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	21	49	70	P
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	AA	32	--	F
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			37	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			32	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			37	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			40	P
404188	PROJECT PHASE I	OR	50	20			44	P

FIRST SEM TOTAL = --/750

B120053027 BAVISKAR TEJASWINI NILKANTH PRATIBHA ,71400754E ,PICT ,B120053027

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	23	56	79	P
404182	COMPUTER NETWORKS	PP	100	40	22	54	76	P
404183	MICROWAVE ENGINEERING	PP	100	40	20	53	73	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	15	52	67	P
404185C	PLCS AND AUTOMATION	PP	100	40	18	52	70	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			44	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			35	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			42	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			40	P
404188	PROJECT PHASE I	OR	50	20			42	P

FIRST SEM TOTAL = 568/750

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120053028 BEDEKAR NIHIT ANAND SUVARNA ,71400756M ,PICT ,B120053028

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	17	45	62	P
404182	COMPUTER NETWORKS	PP	100	40	21	40	61	P
404183	MICROWAVE ENGINEERING	PP	100	40	18	47	65	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	17	38	55	P
404185C	PLCS AND AUTOMATION	PP	100	40	20	46	66	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			39	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			31	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			37	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			35	P
404188	PROJECT PHASE I	OR	50	20			37	P

FIRST SEM TOTAL = 488/750

B120053029 BENDKULE CHETAN RAMESH KALPANA ,71500144C ,PICT ,B120053029

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	19	59	78	P
404182	COMPUTER NETWORKS	PP	100	40	24	44	68	P
404183	MICROWAVE ENGINEERING	PP	100	40	18	57	75	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	22	40	62	P
404185C	PLCS AND AUTOMATION	PP	100	40	16	44	60	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			43	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			31	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			45	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			37	P
404188	PROJECT PHASE I	OR	50	20			40	P

FIRST SEM TOTAL = 539/750

B120053030 BHADANE ISHA DNYANESHWAR MANISHA ,71400759F ,PICT ,B120053030

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	19	62	81	P
404182	COMPUTER NETWORKS	PP	100	40	19	49	68	P
404183	MICROWAVE ENGINEERING	PP	100	40	22	53	75	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	26	43	69	P
404185C	PLCS AND AUTOMATION	PP	100	40	26	48	74	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			40	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			41	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			36	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			39	P
404188	PROJECT PHASE I	OR	50	20			39	P

FIRST SEM TOTAL = 562/750

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120053031 BHANDARKAR SHRUTI PRADEEP JAYASHRI ,71500145M ,PICT ,B120053031

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	19	42	61	P
404182	COMPUTER NETWORKS	PP	100	40	24	49	73	P
404183	MICROWAVE ENGINEERING	PP	100	40	22	47	69	P
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	24	48	72	P
404185C	PLCS AND AUTOMATION	PP	100	40	17	41	58	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			42	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			33	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			45	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			38	P
404188	PROJECT PHASE I	OR	50	20			43	P

FIRST SEM TOTAL = 534/750

B120053032 BHOSALE DANGAT PRIYANKA RAMESH SAVITA ,71500146K ,PICT ,B120053032

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	18	49	67	P
404182	COMPUTER NETWORKS	PP	100	40	22	44	66	P
404183	MICROWAVE ENGINEERING	PP	100	40	22	40	62	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	17	40	57	P
404185C	PLCS AND AUTOMATION	PP	100	40	16	41	57	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			42	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			20	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			43	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			31	P
404188	PROJECT PHASE I	OR	50	20			38	P

FIRST SEM TOTAL = 483/750

B120053033 BHOSALE SHWETA SHIRISH SADHANA ,71400771E ,PICT ,B120053033

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	12	44	56	P
404182	COMPUTER NETWORKS	PP	100	40	16	40	56	P
404183	MICROWAVE ENGINEERING	PP	100	40	20	38	58	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	21	46	67	P
404185C	PLCS AND AUTOMATION	PP	100	40	20	41	61	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			38	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			37	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			36	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			34	P
404188	PROJECT PHASE I	OR	50	20			43	P

FIRST SEM TOTAL = 486/750

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120053034 BOBY ROBERT BINDHU ,71400776F ,PICT ,B120053034

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	17	44	61	P
404182	COMPUTER NETWORKS	PP	100	40	25	37	62	P
404183	MICROWAVE ENGINEERING	PP	100	40	19	31	50	P
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	21	48	69	P
404185C	PLCS AND AUTOMATION	PP	100	40	13	38	51	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			41	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			33	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			40	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			39	P
404188	PROJECT PHASE I	OR	50	20			42	P

FIRST SEM TOTAL = 488/750

B120053035 BORALE ROSHAN SATISH SHUBHANGI ,71400777D ,PICT ,B120053035

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	14	41	55	P
404182	COMPUTER NETWORKS	PP	100	40	21	37	58	P
404183	MICROWAVE ENGINEERING	PP	100	40	15	43	58	P
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	18	44	62	P
404185C	PLCS AND AUTOMATION	PP	100	40	19	39	58	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			40	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			39	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			38	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			39	P
404188	PROJECT PHASE I	OR	50	20			44	P

FIRST SEM TOTAL = 491/750

B120053036 BRAMHANE PRAFUL RAMDAS JAYA ,71500147H ,PICT ,B120053036

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	17	48	65	P
404182	COMPUTER NETWORKS	PP	100	40	19	40	59	P
404183	MICROWAVE ENGINEERING	PP	100	40	20	48	68	P
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	23	52	75	P
404185C	PLCS AND AUTOMATION	PP	100	40	21	49	70	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			41	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			20	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			42	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			40	P
404188	PROJECT PHASE I	OR	50	20			38	P

FIRST SEM TOTAL = 518/750

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120053037 BULBULE VITTHAL MARUTI SAVITA ,71500148F ,PICT ,B120053037

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	23	52	75	P
404182	COMPUTER NETWORKS	PP	100	40	10	31	41	P
404183	MICROWAVE ENGINEERING	PP	100	40	16	45	61	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	8	39	47	P
404185C	PLCS AND AUTOMATION	PP	100	40	15	50	65	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			37	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			27	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			40	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			35	P
404188	PROJECT PHASE I	OR	50	20			40	P

FIRST SEM TOTAL = 468/750

B120053038 CHANDAK SHUBHAM SURYAPRAKASH MANGALA ,71500149D ,PICT ,B120053038

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	23	47	70	P
404182	COMPUTER NETWORKS	PP	100	40	19	41	60	P
404183	MICROWAVE ENGINEERING	PP	100	40	25	50	75	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	20	43	63	P
404185C	PLCS AND AUTOMATION	PP	100	40	19	54	73	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			44	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			34	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			44	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			40	P
404188	PROJECT PHASE I	OR	50	20			41	P

FIRST SEM TOTAL = 544/750

B120053039 CHANDAK SUMIT SUBHASH SUSHMA ,71400781B ,PICT ,B120053039

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	13	42	55	P
404182	COMPUTER NETWORKS	PP	100	40	12	36	48	P
404183	MICROWAVE ENGINEERING	PP	100	40	20	45	65	P
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	19	49	68	P
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40		49	49	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			37	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			36	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			37	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			36	P
404188	PROJECT PHASE I	OR	50	20			40	P

FIRST SEM TOTAL = 471/750

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120053040 CHAUDHARI SHRIKANT SANJAY CHHAYA ,71400784G ,PICT ,B120053040

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	15	54	69	P
404182	COMPUTER NETWORKS	PP	100	40	21	41	62	P
404183	MICROWAVE ENGINEERING	PP	100	40	23	48	71	P
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	25	53	78	P
404185C	PLCS AND AUTOMATION	PP	100	40	18	48	66	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			43	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			38	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			39	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			40	P
404188	PROJECT PHASE I	OR	50	20			44	P

FIRST SEM TOTAL = 550/750

B120053041 CHAVAN SHRADDHA RAJESH SUREKHA ,71500150H ,PICT ,B120053041

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	21	41	62	P
404182	COMPUTER NETWORKS	PP	100	40	20	39	59	P
404183	MICROWAVE ENGINEERING	PP	100	40	22	44	66	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	12	49	61	P
404185C	PLCS AND AUTOMATION	PP	100	40	22	56	78	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			41	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			28	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			44	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			42	P
404188	PROJECT PHASE I	OR	50	20			42	P

FIRST SEM TOTAL = 523/750

B120053042 CHAVAN SNEHAL SHRIKANT SHILPA ,71400788K ,PICT ,B120053042

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	17	40	57	P
404182	COMPUTER NETWORKS	PP	100	40	20	40	60	P
404183	MICROWAVE ENGINEERING	PP	100	40	21	41	62	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	12	41	53	P
404185A	MULTI RATE & ADAP SIGN. PROC	PP	100	40	19	38	57	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			41	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			34	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			42	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			40	P
404188	PROJECT PHASE I	OR	50	20			39	P

FIRST SEM TOTAL = 485/750

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120053043 CHAWLA DIVYA NIHALCHAND SUNITA ,71500151F ,PICT ,B120053043

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	21	48	69	P
404182	COMPUTER NETWORKS	PP	100	40	26	43	69	P
404183	MICROWAVE ENGINEERING	PP	100	40	24	42	66	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	23	48	71	P
404185C	PLCS AND AUTOMATION	PP	100	40	17	64	81	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			44	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			40	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			42	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			44	P
404188	PROJECT PHASE I	OR	50	20			44	P

FIRST SEM TOTAL = 570/750

B120053044 CHHATRE NIKHIL VAIBHAV VRUSHALI ,71400790M ,PICT ,B120053044

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	10	31	41	P
404182	COMPUTER NETWORKS	PP	100	40	16	25	--	F
404183	MICROWAVE ENGINEERING	PP	100	40	15	31	46	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	18	41	59	P
404185C	PLCS AND AUTOMATION	PP	100	40	19	54	73	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			33	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			35	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			32	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			32	P
404188	PROJECT PHASE I	OR	50	20			40	P

FIRST SEM TOTAL = --/750

B120053045 CHIDREWAR RITESH DEELIP KALPANA ,71400791K ,PICT ,B120053045

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	18	41	59	P
404182	COMPUTER NETWORKS	PP	100	40	19	36	55	P
404183	MICROWAVE ENGINEERING	PP	100	40	16	45	61	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	17	36	53	P
404185C	PLCS AND AUTOMATION	PP	100	40	18	51	69	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			36	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			37	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			39	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			30	P
404188	PROJECT PHASE I	OR	50	20			35	P

FIRST SEM TOTAL = 474/750

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120053046 CHINCHKAR JATIN PRAMOD CHITRA ,71400792H ,PICT ,B120053046

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	13	30	43	P
404182	COMPUTER NETWORKS	PP	100	40	16	35	51	P
404183	MICROWAVE ENGINEERING	PP	100	40	17	35	52	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	23	48	71	P
404185C	PLCS AND AUTOMATION	PP	100	40	17	43	60	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			41	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			32	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			39	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			31	P
404188	PROJECT PHASE I	OR	50	20			43	P

FIRST SEM TOTAL = 463/750

B120053047 CHINCHOLE ABHISHEK ANIL SANGEETA ,71500152D ,PICT ,B120053047

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	19	44	63	P
404182	COMPUTER NETWORKS	PP	100	40	19	40	59	P
404183	MICROWAVE ENGINEERING	PP	100	40	23	46	69	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	26	49	75	P
404185C	PLCS AND AUTOMATION	PP	100	40	16	54	70	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			41	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			37	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			44	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			34	P
404188	PROJECT PHASE I	OR	50	20			44	P

FIRST SEM TOTAL = 536/750

B120053048 CHINMAY BALWANT TARWATE NILIMA TARWATE ,71400793F ,PICT ,B120053048

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	14	32	46	P
404182	COMPUTER NETWORKS	PP	100	40	23	35	58	P
404183	MICROWAVE ENGINEERING	PP	100	40	7	39	46	P
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	10	34	44	P
404185C	PLCS AND AUTOMATION	PP	100	40	19	36	55	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			40	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			33	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			35	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			39	P
404188	PROJECT PHASE I	OR	50	20			41	P

FIRST SEM TOTAL = 437/750

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120053049 CHOPADA YASH TUSHAR NEETA ,71400794D ,PICT ,B120053049

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	18	28	46	P
404182	COMPUTER NETWORKS	PP	100	40	25	28	53	P
404183	MICROWAVE ENGINEERING	PP	100	40	18	47	65	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	21	31	52	P
404185C	PLCS AND AUTOMATION	PP	100	40	17	35	52	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			41	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			29	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			38	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			36	P
404188	PROJECT PHASE I	OR	50	20			40	P

FIRST SEM TOTAL = 452/750

B120053050 CHOUGULE AMRUTA ANNASAHEB SUMAN ,71500153B ,PICT ,B120053050

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	13	50	63	P
404182	COMPUTER NETWORKS	PP	100	40	8	37	45	P
404183	MICROWAVE ENGINEERING	PP	100	40	17	38	55	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	20	49	69	P
404185C	PLCS AND AUTOMATION	PP	100	40	19	48	67	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			42	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			27	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			46	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			35	P
404188	PROJECT PHASE I	OR	50	20			40	P

FIRST SEM TOTAL = 489/750

B120053051 DAMATE PRIYANKA VITTHAL MINAKSHI ,71500154L ,PICT ,B120053051

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	16	33	49	P
404182	COMPUTER NETWORKS	PP	100	40	07	32	--	F
404183	MICROWAVE ENGINEERING	PP	100	40	13	46	59	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	14	44	58	P
404185C	PLCS AND AUTOMATION	PP	100	40	12	58	70	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			43	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			32	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			44	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			33	P
404188	PROJECT PHASE I	OR	50	20			38	P

FIRST SEM TOTAL = --/750

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120053052 DARGAD PRATIK PRAMODKUMAR SANGEETA ,71400797J ,PICT ,B120053052

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	13	38	51	P
404182	COMPUTER NETWORKS	PP	100	40	16	36	52	P
404183	MICROWAVE ENGINEERING	PP	100	40	15	37	52	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	17	46	63	P
404185C	PLCS AND AUTOMATION	PP	100	40	24	40	64	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			44	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			38	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			38	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			34	P
404188	PROJECT PHASE I	OR	50	20			43	P

FIRST SEM TOTAL = 479/750

B120053053 DEEPAK DEVENDRA PATEL SHANTI ,71400803G ,PICT ,B120053053

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	15	38	53	P
404182	COMPUTER NETWORKS	PP	100	40	20	38	58	P
404183	MICROWAVE ENGINEERING	PP	100	40	13	36	49	P
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	22	46	68	P
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40		43	43	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			41	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			35	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			44	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			39	P
404188	PROJECT PHASE I	OR	50	20			45	P

FIRST SEM TOTAL = 475/750

B120053054 DEORE AISHWARYA PANDURANG VANDANA ,71400806M ,PICT ,B120053054

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	12	44	56	P
404182	COMPUTER NETWORKS	PP	100	40	18	40	58	P
404183	MICROWAVE ENGINEERING	PP	100	40	21	48	69	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	23	50	73	P
404185C	PLCS AND AUTOMATION	PP	100	40	16	56	72	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			39	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			37	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			36	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			40	P
404188	PROJECT PHASE I	OR	50	20			38	P

FIRST SEM TOTAL = 518/750

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120053055 DESHMUKH TANMAY MANOJ DESHMUKH MUKTA MANOJ ,71400810K ,PICT ,B120053055

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	13	35	48	P
404182	COMPUTER NETWORKS	PP	100	40	23	36	59	P
404183	MICROWAVE ENGINEERING	PP	100	40	20	36	56	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	20	48	68	P
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40		43	43	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			36	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			37	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			35	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			34	P
404188	PROJECT PHASE I	OR	50	20			42	P

FIRST SEM TOTAL = 458/750

B120053056 DESHPANDE KETAN VIJAY APARNA ,71400813D ,PICT ,B120053056

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	13	33	46	P
404182	COMPUTER NETWORKS	PP	100	40	19	32	51	P
404183	MICROWAVE ENGINEERING	PP	100	40	15	37	52	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	11	47	58	P
404185A	MULTI RATE & ADAP SIGN. PROC	PP	100	40	9	36	45	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			36	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			40	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			38	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			32	P
404188	PROJECT PHASE I	OR	50	20			40	P

FIRST SEM TOTAL = 438/750

B120053057 DHEBE DHANASHREE LAXMAN SUNITA ,71500155J ,PICT ,B120053057

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	13	48	61	P
404182	COMPUTER NETWORKS	PP	100	40	16	44	60	P
404183	MICROWAVE ENGINEERING	PP	100	40	21	48	69	P
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	22	54	76	P
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40		46	46	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			40	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			38	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			41	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			38	P
404188	PROJECT PHASE I	OR	50	20			42	P

FIRST SEM TOTAL = 511/750

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120053058 DHORAN ANUPKUMAR UDDHAORAO SWAROOPA ,71400822C ,PICT ,B120053058

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	7	37	44	P
404182	COMPUTER NETWORKS	PP	100	40	21	39	60	P
404183	MICROWAVE ENGINEERING	PP	100	40	17	42	59	P
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	18	44	62	P
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	AA	39	--	F
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			42	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			38	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			42	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			37	P
404188	PROJECT PHASE I	OR	50	20			40	P

FIRST SEM TOTAL = --/750

B120053059 DHRUV PADIA NEETA ,71400823M ,PICT ,B120053059

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	10	41	51	P
404182	COMPUTER NETWORKS	PP	100	40	14	40	54	P
404183	MICROWAVE ENGINEERING	PP	100	40	15	37	52	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	14	34	48	P
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	AA	33	--	F
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			36	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			29	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			34	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			32	P
404188	PROJECT PHASE I	OR	50	20			40	P

FIRST SEM TOTAL = --/750

B120053060 DOMPLE AJAY BALAJI PUSHPA ,71400824K ,PICT ,B120053060

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	12	37	49	P
404182	COMPUTER NETWORKS	PP	100	40	14	41	55	P
404183	MICROWAVE ENGINEERING	PP	100	40	18	39	57	P
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	20	41	61	P
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40		49	49	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			41	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			31	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			33	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			32	P
404188	PROJECT PHASE I	OR	50	20			30	P

FIRST SEM TOTAL = 438/750

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120053061 DUKARE VAIBHAV BABAN DUKARE AASHA BABAN ,71400826F ,PICT ,B120053061

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	21	49	70	P
404182	COMPUTER NETWORKS	PP	100	40	23	45	68	P
404183	MICROWAVE ENGINEERING	PP	100	40	17	41	58	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	19	33	52	P
404185C	PLCS AND AUTOMATION	PP	100	40	19	48	67	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			42	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			32	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			39	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			33	P
404188	PROJECT PHASE I	OR	50	20			39	P

FIRST SEM TOTAL = 500/750

B120053062 EKRE ADITI GOPAL CHARUSHILA ,71400829L ,PICT ,B120053062

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	21	44	65	P
404182	COMPUTER NETWORKS	PP	100	40	24	38	62	P
404183	MICROWAVE ENGINEERING	PP	100	40	22	42	64	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	21	49	70	P
404185C	PLCS AND AUTOMATION	PP	100	40	25	54	79	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			42	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			37	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			41	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			37	P
404188	PROJECT PHASE I	OR	50	20			45	P

FIRST SEM TOTAL = 542/750

B120053063 GADHE OM DEEPAK INDUTAI ,71400834G ,PICT ,B120053063

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	4	44	48	P
404182	COMPUTER NETWORKS	PP	100	40	17	40	57	P
404183	MICROWAVE ENGINEERING	PP	100	40	20	33	53	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	14	44	58	P
404185C	PLCS AND AUTOMATION	PP	100	40	17	50	67	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			38	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			37	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			38	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			33	P
404188	PROJECT PHASE I	OR	50	20			32	P

FIRST SEM TOTAL = 461/750

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120053064 GAGARE ONKAR PRAKASH LATA ,71400835E ,PICT ,B120053064

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	15	37	52	P
404182	COMPUTER NETWORKS	PP	100	40	20	43	63	P
404183	MICROWAVE ENGINEERING	PP	100	40	17	34	51	P
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	18	35	53	P
404185C	PLCS AND AUTOMATION	PP	100	40	18	51	69	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			40	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			39	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			36	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			33	P
404188	PROJECT PHASE I	OR	50	20			41	P

FIRST SEM TOTAL = 477/750

B120053065 GAHIWAL RAHUL NARENDRA PUSHPA ,71500156G ,PICT ,B120053065

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	13	41	54	P
404182	COMPUTER NETWORKS	PP	100	40	20	50	70	P
404183	MICROWAVE ENGINEERING	PP	100	40	17	35	52	P
404184C	SOFTWARE DEFINED RADIO	PP	100	40	25	37	62	P
404185C	PLCS AND AUTOMATION	PP	100	40	17	50	67	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			40	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			31	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			39	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			30	P
404188	PROJECT PHASE I	OR	50	20			40	P

FIRST SEM TOTAL = 485/750

B120053066 GAIKWAD PARAG KIRAN GAIKWAD PUSHPA KIRAN ,71300897M ,PICT ,B120053066

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	18	50	68	P
404182	COMPUTER NETWORKS	PP	100	40	16	56	72	P
404183	MICROWAVE ENGINEERING	PP	100	40	16	47	63	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	20	61	81	P
404185C	PLCS AND AUTOMATION	PP	100	40	22	55	77	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			35	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			29	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			33	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			33	P
404188	PROJECT PHASE I	OR	50	20			38	P

FIRST SEM TOTAL = 529/750

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120053067 GAIKWAD RADHIKA RAMESH SUNITA ,71400837M ,PICT ,B120053067

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	18	47	65	P
404182	COMPUTER NETWORKS	PP	100	40	20	44	64	P
404183	MICROWAVE ENGINEERING	PP	100	40	20	46	66	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	20	48	68	P
404185C	PLCS AND AUTOMATION	PP	100	40	19	46	65	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			41	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			32	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			42	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			44	P
404188	PROJECT PHASE I	OR	50	20			44	P

FIRST SEM TOTAL = 531/750

B120053068 GAIKWAD RATNAKUMAR SURENDRA SEEMA ,71500157E ,PICT ,B120053068

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	13	45	58	P
404182	COMPUTER NETWORKS	PP	100	40	13	45	58	P
404183	MICROWAVE ENGINEERING	PP	100	40	18	50	68	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	19	51	70	P
404185C	PLCS AND AUTOMATION	PP	100	40	23	56	79	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			45	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			36	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			45	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			32	P
404188	PROJECT PHASE I	OR	50	20			45	P

FIRST SEM TOTAL = 536/750

B120053069 GANGARDE AKSHAY AVINASH JYOTI ,71400841K ,PICT ,B120053069

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	5	41	46	P
404182	COMPUTER NETWORKS	PP	100	40	18	40	58	P
404183	MICROWAVE ENGINEERING	PP	100	40	16	49	65	P
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	14	37	51	P
404185C	PLCS AND AUTOMATION	PP	100	40	16	40	56	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			40	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			27	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			41	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			37	P
404188	PROJECT PHASE I	OR	50	20			39	P

FIRST SEM TOTAL = 460/750

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120053070 GAWAI KAILAS PANDURANG SUSHILA ,71500158C ,PICT ,B120053070

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	12	34	46	P
404182	COMPUTER NETWORKS	PP	100	40	17	30	47	P
404183	MICROWAVE ENGINEERING	PP	100	40	17	36	53	P
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	13	28	41	P
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	AA	38	--	F
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			39	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			26	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			36	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			27	P
404188	PROJECT PHASE I	OR	50	20			36	P

FIRST SEM TOTAL = --/750

B120053071 GHADGE RACHNA MOHAN NAMRATA ,71500159M ,PICT ,B120053071

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	22	51	73	P
404182	COMPUTER NETWORKS	PP	100	40	22	56	78	P
404183	MICROWAVE ENGINEERING	PP	100	40	21	51	72	P
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	24	45	69	P
404185C	PLCS AND AUTOMATION	PP	100	40	15	62	77	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			43	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			32	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			42	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			40	P
404188	PROJECT PHASE I	OR	50	20			42	P

FIRST SEM TOTAL = 568/750

B120053072 GHODESWAR AKSHAY SUBHASH KALPANA ,71400844D ,PICT ,B120053072

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	12	47	59	P
404182	COMPUTER NETWORKS	PP	100	40	20	53	73	P
404183	MICROWAVE ENGINEERING	PP	100	40	23	39	62	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	9	44	53	P
404185C	PLCS AND AUTOMATION	PP	100	40	19	58	77	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			37	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			27	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			36	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			28	P
404188	PROJECT PHASE I	OR	50	20			45	P

FIRST SEM TOTAL = 497/750

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120053073 GINDE SAURABH VIJAY RASHMI ,71400849E ,PICT ,B120053073

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	19	42	61	P
404182	COMPUTER NETWORKS	PP	100	40	21	50	71	P
404183	MICROWAVE ENGINEERING	PP	100	40	21	42	63	P
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	25	41	66	P
404185C	PLCS AND AUTOMATION	PP	100	40	12	52	64	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			36	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			29	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			42	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			41	P
404188	PROJECT PHASE I	OR	50	20			42	P

FIRST SEM TOTAL = 515/750

B120053074 GODSE KETKI VILAS SANDHYA ,71400852E ,PICT ,B120053074

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	20	42	62	P
404182	COMPUTER NETWORKS	PP	100	40	23	51	74	P
404183	MICROWAVE ENGINEERING	PP	100	40	24	39	63	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	23	56	79	P
404185C	PLCS AND AUTOMATION	PP	100	40	22	57	79	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			45	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			34	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			42	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			40	P
404188	PROJECT PHASE I	OR	50	20			46	P

FIRST SEM TOTAL = 564/750

B120053075 GOKHALE ANAY RAVINDRA MANJIRI ,71400853C ,PICT ,B120053075

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	10	32	42	P
404182	COMPUTER NETWORKS	PP	100	40	18	42	60	P
404183	MICROWAVE ENGINEERING	PP	100	40	12	36	48	P
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	13	34	47	P
404185D	ARTIFICIAL INTELLIGENCE	PP	100	40	18	36	54	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			37	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			31	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			36	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			40	P
404188	PROJECT PHASE I	OR	50	20			42	P

FIRST SEM TOTAL = 437/750

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120053076 GOKHE MANGESH DNYANESHWAR PADMA ,71500160E ,PICT ,B120053076

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	18	42	60	P
404182	COMPUTER NETWORKS	PP	100	40	16	46	62	P
404183	MICROWAVE ENGINEERING	PP	100	40	20	43	63	P
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	21	37	58	P
404185C	PLCS AND AUTOMATION	PP	100	40	10	50	60	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			41	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			32	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			42	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			37	P
404188	PROJECT PHASE I	OR	50	20			38	P

FIRST SEM TOTAL = 493/750

B120053077 GOLHAR PAWAN RAMESH RANJANA ,71400856H ,PICT ,B120053077

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	07	30	--	F
404182	COMPUTER NETWORKS	PP	100	40	19	39	58	P
404183	MICROWAVE ENGINEERING	PP	100	40	12	31	43	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	12	33	45	P
404185C	PLCS AND AUTOMATION	PP	100	40	8	41	49	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			33	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			25	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			28	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			10	F
404188	PROJECT PHASE I	OR	50	20			32	P

FIRST SEM TOTAL = --/750

B120053078 GORE UNNATI PRAVIN SAAE ,71500161C ,PICT ,B120053078

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	22	52	74	P
404182	COMPUTER NETWORKS	PP	100	40	27	58	85	P
404183	MICROWAVE ENGINEERING	PP	100	40	18	43	61	P
404184C	SOFTWARE DEFINED RADIO	PP	100	40	28	50	78	P
404185C	PLCS AND AUTOMATION	PP	100	40	22	49	71	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			41	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			34	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			42	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			32	P
404188	PROJECT PHASE I	OR	50	20			44	P

FIRST SEM TOTAL = 562/750

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120053079 GUJAR SANKET RAJENDRA MADHURI ,71400861D ,PICT ,B120053079

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	11	48	59	P
404182	COMPUTER NETWORKS	PP	100	40	20	56	76	P
404183	MICROWAVE ENGINEERING	PP	100	40	18	46	64	P
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	15	38	53	P
404185D	ARTIFICIAL INTELLIGENCE	PP	100	40	22	45	67	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			42	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			36	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			45	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			40	P
404188	PROJECT PHASE I	OR	50	20			43	P

FIRST SEM TOTAL = 525/750

B120053080 HEDA00 JATIN SUNIL SUMEDHA ,71400871M ,PICT ,B120053080

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	8	42	50	P
404182	COMPUTER NETWORKS	PP	100	40	21	49	70	P
404183	MICROWAVE ENGINEERING	PP	100	40	13	45	58	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	20	47	67	P
404185C	PLCS AND AUTOMATION	PP	100	40	17	43	60	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			38	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			37	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			42	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			31	P
404188	PROJECT PHASE I	OR	50	20			45	P

FIRST SEM TOTAL = 498/750

B120053081 HURKAT RUSHABH RAJESH PRANITA ,71400874F ,PICT ,B120053081

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	9	33	42	P
404182	COMPUTER NETWORKS	PP	100	40	15	42	57	P
404183	MICROWAVE ENGINEERING	PP	100	40	14	33	47	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	19	37	56	P
404185C	PLCS AND AUTOMATION	PP	100	40	19	37	56	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			40	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			32	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			36	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			36	P
404188	PROJECT PHASE I	OR	50	20			36	P

FIRST SEM TOTAL = 438/750

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120053082 INGAWALE ADITYA BAPURAO KANCHAN ,71400876B ,PICT ,B120053082

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	17	45	62	P
404182	COMPUTER NETWORKS	PP	100	40	23	57	80	P
404183	MICROWAVE ENGINEERING	PP	100	40	22	43	65	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	18	50	68	P
404185C	PLCS AND AUTOMATION	PP	100	40	21	48	69	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			39	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			33	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			33	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			33	P
404188	PROJECT PHASE I	OR	50	20			39	P

FIRST SEM TOTAL = 521/750

B120053083 JADHAV SHARAYU MOHAN SANGITA ,71500162M ,PICT ,B120053083

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	16	44	60	P
404182	COMPUTER NETWORKS	PP	100	40	21	52	73	P
404183	MICROWAVE ENGINEERING	PP	100	40	19	42	61	P
404184C	SOFTWARE DEFINED RADIO	PP	100	40	24	49	73	P
404185C	PLCS AND AUTOMATION	PP	100	40	22	50	72	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			42	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			29	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			37	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			35	P
404188	PROJECT PHASE I	OR	50	20			41	P

FIRST SEM TOTAL = 523/750

B120053084 JADHAV SHRADDHA SAYAJI SANGITA ,71500163K ,PICT ,B120053084

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	25	51	76	P
404182	COMPUTER NETWORKS	PP	100	40	18	57	75	P
404183	MICROWAVE ENGINEERING	PP	100	40	23	52	75	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	20	54	74	P
404185C	PLCS AND AUTOMATION	PP	100	40	19	49	68	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			41	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			38	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			43	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			35	P
404188	PROJECT PHASE I	OR	50	20			35	P

FIRST SEM TOTAL = 560/750

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120053085 JADHAV YASH RAM JADHAV LAXMI RAM ,71400879G ,PICT ,B120053085

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	10	28	--	F
404182	COMPUTER NETWORKS	PP	100	40	12	08	--	F
404183	MICROWAVE ENGINEERING	PP	100	40	14	36	50	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	08	00	--	F
404185C	PLCS AND AUTOMATION	PP	100	40	12	39	51	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			38	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			28	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			42	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			22	P
404188	PROJECT PHASE I	OR	50	20			31	P

FIRST SEM TOTAL = --/750

B120053086 JAGADE NIKHIL KRISHNA KALPANA ,71400880L ,PICT ,B120053086

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	20	41	61	P
404182	COMPUTER NETWORKS	PP	100	40	21	52	73	P
404183	MICROWAVE ENGINEERING	PP	100	40	20	43	63	P
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	16	45	61	P
404185C	PLCS AND AUTOMATION	PP	100	40	18	41	59	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			39	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			33	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			36	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			35	P
404188	PROJECT PHASE I	OR	50	20			38	P

FIRST SEM TOTAL = 498/750

B120053087 JAIN SAKSHI MANISH RENU JAIN ,71400886K ,PICT ,B120053087

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	10	34	44	P
404182	COMPUTER NETWORKS	PP	100	40	22	41	63	P
404183	MICROWAVE ENGINEERING	PP	100	40	11	42	53	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	18	38	56	P
404185C	PLCS AND AUTOMATION	PP	100	40	22	43	65	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			40	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			28	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			26	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			35	P
404188	PROJECT PHASE I	OR	50	20			38	P

FIRST SEM TOTAL = 448/750

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120053088 JAWADE SHIVRAJ ANIL SADHANA ,71400889D ,PICT ,B120053088

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	7	38	45	P
404182	COMPUTER NETWORKS	PP	100	40	19	35	54	P
404183	MICROWAVE ENGINEERING	PP	100	40	11	34	45	P
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	13	33	46	P
404185D	ARTIFICIAL INTELLIGENCE	PP	100	40	18	33	51	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			42	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			36	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			38	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			39	P
404188	PROJECT PHASE I	OR	50	20			44	P

FIRST SEM TOTAL = 440/750

B120053089 JORI NEETA ROHIDAS UJWALA ,71500164H ,PICT ,B120053089

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	19	52	71	P
404182	COMPUTER NETWORKS	PP	100	40	19	61	80	P
404183	MICROWAVE ENGINEERING	PP	100	40	21	47	68	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	24	57	81	P
404185C	PLCS AND AUTOMATION	PP	100	40	16	50	66	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			41	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			34	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			44	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			36	P
404188	PROJECT PHASE I	OR	50	20			42	P

FIRST SEM TOTAL = 563/750

B120053090 JUHI BANSAL RACHNA ,71400897E ,PICT ,B120053090

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	16	34	50	P
404182	COMPUTER NETWORKS	PP	100	40	23	46	69	P
404183	MICROWAVE ENGINEERING	PP	100	40	13	28	41	P
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	13	39	52	P
404185C	PLCS AND AUTOMATION	PP	100	40	13	42	55	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			36	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			34	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			35	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			34	P
404188	PROJECT PHASE I	OR	50	20			39	P

FIRST SEM TOTAL = 445/750

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120053091 KAD SAURABH BHAUSAHEB LALITA ,71500165F ,PICT ,B120053091

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	22	40	62	P
404182	COMPUTER NETWORKS	PP	100	40	24	50	74	P
404183	MICROWAVE ENGINEERING	PP	100	40	19	51	70	P
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	22	46	68	P
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40		46	46	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			44	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			40	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			46	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			45	P
404188	PROJECT PHASE I	OR	50	20			40	P

FIRST SEM TOTAL = 535/750

B120053092 KADALE SHIVANI DILIP CHITRAREKHA ,71400899M ,PICT ,B120053092

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	19	24	--	F
404182	COMPUTER NETWORKS	PP	100	40	26	52	78	P
404183	MICROWAVE ENGINEERING	PP	100	40	15	48	63	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	13	37	50	P
404185D	ARTIFICIAL INTELLIGENCE	PP	100	40	21	42	63	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			42	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			25	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			42	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			33	P
404188	PROJECT PHASE I	OR	50	20			37	P

FIRST SEM TOTAL = --/750

B120053093 KADAM PRIYANKA RAJENDRA ANITA ,71400901G ,PICT ,B120053093

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	11	41	52	P
404182	COMPUTER NETWORKS	PP	100	40	24	55	79	P
404183	MICROWAVE ENGINEERING	PP	100	40	22	49	71	P
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	16	46	62	P
404185C	PLCS AND AUTOMATION	PP	100	40	18	51	69	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			44	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			37	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			42	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			36	P
404188	PROJECT PHASE I	OR	50	20			40	P

FIRST SEM TOTAL = 532/750

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120053094 KADLAG AISHWARYA RAMRAO SNEHA ,71400904M ,PICT ,B120053094

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	12	46	58	P
404182	COMPUTER NETWORKS	PP	100	40	23	51	74	P
404183	MICROWAVE ENGINEERING	PP	100	40	28	53	81	P
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	20	43	63	P
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40		47	47	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			40	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			38	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			45	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			37	P
404188	PROJECT PHASE I	OR	50	20			43	P

FIRST SEM TOTAL = 526/750

B120053095 KAKADE NIKITA SUNIL SNEHAL ,71500167B ,PICT ,B120053095

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	14	31	45	P
404182	COMPUTER NETWORKS	PP	100	40	10	33	43	P
404183	MICROWAVE ENGINEERING	PP	100	40	13	37	50	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	17	43	60	P
404185C	PLCS AND AUTOMATION	PP	100	40	13	36	49	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			41	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			27	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			43	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			38	P
404188	PROJECT PHASE I	OR	50	20			40	P

FIRST SEM TOTAL = 436/750

B120053096 KALE SUSHMITA ANKUSH SUNANDA ,71500168L ,PICT ,B120053096

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	17	31	48	P
404182	COMPUTER NETWORKS	PP	100	40	22	47	69	P
404183	MICROWAVE ENGINEERING	PP	100	40	15	48	63	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	18	33	51	P
404185C	PLCS AND AUTOMATION	PP	100	40	21	43	64	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			42	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			24	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			44	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			31	P
404188	PROJECT PHASE I	OR	50	20			36	P

FIRST SEM TOTAL = 472/750

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120053097 KALE VARAD MAHESH JYOTI ,71400909B ,PICT ,B120053097

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	14	37	51	P
404182	COMPUTER NETWORKS	PP	100	40	21	44	65	P
404183	MICROWAVE ENGINEERING	PP	100	40	18	34	52	P
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	20	38	58	P
404185C	PLCS AND AUTOMATION	PP	100	40	21	46	67	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			41	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			37	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			40	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			37	P
404188	PROJECT PHASE I	OR	50	20			45	P

FIRST SEM TOTAL = 493/750

B120053098 KAMBLE ASHLESHA YASHWANT MADHURI ,71400911D ,PICT ,B120053098

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	09	30	--	F
404182	COMPUTER NETWORKS	PP	100	40	26	46	72	P
404183	MICROWAVE ENGINEERING	PP	100	40	19	39	58	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	12	33	45	P
404185C	PLCS AND AUTOMATION	PP	100	40	18	48	66	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			42	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			24	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			38	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			32	P
404188	PROJECT PHASE I	OR	50	20			38	P

FIRST SEM TOTAL = --/750

B120053099 KAMBLE SHUBHAM SATISH VAISHALI ,71400914J ,PICT ,B120053099

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	15	41	56	P
404182	COMPUTER NETWORKS	PP	100	40	25	43	68	P
404183	MICROWAVE ENGINEERING	PP	100	40	20	42	62	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	17	27	--	F
404185C	PLCS AND AUTOMATION	PP	100	40	19	40	59	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			42	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			28	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			41	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			35	P
404188	PROJECT PHASE I	OR	50	20			38	P

FIRST SEM TOTAL = --/750

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120053100 KANDALKAR SNEHAL NITIN SUJATA ,71400917C ,PICT ,B120053100

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	14	20	--	F
404182	COMPUTER NETWORKS	PP	100	40	17	46	63	P
404183	MICROWAVE ENGINEERING	PP	100	40	16	41	57	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	11	36	47	P
404185C	PLCS AND AUTOMATION	PP	100	40	19	41	60	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			37	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			35	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			35	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			35	P
404188	PROJECT PHASE I	OR	50	20			41	P

FIRST SEM TOTAL = --/750

B120053101 KARANDE ADWAIT DATTATRAY MADHUMATI ,71400922K ,PICT ,B120053101

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	13	34	47	P
404182	COMPUTER NETWORKS	PP	100	40	24	48	72	P
404183	MICROWAVE ENGINEERING	PP	100	40	22	47	69	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	18	47	65	P
404185A	MULTI RATE & ADAP SIGN. PROC	PP	100	40	12	57	69	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			39	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			36	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			39	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			41	P
404188	PROJECT PHASE I	OR	50	20			44	P

FIRST SEM TOTAL = 521/750

B120053102 KARMARKAR AMEY VINAY MANJUSHA ,71400926B ,PICT ,B120053102

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	13	24	--	F
404182	COMPUTER NETWORKS	PP	100	40	25	43	68	P
404183	MICROWAVE ENGINEERING	PP	100	40	20	41	61	P
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	19	41	60	P
404185C	PLCS AND AUTOMATION	PP	100	40	18	38	56	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			43	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			32	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			43	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			35	P
404188	PROJECT PHASE I	OR	50	20			44	P

FIRST SEM TOTAL = --/750

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120053103 KATARE SURAJ SHRIKANT LAXMI ,71400931J ,PICT ,B120053103

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	09	28	--	F
404182	COMPUTER NETWORKS	PP	100	40	12	39	51	P
404183	MICROWAVE ENGINEERING	PP	100	40	13	33	46	P
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	13	30	43	P
404185C	PLCS AND AUTOMATION	PP	100	40	16	36	52	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			41	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			36	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			41	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			38	P
404188	PROJECT PHASE I	OR	50	20			45	P

FIRST SEM TOTAL = --/750

B120053104 KATARIYA TRUPTI PRAMOD ALKA ,71400932G ,PICT ,B120053104

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	11	43	54	P
404182	COMPUTER NETWORKS	PP	100	40	26	47	73	P
404183	MICROWAVE ENGINEERING	PP	100	40	13	50	63	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	17	45	62	P
404185A	MULTI RATE & ADAP SIGN. PROC	PP	100	40	21	50	71	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			40	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			42	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			39	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			35	P
404188	PROJECT PHASE I	OR	50	20			40	P

FIRST SEM TOTAL = 519/750

B120053105 KATE AKASH ABHIMANYU LAXMI ,71500170B ,PICT ,B120053105

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	17	41	58	P
404182	COMPUTER NETWORKS	PP	100	40	16	51	67	P
404183	MICROWAVE ENGINEERING	PP	100	40	16	43	59	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	25	45	70	P
404185C	PLCS AND AUTOMATION	PP	100	40	21	52	73	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			37	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			26	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			43	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			40	P
404188	PROJECT PHASE I	OR	50	20			40	P

FIRST SEM TOTAL = 513/750

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120053106 KAVAR DARSHAN SUBHASH SADHANA ,71400798G ,PICT ,B120053106

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	15	17	--	F
404182	COMPUTER NETWORKS	PP	100	40	19	38	57	P
404183	MICROWAVE ENGINEERING	PP	100	40	6	46	52	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	15	33	48	P
404185C	PLCS AND AUTOMATION	PP	100	40	13	46	59	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			39	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			26	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			35	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			12	F
404188	PROJECT PHASE I	OR	50	20			40	P

FIRST SEM TOTAL = --/750

B120053108 KENJALE YOGITA YASHWANT MEENA ,71500171L ,PICT ,B120053108

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40		45	45	P
404182	COMPUTER NETWORKS	PP	100	40		53	53	P
404183	MICROWAVE ENGINEERING	PP	100	40		46	46	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40		46	46	P
404185C	PLCS AND AUTOMATION	PP	100	40		49	49	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			43	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			29	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			37	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			35	P
404188	PROJECT PHASE I	OR	50	20			36	P

FIRST SEM TOTAL = 419/750

B120053109 KHADSE AKSHAY AJABRAO VANDANA ,71400937H ,PICT ,B120053109

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	9	35	44	P
404182	COMPUTER NETWORKS	PP	100	40	20	44	64	P
404183	MICROWAVE ENGINEERING	PP	100	40	10	47	57	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	11	36	47	P
404185C	PLCS AND AUTOMATION	PP	100	40	17	38	55	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			35	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			22	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			30	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			26	P
404188	PROJECT PHASE I	OR	50	20			27	P

FIRST SEM TOTAL = 407/750

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120053110 KHAIRNAR SALONI VIJAY RAJSHRI ,71400938F ,PICT ,B120053110

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	10	38	48	P
404182	COMPUTER NETWORKS	PP	100	40	20	38	58	P
404183	MICROWAVE ENGINEERING	PP	100	40	12	44	56	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	17	31	48	P
404185C	PLCS AND AUTOMATION	PP	100	40	23	42	65	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			37	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			32	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			34	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			33	P
404188	PROJECT PHASE I	OR	50	20			41	P

FIRST SEM TOTAL = 452/750

B120053111 KHANJIRE PRIYANKA AJIT PADMASHREE ,71500172J ,PICT ,B120053111

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	20	51	71	P
404182	COMPUTER NETWORKS	PP	100	40	22	39	61	P
404183	MICROWAVE ENGINEERING	PP	100	40	16	44	60	P
404184C	SOFTWARE DEFINED RADIO	PP	100	40	23	47	70	P
404185C	PLCS AND AUTOMATION	PP	100	40	18	50	68	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			41	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			31	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			43	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			37	P
404188	PROJECT PHASE I	OR	50	20			41	P

FIRST SEM TOTAL = 523/750

B120053112 KHANZODE ROHIT SUCHET SHRUTI ,71400942D ,PICT ,B120053112

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	8	39	47	P
404182	COMPUTER NETWORKS	PP	100	40	23	49	72	P
404183	MICROWAVE ENGINEERING	PP	100	40	08	31	--	F
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	13	48	61	P
404185A	MULTI RATE & ADAP SIGN. PROC	PP	100	40	12	20	--	F
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			38	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			31	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			38	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			35	P
404188	PROJECT PHASE I	OR	50	20			41	P

FIRST SEM TOTAL = --/750

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120053113 KHAPRE AKASH AMBADAS MEENA ,71500173G ,PICT ,B120053113

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	8	43	51	P
404182	COMPUTER NETWORKS	PP	100	40	17	37	54	P
404183	MICROWAVE ENGINEERING	PP	100	40	15	33	48	P
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	21	41	62	P
404185C	PLCS AND AUTOMATION	PP	100	40	18	48	66	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			34	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			27	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			33	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			25	P
404188	PROJECT PHASE I	OR	50	20			38	P

FIRST SEM TOTAL = 438/750

B120053114 KHARGE SNEHAL SUBHASH KALPANA ,71500174E ,PICT ,B120053114

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	23	56	79	P
404182	COMPUTER NETWORKS	PP	100	40	22	47	69	P
404183	MICROWAVE ENGINEERING	PP	100	40	21	46	67	P
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	27	55	82	P
404185C	PLCS AND AUTOMATION	PP	100	40	22	52	74	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			40	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			35	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			45	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			43	P
404188	PROJECT PHASE I	OR	50	20			39	P

FIRST SEM TOTAL = 573/750

B120053115 KHIRATKAR ANKITA ASHOK SANGITA ,71400946G ,PICT ,B120053115

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	13	37	50	P
404182	COMPUTER NETWORKS	PP	100	40	25	42	67	P
404183	MICROWAVE ENGINEERING	PP	100	40	19	36	55	P
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	25	33	58	P
404185C	PLCS AND AUTOMATION	PP	100	40	17	37	54	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			41	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			39	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			37	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			36	P
404188	PROJECT PHASE I	OR	50	20			45	P

FIRST SEM TOTAL = 482/750

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120053116 KIRAN MISAL ASHA ,71400952M ,PICT ,B120053116

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	14	46	60	P
404182	COMPUTER NETWORKS	PP	100	40	11	44	55	P
404183	MICROWAVE ENGINEERING	PP	100	40	23	46	69	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	13	39	52	P
404185C	PLCS AND AUTOMATION	PP	100	40	20	38	58	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			38	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			32	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			34	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			32	P
404188	PROJECT PHASE I	OR	50	20			41	P

FIRST SEM TOTAL = 471/750

B120053117 KORDE ANIKET UMAJI SULOCHANA ,71500175C ,PICT ,B120053117

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	17	50	67	P
404182	COMPUTER NETWORKS	PP	100	40	13	46	59	P
404183	MICROWAVE ENGINEERING	PP	100	40	19	56	75	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	9	38	47	P
404185C	PLCS AND AUTOMATION	PP	100	40	18	44	62	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			36	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			35	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			34	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			33	P
404188	PROJECT PHASE I	OR	50	20			33	P

FIRST SEM TOTAL = 481/750

B120053118 KOTHADIA SHRUTI MANISH SUPRIYA ,71400958L ,PICT ,B120053118

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	20	46	66	P
404182	COMPUTER NETWORKS	PP	100	40	26	48	74	P
404183	MICROWAVE ENGINEERING	PP	100	40	25	61	86	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	24	49	73	P
404185D	ARTIFICIAL INTELLIGENCE	PP	100	40	21	43	64	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			43	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			38	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			37	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			36	P
404188	PROJECT PHASE I	OR	50	20			45	P

FIRST SEM TOTAL = 562/750

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120053119 KSHIRSAGAR GANESH BHAGWAT MANGAL ,71400963G ,PICT ,B120053119

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	12	51	63	P
404182	COMPUTER NETWORKS	PP	100	40	16	32	48	P
404183	MICROWAVE ENGINEERING	PP	100	40	13	49	62	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	9	36	45	P
404185C	PLCS AND AUTOMATION	PP	100	40	19	43	62	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			40	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			30	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			35	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			37	P
404188	PROJECT PHASE I	OR	50	20			37	P

FIRST SEM TOTAL = 459/750

B120053120 KULKARNI MANISH ARUN KALPANA ,71400965C ,PICT ,B120053120

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	14	38	52	P
404182	COMPUTER NETWORKS	PP	100	40	20	48	68	P
404183	MICROWAVE ENGINEERING	PP	100	40	14	45	59	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	20	40	60	P
404185A	MULTI RATE & ADAP SIGN. PROC	PP	100	40	17	32	49	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			43	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			34	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			42	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			38	P
404188	PROJECT PHASE I	OR	50	20			44	P

FIRST SEM TOTAL = 489/750

B120053121 KULKARNI PRATIK DEEPAK VAISHALI ,71400969F ,PICT ,B120053121

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	12	42	54	P
404182	COMPUTER NETWORKS	PP	100	40	25	42	67	P
404183	MICROWAVE ENGINEERING	PP	100	40	19	47	66	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	18	46	64	P
404185A	MULTI RATE & ADAP SIGN. PROC	PP	100	40	12	33	45	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			39	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			33	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			34	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			36	P
404188	PROJECT PHASE I	OR	50	20			39	P

FIRST SEM TOTAL = 477/750

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120053122 KULKARNI RUSHIKESH LAXMIKANT NEHA ,71400970K ,PICT ,B120053122

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	09	29	--	F
404182	COMPUTER NETWORKS	PP	100	40	15	37	52	P
404183	MICROWAVE ENGINEERING	PP	100	40	10	33	43	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	12	43	55	P
404185C	PLCS AND AUTOMATION	PP	100	40	16	34	50	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			39	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			37	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			34	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			30	P
404188	PROJECT PHASE I	OR	50	20			32	P

FIRST SEM TOTAL = --/750

B120053123 KULKARNI SIMRAN SATISH NANDA ,71400973D ,PICT ,B120053123

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	08	30	--	F
404182	COMPUTER NETWORKS	PP	100	40	27	38	65	P
404183	MICROWAVE ENGINEERING	PP	100	40	18	51	69	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	24	46	70	P
404185A	MULTI RATE & ADAP SIGN. PROC	PP	100	40	21	33	54	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			39	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			39	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			48	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			45	P
404188	PROJECT PHASE I	OR	50	20			43	P

FIRST SEM TOTAL = --/750

B120053124 KURANDE KETAKI UMESH NEELIMA ,71400978E ,PICT ,B120053124

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	17	46	63	P
404182	COMPUTER NETWORKS	PP	100	40	23	45	68	P
404183	MICROWAVE ENGINEERING	PP	100	40	20	48	68	P
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	27	47	74	P
404185C	PLCS AND AUTOMATION	PP	100	40	13	43	56	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			47	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			41	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			47	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			46	P
404188	PROJECT PHASE I	OR	50	20			43	P

FIRST SEM TOTAL = 553/750

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120053125 LAKHKAR RADHIKA ANANDRAO ARUNA ,71400982C ,PICT ,B120053125

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	12	33	45	P
404182	COMPUTER NETWORKS	PP	100	40	26	38	64	P
404183	MICROWAVE ENGINEERING	PP	100	40	18	42	60	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	21	49	70	P
404185A	MULTI RATE & ADAP SIGN. PROC	PP	100	40	10	29	--	F
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			43	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			39	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			38	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			40	P
404188	PROJECT PHASE I	OR	50	20			40	P

FIRST SEM TOTAL = --/750

B120053126 LOHAR BHAGYASHRI RAJENDRA SUNITA ,71400762F ,PICT ,B120053126

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	12	32	44	P
404182	COMPUTER NETWORKS	PP	100	40	23	40	63	P
404183	MICROWAVE ENGINEERING	PP	100	40	16	36	52	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	14	37	51	P
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40		46	46	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			40	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			33	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			38	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			32	P
404188	PROJECT PHASE I	OR	50	20			43	P

FIRST SEM TOTAL = 442/750

B120053127 LOKHANDE PRADNYA BHAGWAN ANITA ,71500176M ,PICT ,B120053127

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	17	47	64	P
404182	COMPUTER NETWORKS	PP	100	40	15	38	53	P
404183	MICROWAVE ENGINEERING	PP	100	40	16	44	60	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	13	37	50	P
404185C	PLCS AND AUTOMATION	PP	100	40	16	47	63	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			40	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			33	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			44	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			30	P
404188	PROJECT PHASE I	OR	50	20			36	P

FIRST SEM TOTAL = 473/750

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120053128 MADHAMSHETTIWAR CHAITALI VISHWAS SANGITA ,71400989L ,PICT ,B120053128

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	07	28	--	F
404182	COMPUTER NETWORKS	PP	100	40	19	36	55	P
404183	MICROWAVE ENGINEERING	PP	100	40	12	37	49	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	12	36	48	P
404185C	PLCS AND AUTOMATION	PP	100	40	15	37	52	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			39	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			29	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			38	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			27	P
404188	PROJECT PHASE I	OR	50	20			38	P

FIRST SEM TOTAL = --/750

B120053129 MAHINDRAKAR ATHARVA ANIL PREMLATA ,71400992L ,PICT ,B120053129

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	07	03	--	F
404182	COMPUTER NETWORKS	PP	100	40	19	33	52	P
404183	MICROWAVE ENGINEERING	PP	100	40	14	26	--	F
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	10	21	--	F
404185C	PLCS AND AUTOMATION	PP	100	40	21	40	61	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			41	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			39	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			39	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			38	P
404188	PROJECT PHASE I	OR	50	20			45	P

FIRST SEM TOTAL = --/750

B120053130 MARGALE SANTOSH SHANKAR TAIBAI MARGALE ,71301073J ,PICT ,B120053130

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	11	34	45	P
404182	COMPUTER NETWORKS	PP	100	40	12	29	41	P
404183	MICROWAVE ENGINEERING	PP	100	40	15	48	63	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	5	36	41	P
404185C	PLCS AND AUTOMATION	PP	100	40	20	35	55	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			36	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			31	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			34	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			27	P
404188	PROJECT PHASE I	OR	50	20			36	P

FIRST SEM TOTAL = 409/750

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120053131 MARYADA MAHESH PALASKAR SEEMA ,71500178H ,PICT ,B120053131

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	20	26	--	F
404182	COMPUTER NETWORKS	PP	100	40	27	46	73	P
404183	MICROWAVE ENGINEERING	PP	100	40	21	53	74	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	21	47	68	P
404185C	PLCS AND AUTOMATION	PP	100	40	26	39	65	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			44	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			38	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			42	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			40	P
404188	PROJECT PHASE I	OR	50	20			41	P

FIRST SEM TOTAL = --/750

B120053132 MATE SHARDA SUBHASH BHAGYASHREE ,71401000G ,PICT ,B120053132

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	14	26	--	F
404182	COMPUTER NETWORKS	PP	100	40	09	28	--	F
404183	MICROWAVE ENGINEERING	PP	100	40	15	29	44	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	13	26	--	F
404185C	PLCS AND AUTOMATION	PP	100	40	10	34	44	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			36	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			35	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			35	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			30	P
404188	PROJECT PHASE I	OR	50	20			33	P

FIRST SEM TOTAL = --/750

B120053133 MAYUR MUKUND SATHE LATA ,71401001E ,PICT ,B120053133

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	18	20	--	F
404182	COMPUTER NETWORKS	PP	100	40	25	28	53	P
404183	MICROWAVE ENGINEERING	PP	100	40	9	34	43	P
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	22	34	56	P
404185C	PLCS AND AUTOMATION	PP	100	40	19	29	48	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			40	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			40	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			36	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			40	P
404188	PROJECT PHASE I	OR	50	20			42	P

FIRST SEM TOTAL = --/750

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120053134 MEMANE ABHISHEK TANAJI KALPANA ,71401003M ,PICT ,B120053134

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	11	35	46	P
404182	COMPUTER NETWORKS	PP	100	40	27	37	64	P
404183	MICROWAVE ENGINEERING	PP	100	40	20	43	63	P
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	18	43	61	P
404185D	ARTIFICIAL INTELLIGENCE	PP	100	40	22	39	61	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			37	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			40	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			39	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			32	P
404188	PROJECT PHASE I	OR	50	20			42	P

FIRST SEM TOTAL = 485/750

B120053135 MHETRE ANKITA RAMDAS SANGEETA ,71500179F ,PICT ,B120053135

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	23	36	59	P
404182	COMPUTER NETWORKS	PP	100	40	28	47	75	P
404183	MICROWAVE ENGINEERING	PP	100	40	20	46	66	P
404184C	SOFTWARE DEFINED RADIO	PP	100	40	28	54	82	P
404185C	PLCS AND AUTOMATION	PP	100	40	24	49	73	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			41	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			40	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			39	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			35	P
404188	PROJECT PHASE I	OR	50	20			41	P

FIRST SEM TOTAL = 551/750

B120053136 MITKAR ISHA JAYANT RAJASHREE ,71500181H ,PICT ,B120053136

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	16	38	54	P
404182	COMPUTER NETWORKS	PP	100	40	19	34	53	P
404183	MICROWAVE ENGINEERING	PP	100	40	8	40	48	P
404184C	SOFTWARE DEFINED RADIO	PP	100	40	23	52	75	P
404185C	PLCS AND AUTOMATION	PP	100	40	16	43	59	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			39	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			39	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			34	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			40	P
404188	PROJECT PHASE I	OR	50	20			30	P

FIRST SEM TOTAL = 471/750

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120053137 MOHITE AISHWARYA VINOD SUNITA ,71500182F ,PICT ,B120053137

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	11	33	44	P
404182	COMPUTER NETWORKS	PP	100	40	19	43	62	P
404183	MICROWAVE ENGINEERING	PP	100	40	18	44	62	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	16	44	60	P
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40		43	43	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			41	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			38	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			43	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			38	P
404188	PROJECT PHASE I	OR	50	20			42	P

FIRST SEM TOTAL = 473/750

B120053138 MOHITE MANALI MAHADEO ANJANA ,71401007D ,PICT ,B120053138

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	13	30	43	P
404182	COMPUTER NETWORKS	PP	100	40	19	41	60	P
404183	MICROWAVE ENGINEERING	PP	100	40	14	45	59	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	16	26	--	F
404185C	PLCS AND AUTOMATION	PP	100	40	19	38	57	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			37	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			37	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			36	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			38	P
404188	PROJECT PHASE I	OR	50	20			43	P

FIRST SEM TOTAL = --/750

B120053139 MOINELLY SRIVALLABH MEENAL ,71401008B ,PICT ,B120053139

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	13	32	45	P
404182	COMPUTER NETWORKS	PP	100	40	24	30	54	P
404183	MICROWAVE ENGINEERING	PP	100	40	8	43	51	P
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	14	34	48	P
404185D	ARTIFICIAL INTELLIGENCE	PP	100	40	20	42	62	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			36	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			37	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			43	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			35	P
404188	PROJECT PHASE I	OR	50	20			38	P

FIRST SEM TOTAL = 449/750

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120053140 MOKSHA RAJAWAT ASHA ,71401009L ,PICT ,B120053140

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	12	26	--	F
404182	COMPUTER NETWORKS	PP	100	40	14	35	49	P
404183	MICROWAVE ENGINEERING	PP	100	40	10	35	45	P
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	20	32	52	P
404185C	PLCS AND AUTOMATION	PP	100	40	16	39	55	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			35	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			38	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			35	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			28	P
404188	PROJECT PHASE I	OR	50	20			40	P

FIRST SEM TOTAL = --/750

B120053141 MORE KAILAS BHANUDAS MANDABAI ,71500183D ,PICT ,B120053141

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	17	41	58	P
404182	COMPUTER NETWORKS	PP	100	40	16	37	53	P
404183	MICROWAVE ENGINEERING	PP	100	40	23	47	70	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	13	42	55	P
404185C	PLCS AND AUTOMATION	PP	100	40	13	46	59	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			38	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			32	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			42	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			35	P
404188	PROJECT PHASE I	OR	50	20			40	P

FIRST SEM TOTAL = 482/750

B120053142 MUKEWAR ADITYA SATISH ARCHANA ,71401014G ,PICT ,B120053142

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	10	30	40	P
404182	COMPUTER NETWORKS	PP	100	40	17	29	46	P
404183	MICROWAVE ENGINEERING	PP	100	40	12	29	41	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	12	31	43	P
404185A	MULTI RATE & ADAP SIGN. PROC	PP	100	40	10	22	--	F
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			37	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			40	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			36	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			32	P
404188	PROJECT PHASE I	OR	50	20			38	P

FIRST SEM TOTAL = --/750

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120053143 MUNDE SWAPNIL BHAGWANRAO CHANDRAKALA ,71401018K ,PICT ,B120053143

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	10	35	45	P
404182	COMPUTER NETWORKS	PP	100	40	14	33	47	P
404183	MICROWAVE ENGINEERING	PP	100	40	15	35	50	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	18	32	50	P
404185C	PLCS AND AUTOMATION	PP	100	40	19	32	51	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			39	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			37	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			42	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			34	P
404188	PROJECT PHASE I	OR	50	20			40	P

FIRST SEM TOTAL = 435/750

B120053144 MUSALE REHA ABHAY ANITA ,71401020M ,PICT ,B120053144

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	13	32	45	P
404182	COMPUTER NETWORKS	PP	100	40	24	36	60	P
404183	MICROWAVE ENGINEERING	PP	100	40	17	40	57	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	16	39	55	P
404185C	PLCS AND AUTOMATION	PP	100	40	24	47	71	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			40	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			38	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			39	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			31	P
404188	PROJECT PHASE I	OR	50	20			38	P

FIRST SEM TOTAL = 474/750

B120053145 NAGARE PRATIKSHA SURESH MINAKSHI ,71500184B ,PICT ,B120053145

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	18	44	62	P
404182	COMPUTER NETWORKS	PP	100	40	22	48	70	P
404183	MICROWAVE ENGINEERING	PP	100	40	19	45	64	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	21	48	69	P
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40		49	49	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			39	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			39	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			41	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			40	P
404188	PROJECT PHASE I	OR	50	20			42	P

FIRST SEM TOTAL = 515/750

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120053146 NAGTILAK SWAPNIL VIJAY SUVARNA ,71500185L ,PICT ,B120053146

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	09	18	--	F
404182	COMPUTER NETWORKS	PP	100	40	19	28	47	P
404183	MICROWAVE ENGINEERING	PP	100	40	12	30	42	P
404184C	SOFTWARE DEFINED RADIO	PP	100	40	16	28	44	P
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	AA	28	--	F
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			39	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			36	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			37	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			07	F
404188	PROJECT PHASE I	OR	50	20			39	P

FIRST SEM TOTAL = --/750

B120053147 NANNAWARE SAGAR SUBHASH SUNANDA ,71401029E ,PICT ,B120053147

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	13	22	--	F
404182	COMPUTER NETWORKS	PP	100	40	5	35	40	P
404183	MICROWAVE ENGINEERING	PP	100	40	10	16	--	F
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	14	22	--	F
404185C	PLCS AND AUTOMATION	PP	100	40	16	28	44	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			34	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			30	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			36	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			25	P
404188	PROJECT PHASE I	OR	50	20			33	P

FIRST SEM TOTAL = --/750

B120053148 NIKAM SHUBHANGI RAVINDRA MANGALA ,71401037F ,PICT ,B120053148

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	17	35	52	P
404182	COMPUTER NETWORKS	PP	100	40	19	47	66	P
404183	MICROWAVE ENGINEERING	PP	100	40	22	55	77	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	17	42	59	P
404185C	PLCS AND AUTOMATION	PP	100	40	26	43	69	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			42	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			40	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			39	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			40	P
404188	PROJECT PHASE I	OR	50	20			42	P

FIRST SEM TOTAL = 526/750

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120053149 NIMAT SANDESH SURESH SUREKHA ,71401040F ,PICT ,B120053149

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	10	30	40	P
404182	COMPUTER NETWORKS	PP	100	40	17	35	52	P
404183	MICROWAVE ENGINEERING	PP	100	40	12	33	45	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	16	42	58	P
404185C	PLCS AND AUTOMATION	PP	100	40	19	37	56	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			42	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			44	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			36	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			39	P
404188	PROJECT PHASE I	OR	50	20			39	P

FIRST SEM TOTAL = 451/750

B120053150 NIMBALKAR ADITEE BHAGAWAN SEEMA ,71401041D ,PICT ,B120053150

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	10	33	43	P
404182	COMPUTER NETWORKS	PP	100	40	25	40	65	P
404183	MICROWAVE ENGINEERING	PP	100	40	23	53	76	P
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	20	35	55	P
404185C	PLCS AND AUTOMATION	PP	100	40	23	46	69	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			42	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			39	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			38	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			33	P
404188	PROJECT PHASE I	OR	50	20			37	P

FIRST SEM TOTAL = 497/750

B120053151 NISHA SINGAL SAROJ SINGAL ,71401044J ,PICT ,B120053151

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	14	44	58	P
404182	COMPUTER NETWORKS	PP	100	40	24	54	78	P
404183	MICROWAVE ENGINEERING	PP	100	40	20	52	72	P
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	21	44	65	P
404185D	ARTIFICIAL INTELLIGENCE	PP	100	40	23	45	68	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			39	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			38	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			37	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			36	P
404188	PROJECT PHASE I	OR	50	20			44	P

FIRST SEM TOTAL = 535/750

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120053152 PALAK PANCHORI MADHU ,71401048M ,PICT ,B120053152

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	18	38	56	P
404182	COMPUTER NETWORKS	PP	100	40	16	40	56	P
404183	MICROWAVE ENGINEERING	PP	100	40	19	47	66	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	14	41	55	P
404185C	PLCS AND AUTOMATION	PP	100	40	23	38	61	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			35	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			38	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			34	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			36	P
404188	PROJECT PHASE I	OR	50	20			36	P

FIRST SEM TOTAL = 473/750

B120053153 PALANDE PURVA ARUN SULBHA ,71500186J ,PICT ,B120053153

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	12	44	56	P
404182	COMPUTER NETWORKS	PP	100	40	27	51	78	P
404183	MICROWAVE ENGINEERING	PP	100	40		57	57	P
404184C	SOFTWARE DEFINED RADIO	PP	100	40	25	46	71	P
404185C	PLCS AND AUTOMATION	PP	100	40	12	49	61	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			41	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			42	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			42	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			33	P
404188	PROJECT PHASE I	OR	50	20			40	P

FIRST SEM TOTAL = 521/750

B120053154 PANAGE PUNAM PANDURANG LATA ,71500187G ,PICT ,B120053154

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	12	46	58	P
404182	COMPUTER NETWORKS	PP	100	40	18	44	62	P
404183	MICROWAVE ENGINEERING	PP	100	40	17	52	69	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	23	29	52	P
404185C	PLCS AND AUTOMATION	PP	100	40	18	38	56	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			43	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			40	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			43	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			36	P
404188	PROJECT PHASE I	OR	50	20			40	P

FIRST SEM TOTAL = 499/750

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120053155 PANDE TANMAYAN PRABODH VARSHA ,71401050C ,PICT ,B120053155

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	8	43	51	P
404182	COMPUTER NETWORKS	PP	100	40	23	50	73	P
404183	MICROWAVE ENGINEERING	PP	100	40	28	52	80	P
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	21	38	59	P
404185D	ARTIFICIAL INTELLIGENCE	PP	100	40	18	44	62	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			41	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			43	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			41	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			40	P
404188	PROJECT PHASE I	OR	50	20			42	P

FIRST SEM TOTAL = 532/750

B120053156 PANKAJ SINGH SUNITA SINGH ,71418269K ,PICT ,B120053156

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	13	45	58	P
404182	COMPUTER NETWORKS	PP	100	40	21	49	70	P
404183	MICROWAVE ENGINEERING	PP	100	40	18	48	66	P
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	15	42	57	P
404185C	PLCS AND AUTOMATION	PP	100	40	18	45	63	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			39	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			42	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			38	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			33	P
404188	PROJECT PHASE I	OR	50	20			44	P

FIRST SEM TOTAL = 510/750

B120053157 PATEL POOJA JITENDRA CHHAYA ,71500188E ,PICT ,B120053157

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	18	59	77	P
404182	COMPUTER NETWORKS	PP	100	40	29	57	86	P
404183	MICROWAVE ENGINEERING	PP	100	40	22	50	72	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	17	50	67	P
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40		49	49	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			44	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			41	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			44	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			41	P
404188	PROJECT PHASE I	OR	50	20			43	P

FIRST SEM TOTAL = 564/750

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120053158 PATHARABE SHIVANI RAJU MEERA ,71500189C ,PICT ,B120053158

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	19	23	--	F
404182	COMPUTER NETWORKS	PP	100	40	24	54	78	P
404183	MICROWAVE ENGINEERING	PP	100	40	18	37	55	P
404184C	SOFTWARE DEFINED RADIO	PP	100	40	27	48	75	P
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40		48	48	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			40	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			38	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			38	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			36	P
404188	PROJECT PHASE I	OR	50	20			40	P
FIRST SEM TOTAL = --/750								

B120053159 PATIL AAISHWARYA KAILAS VANDANA ,71500190G ,PICT ,B120053159

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	22	54	76	P
404182	COMPUTER NETWORKS	PP	100	40	25	60	85	P
404183	MICROWAVE ENGINEERING	PP	100	40	24	48	72	P
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	28	49	77	P
404185C	PLCS AND AUTOMATION	PP	100	40	23	44	67	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			47	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			42	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			46	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			42	P
404188	PROJECT PHASE I	OR	50	20			45	P
FIRST SEM TOTAL = 599/750								

B120053160 PATIL ANUJ SUDHIR RANJANA ,71401059G ,PICT ,B120053160

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	8	33	41	P
404182	COMPUTER NETWORKS	PP	100	40	14	43	57	P
404183	MICROWAVE ENGINEERING	PP	100	40	13	43	56	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	9	42	51	P
404185A	MULTI RATE & ADAP SIGN. PROC	PP	100	40	11	40	51	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			38	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			37	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			34	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			35	P
404188	PROJECT PHASE I	OR	50	20			40	P
FIRST SEM TOTAL = 440/750								

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120053161 PATIL APEKSHA RAVINDRA SUNGANDHA PATIL ,71401060L ,PICT ,B120053161

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	12	38	50	P
404182	COMPUTER NETWORKS	PP	100	40	18	41	59	P
404183	MICROWAVE ENGINEERING	PP	100	40	23	42	65	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	21	36	57	P
404185C	PLCS AND AUTOMATION	PP	100	40	19	39	58	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			37	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			36	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			35	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			36	P
404188	PROJECT PHASE I	OR	50	20			38	P

FIRST SEM TOTAL = 471/750

B120053162 PATIL AVINA AJAY VARSHA ,71500191E ,PICT ,B120053162

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	21	44	65	P
404182	COMPUTER NETWORKS	PP	100	40	26	51	77	P
404183	MICROWAVE ENGINEERING	PP	100	40	17	49	66	P
404184C	SOFTWARE DEFINED RADIO	PP	100	40	27	61	88	P
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40		54	54	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			43	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			37	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			41	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			41	P
404188	PROJECT PHASE I	OR	50	20			38	P

FIRST SEM TOTAL = 550/750

B120053163 PATIL DHANISH P VISHAKHA PATIL ,71401062G ,PICT ,B120053163

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	11	34	45	P
404182	COMPUTER NETWORKS	PP	100	40	15	31	46	P
404183	MICROWAVE ENGINEERING	PP	100	40	14	37	51	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	19	35	54	P
404185C	PLCS AND AUTOMATION	PP	100	40	9	41	50	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			36	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			42	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			32	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			34	P
404188	PROJECT PHASE I	OR	50	20			44	P

FIRST SEM TOTAL = 434/750

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120053164 PATIL GARIMA PRADEEPSING SMITA PRADEEPSING PATIL ,71401063E ,PICT ,B120053164

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	20	28	48	P
404182	COMPUTER NETWORKS	PP	100	40	12	42	54	P
404183	MICROWAVE ENGINEERING	PP	100	40	23	40	63	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	15	45	60	P
404185C	PLCS AND AUTOMATION	PP	100	40	13	41	54	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			36	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			39	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			34	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			33	P
404188	PROJECT PHASE I	OR	50	20			37	P

FIRST SEM TOTAL = 458/750

B120053165 PATIL PALLAVI GULAB RANJANA ,71500192C ,PICT ,B120053165

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	19	44	63	P
404182	COMPUTER NETWORKS	PP	100	40	21	52	73	P
404183	MICROWAVE ENGINEERING	PP	100	40	19	47	66	P
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	19	50	69	P
404185C	PLCS AND AUTOMATION	PP	100	40	23	49	72	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			42	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			40	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			44	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			40	P
404188	PROJECT PHASE I	OR	50	20			43	P

FIRST SEM TOTAL = 552/750

B120053166 PATIL RAHUL RATILAL BHARATI ,71500193M ,PICT ,B120053166

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	20	41	61	P
404182	COMPUTER NETWORKS	PP	100	40	27	52	79	P
404183	MICROWAVE ENGINEERING	PP	100	40	22	44	66	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	21	41	62	P
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40		52	52	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			43	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			40	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			41	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			34	P
404188	PROJECT PHASE I	OR	50	20			40	P

FIRST SEM TOTAL = 518/750

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120053167 PATIL SAURABH SUNIL SUPRIYA SUNIL PATIL ,71401068F ,PICT ,B120053167

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	16	32	48	P
404182	COMPUTER NETWORKS	PP	100	40	20	45	65	P
404183	MICROWAVE ENGINEERING	PP	100	40	10	30	40	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	7	37	44	P
404185C	PLCS AND AUTOMATION	PP	100	40	18	28	46	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			39	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			37	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			25	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			28	P
404188	PROJECT PHASE I	OR	50	20			43	P

FIRST SEM TOTAL = 415/750

B120053168 PATIL SNEHAL UTTAM ALKA ,71500194K ,PICT ,B120053168

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	14	41	55	P
404182	COMPUTER NETWORKS	PP	100	40	17	50	67	P
404183	MICROWAVE ENGINEERING	PP	100	40	21	45	66	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	15	43	58	P
404185A	MULTI RATE & ADAP SIGN. PROC	PP	100	40	12	33	45	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			42	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			36	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			43	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			35	P
404188	PROJECT PHASE I	OR	50	20			39	P

FIRST SEM TOTAL = 486/750

B120053169 PATIL VINAYAK SHIVLAL GEETA ,71401071F ,PICT ,B120053169

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	19	28	47	P
404182	COMPUTER NETWORKS	PP	100	40	12	46	58	P
404183	MICROWAVE ENGINEERING	PP	100	40	19	28	47	P
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	13	31	44	P
404185D	ARTIFICIAL INTELLIGENCE	PP	100	40	18	38	56	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			36	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			40	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			36	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			33	P
404188	PROJECT PHASE I	OR	50	20			37	P

FIRST SEM TOTAL = 434/750

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120053170 PATWARDHAN DEVASHISH NITIN VARSHA ,71401076G ,PICT ,B120053170

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	14	36	50	P
404182	COMPUTER NETWORKS	PP	100	40	25	46	71	P
404183	MICROWAVE ENGINEERING	PP	100	40	15	28	43	P
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	17	39	56	P
404185C	PLCS AND AUTOMATION	PP	100	40	16	31	47	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			36	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			40	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			41	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			36	P
404188	PROJECT PHASE I	OR	50	20			40	P

FIRST SEM TOTAL = 460/750

B120053171 PAWAR DINESH RAJENDRA ASHA ,71500195H ,PICT ,B120053171

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	19	39	58	P
404182	COMPUTER NETWORKS	PP	100	40	25	53	78	P
404183	MICROWAVE ENGINEERING	PP	100	40	16	30	46	P
404184C	SOFTWARE DEFINED RADIO	PP	100	40	27	43	70	P
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40		44	44	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			39	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			36	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			38	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			29	P
404188	PROJECT PHASE I	OR	50	20			40	P

FIRST SEM TOTAL = 478/750

B120053172 PAWAR SNEHAL ASHOK SUNITA ,71500196F ,PICT ,B120053172

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	24	46	70	P
404182	COMPUTER NETWORKS	PP	100	40	25	57	82	P
404183	MICROWAVE ENGINEERING	PP	100	40	25	42	67	P
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	26	50	76	P
404185C	PLCS AND AUTOMATION	PP	100	40	24	51	75	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			43	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			42	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			44	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			45	P
404188	PROJECT PHASE I	OR	50	20			44	P

FIRST SEM TOTAL = 588/750

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120053173 PINGALE ANKITA SUNIL MANISHA ,71401084H ,PICT ,B120053173

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	19	24	--	F
404182	COMPUTER NETWORKS	PP	100	40	14	45	59	P
404183	MICROWAVE ENGINEERING	PP	100	40	13	28	41	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	22	48	70	P
404185C	PLCS AND AUTOMATION	PP	100	40	19	41	60	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			37	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			40	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			36	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			34	P
404188	PROJECT PHASE I	OR	50	20			37	P

FIRST SEM TOTAL = --/750

B120053174 PISKEWAD SHIVAJI VITTHAL SANGITA ,71500197D ,PICT ,B120053174

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	16	28	44	P
404182	COMPUTER NETWORKS	PP	100	40	25	52	77	P
404183	MICROWAVE ENGINEERING	PP	100	40	08	28	--	F
404184C	SOFTWARE DEFINED RADIO	PP	100	40	25	36	61	P
404185C	PLCS AND AUTOMATION	PP	100	40	15	35	50	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			41	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			39	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			35	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			40	P
404188	PROJECT PHASE I	OR	50	20			35	P

FIRST SEM TOTAL = --/750

B120053175 POL SHWETA RAJENDRA BHARATI ,71401085F ,PICT ,B120053175

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	13	36	49	P
404182	COMPUTER NETWORKS	PP	100	40	20	43	63	P
404183	MICROWAVE ENGINEERING	PP	100	40	17	30	47	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	15	33	48	P
404185C	PLCS AND AUTOMATION	PP	100	40	22	31	53	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			36	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			39	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			43	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			41	P
404188	PROJECT PHASE I	OR	50	20			41	P

FIRST SEM TOTAL = 460/750

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120053176 POOJA SANTOSH KADAM MANISHA ,71301163H ,PICT ,B120053176

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	19	28	47	P
404182	COMPUTER NETWORKS	PP	100	40	23	41	64	P
404183	MICROWAVE ENGINEERING	PP	100	40	14	25	--	F
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	12	39	51	P
404185C	PLCS AND AUTOMATION	PP	100	40	16	30	46	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			40	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			38	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			43	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			36	P
404188	PROJECT PHASE I	OR	50	20			38	P

FIRST SEM TOTAL = --/750

B120053177 POTDAR AKHILESH VIJAY VINAYA ,71401087B ,PICT ,B120053177

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	11	24	--	F
404182	COMPUTER NETWORKS	PP	100	40	18	47	65	P
404183	MICROWAVE ENGINEERING	PP	100	40	10	31	41	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	19	41	60	P
404185C	PLCS AND AUTOMATION	PP	100	40	25	34	59	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			41	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			40	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			40	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			32	P
404188	PROJECT PHASE I	OR	50	20			44	P

FIRST SEM TOTAL = --/750

B120053178 PRAKHAR DIXIT RASHMI DIXIT ,71418330L ,PICT ,B120053178

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	15	24	--	F
404182	COMPUTER NETWORKS	PP	100	40	19	49	68	P
404183	MICROWAVE ENGINEERING	PP	100	40	15	31	46	P
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	16	37	53	P
404185C	PLCS AND AUTOMATION	PP	100	40	19	39	58	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			40	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			42	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			42	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			37	P
404188	PROJECT PHASE I	OR	50	20			41	P

FIRST SEM TOTAL = --/750

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120053179 PRASAD MUKESH GOPAL SHILA ,71401092J ,PICT ,B120053179

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	14	39	53	P
404182	COMPUTER NETWORKS	PP	100	40	21	50	71	P
404183	MICROWAVE ENGINEERING	PP	100	40	25	45	70	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	20	47	67	P
404185C	PLCS AND AUTOMATION	PP	100	40	23	52	75	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			38	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			40	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			40	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			37	P
404188	PROJECT PHASE I	OR	50	20			42	P

FIRST SEM TOTAL = 533/750

B120053180 PRASAD VITTHAL PADIR ANITA ,71401093G ,PICT ,B120053180

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	10	32	42	P
404182	COMPUTER NETWORKS	PP	100	40	23	42	65	P
404183	MICROWAVE ENGINEERING	PP	100	40	06	28	--	F
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	19	34	53	P
404185C	PLCS AND AUTOMATION	PP	100	40	16	36	52	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			44	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			39	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			45	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			33	P
404188	PROJECT PHASE I	OR	50	20			41	P

FIRST SEM TOTAL = --/750

B120053181 PRATAP TRISHA DHANRAJ MANORAMA DHANRAJ ,71401094E ,PICT ,B120053181

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	13	37	50	P
404182	COMPUTER NETWORKS	PP	100	40	20	43	63	P
404183	MICROWAVE ENGINEERING	PP	100	40	13	43	56	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	21	41	62	P
404185C	PLCS AND AUTOMATION	PP	100	40	19	51	70	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			38	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			41	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			39	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			30	P
404188	PROJECT PHASE I	OR	50	20			39	P

FIRST SEM TOTAL = 488/750

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120053182 PRATHAMESH MAHESH SAYGAONKAR ARCHANA MAHESH SAYGAONKAR ,71401095C ,PICT ,B120053182

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	19	50	69	P
404182	COMPUTER NETWORKS	PP	100	40	24	46	70	P
404183	MICROWAVE ENGINEERING	PP	100	40	23	53	76	P
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	25	53	78	P
404185C	PLCS AND AUTOMATION	PP	100	40	20	57	77	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			47	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			46	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			42	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			44	P
404188	PROJECT PHASE I	OR	50	20			46	P

FIRST SEM TOTAL = 595/750

B120053183 PULLI KARTIK RAMESH NAGMANI ,71500198B ,PICT ,B120053183

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	19	50	69	P
404182	COMPUTER NETWORKS	PP	100	40	17	41	58	P
404183	MICROWAVE ENGINEERING	PP	100	40	20	58	78	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	20	54	74	P
404185C	PLCS AND AUTOMATION	PP	100	40	19	52	71	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			44	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			40	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			43	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			44	P
404188	PROJECT PHASE I	OR	50	20			45	P

FIRST SEM TOTAL = 566/750

B120053184 RAHANE KRUTIKA SUNIL SNEHAL ,71401102K ,PICT ,B120053184

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	15	40	55	P
404182	COMPUTER NETWORKS	PP	100	40	14	35	49	P
404183	MICROWAVE ENGINEERING	PP	100	40	07	29	--	F
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	18	28	46	P
404185C	PLCS AND AUTOMATION	PP	100	40	16	38	54	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			42	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			39	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			38	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			39	P
404188	PROJECT PHASE I	OR	50	20			41	P

FIRST SEM TOTAL = --/750

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120053185 RAHUL TICKOO PINKEY TICKOO ,71401103H ,PICT ,B120053185

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	9	46	55	P
404182	COMPUTER NETWORKS	PP	100	40	15	31	46	P
404183	MICROWAVE ENGINEERING	PP	100	40	15	33	48	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	16	31	47	P
404185C	PLCS AND AUTOMATION	PP	100	40	20	46	66	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			39	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			39	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			42	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			38	P
404188	PROJECT PHASE I	OR	50	20			41	P

FIRST SEM TOTAL = 461/750

B120053186 RAO YASH RAVISHANKAR GAURI ,71401108J ,PICT ,B120053186

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	20	42	62	P
404182	COMPUTER NETWORKS	PP	100	40	21	43	64	P
404183	MICROWAVE ENGINEERING	PP	100	40	26	36	62	P
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	21	47	68	P
404185C	PLCS AND AUTOMATION	PP	100	40	24	45	69	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			44	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			45	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			42	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			45	P
404188	PROJECT PHASE I	OR	50	20			43	P

FIRST SEM TOTAL = 544/750

B120053187 RATHI RAGHAV SHYAM CHANDA ,71401111J ,PICT ,B120053187

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	21	47	68	P
404182	COMPUTER NETWORKS	PP	100	40	19	29	48	P
404183	MICROWAVE ENGINEERING	PP	100	40	16	34	50	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	15	41	56	P
404185C	PLCS AND AUTOMATION	PP	100	40	14	35	49	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			38	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			37	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			36	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			34	P
404188	PROJECT PHASE I	OR	50	20			45	P

FIRST SEM TOTAL = 461/750

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120053188 RATHOD RAJENDRA RAMESH KAVERABAI ,71401106B ,PICT ,B120053188

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	16	46	62	P
404182	COMPUTER NETWORKS	PP	100	40	19	41	60	P
404183	MICROWAVE ENGINEERING	PP	100	40	19	43	62	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	19	46	65	P
404185C	PLCS AND AUTOMATION	PP	100	40	18	46	64	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			36	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			42	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			35	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			32	P
404188	PROJECT PHASE I	OR	50	20			40	P

FIRST SEM TOTAL = 498/750

B120053189 RAUT SHRADDHA SUDHIR SHOBHA ,71401113E ,PICT ,B120053189

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	10	47	57	P
404182	COMPUTER NETWORKS	PP	100	40	22	38	60	P
404183	MICROWAVE ENGINEERING	PP	100	40	20	47	67	P
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	17	43	60	P
404185D	ARTIFICIAL INTELLIGENCE	PP	100	40	20	33	53	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			40	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			39	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			41	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			41	P
404188	PROJECT PHASE I	OR	50	20			43	P

FIRST SEM TOTAL = 501/750

B120053190 RAUT VISHAL VIVEK VAISHALI ,71401114C ,PICT ,B120053190

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	10	41	51	P
404182	COMPUTER NETWORKS	PP	100	40	23	39	62	P
404183	MICROWAVE ENGINEERING	PP	100	40	18	42	60	P
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	24	48	72	P
404185D	ARTIFICIAL INTELLIGENCE	PP	100	40	18	44	62	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			37	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			38	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			39	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			39	P
404188	PROJECT PHASE I	OR	50	20			41	P

FIRST SEM TOTAL = 501/750

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120053191 REKHATE DEEPAK RAMDAS LEELA ,71500199L ,PICT ,B120053191

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	16	46	62	P
404182	COMPUTER NETWORKS	PP	100	40	20	52	72	P
404183	MICROWAVE ENGINEERING	PP	100	40	19	48	67	P
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	22	41	63	P
404185C	PLCS AND AUTOMATION	PP	100	40	19	40	59	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			44	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			37	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			40	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			40	P
404188	PROJECT PHASE I	OR	50	20			42	P

FIRST SEM TOTAL = 526/750

B120053192 RUSHIKESH HRUDESH MALAKWADE SWARNALATA ,71401125J ,PICT ,B120053192

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	6	46	52	P
404182	COMPUTER NETWORKS	PP	100	40	10	29	--	F
404183	MICROWAVE ENGINEERING	PP	100	40	16	41	57	P
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	12	40	52	P
404185D	ARTIFICIAL INTELLIGENCE	PP	100	40	21	36	57	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			39	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			38	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			35	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			41	P
404188	PROJECT PHASE I	OR	50	20			44	P

FIRST SEM TOTAL = --/750

B120053193 SABLE KOMAL VIJAY SUNITA ,71500200H ,PICT ,B120053193

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	19	47	66	P
404182	COMPUTER NETWORKS	PP	100	40	24	55	79	P
404183	MICROWAVE ENGINEERING	PP	100	40	26	53	79	P
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	26	64	90	P
404185C	PLCS AND AUTOMATION	PP	100	40	20	45	65	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			44	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			42	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			41	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			42	P
404188	PROJECT PHASE I	OR	50	20			42	P

FIRST SEM TOTAL = 590/750

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120053194 SADADEKAR ADITYA ANIL ANKITA ,71500201F ,PICT ,B120053194

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	16	42	58	P
404182	COMPUTER NETWORKS	PP	100	40	25	46	71	P
404183	MICROWAVE ENGINEERING	PP	100	40	16	44	60	P
404184C	SOFTWARE DEFINED RADIO	PP	100	40	25	44	69	P
404185C	PLCS AND AUTOMATION	PP	100	40	19	45	64	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			39	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			35	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			36	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			27	P
404188	PROJECT PHASE I	OR	50	20			38	P

FIRST SEM TOTAL = 497/750

B120053195 SAKRIYA SOMESH ATULKUMAR RAKSHA ,71401130E ,PICT ,B120053195

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	11	45	56	P
404182	COMPUTER NETWORKS	PP	100	40	21	43	64	P
404183	MICROWAVE ENGINEERING	PP	100	40	16	40	56	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	22	38	60	P
404185C	PLCS AND AUTOMATION	PP	100	40	21	36	57	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			43	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			43	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			42	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			37	P
404188	PROJECT PHASE I	OR	50	20			42	P

FIRST SEM TOTAL = 500/750

B120053196 SALGAR DARSHAN SUNIL AMRUTA SALGAR ,71401131C ,PICT ,B120053196

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	18	43	61	P
404182	COMPUTER NETWORKS	PP	100	40	21	42	63	P
404183	MICROWAVE ENGINEERING	PP	100	40	20	55	75	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	16	45	61	P
404185C	PLCS AND AUTOMATION	PP	100	40	21	52	73	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			42	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			39	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			35	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			39	P
404188	PROJECT PHASE I	OR	50	20			41	P

FIRST SEM TOTAL = 529/750

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120053197 SALONI TAKAWALE VANDANA TAKAWALE ,71401132M ,PICT ,B120053197

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	18	45	63	P
404182	COMPUTER NETWORKS	PP	100	40	24	46	70	P
404183	MICROWAVE ENGINEERING	PP	100	40	22	45	67	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	13	43	56	P
404185D	ARTIFICIAL INTELLIGENCE	PP	100	40	21	48	69	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			40	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			41	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			36	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			41	P
404188	PROJECT PHASE I	OR	50	20			43	P

FIRST SEM TOTAL = 526/750

B120053198 SALVE MAYURI ANIL SUJATA ,71500202D ,PICT ,B120053198

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	18	43	61	P
404182	COMPUTER NETWORKS	PP	100	40	21	41	62	P
404183	MICROWAVE ENGINEERING	PP	100	40	20	53	73	P
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	19	51	70	P
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40		51	51	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			42	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			38	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			40	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			35	P
404188	PROJECT PHASE I	OR	50	20			37	P

FIRST SEM TOTAL = 509/750

B120053199 SALVE VAIBHAV RAOSAHEB MANDAKINI ,71401134H ,PICT ,B120053199

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	15	51	66	P
404182	COMPUTER NETWORKS	PP	100	40	23	49	72	P
404183	MICROWAVE ENGINEERING	PP	100	40	24	45	69	P
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	17	45	62	P
404185C	PLCS AND AUTOMATION	PP	100	40	24	37	61	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			38	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			43	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			38	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			37	P
404188	PROJECT PHASE I	OR	50	20			40	P

FIRST SEM TOTAL = 526/750

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120053200 SANAP SUYOG SANDEEP SMITA ,71500203B ,PICT ,B120053200

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	18	51	69	P
404182	COMPUTER NETWORKS	PP	100	40	28	51	79	P
404183	MICROWAVE ENGINEERING	PP	100	40	20	56	76	P
404184C	SOFTWARE DEFINED RADIO	PP	100	40	30	54	84	P
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40		44	44	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			41	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			37	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			38	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			30	P
404188	PROJECT PHASE I	OR	50	20			40	P

FIRST SEM TOTAL = 538/750

B120053201 SARVESH RAJKUMAR AGRAWAL URMILA ,71401144E ,PICT ,B120053201

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	15	48	63	P
404182	COMPUTER NETWORKS	PP	100	40	7	47	54	P
404183	MICROWAVE ENGINEERING	PP	100	40	11	47	58	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	10	38	48	P
404185C	PLCS AND AUTOMATION	PP	100	40	19	42	61	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			35	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			30	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			25	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			33	P
404188	PROJECT PHASE I	OR	50	20			38	P

FIRST SEM TOTAL = 445/750

B120053202 SATOTE MONALI BHAULAL LATA ,71401146M ,PICT ,B120053202

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	14	48	62	P
404182	COMPUTER NETWORKS	PP	100	40	11	50	61	P
404183	MICROWAVE ENGINEERING	PP	100	40	18	49	67	P
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	19	50	69	P
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40		42	42	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			39	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			39	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			39	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			35	P
404188	PROJECT PHASE I	OR	50	20			38	P

FIRST SEM TOTAL = 491/750

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120053203 SATPUTE MAYURESH DEEPAK DIPTI ,71500204L ,PICT ,B120053203

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	21	50	71	P
404182	COMPUTER NETWORKS	PP	100	40	24	54	78	P
404183	MICROWAVE ENGINEERING	PP	100	40	23	55	78	P
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	29	51	80	P
404185C	PLCS AND AUTOMATION	PP	100	40	24	50	74	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			44	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			43	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			43	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			37	P
404188	PROJECT PHASE I	OR	50	20			40	P

FIRST SEM TOTAL = 588/750

B120053204 SAVKAR MALVIKA UDDHAV ALKA ,71500205J ,PICT ,B120053204

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	14	49	63	P
404182	COMPUTER NETWORKS	PP	100	40	20	50	70	P
404183	MICROWAVE ENGINEERING	PP	100	40	17	43	60	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	13	43	56	P
404185C	PLCS AND AUTOMATION	PP	100	40	24	52	76	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			43	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			40	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			44	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			36	P
404188	PROJECT PHASE I	OR	50	20			41	P

FIRST SEM TOTAL = 529/750

B120053205 SHAH POOJA HASMUKH NAMRATA ,71401157G ,PICT ,B120053205

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	13	45	58	P
404182	COMPUTER NETWORKS	PP	100	40	19	47	66	P
404183	MICROWAVE ENGINEERING	PP	100	40	25	42	67	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	7	44	51	P
404185C	PLCS AND AUTOMATION	PP	100	40	16	32	48	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			42	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			40	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			38	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			39	P
404188	PROJECT PHASE I	OR	50	20			41	P

FIRST SEM TOTAL = 490/750

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120053206 SHAKYEMUNI SHUBHAM MANIK SANGITA ,71401163M ,PICT ,B120053206

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	15	37	52	P
404182	COMPUTER NETWORKS	PP	100	40	18	28	46	P
404183	MICROWAVE ENGINEERING	PP	100	40	16	38	54	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	20	24	--	F
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	AA	34	--	F
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			41	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			33	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			43	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			31	P
404188	PROJECT PHASE I	OR	50	20			41	P

FIRST SEM TOTAL = --/750

B120053207 SHARMA NIKHILKUMAR SUDHIR MANJU ,71500207E ,PICT ,B120053207

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	10	33	43	P
404182	COMPUTER NETWORKS	PP	100	40	13	34	47	P
404183	MICROWAVE ENGINEERING	PP	100	40	18	38	56	P
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	13	24	--	F
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	AA	38	--	F
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			41	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			33	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			39	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			32	P
404188	PROJECT PHASE I	OR	50	20			44	P

FIRST SEM TOTAL = --/750

B120053208 SHENDAGE AISHWARYA SUHAS VAISHALI ,71401166F ,PICT ,B120053208

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	18	45	63	P
404182	COMPUTER NETWORKS	PP	100	40	24	47	71	P
404183	MICROWAVE ENGINEERING	PP	100	40	21	49	70	P
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	19	40	59	P
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40		43	43	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			42	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			39	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			38	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			36	P
404188	PROJECT PHASE I	OR	50	20			39	P

FIRST SEM TOTAL = 500/750

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120053209 SHENDE PURVA RAJENDRA JYOTI ,71500208C ,PICT ,B120053209

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	20	46	66	P
404182	COMPUTER NETWORKS	PP	100	40	20	43	63	P
404183	MICROWAVE ENGINEERING	PP	100	40	24	56	80	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	14	46	60	P
404185C	PLCS AND AUTOMATION	PP	100	40	21	47	68	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			44	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			40	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			43	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			39	P
404188	PROJECT PHASE I	OR	50	20			39	P

FIRST SEM TOTAL = 542/750

B120053210 SHINDE GAURI RAJENDRA VIDYA ,71500209M ,PICT ,B120053210

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	18	50	68	P
404182	COMPUTER NETWORKS	PP	100	40	17	34	51	P
404183	MICROWAVE ENGINEERING	PP	100	40	21	55	76	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	22	50	72	P
404185C	PLCS AND AUTOMATION	PP	100	40	24	45	69	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			42	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			39	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			44	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			34	P
404188	PROJECT PHASE I	OR	50	20			40	P

FIRST SEM TOTAL = 535/750

B120053211 SHINDE NIKHIL RAMDAS MANGAL ,71500210E ,PICT ,B120053211

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	19	48	67	P
404182	COMPUTER NETWORKS	PP	100	40	22	43	65	P
404183	MICROWAVE ENGINEERING	PP	100	40	25	54	79	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	26	42	68	P
404185C	PLCS AND AUTOMATION	PP	100	40	22	44	66	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			42	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			39	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			41	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			37	P
404188	PROJECT PHASE I	OR	50	20			39	P

FIRST SEM TOTAL = 543/750

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120053212 SHINDE SUPRIYA SUBHASH SHUBHANGI ,71401176C ,PICT ,B120053212

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	13	38	51	P
404182	COMPUTER NETWORKS	PP	100	40	18	36	54	P
404183	MICROWAVE ENGINEERING	PP	100	40	19	42	61	P
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	22	50	72	P
404185D	ARTIFICIAL INTELLIGENCE	PP	100	40	14	39	53	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			42	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			38	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			42	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			40	P
404188	PROJECT PHASE I	OR	50	20			39	P

FIRST SEM TOTAL = 492/750

B120053213 SHINDE VISHAKHA DATTATRAY SANGEETA ,71401177M ,PICT ,B120053213

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	13	42	55	P
404182	COMPUTER NETWORKS	PP	100	40	21	36	57	P
404183	MICROWAVE ENGINEERING	PP	100	40	12	42	54	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	14	36	50	P
404185C	PLCS AND AUTOMATION	PP	100	40	17	31	48	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			40	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			38	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			39	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			35	P
404188	PROJECT PHASE I	OR	50	20			44	P

FIRST SEM TOTAL = 460/750

B120053214 SHINKAR RUCHA KISHOR VISHAKHA ,71401178K ,PICT ,B120053214

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	12	36	48	P
404182	COMPUTER NETWORKS	PP	100	40	23	37	60	P
404183	MICROWAVE ENGINEERING	PP	100	40	13	38	51	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	19	32	51	P
404185C	PLCS AND AUTOMATION	PP	100	40	18	36	54	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			40	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			38	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			36	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			36	P
404188	PROJECT PHASE I	OR	50	20			40	P

FIRST SEM TOTAL = 454/750

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120053215 SHREYASH AJIT PANDE JAYA AJIT PANDE ,71401187J ,PICT ,B120053215

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	21	48	69	P
404182	COMPUTER NETWORKS	PP	100	40	21	45	66	P
404183	MICROWAVE ENGINEERING	PP	100	40	22	40	62	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	12	47	59	P
404185D	ARTIFICIAL INTELLIGENCE	PP	100	40	21	43	64	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			41	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			40	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			42	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			31	P
404188	PROJECT PHASE I	OR	50	20			40	P

FIRST SEM TOTAL = 514/750

B120053216 SHRILESH VIJAY KALE SANGITA ,71401188G ,PICT ,B120053216

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	22	52	74	P
404182	COMPUTER NETWORKS	PP	100	40	27	49	76	P
404183	MICROWAVE ENGINEERING	PP	100	40	23	51	74	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	26	47	73	P
404185C	PLCS AND AUTOMATION	PP	100	40	21	45	66	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			42	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			42	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			40	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			38	P
404188	PROJECT PHASE I	OR	50	20			40	P

FIRST SEM TOTAL = 565/750

B120053217 SHUBHAM INANI REKHA DEVI INANI ,71401192E ,PICT ,B120053217

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	17	47	64	P
404182	COMPUTER NETWORKS	PP	100	40	21	41	62	P
404183	MICROWAVE ENGINEERING	PP	100	40	24	49	73	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	20	45	65	P
404185C	PLCS AND AUTOMATION	PP	100	40	20	40	60	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			46	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			40	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			44	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			40	P
404188	PROJECT PHASE I	OR	50	20			42	P

FIRST SEM TOTAL = 536/750

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120053218 SHUBHAM KIRAN INGALE SUNITA KIRAN INGALE ,71418504D ,PICT ,B120053218

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	19	45	64	P
404182	COMPUTER NETWORKS	PP	100	40	18	41	59	P
404183	MICROWAVE ENGINEERING	PP	100	40	17	43	60	P
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	15	43	58	P
404185D	ARTIFICIAL INTELLIGENCE	PP	100	40	16	49	65	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			37	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			42	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			38	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			36	P
404188	PROJECT PHASE I	OR	50	20			40	P

FIRST SEM TOTAL = 499/750

B120053219 SHWETA SANJAY WAKCHAURE SUNITA SANJAY WAKCHAURE ,71418518D ,PICT ,B120053219

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	13	41	54	P
404182	COMPUTER NETWORKS	PP	100	40	20	44	64	P
404183	MICROWAVE ENGINEERING	PP	100	40	14	38	52	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	10	37	47	P
404185C	PLCS AND AUTOMATION	PP	100	40	20	37	57	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			40	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			39	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			36	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			35	P
404188	PROJECT PHASE I	OR	50	20			45	P

FIRST SEM TOTAL = 469/750

B120053220 SIDDIQUI AHMED MUHIYODDIN MOHD NAZEERUDDIN SIRAJBEGUM ,71500211C ,PICT ,B120053220

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	17	45	62	P
404182	COMPUTER NETWORKS	PP	100	40	17	37	54	P
404183	MICROWAVE ENGINEERING	PP	100	40	15	51	66	P
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	19	49	68	P
404185C	PLCS AND AUTOMATION	PP	100	40	22	43	65	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			43	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			42	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			40	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			34	P
404188	PROJECT PHASE I	OR	50	20			40	P

FIRST SEM TOTAL = 514/750

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120053221 SIKCHI SHRUTI RAJENDRA PUSHPA ,71401194M ,PICT ,B120053221

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	14	48	62	P
404182	COMPUTER NETWORKS	PP	100	40	17	42	59	P
404183	MICROWAVE ENGINEERING	PP	100	40	17	46	63	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	11	54	65	P
404185C	PLCS AND AUTOMATION	PP	100	40	13	39	52	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			38	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			39	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			37	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			34	P
404188	PROJECT PHASE I	OR	50	20			45	P

FIRST SEM TOTAL = 494/750

B120053222 SMEET KUSHAL SHAH PAYAL ,71401197F ,PICT ,B120053222

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	8	39	47	P
404182	COMPUTER NETWORKS	PP	100	40	22	36	58	P
404183	MICROWAVE ENGINEERING	PP	100	40	17	45	62	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	21	39	60	P
404185C	PLCS AND AUTOMATION	PP	100	40	26	34	60	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			43	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			39	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			43	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			37	P
404188	PROJECT PHASE I	OR	50	20			39	P

FIRST SEM TOTAL = 488/750

B120053223 SOLANKE SURAJ PRALHAD SADHANA ,71500212M ,PICT ,B120053223

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	9	32	41	P
404182	COMPUTER NETWORKS	PP	100	40	22	42	64	P
404183	MICROWAVE ENGINEERING	PP	100	40	15	43	58	P
404184C	SOFTWARE DEFINED RADIO	PP	100	40	25	50	75	P
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	AA	36	--	F
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			33	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			25	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			32	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			12	F
404188	PROJECT PHASE I	OR	50	20			28	P

FIRST SEM TOTAL = --/750

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120053224 SONAR SHRUTI JAGDISH SEEMA ,71401200K ,PICT ,B120053224

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	16	46	62	P
404182	COMPUTER NETWORKS	PP	100	40	21	39	60	P
404183	MICROWAVE ENGINEERING	PP	100	40	22	45	67	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	9	32	41	P
404185D	ARTIFICIAL INTELLIGENCE	PP	100	40	15	37	52	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			40	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			42	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			40	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			41	P
404188	PROJECT PHASE I	OR	50	20			40	P

FIRST SEM TOTAL = 485/750

B120053225 SONAR SHUBHAM ARUN ARCHANA ,71401202F ,PICT ,B120053225

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	15	42	57	P
404182	COMPUTER NETWORKS	PP	100	40	20	44	64	P
404183	MICROWAVE ENGINEERING	PP	100	40	20	44	64	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	20	41	61	P
404185D	ARTIFICIAL INTELLIGENCE	PP	100	40	12	37	49	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			41	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			37	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			42	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			37	P
404188	PROJECT PHASE I	OR	50	20			45	P

FIRST SEM TOTAL = 497/750

B120053226 SONAVANE SAYALI ROHIDAS SHARDHA ,71500213K ,PICT ,B120053226

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	18	51	69	P
404182	COMPUTER NETWORKS	PP	100	40	18	49	67	P
404183	MICROWAVE ENGINEERING	PP	100	40	20	48	68	P
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	25	54	79	P
404185D	ARTIFICIAL INTELLIGENCE	PP	100	40	18	43	61	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			38	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			37	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			38	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			35	P
404188	PROJECT PHASE I	OR	50	20			39	P

FIRST SEM TOTAL = 531/750

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120053227 SONAWANE MRUNAL SHAILESH PRATIKSHA ,71401204B ,PICT ,B120053227

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	12	46	58	P
404182	COMPUTER NETWORKS	PP	100	40	12	36	48	P
404183	MICROWAVE ENGINEERING	PP	100	40	13	41	54	P
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	13	37	50	P
404185D	ARTIFICIAL INTELLIGENCE	PP	100	40	15	36	51	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			39	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			41	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			36	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			40	P
404188	PROJECT PHASE I	OR	50	20			45	P

FIRST SEM TOTAL = 462/750

B120053228 SOUMYA SHUKLA SANGEETA ,71401209C ,PICT ,B120053228

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	19	39	58	P
404182	COMPUTER NETWORKS	PP	100	40	14	39	53	P
404183	MICROWAVE ENGINEERING	PP	100	40	13	39	52	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	12	41	53	P
404185C	PLCS AND AUTOMATION	PP	100	40	7	35	42	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			36	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			39	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			38	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			33	P
404188	PROJECT PHASE I	OR	50	20			40	P

FIRST SEM TOTAL = 444/750

B120053229 SRIBHASHYAM SIDDHANT SRINIVAS JYOTI ,71401210G ,PICT ,B120053229

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	9	34	43	P
404182	COMPUTER NETWORKS	PP	100	40	16	43	59	P
404183	MICROWAVE ENGINEERING	PP	100	40	08	22	--	F
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	21	39	60	P
404185C	PLCS AND AUTOMATION	PP	100	40	13	36	49	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			38	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			38	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			43	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			30	P
404188	PROJECT PHASE I	OR	50	20			33	P

FIRST SEM TOTAL = --/750

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120053230 SUCHETA JAGDISH BIYANI SUNITA ,71401211E ,PICT ,B120053230

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	19	42	61	P
404182	COMPUTER NETWORKS	PP	100	40	16	46	62	P
404183	MICROWAVE ENGINEERING	PP	100	40	19	37	56	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	21	50	71	P
404185C	PLCS AND AUTOMATION	PP	100	40	19	34	53	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			38	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			38	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			36	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			40	P
404188	PROJECT PHASE I	OR	50	20			40	P

FIRST SEM TOTAL = 495/750

B120053231 SUDHIR KUMAR OJHA MEENA KUMARI OJHA ,71401212C ,PICT ,B120053231

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	16	36	52	P
404182	COMPUTER NETWORKS	PP	100	40	14	36	50	P
404183	MICROWAVE ENGINEERING	PP	100	40	11	38	49	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	13	42	55	P
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40		53	53	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			39	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			37	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			36	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			33	P
404188	PROJECT PHASE I	OR	50	20			41	P

FIRST SEM TOTAL = 445/750

B120053232 SURAJ YASHWANT WANDILE CHANDA ,71401215H ,PICT ,B120053232

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	16	40	56	P
404182	COMPUTER NETWORKS	PP	100	40	21	44	65	P
404183	MICROWAVE ENGINEERING	PP	100	40	17	43	60	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	20	37	57	P
404185C	PLCS AND AUTOMATION	PP	100	40	20	40	60	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			40	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			41	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			43	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			42	P
404188	PROJECT PHASE I	OR	50	20			33	P

FIRST SEM TOTAL = 497/750

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120053233 SURWASE NEHA KHANDU SHEELA ,71401216F ,PICT ,B120053233

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	17	42	59	P
404182	COMPUTER NETWORKS	PP	100	40	19	43	62	P
404183	MICROWAVE ENGINEERING	PP	100	40	20	50	70	P
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	21	51	72	P
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40		55	55	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			39	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			35	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			37	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			25	P
404188	PROJECT PHASE I	OR	50	20			37	P

FIRST SEM TOTAL = 491/750

B120053234 SURYAWANSHI PRADUMNA VILAS JAYSHREE ,71500214H ,PICT ,B120053234

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	18	40	58	P
404182	COMPUTER NETWORKS	PP	100	40	27	46	73	P
404183	MICROWAVE ENGINEERING	PP	100	40	23	40	63	P
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	22	52	74	P
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40		51	51	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			42	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			41	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			41	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			40	P
404188	PROJECT PHASE I	OR	50	20			41	P

FIRST SEM TOTAL = 524/750

B120053235 TANUSHREE KUSHWAH SANGEETA ,71401226C ,PICT ,B120053235

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	19	40	59	P
404182	COMPUTER NETWORKS	PP	100	40	17	48	65	P
404183	MICROWAVE ENGINEERING	PP	100	40	21	37	58	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	9	45	54	P
404185C	PLCS AND AUTOMATION	PP	100	40	17	41	58	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			38	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			37	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			40	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			36	P
404188	PROJECT PHASE I	OR	50	20			40	P

FIRST SEM TOTAL = 485/750

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120053236 THAKUR RAJ RAJESH MAMTA ,71301281B ,PICT ,B120053236

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	14	17	--	F
404182	COMPUTER NETWORKS	PP	100	40	05	33	--	F
404183	MICROWAVE ENGINEERING	PP	100	40	18	24	--	F
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	06	30	--	F
404185C	PLCS AND AUTOMATION	PP	100	40	16	34	50	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			35	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			38	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			25	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			33	P
404188	PROJECT PHASE I	OR	50	20			36	P

FIRST SEM TOTAL = --/750

B120053237 THOMBRE ANIL DAMODHAR MEERA ,71401232H ,PICT ,B120053237

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	18	44	62	P
404182	COMPUTER NETWORKS	PP	100	40	26	40	66	P
404183	MICROWAVE ENGINEERING	PP	100	40	22	39	61	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	17	36	53	P
404185C	PLCS AND AUTOMATION	PP	100	40	21	29	50	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			41	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			38	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			38	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			33	P
404188	PROJECT PHASE I	OR	50	20			34	P

FIRST SEM TOTAL = 476/750

B120053238 TILLU GAURI SACHIN MANJUSHA ,71401236L ,PICT ,B120053238

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	22	42	64	P
404182	COMPUTER NETWORKS	PP	100	40	14	44	58	P
404183	MICROWAVE ENGINEERING	PP	100	40	20	48	68	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	17	35	52	P
404185A	MULTI RATE & ADAP SIGN. PROC	PP	100	40	18	29	47	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			43	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			40	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			44	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			40	P
404188	PROJECT PHASE I	OR	50	20			42	P

FIRST SEM TOTAL = 498/750

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120053239 TIWARI KALYANI RAJARAM BABITA ,71401238G ,PICT ,B120053239

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	13	44	57	P
404182	COMPUTER NETWORKS	PP	100	40	21	37	58	P
404183	MICROWAVE ENGINEERING	PP	100	40	14	43	57	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	17	41	58	P
404185C	PLCS AND AUTOMATION	PP	100	40	16	34	50	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			38	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			33	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			45	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			39	P
404188	PROJECT PHASE I	OR	50	20			38	P

FIRST SEM TOTAL = 473/750

B120053240 TUPE NIKITA SHRIKANT REKHA ,71500215F ,PICT ,B120053240

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	20	48	68	P
404182	COMPUTER NETWORKS	PP	100	40	23	47	70	P
404183	MICROWAVE ENGINEERING	PP	100	40	18	44	62	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	27	41	68	P
404185C	PLCS AND AUTOMATION	PP	100	40	19	41	60	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			39	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			37	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			46	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			37	P
404188	PROJECT PHASE I	OR	50	20			40	P

FIRST SEM TOTAL = 527/750

B120053241 TWINKLE H. KARIYA SHEELA H. KARIYA ,71401012L ,PICT ,B120053241

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	22	48	70	P
404182	COMPUTER NETWORKS	PP	100	40	24	57	81	P
404183	MICROWAVE ENGINEERING	PP	100	40	20	49	69	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	15	49	64	P
404185C	PLCS AND AUTOMATION	PP	100	40	23	48	71	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			44	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			40	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			37	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			40	P
404188	PROJECT PHASE I	OR	50	20			39	P

FIRST SEM TOTAL = 555/750

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120053242 UKE SUNIL HANSARAJ SUNITA ,71500216D ,PICT ,B120053242

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	18	40	58	P
404182	COMPUTER NETWORKS	PP	100	40	26	51	77	P
404183	MICROWAVE ENGINEERING	PP	100	40	23	38	61	P
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	23	49	72	P
404185C	PLCS AND AUTOMATION	PP	100	40	18	40	58	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			42	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			39	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			41	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			39	P
404188	PROJECT PHASE I	OR	50	20			41	P

FIRST SEM TOTAL = 528/750

B120053243 UPGANLAWAR SAKET SANJAY SADHANA ,71401244M ,PICT ,B120053243

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	17	41	58	P
404182	COMPUTER NETWORKS	PP	100	40	24	40	64	P
404183	MICROWAVE ENGINEERING	PP	100	40	14	29	43	P
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	15	33	48	P
404185C	PLCS AND AUTOMATION	PP	100	40	19	30	49	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			40	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			41	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			37	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			36	P
404188	PROJECT PHASE I	OR	50	20			40	P

FIRST SEM TOTAL = 456/750

B120053244 VAIDYA NANDAN SHREENIVAS MEENAL ,71401248D ,PICT ,B120053244

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	18	47	65	P
404182	COMPUTER NETWORKS	PP	100	40	26	43	69	P
404183	MICROWAVE ENGINEERING	PP	100	40	17	52	69	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	23	37	60	P
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40		45	45	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			45	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			42	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			44	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			45	P
404188	PROJECT PHASE I	OR	50	20			45	P

FIRST SEM TOTAL = 529/750

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120053245 VARMA SAGAR SANJAY JYOTI ,71500217B ,PICT ,B120053245

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	21	54	75	P
404182	COMPUTER NETWORKS	PP	100	40	25	55	80	P
404183	MICROWAVE ENGINEERING	PP	100	40	21	57	78	P
404184C	SOFTWARE DEFINED RADIO	PP	100	40	28	59	87	P
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40		50	50	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			45	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			38	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			43	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			28	P
404188	PROJECT PHASE I	OR	50	20			40	P

FIRST SEM TOTAL = 564/750

B120053246 VEKHANDI NIVEDITA DILIP VIDYA ,71500218L ,PICT ,B120053246

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	16	46	62	P
404182	COMPUTER NETWORKS	PP	100	40	22	48	70	P
404183	MICROWAVE ENGINEERING	PP	100	40	21	46	67	P
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	17	36	53	P
404185C	PLCS AND AUTOMATION	PP	100	40	18	37	55	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			39	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			39	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			40	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			39	P
404188	PROJECT PHASE I	OR	50	20			37	P

FIRST SEM TOTAL = 501/750

B120053247 VERMA POOJA MOHAN GANGA ,71500219J ,PICT ,B120053247

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	14	38	52	P
404182	COMPUTER NETWORKS	PP	100	40	19	47	66	P
404183	MICROWAVE ENGINEERING	PP	100	40	17	49	66	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	16	28	44	P
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40		47	47	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			40	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			40	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			38	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			33	P
404188	PROJECT PHASE I	OR	50	20			43	P

FIRST SEM TOTAL = 469/750

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120053248 VETAL SHREYASH JALINDAR SUNITA ,71401255G ,PICT ,B120053248

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	12	29	41	P
404182	COMPUTER NETWORKS	PP	100	40	21	38	59	P
404183	MICROWAVE ENGINEERING	PP	100	40	11	33	44	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	11	26	--	F
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	AA	38	--	F
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			38	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			33	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			39	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			31	P
404188	PROJECT PHASE I	OR	50	20			35	P

FIRST SEM TOTAL = --/750

B120053249 VHATKAR ONKAR MAHADEO CHHAYA ,71401256E ,PICT ,B120053249

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	12	32	44	P
404182	COMPUTER NETWORKS	PP	100	40	9	50	59	P
404183	MICROWAVE ENGINEERING	PP	100	40	20	41	61	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	9	40	49	P
404185C	PLCS AND AUTOMATION	PP	100	40	16	34	50	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			36	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			38	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			38	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			33	P
404188	PROJECT PHASE I	OR	50	20			38	P

FIRST SEM TOTAL = 446/750

B120053250 VIKAS ARUN RAYKAR SUNITA ,71401259K ,PICT ,B120053250

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	18	42	60	P
404182	COMPUTER NETWORKS	PP	100	40	22	55	77	P
404183	MICROWAVE ENGINEERING	PP	100	40	16	51	67	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	16	40	56	P
404185C	PLCS AND AUTOMATION	PP	100	40	15	39	54	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			37	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			40	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			34	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			34	P
404188	PROJECT PHASE I	OR	50	20			41	P

FIRST SEM TOTAL = 500/750

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120053251 VIKAS PATHAK MANJU PATHAK ,71401260C ,PICT ,B120053251

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	13	41	54	P
404182	COMPUTER NETWORKS	PP	100	40	20	41	61	P
404183	MICROWAVE ENGINEERING	PP	100	40	14	34	48	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	15	42	57	P
404185C	PLCS AND AUTOMATION	PP	100	40	16	38	54	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			38	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			35	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			42	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			33	P
404188	PROJECT PHASE I	OR	50	20			38	P

FIRST SEM TOTAL = 460/750

B120053252 VISHNU VIVEK LAKSHMI ,71401262K ,PICT ,B120053252

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	22	47	69	P
404182	COMPUTER NETWORKS	PP	100	40	24	39	63	P
404183	MICROWAVE ENGINEERING	PP	100	40	18	48	66	P
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	18	52	70	P
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40		55	55	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			41	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			40	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			38	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			42	P
404188	PROJECT PHASE I	OR	50	20			43	P

FIRST SEM TOTAL = 527/750

B120053253 VRUSHABH PRASHANT MUDHALE VAISHALI ,71401264F ,PICT ,B120053253

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	15	40	55	P
404182	COMPUTER NETWORKS	PP	100	40	11	36	47	P
404183	MICROWAVE ENGINEERING	PP	100	40	17	44	61	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	4	41	45	P
404185C	PLCS AND AUTOMATION	PP	100	40	17	28	45	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			35	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			38	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			33	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			30	P
404188	PROJECT PHASE I	OR	50	20			39	P

FIRST SEM TOTAL = 428/750

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120053254 WAGH MADHURI SHEKHAR MANDA ,71500220B ,PICT ,B120053254

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	20	52	72	P
404182	COMPUTER NETWORKS	PP	100	40	23	56	79	P
404183	MICROWAVE ENGINEERING	PP	100	40	23	54	77	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	15	54	69	P
404185C	PLCS AND AUTOMATION	PP	100	40	23	49	72	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			41	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			38	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			44	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			34	P
404188	PROJECT PHASE I	OR	50	20			37	P

FIRST SEM TOTAL = 563/750

B120053255 WAGHMARE AKASH SANJAY ANITA ,71401270L ,PICT ,B120053255

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	17	46	63	P
404182	COMPUTER NETWORKS	PP	100	40	26	46	72	P
404183	MICROWAVE ENGINEERING	PP	100	40	21	43	64	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	26	44	70	P
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40		60	60	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			42	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			39	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			42	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			35	P
404188	PROJECT PHASE I	OR	50	20			42	P

FIRST SEM TOTAL = 529/750

B120053256 WALUNJ PRIYANKA VITTHAL SAVITA ,71500221L ,PICT ,B120053256

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	19	44	63	P
404182	COMPUTER NETWORKS	PP	100	40	23	51	74	P
404183	MICROWAVE ENGINEERING	PP	100	40	23	53	76	P
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	24	62	86	P
404185C	PLCS AND AUTOMATION	PP	100	40	22	49	71	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			42	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			40	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			43	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			43	P
404188	PROJECT PHASE I	OR	50	20			45	P

FIRST SEM TOTAL = 583/750

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120053257 WANKHADE CHETAN SAMADHAN SUMAN ,71500222J ,PICT ,B120053257

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	19	45	64	P
404182	COMPUTER NETWORKS	PP	100	40	22	51	73	P
404183	MICROWAVE ENGINEERING	PP	100	40	21	52	73	P
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	19	52	71	P
404185C	PLCS AND AUTOMATION	PP	100	40	23	44	67	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			40	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			36	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			42	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			35	P
404188	PROJECT PHASE I	OR	50	20			42	P

FIRST SEM TOTAL = 543/750

B120053258 YADAV AKANKSHA VILAS SANGITA ,71401277H ,PICT ,B120053258

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	11	22	--	F
404182	COMPUTER NETWORKS	PP	100	40	13	35	48	P
404183	MICROWAVE ENGINEERING	PP	100	40	17	29	46	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	11	35	46	P
404185C	PLCS AND AUTOMATION	PP	100	40	16	40	56	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			39	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			37	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			36	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			30	P
404188	PROJECT PHASE I	OR	50	20			38	P

FIRST SEM TOTAL = --/750

B120053259 YESHWANTE DEEPA BALASAHEB REKHA ,71301310K ,PICT ,B120053259

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	17	38	55	P
404182	COMPUTER NETWORKS	PP	100	40	8	34	42	P
404183	MICROWAVE ENGINEERING	PP	100	40	15	43	58	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	6	37	43	P
404185C	PLCS AND AUTOMATION	PP	100	40	10	34	44	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			39	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			36	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			36	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			34	P
404188	PROJECT PHASE I	OR	50	20			34	P

FIRST SEM TOTAL = 421/750

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120053260 ZAREKAR MAYURI BALASAHEB MANGAL ,71418641E ,PICT ,B120053260

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	17	36	53	P
404182	COMPUTER NETWORKS	PP	100	40	17	38	55	P
404183	MICROWAVE ENGINEERING	PP	100	40	15	50	65	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	12	45	57	P
404185C	PLCS AND AUTOMATION	PP	100	40	22	48	70	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			37	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			39	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			41	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			38	P
404188	PROJECT PHASE I	OR	50	20			40	P

FIRST SEM TOTAL = 495/750

B120053261 ZAREKAR PRATIBHA SANJAY ALKA ,71500223G ,PICT ,B120053261

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	21	44	65	P
404182	COMPUTER NETWORKS	PP	100	40	20	52	72	P
404183	MICROWAVE ENGINEERING	PP	100	40	9	53	62	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	12	51	63	P
404185C	PLCS AND AUTOMATION	PP	100	40	20	48	68	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			41	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			39	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			42	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			12	F
404188	PROJECT PHASE I	OR	50	20			44	P

FIRST SEM TOTAL = --/750

B120053262 ZENDE ROHIT SADHU RESHMA ,71401287E ,PICT ,B120053262

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	14	32	46	P
404182	COMPUTER NETWORKS	PP	100	40	21	43	64	P
404183	MICROWAVE ENGINEERING	PP	100	40	16	34	50	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	03	28	--	F
404185C	PLCS AND AUTOMATION	PP	100	40	17	44	61	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			37	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			41	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			43	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			37	P
404188	PROJECT PHASE I	OR	50	20			33	P

FIRST SEM TOTAL = --/750

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120053263 CHADHA SWASTIK ANIL KUMAR RUCHI CHADHA ,71300840H ,PICT ,B120053263

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	11	29	40	P	C
404182	COMPUTER NETWORKS	PP	100	40	14	30	44	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	10	36	46	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	12	28	40	P	C
404185D	ARTIFICIAL INTELLIGENCE	PP	100	40	9	34	43	P	
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			30	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			37	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			32	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			32	P	C
404188	PROJECT PHASE I	OR	50	20			32	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	11	30	41	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	24	30	54	P	C
404191A	SPEECH & AUDIO SIGNAL PROC.	PP	100	40	15	29	44	P	C
404192D	WIRELESS NETWORKS	PP	100	40	15	32	47	P	C
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			35	P	C
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			37	P	C
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			37	P	C
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			33	P	C
404195	PROJECT PHASE II	TW	100	40			91	P	C
404195	PROJECT PHASE II	PR	50	20			44	P	C

GRAND TOTAL = 839/1500 , Result : HIGHER SECOND CLASS

B120053264 DHODI SHRADDHA MADHU ARUNA ,71045415F ,PICT ,B120053264

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	14	35	49	P	C
404182	COMPUTER NETWORKS	PP	100	40	14	43	57	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	15	28	43	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	8	34	42	P	C
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	18	35	53	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			36	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			28	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			30	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			30	P	C
404188	PROJECT PHASE I	OR	50	20			42	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	13	36	49	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	23	28	51	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	19	45	64	P	C
404192D	WIRELESS NETWORKS	PP	100	40	15	36	51	P	C
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			39	P	C
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			32	P	C
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			30	P	C
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			38	P	C
404195	PROJECT PHASE II	TW	100	40			85	P	C
404195	PROJECT PHASE II	PR	50	20			40	P	C

GRAND TOTAL = 889/1500 , Result : HIGHER SECOND CLASS

B120053265 KEJDIWAL GOVIND SUBHASH MANISHA KEJDIWAL ,71301006B ,PICT ,B120053265

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	12	37	49	P	C
404182	COMPUTER NETWORKS	PP	100	40	5	36	41	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	04	AA	--	F	
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	11	29	40	P	C
404185C	PLCS AND AUTOMATION	PP	100	40	8	32	40	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			41	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			30	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			28	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			30	P	C
404188	PROJECT PHASE I	OR	50	20			34	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	17	28\$	45	P	C
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	16	36	52	P	C
404191A	SPEECH & AUDIO SIGNAL PROC.	PP	100	40	14	28	42	P	C
404192D	WIRELESS NETWORKS	PP	100	40	10	30	40	P	C
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			28	P	C
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			32	P	C
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			32	P	C
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			32	P	C
404195	PROJECT PHASE II	TW	100	40			89	P	C
404195	PROJECT PHASE II	PR	50	20			40	P	C

GRAND TOTAL = --/1500 , Result : FAILS [\$ 0.1]

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120053266 KHANDAWE OMKAR MADHUKAR KHANDAWE MAYA MADHUKAR ,71301016K ,PICT ,B120053266

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	5	42	47	P	C
404182	COMPUTER NETWORKS	PP	100	40	13	29	42	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	9	31	40	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	14	28	42	P	C
404185A	MULTI RATE & ADAP SIGN. PROC	PP	100	40	07	09	--	F	
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			36	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			30	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			32	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			30	P	C
404188	PROJECT PHASE I	OR	50	20			42	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	8	33	41	P	C
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	17	32	49	P	C
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	16	29	45	P	C
404192D	WIRELESS NETWORKS	PP	100	40	13	38	51	P	C
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			28	P	C
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			35	P	C
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			31	P	C
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			22	P	C
404195	PROJECT PHASE II	TW	100	40			90	P	C
404195	PROJECT PHASE II	PR	50	20			41	P	C

GRAND TOTAL = --/1500 , Result : FAILS

B120053267 NEHA NIKUM KAUSHALYA ,71200984B ,PICT ,B120053267

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	6	39	45	P	C
404182	COMPUTER NETWORKS	PP	100	40	6	34\$	40	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	11	38	49	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	15	35	50	P	
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	14	34	48	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			39	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			34	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			28	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			22	P	C
404188	PROJECT PHASE I	OR	50	20			30	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	18	28\$	46	P	C
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	13	34	47	P	C
404191A	SPEECH & AUDIO SIGNAL PROC.	PP	100	40	11	43	54	P	C
404192D	WIRELESS NETWORKS	PP	100	40	15	38	53	P	C
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			36	P	C
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			36	P	C
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			38	P	C
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			33	P	C
404195	PROJECT PHASE II	TW	100	40			80	P	C
404195	PROJECT PHASE II	PR	50	20			35	P	C

GRAND TOTAL = 843/1500 , Result : HIGHER SECOND CLASS [\$ 0.1]

B120053268 PAKHALE MIHIR GOVIND JAYASHRI ,71301115H ,PICT ,B120053268

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	15	39	54	P	C
404182	COMPUTER NETWORKS	PP	100	40	17	31	48	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	9	34	43	P	C
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	11	37	48	P	C
404185C	PLCS AND AUTOMATION	PP	100	40	15	35	50	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			42	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			37	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			43	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			38	P	C
404188	PROJECT PHASE I	OR	50	20			41	P	C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	16	28\$	44	P	C
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	20	47	67	P	C
404191D	SOFT COMPUTING	PP	100	40	12	29	41	P	C
404192D	WIRELESS NETWORKS	PP	100	40	15	31	46	P	C
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			34	P	C
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			32	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			36	P	C
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			39	P	C
404195	PROJECT PHASE II	TW	100	40			79	P	C
404195	PROJECT PHASE II	PR	50	20			35	P	C

GRAND TOTAL = 897/1500 , Result : HIGHER SECOND CLASS [\$ 0.1]

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120053269 SACHDEV YASH ANIL SONA ,71301198L ,PICT ,B120053269

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	8	35	43	P
404182	COMPUTER NETWORKS	PP	100	40	12	28	40	P C
404183	MICROWAVE ENGINEERING	PP	100	40	11	29	40	P C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	8	39	47	P C
404185C	PLCS AND AUTOMATION	PP	100	40	17	28	45	P C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			39	P C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			38	P C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			30	P C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			35	P C
404188	PROJECT PHASE I	OR	50	20			35	P C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	15	34	49	P C
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	12	35	47	P
404191A	SPEECH & AUDIO SIGNAL PROC.	PP	100	40	13	40	53	P C
404192D	WIRELESS NETWORKS	PP	100	40	12	28	40	P C
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			29	P C
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			36	P C
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			43	P C
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			36	P C
404195	PROJECT PHASE II	TW	100	40			94	P C
404195	PROJECT PHASE II	PR	50	20			45	P C

GRAND TOTAL = 864/1500 , Result : HIGHER SECOND CLASS [\$ 0.1]

B120053270 YASH MANGESH SAWANT MAITHILEE ,71301308H ,PICT ,B120053270

SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	11	29	40	P C
404182	COMPUTER NETWORKS	PP	100	40	7	33	40	P C
404183	MICROWAVE ENGINEERING	PP	100	40	11	31	42	P C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	10	35	45	P C
404185C	PLCS AND AUTOMATION	PP	100	40	15	39	54	P C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			37	P C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			30	P C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			36	P C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			30	P C
404188	PROJECT PHASE I	OR	50	20			38	P C

SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	15	36	51	P C
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	12	41	53	P C
404191A	SPEECH & AUDIO SIGNAL PROC.	PP	100	40	14	46	60	P C
404192A	BIOMEDICAL SIGNAL PROCESSING	PP	100	40	24	49	73	P C
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			21	P C
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			29	P
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			25	P
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			25	P
404195	PROJECT PHASE II	TW	100	40			87	P C
404195	PROJECT PHASE II	PR	50	20			38	P C

GRAND TOTAL = 854/1500 , Result : HIGHER SECOND CLASS

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

BRANCH: 76 B.E. (2012 PAT.)(COMPUTER)

DATE : 02 FEB 2017

CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120054201 BAYAS AISHWARYA KIRTISINGH VARSHA KIRTISINGH BAYAS ,71400699J ,PICT ,B120054201

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	16	41	57	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	07	28	--	F
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	15	42	57	P
410444D	DATA MINING TECH. & APPL.	PP	100	40	15	39	54	P
410445B	PERVASIVE COMPUTING	PP	100	40	18	31	49	P
410446	COMPUTER LABORATORY-I	PR	50	20			39	P
410446	COMPUTER LABORATORY-I	OR	50	20			38	P
410447	COMPUTER LABORATORY-II	TW	50	20			38	P
410447	COMPUTER LABORATORY-II	OR	50	20			35	P
410448	PROJECT	TW	50	20			40	P

FIRST SEM TOTAL = --/750

B120054202 AAKASH RAINA SUNITA ,71400678F ,PICT ,B120054202

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	17	44	61	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	24	44	68	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	19	35	54	P
410444D	DATA MINING TECH. & APPL.	PP	100	40	18	37	55	P
410445B	PERVASIVE COMPUTING	PP	100	40	13	38	51	P
410446	COMPUTER LABORATORY-I	PR	50	20			42	P
410446	COMPUTER LABORATORY-I	OR	50	20			41	P
410447	COMPUTER LABORATORY-II	TW	50	20			42	P
410447	COMPUTER LABORATORY-II	OR	50	20			42	P
410448	PROJECT	TW	50	20			45	P

FIRST SEM TOTAL = 501/750

B120054203 ABAK KOMAL RAVINDRA SAVITA ,71500224E ,PICT ,B120054203

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	20	47	67	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	23	55	78	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	21	49	70	P
410444C	ADVANCED COMPUTER PROG.	PP	100	40	24	54	78	P
410445B	PERVASIVE COMPUTING	PP	100	40	21	47	68	P
410446	COMPUTER LABORATORY-I	PR	50	20			40	P
410446	COMPUTER LABORATORY-I	OR	50	20			39	P
410447	COMPUTER LABORATORY-II	TW	50	20			40	P
410447	COMPUTER LABORATORY-II	OR	50	20			37	P
410448	PROJECT	TW	50	20			40	P

FIRST SEM TOTAL = 557/750

BRANCH: 76 B.E. (2012 PAT.)(COMPUTER)

DATE : 02 FEB 2017

CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
 OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120054204 ABDUL OWAIS NURUL HAQUE KAZI SHAHENAZ PARVEEN ,71400681F ,PICT ,B120054204

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	23	44	67	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	18	51	69	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	20	47	67	P
410444B	COMP. NET. DESIGN & MODELING	PP	100	40	18	48	66	P
410445B	PERVASIVE COMPUTING	PP	100	40	19	37	56	P
410446	COMPUTER LABORATORY-I	PR	50	20			45	P
410446	COMPUTER LABORATORY-I	OR	50	20			43	P
410447	COMPUTER LABORATORY-II	TW	50	20			42	P
410447	COMPUTER LABORATORY-II	OR	50	20			40	P
410448	PROJECT	TW	50	20			46	P

FIRST SEM TOTAL = 541/750

B120054205 ABHANG AKSHAY DILIP SMITA ,71400682D ,PICT ,B120054205

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	18	46	64	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	22	45	67	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	17	43	60	P
410444D	DATA MINING TECH. & APPL.	PP	100	40	20	42	62	P
410445D	MULTIDISCIPLINARY NLP	PP	100	40	22	47	69	P
410446	COMPUTER LABORATORY-I	PR	50	20			43	P
410446	COMPUTER LABORATORY-I	OR	50	20			44	P
410447	COMPUTER LABORATORY-II	TW	50	20			39	P
410447	COMPUTER LABORATORY-II	OR	50	20			37	P
410448	PROJECT	TW	50	20			42	P

FIRST SEM TOTAL = 527/750

B120054206 ABHANG PRERNA MANGESH MEGHA ,71500225C ,PICT ,B120054206

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	12	42	54	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	23	43	66	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	15	37	52	P
410444B	COMP. NET. DESIGN & MODELING	PP	100	40	24	50	74	P
410445B	PERVASIVE COMPUTING	PP	100	40	19	40	59	P
410446	COMPUTER LABORATORY-I	PR	50	20			45	P
410446	COMPUTER LABORATORY-I	OR	50	20			40	P
410447	COMPUTER LABORATORY-II	TW	50	20			42	P
410447	COMPUTER LABORATORY-II	OR	50	20			40	P
410448	PROJECT	TW	50	20			40	P

FIRST SEM TOTAL = 512/750

BRANCH: 76 B.E. (2012 PAT.)(COMPUTER)

DATE : 02 FEB 2017

CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
 OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120054207 ABHISHEK DNYANESHWAR KASHID LATA ,71400686G ,PICT ,B120054207

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	19	50	69	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	19	45	64	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	14	44	58	P
410444B	COMP. NET. DESIGN & MODELING	PP	100	40	18	48	66	P
410445B	PERVASIVE COMPUTING	PP	100	40	18	36	54	P
410446	COMPUTER LABORATORY-I	PR	50	20			45	P
410446	COMPUTER LABORATORY-I	OR	50	20			42	P
410447	COMPUTER LABORATORY-II	TW	50	20			38	P
410447	COMPUTER LABORATORY-II	OR	50	20			38	P
410448	PROJECT	TW	50	20			43	P

FIRST SEM TOTAL = 517/750

B120054208 ABHISHEK LAXMAN DEORE SANGITA ,71500226M ,PICT ,B120054208

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	21	48	69	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	23	49	72	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	16	39	55	P
410444B	COMP. NET. DESIGN & MODELING	PP	100	40	24	46	70	P
410445B	PERVASIVE COMPUTING	PP	100	40	19	42	61	P
410446	COMPUTER LABORATORY-I	PR	50	20			43	P
410446	COMPUTER LABORATORY-I	OR	50	20			42	P
410447	COMPUTER LABORATORY-II	TW	50	20			35	P
410447	COMPUTER LABORATORY-II	OR	50	20			36	P
410448	PROJECT	TW	50	20			43	P

FIRST SEM TOTAL = 526/750

B120054209 ADESH KUMAR RANJANA ,71400688C ,PICT ,B120054209

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	14	46	60	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	17	24	--	F
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	12	35	47	P
410444D	DATA MINING TECH. & APPL.	PP	100	40	13	31	44	P
410445B	PERVASIVE COMPUTING	PP	100	40	17	33	50	P
410446	COMPUTER LABORATORY-I	PR	50	20			38	P
410446	COMPUTER LABORATORY-I	OR	50	20			36	P
410447	COMPUTER LABORATORY-II	TW	50	20			40	P
410447	COMPUTER LABORATORY-II	OR	50	20			40	P
410448	PROJECT	TW	50	20			38	P

FIRST SEM TOTAL = --/750

BRANCH: 76 B.E. (2012 PAT.)(COMPUTER)

DATE : 02 FEB 2017

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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B120054210 ADITI SAHNI KIRAN ,71400689M ,PICT ,B120054210

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	22	45	67	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	26	50	76	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	20	40	60	P
410444C	ADVANCED COMPUTER PROG.	PP	100	40	28	51	79	P
410445B	PERVASIVE COMPUTING	PP	100	40	20	44	64	P
410446	COMPUTER LABORATORY-I	PR	50	20			43	P
410446	COMPUTER LABORATORY-I	OR	50	20			43	P
410447	COMPUTER LABORATORY-II	TW	50	20			44	P
410447	COMPUTER LABORATORY-II	OR	50	20			42	P
410448	PROJECT	TW	50	20			43	P

FIRST SEM TOTAL = 561/750

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B120054211 ADITYA POLE ALKA ,71400691C ,PICT ,B120054211

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	20	43	63	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	27	40	67	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	14	39	53	P
410444B	COMP. NET. DESIGN & MODELING	PP	100	40	24	34	58	P
410445B	PERVASIVE COMPUTING	PP	100	40	14	38	52	P
410446	COMPUTER LABORATORY-I	PR	50	20			43	P
410446	COMPUTER LABORATORY-I	OR	50	20			40	P
410447	COMPUTER LABORATORY-II	TW	50	20			40	P
410447	COMPUTER LABORATORY-II	OR	50	20			38	P
410448	PROJECT	TW	50	20			42	P

FIRST SEM TOTAL = 496/750

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B120054212 ADOKAR SHAMBHAVI RAJESH PRATIBHA ,71500227K ,PICT ,B120054212

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	20	45	65	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	25	47	72	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	13	36	49	P
410444B	COMP. NET. DESIGN & MODELING	PP	100	40	22	50	72	P
410445B	PERVASIVE COMPUTING	PP	100	40	15	33	48	P
410446	COMPUTER LABORATORY-I	PR	50	20			43	P
410446	COMPUTER LABORATORY-I	OR	50	20			42	P
410447	COMPUTER LABORATORY-II	TW	50	20			42	P
410447	COMPUTER LABORATORY-II	OR	50	20			42	P
410448	PROJECT	TW	50	20			41	P

FIRST SEM TOTAL = 516/750

BRANCH: 76 B.E. (2012 PAT.)(COMPUTER)

DATE : 02 FEB 2017

CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
 OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120054213 AGAM JAIN NEETA ,71400692M ,PICT ,B120054213

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	13	44	57	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	14	45	59	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	12	36	48	P
410444D	DATA MINING TECH. & APPL.	PP	100	40	18	36	54	P
410445A	PROB. SOLV. WITH GAMIFICATION	PP	100	40	18	41	59	P
410446	COMPUTER LABORATORY-I	PR	50	20			40	P
410446	COMPUTER LABORATORY-I	OR	50	20			38	P
410447	COMPUTER LABORATORY-II	TW	50	20			40	P
410447	COMPUTER LABORATORY-II	OR	50	20			39	P
410448	PROJECT	TW	50	20			39	P

FIRST SEM TOTAL = 473/750

B120054214 AGRAWAL KANAK DIVESH RADHIKA ,71400694H ,PICT ,B120054214

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	22	43	65	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	19	44	63	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	19	40	59	P
410444D	DATA MINING TECH. & APPL.	PP	100	40	24	32	56	P
410445A	PROB. SOLV. WITH GAMIFICATION	PP	100	40	19	41	60	P
410446	COMPUTER LABORATORY-I	PR	50	20			40	P
410446	COMPUTER LABORATORY-I	OR	50	20			38	P
410447	COMPUTER LABORATORY-II	TW	50	20			35	P
410447	COMPUTER LABORATORY-II	OR	50	20			35	P
410448	PROJECT	TW	50	20			42	P

FIRST SEM TOTAL = 493/750

B120054215 AKASH ALOK VARTAK MRUDULA ,71400702B ,PICT ,B120054215

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	18	50	68	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	24	46	70	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	23	37	60	P
410444D	DATA MINING TECH. & APPL.	PP	100	40	21	32	53	P
410445B	PERVASIVE COMPUTING	PP	100	40	13	34	47	P
410446	COMPUTER LABORATORY-I	PR	50	20			43	P
410446	COMPUTER LABORATORY-I	OR	50	20			43	P
410447	COMPUTER LABORATORY-II	TW	50	20			43	P
410447	COMPUTER LABORATORY-II	OR	50	20			44	P
410448	PROJECT	TW	50	20			45	P

FIRST SEM TOTAL = 516/750

BRANCH: 76 B.E. (2012 PAT.)(COMPUTER)

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
 OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120054216 AKHADE SHITAL PRAKASH SEEMA ,71500228H ,PICT ,B120054216

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	16	48	64	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	27	48	75	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	16	42	58	P
410444C	ADVANCED COMPUTER PROG.	PP	100	40	27	47	74	P
410445B	PERVASIVE COMPUTING	PP	100	40	19	44	63	P
410446	COMPUTER LABORATORY-I	PR	50	20			39	P
410446	COMPUTER LABORATORY-I	OR	50	20			38	P
410447	COMPUTER LABORATORY-II	TW	50	20			40	P
410447	COMPUTER LABORATORY-II	OR	50	20			38	P
410448	PROJECT	TW	50	20			38	P

FIRST SEM TOTAL = 527/750

B120054217 AKOLE ANAGHA SANJAY SANGEETA SANJAY WANI ,71400704J ,PICT ,B120054217

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	17	48	65	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	29	50	79	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	20	42	62	P
410444D	DATA MINING TECH. & APPL.	PP	100	40	28	36	64	P
410445B	PERVASIVE COMPUTING	PP	100	40	19	45	64	P
410446	COMPUTER LABORATORY-I	PR	50	20			45	P
410446	COMPUTER LABORATORY-I	OR	50	20			44	P
410447	COMPUTER LABORATORY-II	TW	50	20			45	P
410447	COMPUTER LABORATORY-II	OR	50	20			41	P
410448	PROJECT	TW	50	20			45	P

FIRST SEM TOTAL = 554/750

B120054218 AKRITI GOYAL ASHA ,71400705G ,PICT ,B120054218

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	22	47	69	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	19	45	64	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	16	42	58	P
410444D	DATA MINING TECH. & APPL.	PP	100	40	21	39	60	P
410445B	PERVASIVE COMPUTING	PP	100	40	19	41	60	P
410446	COMPUTER LABORATORY-I	PR	50	20			44	P
410446	COMPUTER LABORATORY-I	OR	50	20			43	P
410447	COMPUTER LABORATORY-II	TW	50	20			41	P
410447	COMPUTER LABORATORY-II	OR	50	20			40	P
410448	PROJECT	TW	50	20			42	P

FIRST SEM TOTAL = 521/750

BRANCH: 76 B.E. (2012 PAT.)(COMPUTER)

DATE : 02 FEB 2017

CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
 OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120054219 AKSHAT JAIN NOOPUR ,71400706E ,PICT ,B120054219

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	19	48	67	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	15	31	46	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	16	41	57	P
410444B	COMP. NET. DESIGN & MODELING	PP	100	40	22	48	70	P
410445B	PERVASIVE COMPUTING	PP	100	40	17	30	47	P
410446	COMPUTER LABORATORY-I	PR	50	20			43	P
410446	COMPUTER LABORATORY-I	OR	50	20			42	P
410447	COMPUTER LABORATORY-II	TW	50	20			43	P
410447	COMPUTER LABORATORY-II	OR	50	20			42	P
410448	PROJECT	TW	50	20			45	P

FIRST SEM TOTAL = 502/750

B120054220 AKSHIT PASRJIJA ANJU PASRIJA ,71400707C ,PICT ,B120054220

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	16	42	58	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	15	23	--	F
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	13	33	46	P
410444D	DATA MINING TECH. & APPL.	PP	100	40	13	28	41	P
410445B	PERVASIVE COMPUTING	PP	100	40	15	30	45	P
410446	COMPUTER LABORATORY-I	PR	50	20			41	P
410446	COMPUTER LABORATORY-I	OR	50	20			40	P
410447	COMPUTER LABORATORY-II	TW	50	20			42	P
410447	COMPUTER LABORATORY-II	OR	50	20			40	P
410448	PROJECT	TW	50	20			39	P

FIRST SEM TOTAL = --/750

B120054221 AMIT LAL MANJU LAL ,71400710C ,PICT ,B120054221

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	11	33	44	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	14	29	43	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	13	34	47	P
410444C	ADVANCED COMPUTER PROG.	PP	100	40	16	38	54	P
410445A	PROB. SOLV. WITH GAMIFICATION	PP	100	40	19	40	59	P
410446	COMPUTER LABORATORY-I	PR	50	20			41	P
410446	COMPUTER LABORATORY-I	OR	50	20			40	P
410447	COMPUTER LABORATORY-II	TW	50	20			38	P
410447	COMPUTER LABORATORY-II	OR	50	20			38	P
410448	PROJECT	TW	50	20			41	P

FIRST SEM TOTAL = 445/750

BRANCH: 76 B.E. (2012 PAT.)(COMPUTER)

DATE : 02 FEB 2017

CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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B120054222 AMLEKAR PRANAV SHRIPAD MADHAVI ,71400711M ,PICT ,B120054222

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	13	45	58	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	21	46	67	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	17	39	56	P
410444D	DATA MINING TECH. & APPL.	PP	100	40	21	41	62	P
410445B	PERVASIVE COMPUTING	PP	100	40	19	37	56	P
410446	COMPUTER LABORATORY-I	PR	50	20			45	P
410446	COMPUTER LABORATORY-I	OR	50	20			44	P
410447	COMPUTER LABORATORY-II	TW	50	20			36	P
410447	COMPUTER LABORATORY-II	OR	50	20			38	P
410448	PROJECT	TW	50	20			43	P

FIRST SEM TOTAL = 505/750

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B120054223 AMRUTA ABHIJEET RANADE MRUDULA ,71400713H ,PICT ,B120054223

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	14	50	64	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	19	41	60	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	20	42	62	P
410444C	ADVANCED COMPUTER PROG.	PP	100	40	27	41	68	P
410445C	EMBEDDED SECURITY	PP	100	40	23	48	71	P
410446	COMPUTER LABORATORY-I	PR	50	20			39	P
410446	COMPUTER LABORATORY-I	OR	50	20			37	P
410447	COMPUTER LABORATORY-II	TW	50	20			40	P
410447	COMPUTER LABORATORY-II	OR	50	20			38	P
410448	PROJECT	TW	50	20			39	P

FIRST SEM TOTAL = 518/750

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B120054224 AMRUTKAR APARNA RAJENDRA JYOTI ,71400714F ,PICT ,B120054224

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	26	44	70	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	26	42	68	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	18	36	54	P
410444C	ADVANCED COMPUTER PROG.	PP	100	40	26	44	70	P
410445B	PERVASIVE COMPUTING	PP	100	40	16	40	56	P
410446	COMPUTER LABORATORY-I	PR	50	20			44	P
410446	COMPUTER LABORATORY-I	OR	50	20			44	P
410447	COMPUTER LABORATORY-II	TW	50	20			46	P
410447	COMPUTER LABORATORY-II	OR	50	20			42	P
410448	PROJECT	TW	50	20			45	P

FIRST SEM TOTAL = 539/750

BRANCH: 76 B.E. (2012 PAT.)(COMPUTER)

DATE : 02 FEB 2017

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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B120054225 ANAY VIVEK JOSHI VINITA VIVEK JOSHI ,71400715D ,PICT ,B120054225

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	15	38	53	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	06	28	--	F
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	12	31	43	P
410444D	DATA MINING TECH. & APPL.	PP	100	40	12	28	40	P
410445B	PERVASIVE COMPUTING	PP	100	40	12	28	40	P
410446	COMPUTER LABORATORY-I	PR	50	20			37	P
410446	COMPUTER LABORATORY-I	OR	50	20			37	P
410447	COMPUTER LABORATORY-II	TW	50	20			38	P
410447	COMPUTER LABORATORY-II	OR	50	20			36	P
410448	PROJECT	TW	50	20			35	P

FIRST SEM TOTAL = --/750

.....

B120054226 ANEESHA MATHUR MALVIKA ,71400716B ,PICT ,B120054226

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	21	46	67	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	17	38	55	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	12	40	52	P
410444D	DATA MINING TECH. & APPL.	PP	100	40	19	40	59	P
410445B	PERVASIVE COMPUTING	PP	100	40	18	41	59	P
410446	COMPUTER LABORATORY-I	PR	50	20			45	P
410446	COMPUTER LABORATORY-I	OR	50	20			44	P
410447	COMPUTER LABORATORY-II	TW	50	20			45	P
410447	COMPUTER LABORATORY-II	OR	50	20			40	P
410448	PROJECT	TW	50	20			43	P

FIRST SEM TOTAL = 509/750

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B120054227 ANGADSINGH RAVINDRASINGH KALRA PARMINDERKAUR ,71400717L ,PICT ,B120054227

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	21	33	54	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	22	29	51	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	13	31	44	P
410444D	DATA MINING TECH. & APPL.	PP	100	40	17	31	48	P
410445B	PERVASIVE COMPUTING	PP	100	40	15	33	48	P
410446	COMPUTER LABORATORY-I	PR	50	20			40	P
410446	COMPUTER LABORATORY-I	OR	50	20			39	P
410447	COMPUTER LABORATORY-II	TW	50	20			44	P
410447	COMPUTER LABORATORY-II	OR	50	20			42	P
410448	PROJECT	TW	50	20			41	P

FIRST SEM TOTAL = 451/750

BRANCH: 76 B.E. (2012 PAT.)(COMPUTER)

DATE : 02 FEB 2017

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
 OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120054228 ARIHANT JAIN R VANITA ,71400723E ,PICT ,B120054228

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	21	45	66	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	16	28	44	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	14	42	56	P
410444D	DATA MINING TECH. & APPL.	PP	100	40	16	31	47	P
410445B	PERVASIVE COMPUTING	PP	100	40	18	34	52	P
410446	COMPUTER LABORATORY-I	PR	50	20			36	P
410446	COMPUTER LABORATORY-I	OR	50	20			35	P
410447	COMPUTER LABORATORY-II	TW	50	20			43	P
410447	COMPUTER LABORATORY-II	OR	50	20			40	P
410448	PROJECT	TW	50	20			41	P

FIRST SEM TOTAL = 460/750

B120054229 ASHUTOSH KUMAR REENA ,71400727H ,PICT ,B120054229

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	17	47	64	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	21	32	53	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	13	40	53	P
410444B	COMP. NET. DESIGN & MODELING	PP	100	40	20	48	68	P
410445B	PERVASIVE COMPUTING	PP	100	40	16	38	54	P
410446	COMPUTER LABORATORY-I	PR	50	20			43	P
410446	COMPUTER LABORATORY-I	OR	50	20			44	P
410447	COMPUTER LABORATORY-II	TW	50	20			38	P
410447	COMPUTER LABORATORY-II	OR	50	20			33	P
410448	PROJECT	TW	50	20			36	P

FIRST SEM TOTAL = 486/750

B120054230 AYUSH ROONGTA ANJALI ,71400730H ,PICT ,B120054230

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	18	44	62	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	17	28	45	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	14	40	54	P
410444D	DATA MINING TECH. & APPL.	PP	100	40	16	36	52	P
410445A	PROB. SOLV. WITH GAMIFICATION	PP	100	40	21	35	56	P
410446	COMPUTER LABORATORY-I	PR	50	20			38	P
410446	COMPUTER LABORATORY-I	OR	50	20			36	P
410447	COMPUTER LABORATORY-II	TW	50	20			40	P
410447	COMPUTER LABORATORY-II	OR	50	20			38	P
410448	PROJECT	TW	50	20			38	P

FIRST SEM TOTAL = 459/750

BRANCH: 76 B.E. (2012 PAT.)(COMPUTER)

DATE : 02 FEB 2017

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
 OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120054231 BACHHAV PURVA BHAUSAHEB MANISHA ,71400732D ,PICT ,B120054231

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	17	34	51	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	20	41	61	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	17	36	53	P
410444D	DATA MINING TECH. & APPL.	PP	100	40	16	40	56	P
410445D	MULTIDISCIPLINARY NLP	PP	100	40	12	53	65	P
410446	COMPUTER LABORATORY-I	PR	50	20			43	P
410446	COMPUTER LABORATORY-I	OR	50	20			42	P
410447	COMPUTER LABORATORY-II	TW	50	20			43	P
410447	COMPUTER LABORATORY-II	OR	50	20			40	P
410448	PROJECT	TW	50	20			47	P

FIRST SEM TOTAL = 501/750

B120054232 BAGADE SHAIKESH VIKAS SANGITA ,71400734L ,PICT ,B120054232

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	10	43	53	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	20	49	69	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	14	42	56	P
410444C	ADVANCED COMPUTER PROG.	PP	100	40	20	49	69	P
410445B	PERVASIVE COMPUTING	PP	100	40	18	39	57	P
410446	COMPUTER LABORATORY-I	PR	50	20			42	P
410446	COMPUTER LABORATORY-I	OR	50	20			41	P
410447	COMPUTER LABORATORY-II	TW	50	20			41	P
410447	COMPUTER LABORATORY-II	OR	50	20			38	P
410448	PROJECT	TW	50	20			43	P

FIRST SEM TOTAL = 509/750

B120054233 BAHETI DEVRAJ NARENDRA SANGEETA ,71400736G ,PICT ,B120054233

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	22	37	59	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	29	45	74	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	19	37	56	P
410444D	DATA MINING TECH. & APPL.	PP	100	40	14	34	48	P
410445A	PROB. SOLV. WITH GAMIFICATION	PP	100	40	21	41	62	P
410446	COMPUTER LABORATORY-I	PR	50	20			41	P
410446	COMPUTER LABORATORY-I	OR	50	20			40	P
410447	COMPUTER LABORATORY-II	TW	50	20			42	P
410447	COMPUTER LABORATORY-II	OR	50	20			39	P
410448	PROJECT	TW	50	20			39	P

FIRST SEM TOTAL = 500/750

BRANCH: 76 B.E. (2012 PAT.)(COMPUTER)

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
 OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120054234 BAJAJ PIYUSH RAJENDRA SUNITA ,71400741C ,PICT ,B120054234

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	24	44	68	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	23	44	67	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	24	46	70	P
410444D	DATA MINING TECH. & APPL.	PP	100	40	22	37	59	P
410445D	MULTIDISCIPLINARY NLP	PP	100	40	23	49	72	P
410446	COMPUTER LABORATORY-I	PR	50	20			40	P
410446	COMPUTER LABORATORY-I	OR	50	20			39	P
410447	COMPUTER LABORATORY-II	TW	50	20			42	P
410447	COMPUTER LABORATORY-II	OR	50	20			39	P
410448	PROJECT	TW	50	20			43	P

FIRST SEM TOTAL = 539/750

B120054235 BAKRE SHUBHAM KIRAN CHHAYA ,71400743K ,PICT ,B120054235

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	22	39	61	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	16	37	53	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	17	46	63	P
410444D	DATA MINING TECH. & APPL.	PP	100	40	17	38	55	P
410445B	PERVASIVE COMPUTING	PP	100	40	18	46	64	P
410446	COMPUTER LABORATORY-I	PR	50	20			43	P
410446	COMPUTER LABORATORY-I	OR	50	20			42	P
410447	COMPUTER LABORATORY-II	TW	50	20			42	P
410447	COMPUTER LABORATORY-II	OR	50	20			39	P
410448	PROJECT	TW	50	20			39	P

FIRST SEM TOTAL = 501/750

B120054236 BALWANI HITESH SURESH KIRAN ,71400745F ,PICT ,B120054236

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	22	23	--	F
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	22	38	60	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	16	36	52	P
410444B	COMP. NET. DESIGN & MODELING	PP	100	40	16	42	58	P
410445A	PROB. SOLV. WITH GAMIFICATION	PP	100	40	23	42	65	P
410446	COMPUTER LABORATORY-I	PR	50	20			44	P
410446	COMPUTER LABORATORY-I	OR	50	20			39	P
410447	COMPUTER LABORATORY-II	TW	50	20			40	P
410447	COMPUTER LABORATORY-II	OR	50	20			36	P
410448	PROJECT	TW	50	20			44	P

FIRST SEM TOTAL = --/750

BRANCH: 76 B.E. (2012 PAT.)(COMPUTER)

DATE : 02 FEB 2017

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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B120054237 BANG YOGITA HANUMANPRASAD SUSHILADEVI ,71400747B ,PICT ,B120054237

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	23	28	51	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	24	52	76	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	19	41	60	P
410444C	ADVANCED COMPUTER PROG.	PP	100	40	23	41	64	P
410445B	PERVASIVE COMPUTING	PP	100	40	14	36	50	P
410446	COMPUTER LABORATORY-I	PR	50	20			44	P
410446	COMPUTER LABORATORY-I	OR	50	20			45	P
410447	COMPUTER LABORATORY-II	TW	50	20			44	P
410447	COMPUTER LABORATORY-II	OR	50	20			42	P
410448	PROJECT	TW	50	20			46	P

FIRST SEM TOTAL = 522/750

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B120054238 BARTAKKE PRATIK SANJEEV VINITA ,71400751L ,PICT ,B120054238

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	17	44	61	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	25	47	72	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	19	47	66	P
410444D	DATA MINING TECH. & APPL.	PP	100	40	24	34	58	P
410445A	PROB. SOLV. WITH GAMIFICATION	PP	100	40	21	44	65	P
410446	COMPUTER LABORATORY-I	PR	50	20			46	P
410446	COMPUTER LABORATORY-I	OR	50	20			45	P
410447	COMPUTER LABORATORY-II	TW	50	20			41	P
410447	COMPUTER LABORATORY-II	OR	50	20			40	P
410448	PROJECT	TW	50	20			45	P

FIRST SEM TOTAL = 539/750

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B120054239 BASANTWANI SHRADDHA HARESH MADHU ,71400753G ,PICT ,B120054239

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	27	42	69	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	20	39	59	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	19	50	69	P
410444C	ADVANCED COMPUTER PROG.	PP	100	40	29	47	76	P
410445A	PROB. SOLV. WITH GAMIFICATION	PP	100	40	23	41	64	P
410446	COMPUTER LABORATORY-I	PR	50	20			44	P
410446	COMPUTER LABORATORY-I	OR	50	20			43	P
410447	COMPUTER LABORATORY-II	TW	50	20			40	P
410447	COMPUTER LABORATORY-II	OR	50	20			36	P
410448	PROJECT	TW	50	20			45	P

FIRST SEM TOTAL = 545/750

BRANCH: 76 B.E. (2012 PAT.)(COMPUTER)

DATE : 02 FEB 2017

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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B120054240 BAWANE SHIVAM NARENDRA MADHURI ,71400755C ,PICT ,B120054240

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	15	37	52	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	08	31	--	F
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	12	28	40	P
410444B	COMP. NET. DESIGN & MODELING	PP	100	40	20	40	60	P
410445B	PERVASIVE COMPUTING	PP	100	40	11	29	40	P
410446	COMPUTER LABORATORY-I	PR	50	20			37	P
410446	COMPUTER LABORATORY-I	OR	50	20			37	P
410447	COMPUTER LABORATORY-II	TW	50	20			42	P
410447	COMPUTER LABORATORY-II	OR	50	20			38	P
410448	PROJECT	TW	50	20			42	P

FIRST SEM TOTAL = --/750

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B120054241 BEDMUTHA MOHIT MAHENDRA PRATIBHA ,71400757K ,PICT ,B120054241

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	21	37	58	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	20	37	57	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	15	47	62	P
410444D	DATA MINING TECH. & APPL.	PP	100	40	14	43	57	P
410445B	PERVASIVE COMPUTING	PP	100	40	20	47	67	P
410446	COMPUTER LABORATORY-I	PR	50	20			43	P
410446	COMPUTER LABORATORY-I	OR	50	20			42	P
410447	COMPUTER LABORATORY-II	TW	50	20			41	P
410447	COMPUTER LABORATORY-II	OR	50	20			38	P
410448	PROJECT	TW	50	20			36	P

FIRST SEM TOTAL = 501/750

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B120054242 BELORKAR YASH ANIL SEEMA ANIL BELORKAR ,71400758H ,PICT ,B120054242

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	19	38	57	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	25	46	71	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	16	43	59	P
410444C	ADVANCED COMPUTER PROG.	PP	100	40	21	47	68	P
410445A	PROB. SOLV. WITH GAMIFICATION	PP	100	40	21	46	67	P
410446	COMPUTER LABORATORY-I	PR	50	20			42	P
410446	COMPUTER LABORATORY-I	OR	50	20			41	P
410447	COMPUTER LABORATORY-II	TW	50	20			39	P
410447	COMPUTER LABORATORY-II	OR	50	20			38	P
410448	PROJECT	TW	50	20			42	P

FIRST SEM TOTAL = 524/750

BRANCH: 76 B.E. (2012 PAT.)(COMPUTER)

DATE : 02 FEB 2017

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120054243 BHAT ASMITA ADHIKRAO PRANJALI ,71400765L ,PICT ,B120054243

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	17	44	61	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	19	40	59	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	16	54	70	P
410444B	COMP. NET. DESIGN & MODELING	PP	100	40	18	47	65	P
410445B	PERVASIVE COMPUTING	PP	100	40	18	39	57	P
410446	COMPUTER LABORATORY-I	PR	50	20			43	P
410446	COMPUTER LABORATORY-I	OR	50	20			43	P
410447	COMPUTER LABORATORY-II	TW	50	20			39	P
410447	COMPUTER LABORATORY-II	OR	50	20			37	P
410448	PROJECT	TW	50	20			45	P

FIRST SEM TOTAL = 519/750

B120054244 Bhide MAITHILI SANTOSH DHANASHREE ,71400766J ,PICT ,B120054244

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	21	44	65	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	16	42	58	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	13	39	52	P
410444D	DATA MINING TECH. & APPL.	PP	100	40	21	40	61	P
410445D	MULTIDISCIPLINARY NLP	PP	100	40	20	49	69	P
410446	COMPUTER LABORATORY-I	PR	50	20			43	P
410446	COMPUTER LABORATORY-I	OR	50	20			42	P
410447	COMPUTER LABORATORY-II	TW	50	20			39	P
410447	COMPUTER LABORATORY-II	OR	50	20			42	P
410448	PROJECT	TW	50	20			43	P

FIRST SEM TOTAL = 514/750

B120054245 BHOR ROSHAN PRADEEP RESHMA ,71400769C ,PICT ,B120054245

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	22	42	64	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	23	40	63	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	20	38	58	P
410444C	ADVANCED COMPUTER PROG.	PP	100	40	21	53	74	P
410445D	MULTIDISCIPLINARY NLP	PP	100	40	22	52	74	P
410446	COMPUTER LABORATORY-I	PR	50	20			43	P
410446	COMPUTER LABORATORY-I	OR	50	20			42	P
410447	COMPUTER LABORATORY-II	TW	50	20			42	P
410447	COMPUTER LABORATORY-II	OR	50	20			38	P
410448	PROJECT	TW	50	20			41	P

FIRST SEM TOTAL = 539/750

BRANCH: 76 B.E. (2012 PAT.)(COMPUTER)

DATE : 02 FEB 2017

CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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B120054246 BHORDE DHANSHREE VINAYAK USHA ,71500229F ,PICT ,B120054246

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	21	50	71	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	19	44	63	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	17	50	67	P
410444D	DATA MINING TECH. & APPL.	PP	100	40	21	47	68	P
410445B	PERVASIVE COMPUTING	PP	100	40	19	41	60	P
410446	COMPUTER LABORATORY-I	PR	50	20			43	P
410446	COMPUTER LABORATORY-I	OR	50	20			41	P
410447	COMPUTER LABORATORY-II	TW	50	20			40	P
410447	COMPUTER LABORATORY-II	OR	50	20			38	P
410448	PROJECT	TW	50	20			42	P

FIRST SEM TOTAL = 533/750

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B120054247 BHUKTE DISHA PRAKASH VAISHALI BHUKTE ,71400772C ,PICT ,B120054247

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	23	51	74	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	26	45	71	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	19	30	49	P
410444C	ADVANCED COMPUTER PROG.	PP	100	40	20	52	72	P
410445B	PERVASIVE COMPUTING	PP	100	40	14	33	47	P
410446	COMPUTER LABORATORY-I	PR	50	20			43	P
410446	COMPUTER LABORATORY-I	OR	50	20			43	P
410447	COMPUTER LABORATORY-II	TW	50	20			42	P
410447	COMPUTER LABORATORY-II	OR	50	20			40	P
410448	PROJECT	TW	50	20			45	P

FIRST SEM TOTAL = 526/750

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B120054248 BOBADE DURGA LEJINDRA SUJATA ,71400775H ,PICT ,B120054248

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	22	43	65	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	20	51	71	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	17	35	52	P
410444A	IMAGE PROCESSING	PP	100	40	22	43	65	P
410445A	PROB. SOLV. WITH GAMIFICATION	PP	100	40	25	41	66	P
410446	COMPUTER LABORATORY-I	PR	50	20			45	P
410446	COMPUTER LABORATORY-I	OR	50	20			45	P
410447	COMPUTER LABORATORY-II	TW	50	20			40	P
410447	COMPUTER LABORATORY-II	OR	50	20			40	P
410448	PROJECT	TW	50	20			42	P

FIRST SEM TOTAL = 531/750

BRANCH: 76 B.E. (2012 PAT.)(COMPUTER)

DATE : 02 FEB 2017

CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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B120054249 BORKAR KAVITA DEEPAKRAO SHOBHA ,71400778B ,PICT ,B120054249

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	21	47	68	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	21	55	76	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	24	49	73	P
410444C	ADVANCED COMPUTER PROG.	PP	100	40	26	46	72	P
410445B	PERVASIVE COMPUTING	PP	100	40	19	45	64	P
410446	COMPUTER LABORATORY-I	PR	50	20			41	P
410446	COMPUTER LABORATORY-I	OR	50	20			41	P
410447	COMPUTER LABORATORY-II	TW	50	20			44	P
410447	COMPUTER LABORATORY-II	OR	50	20			41	P
410448	PROJECT	TW	50	20			43	P

FIRST SEM TOTAL = 563/750

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B120054250 CHAITALI JOSHI PADMA ,71400779L ,PICT ,B120054250

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	16	44	60	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	28	40	68	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	15	34	49	P
410444D	DATA MINING TECH. & APPL.	PP	100	40	18	38	56	P
410445A	PROB. SOLV. WITH GAMIFICATION	PP	100	40	21	31	52	P
410446	COMPUTER LABORATORY-I	PR	50	20			43	P
410446	COMPUTER LABORATORY-I	OR	50	20			43	P
410447	COMPUTER LABORATORY-II	TW	50	20			39	P
410447	COMPUTER LABORATORY-II	OR	50	20			40	P
410448	PROJECT	TW	50	20			43	P

FIRST SEM TOTAL = 493/750

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B120054251 CHAKRAWAR SHUBHAM HEMANT SUJATA ,71400780D ,PICT ,B120054251

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	16	47	63	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	23	40	63	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	15	40	55	P
410444D	DATA MINING TECH. & APPL.	PP	100	40	20	37	57	P
410445B	PERVASIVE COMPUTING	PP	100	40	16	41	57	P
410446	COMPUTER LABORATORY-I	PR	50	20			44	P
410446	COMPUTER LABORATORY-I	OR	50	20			43	P
410447	COMPUTER LABORATORY-II	TW	50	20			41	P
410447	COMPUTER LABORATORY-II	OR	50	20			39	P
410448	PROJECT	TW	50	20			44	P

FIRST SEM TOTAL = 506/750

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120054252 CHANDAVARKAR ROHAN GIRISH GAURI ,71400782L ,PICT ,B120054252

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	15	42	57	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	22	42	64	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	16	34	50	P
410444D	DATA MINING TECH. & APPL.	PP	100	40	19	37	56	P
410445B	PERVASIVE COMPUTING	PP	100	40	19	39	58	P
410446	COMPUTER LABORATORY-I	PR	50	20			43	P
410446	COMPUTER LABORATORY-I	OR	50	20			42	P
410447	COMPUTER LABORATORY-II	TW	50	20			42	P
410447	COMPUTER LABORATORY-II	OR	50	20			38	P
410448	PROJECT	TW	50	20			44	P

FIRST SEM TOTAL = 494/750

B120054253 CHANDURKAR APOORV DHANANJAY INDRAYANI ,71400783J ,PICT ,B120054253

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	18	33	51	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	25	44	69	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	19	40	59	P
410444D	DATA MINING TECH. & APPL.	PP	100	40	24	42	66	P
410445D	MULTIDISCIPLINARY NLP	PP	100	40	20	47	67	P
410446	COMPUTER LABORATORY-I	PR	50	20			43	P
410446	COMPUTER LABORATORY-I	OR	50	20			42	P
410447	COMPUTER LABORATORY-II	TW	50	20			43	P
410447	COMPUTER LABORATORY-II	OR	50	20			40	P
410448	PROJECT	TW	50	20			45	P

FIRST SEM TOTAL = 525/750

B120054254 CHAUDHARI VAIBHAV JAYPRAKASH MRUDULA ,71400785E ,PICT ,B120054254

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	16	44	60	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	21	40	61	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	15	30	45	P
410444A	IMAGE PROCESSING	PP	100	40	20	37	57	P
410445B	PERVASIVE COMPUTING	PP	100	40	17	41	58	P
410446	COMPUTER LABORATORY-I	PR	50	20			45	P
410446	COMPUTER LABORATORY-I	OR	50	20			44	P
410447	COMPUTER LABORATORY-II	TW	50	20			40	P
410447	COMPUTER LABORATORY-II	OR	50	20			35	P
410448	PROJECT	TW	50	20			47	P

FIRST SEM TOTAL = 492/750

BRANCH: 76 B.E. (2012 PAT.)(COMPUTER)

DATE : 02 FEB 2017

CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
 OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120054255 CHAUDHARY AMEY BHASKAR SMITA ,71400786C ,PICT ,B120054255

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	9	39	48	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	18	30	48	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	13	29	42	P
410444C	ADVANCED COMPUTER PROG.	PP	100	40	18	37	55	P
410445D	MULTIDISCIPLINARY NLP	PP	100	40	18	29	47	P
410446	COMPUTER LABORATORY-I	PR	50	20			44	P
410446	COMPUTER LABORATORY-I	OR	50	20			44	P
410447	COMPUTER LABORATORY-II	TW	50	20			44	P
410447	COMPUTER LABORATORY-II	OR	50	20			40	P
410448	PROJECT	TW	50	20			44	P

FIRST SEM TOTAL = 456/750

B120054256 CHAVAN SANKET RAMBHAU LEELA ,71400787M ,PICT ,B120054256

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	22	32	54	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	20	39	59	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	15	40	55	P
410444B	COMP. NET. DESIGN & MODELING	PP	100	40	18	43	61	P
410445B	PERVASIVE COMPUTING	PP	100	40	16	42	58	P
410446	COMPUTER LABORATORY-I	PR	50	20			44	P
410446	COMPUTER LABORATORY-I	OR	50	20			43	P
410447	COMPUTER LABORATORY-II	TW	50	20			41	P
410447	COMPUTER LABORATORY-II	OR	50	20			40	P
410448	PROJECT	TW	50	20			42	P

FIRST SEM TOTAL = 497/750

B120054257 CHAVAN SHALINI DEVIDAS SANGITA ,71500230K ,PICT ,B120054257

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	21	52	73	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	26	52	78	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	13	55	68	P
410444C	ADVANCED COMPUTER PROG.	PP	100	40	27	55	82	P
410445B	PERVASIVE COMPUTING	PP	100	40	19	47	66	P
410446	COMPUTER LABORATORY-I	PR	50	20			45	P
410446	COMPUTER LABORATORY-I	OR	50	20			44	P
410447	COMPUTER LABORATORY-II	TW	50	20			39	P
410447	COMPUTER LABORATORY-II	OR	50	20			38	P
410448	PROJECT	TW	50	20			40	P

FIRST SEM TOTAL = 573/750

BRANCH: 76 B.E. (2012 PAT.)(COMPUTER)

DATE : 02 FEB 2017

CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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B120054258 CHENDAKE RAJVARDHAN PRASHANT ASHA ,71400789H ,PICT ,B120054258

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	19	50	69	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	19	41	60	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	16	37	53	P
410444C	ADVANCED COMPUTER PROG.	PP	100	40	22	46	68	P
410445A	PROB. SOLV. WITH GAMIFICATION	PP	100	40	18	34	52	P
410446	COMPUTER LABORATORY-I	PR	50	20			41	P
410446	COMPUTER LABORATORY-I	OR	50	20			40	P
410447	COMPUTER LABORATORY-II	TW	50	20			40	P
410447	COMPUTER LABORATORY-II	OR	50	20			37	P
410448	PROJECT	TW	50	20			39	P

FIRST SEM TOTAL = 499/750

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B120054259 CHINTI VINAYA MAHESH SUVARNA ,71500231H ,PICT ,B120054259

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	24	49	73	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	26	51	77	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	17	49	66	P
410444D	DATA MINING TECH. & APPL.	PP	100	40	27	43	70	P
410445B	PERVASIVE COMPUTING	PP	100	40	18	47	65	P
410446	COMPUTER LABORATORY-I	PR	50	20			43	P
410446	COMPUTER LABORATORY-I	OR	50	20			42	P
410447	COMPUTER LABORATORY-II	TW	50	20			41	P
410447	COMPUTER LABORATORY-II	OR	50	20			38	P
410448	PROJECT	TW	50	20			41	P

FIRST SEM TOTAL = 556/750

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B120054260 DANDE AKANKSHA ANIL SUCHITA ,71400796L ,PICT ,B120054260

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	16	43	59	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	20	42	62	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	18	33	51	P
410444D	DATA MINING TECH. & APPL.	PP	100	40	13	35	48	P
410445D	MULTIDISCIPLINARY NLP	PP	100	40	21	53	74	P
410446	COMPUTER LABORATORY-I	PR	50	20			35	P
410446	COMPUTER LABORATORY-I	OR	50	20			44	P
410447	COMPUTER LABORATORY-II	TW	50	20			39	P
410447	COMPUTER LABORATORY-II	OR	50	20			39	P
410448	PROJECT	TW	50	20			42	P

FIRST SEM TOTAL = 493/750

BRANCH: 76 B.E. (2012 PAT.)(COMPUTER)

DATE : 02 FEB 2017

CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120054261 DEEPAK KUMAR KIRAN ,71400804E ,PICT ,B120054261

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	16	47	63	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	23	35	58	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	14	51	65	P
410444D	DATA MINING TECH. & APPL.	PP	100	40	25	37	62	P
410445A	PROB. SOLV. WITH GAMIFICATION	PP	100	40	22	37	59	P
410446	COMPUTER LABORATORY-I	PR	50	20			45	P
410446	COMPUTER LABORATORY-I	OR	50	20			44	P
410447	COMPUTER LABORATORY-II	TW	50	20			40	P
410447	COMPUTER LABORATORY-II	OR	50	20			37	P
410448	PROJECT	TW	50	20			44	P

FIRST SEM TOTAL = 517/750

B120054262 DEORE SHREYASI SHANTARAM DEORE VAISHALI SHANTARAM ,71300864E ,PICT ,B120054262

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	12	35	47	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	13	16	--	F
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	12	15	--	F
410444A	IMAGE PROCESSING	PP	100	40	11	24	--	F
410445A	PROB. SOLV. WITH GAMIFICATION	PP	100	40	21	34	55	P
410446	COMPUTER LABORATORY-I	PR	50	20			AA	F
410446	COMPUTER LABORATORY-I	OR	50	20			AA	F
410447	COMPUTER LABORATORY-II	TW	50	20			AA	F
410447	COMPUTER LABORATORY-II	OR	50	20			AA	F
410448	PROJECT	TW	50	20			35	P

FIRST SEM TOTAL = --/750

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B120054263 DESHPANDE ABHISHEK HEMANT ANJALI ,71400811H ,PICT ,B120054263

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	24	54	78	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	25	54	79	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	21	57	78	P
410444D	DATA MINING TECH. & APPL.	PP	100	40	22	52	74	P
410445D	MULTIDISCIPLINARY NLP	PP	100	40	25	61	86	P
410446	COMPUTER LABORATORY-I	PR	50	20			43	P
410446	COMPUTER LABORATORY-I	OR	50	20			42	P
410447	COMPUTER LABORATORY-II	TW	50	20			43	P
410447	COMPUTER LABORATORY-II	OR	50	20			39	P
410448	PROJECT	TW	50	20			46	P

FIRST SEM TOTAL = 608/750

BRANCH: 76 B.E. (2012 PAT.)(COMPUTER)

DATE : 02 FEB 2017

CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
 OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120054264 DESHPANDE AKHIL SUNIL SUMEDHA ,71400812F ,PICT ,B120054264

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	19	45	64	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	16	28	44	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	13	24	--	F
410444D	DATA MINING TECH. & APPL.	PP	100	40	17	38	55	P
410445D	MULTIDISCIPLINARY NLP	PP	100	40	17	36	53	P
410446	COMPUTER LABORATORY-I	PR	50	20			40	P
410446	COMPUTER LABORATORY-I	OR	50	20			39	P
410447	COMPUTER LABORATORY-II	TW	50	20			40	P
410447	COMPUTER LABORATORY-II	OR	50	20			38	P
410448	PROJECT	TW	50	20			37	P

FIRST SEM TOTAL = --/750

B120054265 DESHPANDE RASIKA RAJESH RADHIKA ,71400814B ,PICT ,B120054265

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	17	41	58	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	13	30	43	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	12	21	--	F
410444D	DATA MINING TECH. & APPL.	PP	100	40	14	44	58	P
410445B	PERVASIVE COMPUTING	PP	100	40	16	33	49	P
410446	COMPUTER LABORATORY-I	PR	50	20			41	P
410446	COMPUTER LABORATORY-I	OR	50	20			40	P
410447	COMPUTER LABORATORY-II	TW	50	20			40	P
410447	COMPUTER LABORATORY-II	OR	50	20			34	P
410448	PROJECT	TW	50	20			42	P

FIRST SEM TOTAL = --/750

B120054266 DEVARSH THAKKAR ANJU ,71400816J ,PICT ,B120054266

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	17	43	60	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	14	13	--	F
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	12	22	--	F
410444C	ADVANCED COMPUTER PROG.	PP	100	40	17	46	63	P
410445A	PROB. SOLV. WITH GAMIFICATION	PP	100	40	17	24	--	F
410446	COMPUTER LABORATORY-I	PR	50	20			43	P
410446	COMPUTER LABORATORY-I	OR	50	20			41	P
410447	COMPUTER LABORATORY-II	TW	50	20			39	P
410447	COMPUTER LABORATORY-II	OR	50	20			37	P
410448	PROJECT	TW	50	20			35	P

FIRST SEM TOTAL = --/750

BRANCH: 76 B.E. (2012 PAT.)(COMPUTER)

DATE : 02 FEB 2017

CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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B120054267 DHARANGUTTE PRATHAMESH TATYASAHEB KALYANI ,71400818E ,PICT ,B120054267

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	25	50	75	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	21	49	70	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	12	47	59	P
410444D	DATA MINING TECH. & APPL.	PP	100	40	21	43	64	P
410445D	MULTIDISCIPLINARY NLP	PP	100	40	20	53	73	P
410446	COMPUTER LABORATORY-I	PR	50	20			15	F
410446	COMPUTER LABORATORY-I	OR	50	20			25	P
410447	COMPUTER LABORATORY-II	TW	50	20			43	P
410447	COMPUTER LABORATORY-II	OR	50	20			40	P
410448	PROJECT	TW	50	20			47	P

FIRST SEM TOTAL = --/750

.....

B120054268 DHENDE PRIYANKA VIJAY MANDAKINI ,71500232F ,PICT ,B120054268

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	18	44	62	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	17	29	46	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	13	51	64	P
410444A	IMAGE PROCESSING	PP	100	40	17	36	53	P
410445A	PROB. SOLV. WITH GAMIFICATION	PP	100	40	23	39	62	P
410446	COMPUTER LABORATORY-I	PR	50	20			44	P
410446	COMPUTER LABORATORY-I	OR	50	20			43	P
410447	COMPUTER LABORATORY-II	TW	50	20			42	P
410447	COMPUTER LABORATORY-II	OR	50	20			35	P
410448	PROJECT	TW	50	20			42	P

FIRST SEM TOTAL = 493/750

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B120054269 DHONE SANKET DILIP UJWALA DHONE ,71400820G ,PICT ,B120054269

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	17	44	61	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	17	27	--	F
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	16	26	--	F
410444C	ADVANCED COMPUTER PROG.	PP	100	40	22	51	73	P
410445A	PROB. SOLV. WITH GAMIFICATION	PP	100	40	22	38	60	P
410446	COMPUTER LABORATORY-I	PR	50	20			44	P
410446	COMPUTER LABORATORY-I	OR	50	20			43	P
410447	COMPUTER LABORATORY-II	TW	50	20			40	P
410447	COMPUTER LABORATORY-II	OR	50	20			37	P
410448	PROJECT	TW	50	20			45	P

FIRST SEM TOTAL = --/750

BRANCH: 76 B.E. (2012 PAT.)(COMPUTER)

DATE : 02 FEB 2017

CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
 OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120054270 DHONGADE SAURABHA SUHAS SUNITA ,71400821E ,PICT ,B120054270

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	17	48	65	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	10	27	--	F
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	14	24	--	F
410444D	DATA MINING TECH. & APPL.	PP	100	40	20	39	59	P
410445A	PROB. SOLV. WITH GAMIFICATION	PP	100	40	21	38	59	P
410446	COMPUTER LABORATORY-I	PR	50	20			40	P
410446	COMPUTER LABORATORY-I	OR	50	20			39	P
410447	COMPUTER LABORATORY-II	TW	50	20			43	P
410447	COMPUTER LABORATORY-II	OR	50	20			40	P
410448	PROJECT	TW	50	20			44	P

FIRST SEM TOTAL = --/750

B120054271 DONGRE VAISHNAVI PRAKASH SUJATA ,71400825H ,PICT ,B120054271

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	19	41	60	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	20	39	59	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	12	33	45	P
410444C	ADVANCED COMPUTER PROG.	PP	100	40	22	41	63	P
410445B	PERVASIVE COMPUTING	PP	100	40	17	36	53	P
410446	COMPUTER LABORATORY-I	PR	50	20			38	P
410446	COMPUTER LABORATORY-I	OR	50	20			37	P
410447	COMPUTER LABORATORY-II	TW	50	20			40	P
410447	COMPUTER LABORATORY-II	OR	50	20			33	P
410448	PROJECT	TW	50	20			40	P

FIRST SEM TOTAL = 468/750

B120054272 DURGUDE ASHISH SUNEEL ANITA ,71400827D ,PICT ,B120054272

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	16	42	58	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	11	25	--	F
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	16	25	--	F
410444C	ADVANCED COMPUTER PROG.	PP	100	40	21	38	59	P
410445B	PERVASIVE COMPUTING	PP	100	40	16	33	49	P
410446	COMPUTER LABORATORY-I	PR	50	20			39	P
410446	COMPUTER LABORATORY-I	OR	50	20			39	P
410447	COMPUTER LABORATORY-II	TW	50	20			42	P
410447	COMPUTER LABORATORY-II	OR	50	20			39	P
410448	PROJECT	TW	50	20			42	P

FIRST SEM TOTAL = --/750

BRANCH: 76 B.E. (2012 PAT.)(COMPUTER)

DATE : 02 FEB 2017

CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
 OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120054273 EDAKE SOURABH DNYANESHWAR JAYASHREE ,71400828B ,PICT ,B120054273

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	15	44	59	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	12	35	47	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	15	32	47	P
410444C	ADVANCED COMPUTER PROG.	PP	100	40	26	47	73	P
410445A	PROB. SOLV. WITH GAMIFICATION	PP	100	40	22	37	59	P
410446	COMPUTER LABORATORY-I	PR	50	20			45	P
410446	COMPUTER LABORATORY-I	OR	50	20			45	P
410447	COMPUTER LABORATORY-II	TW	50	20			37	P
410447	COMPUTER LABORATORY-II	OR	50	20			38	P
410448	PROJECT	TW	50	20			41	P

FIRST SEM TOTAL = 491/750

B120054274 FADNAVIS SHREYAS SANJEEV SUVARNA ,71400830D ,PICT ,B120054274

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	19	52	71	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	23	53	76	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	19	40	59	P
410444D	DATA MINING TECH. & APPL.	PP	100	40	26	42	68	P
410445B	PERVASIVE COMPUTING	PP	100	40	16	44	60	P
410446	COMPUTER LABORATORY-I	PR	50	20			45	P
410446	COMPUTER LABORATORY-I	OR	50	20			44	P
410447	COMPUTER LABORATORY-II	TW	50	20			43	P
410447	COMPUTER LABORATORY-II	OR	50	20			37	P
410448	PROJECT	TW	50	20			42	P

FIRST SEM TOTAL = 545/750

B120054275 FATEMA ASGARI HASTA RASHIDA ,71400831B ,PICT ,B120054275

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	17	54	71	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	25	38	63	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	15	49	64	P
410444D	DATA MINING TECH. & APPL.	PP	100	40	21	47	68	P
410445B	PERVASIVE COMPUTING	PP	100	40	19	39	58	P
410446	COMPUTER LABORATORY-I	PR	50	20			40	P
410446	COMPUTER LABORATORY-I	OR	50	20			40	P
410447	COMPUTER LABORATORY-II	TW	50	20			44	P
410447	COMPUTER LABORATORY-II	OR	50	20			38	P
410448	PROJECT	TW	50	20			44	P

FIRST SEM TOTAL = 530/750

BRANCH: 76 B.E. (2012 PAT.)(COMPUTER)

DATE : 02 FEB 2017

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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B120054276 FIRODIYA SHIVANI BHUSHAN SUNITA ,71400832L ,PICT ,B120054276

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	17	52	69	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	13	39	52	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	15	44	59	P
410444D	DATA MINING TECH. & APPL.	PP	100	40	22	42	64	P
410445D	MULTIDISCIPLINARY NLP	PP	100	40	22	49	71	P
410446	COMPUTER LABORATORY-I	PR	50	20			45	P
410446	COMPUTER LABORATORY-I	OR	50	20			44	P
410447	COMPUTER LABORATORY-II	TW	50	20			47	P
410447	COMPUTER LABORATORY-II	OR	50	20			40	P
410448	PROJECT	TW	50	20			43	P

FIRST SEM TOTAL = 534/750

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B120054277 GAJARMAL KAPIL ANAND SUSHMA ,71400838K ,PICT ,B120054277

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	16	50	66	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	12	35	47	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	17	13	--	F
410444C	ADVANCED COMPUTER PROG.	PP	100	40	20	49	69	P
410445A	PROB. SOLV. WITH GAMIFICATION	PP	100	40	25	46	71	P
410446	COMPUTER LABORATORY-I	PR	50	20			42	P
410446	COMPUTER LABORATORY-I	OR	50	20			41	P
410447	COMPUTER LABORATORY-II	TW	50	20			44	P
410447	COMPUTER LABORATORY-II	OR	50	20			41	P
410448	PROJECT	TW	50	20			39	P

FIRST SEM TOTAL = --/750

.....

B120054278 GANDHI KARAN SHRIKANT SHOBHA ,71400839H ,PICT ,B120054278

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	22	42	64	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	23	46	69	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	12	44	56	P
410444D	DATA MINING TECH. & APPL.	PP	100	40	16	35	51	P
410445B	PERVASIVE COMPUTING	PP	100	40	18	44	62	P
410446	COMPUTER LABORATORY-I	PR	50	20			38	P
410446	COMPUTER LABORATORY-I	OR	50	20			37	P
410447	COMPUTER LABORATORY-II	TW	50	20			37	P
410447	COMPUTER LABORATORY-II	OR	50	20			36	P
410448	PROJECT	TW	50	20			40	P

FIRST SEM TOTAL = 490/750

BRANCH: 76 B.E. (2012 PAT.)(COMPUTER)

DATE : 02 FEB 2017

CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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B120054279 GANGURDE SURAJ BALASAHEB SANGITA ,71500233D ,PICT ,B120054279

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	12	39	51	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	08	28	--	F
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	12	36	48	P
410444C	ADVANCED COMPUTER PROG.	PP	100	40	20	38	58	P
410445B	PERVASIVE COMPUTING	PP	100	40	18	38	56	P
410446	COMPUTER LABORATORY-I	PR	50	20			39	P
410446	COMPUTER LABORATORY-I	OR	50	20			38	P
410447	COMPUTER LABORATORY-II	TW	50	20			40	P
410447	COMPUTER LABORATORY-II	OR	50	20			36	P
410448	PROJECT	TW	50	20			30	P

FIRST SEM TOTAL = --/750

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B120054280 GARUD PRANIT DEEPAK SEEMA ,71400842H ,PICT ,B120054280

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	13	44	57	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	04	24	--	F
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	12	38	50	P
410444B	COMP. NET. DESIGN & MODELING	PP	100	40	17	44	61	P
410445A	PROB. SOLV. WITH GAMIFICATION	PP	100	40	23	49	72	P
410446	COMPUTER LABORATORY-I	PR	50	20			42	P
410446	COMPUTER LABORATORY-I	OR	50	20			41	P
410447	COMPUTER LABORATORY-II	TW	50	20			37	P
410447	COMPUTER LABORATORY-II	OR	50	20			37	P
410448	PROJECT	TW	50	20			40	P

FIRST SEM TOTAL = --/750

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B120054281 GAURAV SINGH SUNITA SINGH ,71300904H ,PICT ,B120054281

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	14	34	48	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	14	15	--	F
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	12	28	40	P
410444D	DATA MINING TECH. & APPL.	PP	100	40	13	35	48	P
410445A	PROB. SOLV. WITH GAMIFICATION	PP	100	40	25	37	62	P
410446	COMPUTER LABORATORY-I	PR	50	20			44	P
410446	COMPUTER LABORATORY-I	OR	50	20			45	P
410447	COMPUTER LABORATORY-II	TW	50	20			37	P
410447	COMPUTER LABORATORY-II	OR	50	20			37	P
410448	PROJECT	TW	50	20			43	P

FIRST SEM TOTAL = --/750

BRANCH: 76 B.E. (2012 PAT.)(COMPUTER)

DATE : 02 FEB 2017

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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B120054282 GAVHANE NILESH MACHINDRA SAVITA ,71400843F ,PICT ,B120054282

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	16	52	68	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	24	50	74	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	15	47	62	P
410444C	ADVANCED COMPUTER PROG.	PP	100	40	24	50	74	P
410445B	PERVASIVE COMPUTING	PP	100	40	17	40	57	P
410446	COMPUTER LABORATORY-I	PR	50	20			44	P
410446	COMPUTER LABORATORY-I	OR	50	20			43	P
410447	COMPUTER LABORATORY-II	TW	50	20			44	P
410447	COMPUTER LABORATORY-II	OR	50	20			40	P
410448	PROJECT	TW	50	20			42	P

FIRST SEM TOTAL = 548/750

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B120054283 GHODKE VIKAS SUBHASH ANITA ,71400845B ,PICT ,B120054283

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	16	41	57	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	14	33	47	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	18	31	49	P
410444D	DATA MINING TECH. & APPL.	PP	100	40	14	39	53	P
410445D	MULTIDISCIPLINARY NLP	PP	100	40	23	42	65	P
410446	COMPUTER LABORATORY-I	PR	50	20			41	P
410446	COMPUTER LABORATORY-I	OR	50	20			41	P
410447	COMPUTER LABORATORY-II	TW	50	20			41	P
410447	COMPUTER LABORATORY-II	OR	50	20			38	P
410448	PROJECT	TW	50	20			43	P

FIRST SEM TOTAL = 475/750

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B120054284 GHOGARE MANGAL HANUMANT LALITA ,71500234B ,PICT ,B120054284

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	14	54	68	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	21	52	73	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	17	57	74	P
410444D	DATA MINING TECH. & APPL.	PP	100	40	25	57	82	P
410445B	PERVASIVE COMPUTING	PP	100	40	22	48	70	P
410446	COMPUTER LABORATORY-I	PR	50	20			43	P
410446	COMPUTER LABORATORY-I	OR	50	20			43	P
410447	COMPUTER LABORATORY-II	TW	50	20			42	P
410447	COMPUTER LABORATORY-II	OR	50	20			37	P
410448	PROJECT	TW	50	20			44	P

FIRST SEM TOTAL = 576/750

BRANCH: 76 B.E. (2012 PAT.)(COMPUTER)

DATE : 02 FEB 2017

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
 OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120054285 GHOLAP SURAJ SURYAKANT SUSHAMA ,71400846L ,PICT ,B120054285

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	23	42	65	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	20	27	--	F
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	14	28	42	P
410444B	COMP. NET. DESIGN & MODELING	PP	100	40	16	33	49	P
410445B	PERVASIVE COMPUTING	PP	100	40	17	28	45	P
410446	COMPUTER LABORATORY-I	PR	50	20			42	P
410446	COMPUTER LABORATORY-I	OR	50	20			41	P
410447	COMPUTER LABORATORY-II	TW	50	20			43	P
410447	COMPUTER LABORATORY-II	OR	50	20			40	P
410448	PROJECT	TW	50	20			40	P

FIRST SEM TOTAL = --/750

B120054286 GHORPADE ABHISHEK ARVIND ANUPAMA ,71400848G ,PICT ,B120054286

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	22	46	68	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	13	22	--	F
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	12	31	43	P
410444D	DATA MINING TECH. & APPL.	PP	100	40	14	34	48	P
410445B	PERVASIVE COMPUTING	PP	100	40	14	35	49	P
410446	COMPUTER LABORATORY-I	PR	50	20			40	P
410446	COMPUTER LABORATORY-I	OR	50	20			38	P
410447	COMPUTER LABORATORY-II	TW	50	20			43	P
410447	COMPUTER LABORATORY-II	OR	50	20			43	P
410448	PROJECT	TW	50	20			40	P

FIRST SEM TOTAL = --/750

B120054287 GILADA SHUBHAM SHEKHERJI MANGAL ,71500235L ,PICT ,B120054287

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	17	42	59	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	27	40	67	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	15	29	44	P
410444B	COMP. NET. DESIGN & MODELING	PP	100	40	25	50	75	P
410445B	PERVASIVE COMPUTING	PP	100	40	17	42	59	P
410446	COMPUTER LABORATORY-I	PR	50	20			44	P
410446	COMPUTER LABORATORY-I	OR	50	20			44	P
410447	COMPUTER LABORATORY-II	TW	50	20			43	P
410447	COMPUTER LABORATORY-II	OR	50	20			41	P
410448	PROJECT	TW	50	20			43	P

FIRST SEM TOTAL = 519/750

BRANCH: 76 B.E. (2012 PAT.)(COMPUTER)

DATE : 02 FEB 2017

CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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B120054288 GODASE ANUJ BALASAHEB ARUNA ,71400850J ,PICT ,B120054288

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	15	43	58	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	22	27	--	F
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	16	39	55	P
410444D	DATA MINING TECH. & APPL.	PP	100	40	14	39	53	P
410445D	MULTIDISCIPLINARY NLP	PP	100	40	19	53	72	P
410446	COMPUTER LABORATORY-I	PR	50	20			45	P
410446	COMPUTER LABORATORY-I	OR	50	20			44	P
410447	COMPUTER LABORATORY-II	TW	50	20			41	P
410447	COMPUTER LABORATORY-II	OR	50	20			38	P
410448	PROJECT	TW	50	20			43	P

FIRST SEM TOTAL = --/750

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B120054289 GOKHALE ASHLESHA DHARMARAJ UJAWALA ,71400854M ,PICT ,B120054289

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	23	50	73	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	20	43	63	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	18	34	52	P
410444C	ADVANCED COMPUTER PROG.	PP	100	40	25	48	73	P
410445B	PERVASIVE COMPUTING	PP	100	40	18	43	61	P
410446	COMPUTER LABORATORY-I	PR	50	20			42	P
410446	COMPUTER LABORATORY-I	OR	50	20			42	P
410447	COMPUTER LABORATORY-II	TW	50	20			40	P
410447	COMPUTER LABORATORY-II	OR	50	20			35	P
410448	PROJECT	TW	50	20			38	P

FIRST SEM TOTAL = 519/750

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B120054290 GOKHALE PRANAV NARAYAN MUGDHA ,71400855K ,PICT ,B120054290

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	23	48	71	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	20	39	59	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	14	41	55	P
410444D	DATA MINING TECH. & APPL.	PP	100	40	19	41	60	P
410445D	MULTIDISCIPLINARY NLP	PP	100	40	24	46	70	P
410446	COMPUTER LABORATORY-I	PR	50	20			46	P
410446	COMPUTER LABORATORY-I	OR	50	20			44	P
410447	COMPUTER LABORATORY-II	TW	50	20			42	P
410447	COMPUTER LABORATORY-II	OR	50	20			41	P
410448	PROJECT	TW	50	20			45	P

FIRST SEM TOTAL = 533/750

BRANCH: 76 B.E. (2012 PAT.)(COMPUTER)

DATE : 02 FEB 2017

CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120054291 GOLHAR PRACHI SHIVAJI SUNANDA ,71500236J ,PICT ,B120054291

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	13	47	60	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	24	54	78	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	19	49	68	P
410444B	COMP. NET. DESIGN & MODELING	PP	100	40	24	50	74	P
410445B	PERVASIVE COMPUTING	PP	100	40	19	45	64	P
410446	COMPUTER LABORATORY-I	PR	50	20			41	P
410446	COMPUTER LABORATORY-I	OR	50	20			41	P
410447	COMPUTER LABORATORY-II	TW	50	20			40	P
410447	COMPUTER LABORATORY-II	OR	50	20			38	P
410448	PROJECT	TW	50	20			39	P

FIRST SEM TOTAL = 543/750

B120054292 GOSAVI PRAVIN SUDAM SHUBHANGI GOSAVI ,71300928E ,PICT ,B120054292

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	11	38	49	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	8	52	60	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	13	48	61	P
410444B	COMP. NET. DESIGN & MODELING	PP	100	40	14	49	63	P
410445B	PERVASIVE COMPUTING	PP	100	40	16	42	58	P
410446	COMPUTER LABORATORY-I	PR	50	20			44	P
410446	COMPUTER LABORATORY-I	OR	50	20			43	P
410447	COMPUTER LABORATORY-II	TW	50	20			35	P
410447	COMPUTER LABORATORY-II	OR	50	20			22	P
410448	PROJECT	TW	50	20			31	P

FIRST SEM TOTAL = 466/750

B120054293 GOUR SONALI UMESH VIDYA ,71400858D ,PICT ,B120054293

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	20	47	67	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	21	42	63	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	18	46	64	P
410444B	COMP. NET. DESIGN & MODELING	PP	100	40	21	50	71	P
410445B	PERVASIVE COMPUTING	PP	100	40	19	39	58	P
410446	COMPUTER LABORATORY-I	PR	50	20			42	P
410446	COMPUTER LABORATORY-I	OR	50	20			42	P
410447	COMPUTER LABORATORY-II	TW	50	20			40	P
410447	COMPUTER LABORATORY-II	OR	50	20			37	P
410448	PROJECT	TW	50	20			43	P

FIRST SEM TOTAL = 527/750

BRANCH: 76 B.E. (2012 PAT.)(COMPUTER)

DATE : 02 FEB 2017

CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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B120054294 GOVINDWAD AKILESH BALAJIRAO GOVINDWAD VIJAYA BALAJIRAO ,71300931E ,PICT ,B120054294

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	14	AA	--	F
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	16	AA	--	F
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	12	AA	--	F
410444B	COMP. NET. DESIGN & MODELING	PP	100	40	18	44	62	P
410445B	PERVASIVE COMPUTING	PP	100	40	18	38	56	P
410446	COMPUTER LABORATORY-I	PR	50	20			39	P
410446	COMPUTER LABORATORY-I	OR	50	20			38	P
410447	COMPUTER LABORATORY-II	TW	50	20			40	P
410447	COMPUTER LABORATORY-II	OR	50	20			35	P
410448	PROJECT	TW	50	20			34	P

FIRST SEM TOTAL = --/750

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B120054295 GUJAR ANAND VIJAY REKHA ,71400859B ,PICT ,B120054295

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	9	37	46	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	5	37	42	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	12	45	57	P
410444D	DATA MINING TECH. & APPL.	PP	100	40	12	41	53	P
410445B	PERVASIVE COMPUTING	PP	100	40	16	37	53	P
410446	COMPUTER LABORATORY-I	PR	50	20			36	P
410446	COMPUTER LABORATORY-I	OR	50	20			36	P
410447	COMPUTER LABORATORY-II	TW	50	20			41	P
410447	COMPUTER LABORATORY-II	OR	50	20			38	P
410448	PROJECT	TW	50	20			42	P

FIRST SEM TOTAL = 444/750

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B120054296 GUJAR KETAKI SHAHAJI NEETA ,71400860F ,PICT ,B120054296

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	18	38	56	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	21	45	66	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	14	44	58	P
410444D	DATA MINING TECH. & APPL.	PP	100	40	14	37	51	P
410445D	MULTIDISCIPLINARY NLP	PP	100	40	22	56	78	P
410446	COMPUTER LABORATORY-I	PR	50	20			43	P
410446	COMPUTER LABORATORY-I	OR	50	20			42	P
410447	COMPUTER LABORATORY-II	TW	50	20			41	P
410447	COMPUTER LABORATORY-II	OR	50	20			40	P
410448	PROJECT	TW	50	20			42	P

FIRST SEM TOTAL = 517/750

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120054297 GUND GITESH RAMESH SUNANDA ,71500237G ,PICT ,B120054297

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	16	41	57	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	26	42	68	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	17	46	63	P
410444B	COMP. NET. DESIGN & MODELING	PP	100	40	19	47	66	P
410445B	PERVASIVE COMPUTING	PP	100	40	19	41	60	P
410446	COMPUTER LABORATORY-I	PR	50	20			46	P
410446	COMPUTER LABORATORY-I	OR	50	20			45	P
410447	COMPUTER LABORATORY-II	TW	50	20			42	P
410447	COMPUTER LABORATORY-II	OR	50	20			42	P
410448	PROJECT	TW	50	20			45	P

FIRST SEM TOTAL = 534/750

B120054298 GUNJAL SURAJKUMAR SAKHARAM BEBITAI ,71400862B ,PICT ,B120054298

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	22	41	63	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	21	46	67	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	16	52	68	P
410444D	DATA MINING TECH. & APPL.	PP	100	40	20	41	61	P
410445D	MULTIDISCIPLINARY NLP	PP	100	40	22	50	72	P
410446	COMPUTER LABORATORY-I	PR	50	20			43	P
410446	COMPUTER LABORATORY-I	OR	50	20			43	P
410447	COMPUTER LABORATORY-II	TW	50	20			47	P
410447	COMPUTER LABORATORY-II	OR	50	20			43	P
410448	PROJECT	TW	50	20			42	P

FIRST SEM TOTAL = 549/750

B120054299 GUPTA KETUL RAJENDRA URMILA ,71400864J ,PICT ,B120054299

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	20	48	68	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	26	50	76	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	17	49	66	P
410444D	DATA MINING TECH. & APPL.	PP	100	40	26	46	72	P
410445D	MULTIDISCIPLINARY NLP	PP	100	40	22	55	77	P
410446	COMPUTER LABORATORY-I	PR	50	20			45	P
410446	COMPUTER LABORATORY-I	OR	50	20			44	P
410447	COMPUTER LABORATORY-II	TW	50	20			42	P
410447	COMPUTER LABORATORY-II	OR	50	20			37	P
410448	PROJECT	TW	50	20			40	P

FIRST SEM TOTAL = 567/750

BRANCH: 76 B.E. (2012 PAT.)(COMPUTER)

DATE : 02 FEB 2017

CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120054300 GUPTA SHUBHAM NARESHBABU GIRIRAJKISHORI GUPTA ,71400866E ,PICT ,B120054300

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	18	46	64	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	18	37	55	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	15	44	59	P
410444D	DATA MINING TECH. & APPL.	PP	100	40	18	40	58	P
410445B	PERVASIVE COMPUTING	PP	100	40	18	37	55	P
410446	COMPUTER LABORATORY-I	PR	50	20			46	P
410446	COMPUTER LABORATORY-I	OR	50	20			44	P
410447	COMPUTER LABORATORY-II	TW	50	20			41	P
410447	COMPUTER LABORATORY-II	OR	50	20			37	P
410448	PROJECT	TW	50	20			40	P

FIRST SEM TOTAL = 499/750

B120054301 GURNALKAR SHASHANK BHAGWAN SARITA ,71400867C ,PICT ,B120054301

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	14	40	54	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	21	39	60	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	19	27	--	F
410444A	IMAGE PROCESSING	PP	100	40	20	28	48	P
410445B	PERVASIVE COMPUTING	PP	100	40	17	38	55	P
410446	COMPUTER LABORATORY-I	PR	50	20			44	P
410446	COMPUTER LABORATORY-I	OR	50	20			42	P
410447	COMPUTER LABORATORY-II	TW	50	20			42	P
410447	COMPUTER LABORATORY-II	OR	50	20			37	P
410448	PROJECT	TW	50	20			40	P

FIRST SEM TOTAL = --/750

B120054302 HADKE KADAMBARI SUNIL MANISHA ,71500238E ,PICT ,B120054302

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	20	44	64	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	24	53	77	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	15	52	67	P
410444B	COMP. NET. DESIGN & MODELING	PP	100	40	21	54	75	P
410445B	PERVASIVE COMPUTING	PP	100	40	20	56	76	P
410446	COMPUTER LABORATORY-I	PR	50	20			44	P
410446	COMPUTER LABORATORY-I	OR	50	20			43	P
410447	COMPUTER LABORATORY-II	TW	50	20			41	P
410447	COMPUTER LABORATORY-II	OR	50	20			38	P
410448	PROJECT	TW	50	20			39	P

FIRST SEM TOTAL = 564/750

BRANCH: 76 B.E. (2012 PAT.)(COMPUTER)

DATE : 02 FEB 2017

CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120054303 HARDIKAR SHRIYA SHIRISH SUCHETA SHIRISH HARDIKAR ,71400868M ,PICT ,B120054303

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	17	43	60	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	24	53	77	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	17	39	56	P
410444D	DATA MINING TECH. & APPL.	PP	100	40	21	48	69	P
410445A	PROB. SOLV. WITH GAMIFICATION	PP	100	40	25	51	76	P
410446	COMPUTER LABORATORY-I	PR	50	20			44	P
410446	COMPUTER LABORATORY-I	OR	50	20			43	P
410447	COMPUTER LABORATORY-II	TW	50	20			43	P
410447	COMPUTER LABORATORY-II	OR	50	20			39	P
410448	PROJECT	TW	50	20			38	P

FIRST SEM TOTAL = 545/750

B120054304 HAVANURKAR PRANAV ASHOK VEENA ,71400869K ,PICT ,B120054304

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	22	44	66	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	19	50	69	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	13	23	--	F
410444D	DATA MINING TECH. & APPL.	PP	100	40	20	36	56	P
410445B	PERVASIVE COMPUTING	PP	100	40	18	44	62	P
410446	COMPUTER LABORATORY-I	PR	50	20			45	P
410446	COMPUTER LABORATORY-I	OR	50	20			45	P
410447	COMPUTER LABORATORY-II	TW	50	20			41	P
410447	COMPUTER LABORATORY-II	OR	50	20			42	P
410448	PROJECT	TW	50	20			45	P

FIRST SEM TOTAL = --/750

B120054305 HEDA PRIYA SUBHASH MAMTA ,71400870C ,PICT ,B120054305

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	21	43	64	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	28	49	77	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	20	44	64	P
410444D	DATA MINING TECH. & APPL.	PP	100	40	19	49	68	P
410445A	PROB. SOLV. WITH GAMIFICATION	PP	100	40	27	47	74	P
410446	COMPUTER LABORATORY-I	PR	50	20			45	P
410446	COMPUTER LABORATORY-I	OR	50	20			45	P
410447	COMPUTER LABORATORY-II	TW	50	20			41	P
410447	COMPUTER LABORATORY-II	OR	50	20			43	P
410448	PROJECT	TW	50	20			39	P

FIRST SEM TOTAL = 560/750

BRANCH: 76 B.E. (2012 PAT.)(COMPUTER)

DATE : 02 FEB 2017

CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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B120054306 HIMANE NEHA SUDHIR UJJWALA ,71500239C ,PICT ,B120054306

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	20	47	67	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	29	47	76	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	16	38	54	P
410444C	ADVANCED COMPUTER PROG.	PP	100	40	27	51	78	P
410445A	PROB. SOLV. WITH GAMIFICATION	PP	100	40	25	43	68	P
410446	COMPUTER LABORATORY-I	PR	50	20			42	P
410446	COMPUTER LABORATORY-I	OR	50	20			41	P
410447	COMPUTER LABORATORY-II	TW	50	20			43	P
410447	COMPUTER LABORATORY-II	OR	50	20			40	P
410448	PROJECT	TW	50	20			40	P

FIRST SEM TOTAL = 549/750

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B120054307 HINGMIRE MOHINI SIDDHLINGRAO SANDHYA ,71500240G ,PICT ,B120054307

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	14	39	53	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	25	51	76	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	18	47	65	P
410444C	ADVANCED COMPUTER PROG.	PP	100	40	26	51	77	P
410445B	PERVASIVE COMPUTING	PP	100	40	18	56	74	P
410446	COMPUTER LABORATORY-I	PR	50	20			46	P
410446	COMPUTER LABORATORY-I	OR	50	20			45	P
410447	COMPUTER LABORATORY-II	TW	50	20			40	P
410447	COMPUTER LABORATORY-II	OR	50	20			40	P
410448	PROJECT	TW	50	20			42	P

FIRST SEM TOTAL = 558/750

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B120054308 HIRAY SNEHAL SANJAY ASHWINI ,71400873H ,PICT ,B120054308

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	18	48	66	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	22	43	65	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	22	47	69	P
410444D	DATA MINING TECH. & APPL.	PP	100	40	21	37	58	P
410445B	PERVASIVE COMPUTING	PP	100	40	17	57	74	P
410446	COMPUTER LABORATORY-I	PR	50	20			43	P
410446	COMPUTER LABORATORY-I	OR	50	20			43	P
410447	COMPUTER LABORATORY-II	TW	50	20			40	P
410447	COMPUTER LABORATORY-II	OR	50	20			39	P
410448	PROJECT	TW	50	20			38	P

FIRST SEM TOTAL = 535/750

BRANCH: 76 B.E. (2012 PAT.)(COMPUTER)

DATE : 02 FEB 2017

CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
 OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120054309 IBTISAAM JAVID DALVI SAMINA ,71400875D ,PICT ,B120054309

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	12	27	--	F
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	24	42	66	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	15	27	--	F
410444C	ADVANCED COMPUTER PROG.	PP	100	40	22	43	65	P
410445B	PERVASIVE COMPUTING	PP	100	40	16	47	63	P
410446	COMPUTER LABORATORY-I	PR	50	20			42	P
410446	COMPUTER LABORATORY-I	OR	50	20			40	P
410447	COMPUTER LABORATORY-II	TW	50	20			40	P
410447	COMPUTER LABORATORY-II	OR	50	20			38	P
410448	PROJECT	TW	50	20			35	P

FIRST SEM TOTAL = --/750

B120054310 JADHAV RAJASHREE SAHEBRAO BABITA ,71500241E ,PICT ,B120054310

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	16	42	58	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	16	48	64	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	15	47	62	P
410444B	COMP. NET. DESIGN & MODELING	PP	100	40	21	47	68	P
410445B	PERVASIVE COMPUTING	PP	100	40	18	53	71	P
410446	COMPUTER LABORATORY-I	PR	50	20			43	P
410446	COMPUTER LABORATORY-I	OR	50	20			42	P
410447	COMPUTER LABORATORY-II	TW	50	20			41	P
410447	COMPUTER LABORATORY-II	OR	50	20			34	P
410448	PROJECT	TW	50	20			44	P

FIRST SEM TOTAL = 527/750

B120054311 JANBANDHU SHREEJEET PRADIP JOTSHNA ,71400888F ,PICT ,B120054311

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	17	35	52	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	26	43	69	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	18	42	60	P
410444C	ADVANCED COMPUTER PROG.	PP	100	40	26	38	64	P
410445B	PERVASIVE COMPUTING	PP	100	40	18	55	73	P
410446	COMPUTER LABORATORY-I	PR	50	20			43	P
410446	COMPUTER LABORATORY-I	OR	50	20			44	P
410447	COMPUTER LABORATORY-II	TW	50	20			40	P
410447	COMPUTER LABORATORY-II	OR	50	20			39	P
410448	PROJECT	TW	50	20			41	P

FIRST SEM TOTAL = 525/750

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120054312 JAWALE JAYASHRI PARASRAM CHANDRAKALA ,71500242C ,PICT ,B120054312

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	20	46	66	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	23	52	75	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	17	53	70	P
410444C	ADVANCED COMPUTER PROG.	PP	100	40	26	49	75	P
410445B	PERVASIVE COMPUTING	PP	100	40	20	59	79	P
410446	COMPUTER LABORATORY-I	PR	50	20			44	P
410446	COMPUTER LABORATORY-I	OR	50	20			43	P
410447	COMPUTER LABORATORY-II	TW	50	20			41	P
410447	COMPUTER LABORATORY-II	OR	50	20			34	P
410448	PROJECT	TW	50	20			42	P

FIRST SEM TOTAL = 569/750

B120054313 JAY MODI NANDITA ,71400890H ,PICT ,B120054313

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	20	39	59	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	7	45	52	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	18	41	59	P
410444B	COMP. NET. DESIGN & MODELING	PP	100	40	22	48	70	P
410445B	PERVASIVE COMPUTING	PP	100	40	19	53	72	P
410446	COMPUTER LABORATORY-I	PR	50	20			42	P
410446	COMPUTER LABORATORY-I	OR	50	20			42	P
410447	COMPUTER LABORATORY-II	TW	50	20			40	P
410447	COMPUTER LABORATORY-II	OR	50	20			36	P
410448	PROJECT	TW	50	20			37	P

FIRST SEM TOTAL = 509/750

B120054314 JOSHI AKANKSHA AJAY ASMITA ,71400895J ,PICT ,B120054314

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	23	40	63	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	27	47	74	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	19	41	60	P
410444B	COMP. NET. DESIGN & MODELING	PP	100	40	26	50	76	P
410445B	PERVASIVE COMPUTING	PP	100	40	17	56	73	P
410446	COMPUTER LABORATORY-I	PR	50	20			45	P
410446	COMPUTER LABORATORY-I	OR	50	20			45	P
410447	COMPUTER LABORATORY-II	TW	50	20			43	P
410447	COMPUTER LABORATORY-II	OR	50	20			39	P
410448	PROJECT	TW	50	20			43	P

FIRST SEM TOTAL = 561/750

BRANCH: 76 B.E. (2012 PAT.)(COMPUTER)

DATE : 02 FEB 2017

CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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B120054315 KABRA RUSHABH RAJESH DURGA ,71400898C ,PICT ,B120054315

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	25	45	70	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	20	37	57	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	12	32	44	P
410444B	COMP. NET. DESIGN & MODELING	PP	100	40	22	43	65	P
410445B	PERVASIVE COMPUTING	PP	100	40	20	46	66	P
410446	COMPUTER LABORATORY-I	PR	50	20			46	P
410446	COMPUTER LABORATORY-I	OR	50	20			45	P
410447	COMPUTER LABORATORY-II	TW	50	20			42	P
410447	COMPUTER LABORATORY-II	OR	50	20			41	P
410448	PROJECT	TW	50	20			40	P

FIRST SEM TOTAL = 516/750

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B120054316 KADAM SAGAR BHANUDAS PUSHPA ,71400902E ,PICT ,B120054316

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	21	38	59	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	18	44	62	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	14	33	47	P
410444D	DATA MINING TECH. & APPL.	PP	100	40	17	41	58	P
410445B	PERVASIVE COMPUTING	PP	100	40	16	55	71	P
410446	COMPUTER LABORATORY-I	PR	50	20			40	P
410446	COMPUTER LABORATORY-I	OR	50	20			38	P
410447	COMPUTER LABORATORY-II	TW	50	20			40	P
410447	COMPUTER LABORATORY-II	OR	50	20			40	P
410448	PROJECT	TW	50	20			42	P

FIRST SEM TOTAL = 497/750

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B120054317 KADAM SONAL VILAS VAISHALI ,71500243M ,PICT ,B120054317

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	14	37	51	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	13	49	62	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	14	46	60	P
410444D	DATA MINING TECH. & APPL.	PP	100	40	14	46	60	P
410445B	PERVASIVE COMPUTING	PP	100	40	17	55	72	P
410446	COMPUTER LABORATORY-I	PR	50	20			36	P
410446	COMPUTER LABORATORY-I	OR	50	20			34	P
410447	COMPUTER LABORATORY-II	TW	50	20			35	P
410447	COMPUTER LABORATORY-II	OR	50	20			22	P
410448	PROJECT	TW	50	20			28	P

FIRST SEM TOTAL = 460/750

BRANCH: 76 B.E. (2012 PAT.)(COMPUTER)

DATE : 02 FEB 2017

CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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B120054318 KAKADE KUNAL VIJAY PRAMILA ,71400905K ,PICT ,B120054318

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	19	36	55	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	26	44	70	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	17	26	--	F
410444D	DATA MINING TECH. & APPL.	PP	100	40	22	36	58	P
410445D	MULTIDISCIPLINARY NLP	PP	100	40	16	43	59	P
410446	COMPUTER LABORATORY-I	PR	50	20			46	P
410446	COMPUTER LABORATORY-I	OR	50	20			45	P
410447	COMPUTER LABORATORY-II	TW	50	20			43	P
410447	COMPUTER LABORATORY-II	OR	50	20			42	P
410448	PROJECT	TW	50	20			44	P

FIRST SEM TOTAL = --/750

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B120054319 KAKADE SHRUTI PRADEEP ANUJA ,71400906H ,PICT ,B120054319

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	23	43	66	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	21	54	75	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	17	37	54	P
410444C	ADVANCED COMPUTER PROG.	PP	100	40	25	39	64	P
410445D	MULTIDISCIPLINARY NLP	PP	100	40	26	55	81	P
410446	COMPUTER LABORATORY-I	PR	50	20			43	P
410446	COMPUTER LABORATORY-I	OR	50	20			44	P
410447	COMPUTER LABORATORY-II	TW	50	20			39	P
410447	COMPUTER LABORATORY-II	OR	50	20			32	P
410448	PROJECT	TW	50	20			41	P

FIRST SEM TOTAL = 539/750

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B120054320 KALAL DIPTI RAVINDRA MANDAKINI ,71500244K ,PICT ,B120054320

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	22	48	70	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	26	51	77	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	16	41	57	P
410444C	ADVANCED COMPUTER PROG.	PP	100	40	24	46	70	P
410445B	PERVASIVE COMPUTING	PP	100	40	19	57	76	P
410446	COMPUTER LABORATORY-I	PR	50	20			43	P
410446	COMPUTER LABORATORY-I	OR	50	20			43	P
410447	COMPUTER LABORATORY-II	TW	50	20			43	P
410447	COMPUTER LABORATORY-II	OR	50	20			43	P
410448	PROJECT	TW	50	20			43	P

FIRST SEM TOTAL = 565/750

BRANCH: 76 B.E. (2012 PAT.)(COMPUTER)

DATE : 02 FEB 2017

CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120054321 KALANTRI SEJAL SANJAY SHWETA ,71400907F ,PICT ,B120054321

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	15	41	56	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	25	46	71	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	21	47	68	P
410444D	DATA MINING TECH. & APPL.	PP	100	40	24	42	66	P
410445B	PERVASIVE COMPUTING	PP	100	40	17	50	67	P
410446	COMPUTER LABORATORY-I	PR	50	20			43	P
410446	COMPUTER LABORATORY-I	OR	50	20			41	P
410447	COMPUTER LABORATORY-II	TW	50	20			44	P
410447	COMPUTER LABORATORY-II	OR	50	20			41	P
410448	PROJECT	TW	50	20			44	P

FIRST SEM TOTAL = 541/750

B120054322 KALE TANWI JAYANT AARTI ,71400908D ,PICT ,B120054322

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	19	41	60	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	25	39	64	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	13	48	61	P
410444C	ADVANCED COMPUTER PROG.	PP	100	40	27	42	69	P
410445A	PROB. SOLV. WITH GAMIFICATION	PP	100	40	22	36	58	P
410446	COMPUTER LABORATORY-I	PR	50	20			44	P
410446	COMPUTER LABORATORY-I	OR	50	20			43	P
410447	COMPUTER LABORATORY-II	TW	50	20			44	P
410447	COMPUTER LABORATORY-II	OR	50	20			40	P
410448	PROJECT	TW	50	20			42	P

FIRST SEM TOTAL = 525/750

B120054323 KALE YASH HEMANT MANJIRI ,71400910F ,PICT ,B120054323

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	23	38	61	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	25	42	67	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	19	46	65	P
410444D	DATA MINING TECH. & APPL.	PP	100	40	24	43	67	P
410445B	PERVASIVE COMPUTING	PP	100	40	18	55	73	P
410446	COMPUTER LABORATORY-I	PR	50	20			46	P
410446	COMPUTER LABORATORY-I	OR	50	20			45	P
410447	COMPUTER LABORATORY-II	TW	50	20			41	P
410447	COMPUTER LABORATORY-II	OR	50	20			41	P
410448	PROJECT	TW	50	20			45	P

FIRST SEM TOTAL = 551/750

BRANCH: 76 B.E. (2012 PAT.)(COMPUTER)

DATE : 02 FEB 2017

CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
 OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120054324 KAMBLE SHRADHA NILESH KIRAN ,71500245H ,PICT ,B120054324

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	22	49	71	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	23	49	72	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	19	46	65	P
410444C	ADVANCED COMPUTER PROG.	PP	100	40	23	42	65	P
410445B	PERVASIVE COMPUTING	PP	100	40	18	58	76	P
410446	COMPUTER LABORATORY-I	PR	50	20			41	P
410446	COMPUTER LABORATORY-I	OR	50	20			40	P
410447	COMPUTER LABORATORY-II	TW	50	20			41	P
410447	COMPUTER LABORATORY-II	OR	50	20			37	P
410448	PROJECT	TW	50	20			41	P

FIRST SEM TOTAL = 549/750

B120054325 KANAWADAY AMEETH . ,71400916E ,PICT ,B120054325

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	21	44	65	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	25	53	78	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	16	45	61	P
410444D	DATA MINING TECH. & APPL.	PP	100	40	24	40	64	P
410445A	PROB. SOLV. WITH GAMIFICATION	PP	100	40	21	48	69	P
410446	COMPUTER LABORATORY-I	PR	50	20			46	P
410446	COMPUTER LABORATORY-I	OR	50	20			45	P
410447	COMPUTER LABORATORY-II	TW	50	20			46	P
410447	COMPUTER LABORATORY-II	OR	50	20			43	P
410448	PROJECT	TW	50	20			46	P

FIRST SEM TOTAL = 563/750

B120054326 KAPURE RAJAS SANJAY RAJASHREE ,71400920C ,PICT ,B120054326

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	23	40	63	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	19	37	56	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	16	39	55	P
410444D	DATA MINING TECH. & APPL.	PP	100	40	24	38	62	P
410445B	PERVASIVE COMPUTING	PP	100	40	16	46	62	P
410446	COMPUTER LABORATORY-I	PR	50	20			44	P
410446	COMPUTER LABORATORY-I	OR	50	20			42	P
410447	COMPUTER LABORATORY-II	TW	50	20			44	P
410447	COMPUTER LABORATORY-II	OR	50	20			41	P
410448	PROJECT	TW	50	20			45	P

FIRST SEM TOTAL = 514/750

BRANCH: 76 B.E. (2012 PAT.)(COMPUTER)

DATE : 02 FEB 2017

CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
 OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120054327 KARE NIKITA NAMDEO SUVARNA ,71400924F ,PICT ,B120054327

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	23	35	58	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	20	33	53	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	15	34	49	P
410444B	COMP. NET. DESIGN & MODELING	PP	100	40	25	42	67	P
410445B	PERVASIVE COMPUTING	PP	100	40	16	38	54	P
410446	COMPUTER LABORATORY-I	PR	50	20			41	P
410446	COMPUTER LABORATORY-I	OR	50	20			40	P
410447	COMPUTER LABORATORY-II	TW	50	20			41	P
410447	COMPUTER LABORATORY-II	OR	50	20			37	P
410448	PROJECT	TW	50	20			41	P

FIRST SEM TOTAL = 481/750

B120054328 KARNEWAR ANIMESH SANJAY RANJANA ,71400927L ,PICT ,B120054328

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	16	43	59	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	27	47	74	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	20	43	63	P
410444A	IMAGE PROCESSING	PP	100	40	22	48	70	P
410445D	MULTIDISCIPLINARY NLP	PP	100	40	28	62	90	P
410446	COMPUTER LABORATORY-I	PR	50	20			46	P
410446	COMPUTER LABORATORY-I	OR	50	20			46	P
410447	COMPUTER LABORATORY-II	TW	50	20			42	P
410447	COMPUTER LABORATORY-II	OR	50	20			40	P
410448	PROJECT	TW	50	20			46	P

FIRST SEM TOTAL = 576/750

B120054329 KARTIKEYA BHATNAGAR SHUBHRA ,71400929G ,PICT ,B120054329

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	24	41	65	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	21	43	64	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	16	49	65	P
410444B	COMP. NET. DESIGN & MODELING	PP	100	40	24	46	70	P
410445B	PERVASIVE COMPUTING	PP	100	40	18	55	73	P
410446	COMPUTER LABORATORY-I	PR	50	20			43	P
410446	COMPUTER LABORATORY-I	OR	50	20			42	P
410447	COMPUTER LABORATORY-II	TW	50	20			43	P
410447	COMPUTER LABORATORY-II	OR	50	20			40	P
410448	PROJECT	TW	50	20			40	P

FIRST SEM TOTAL = 545/750

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120054330 KAUSTUBH SABLE NALINI SABLE ,71400933E ,PICT ,B120054330

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	23	34	57	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	17	42	59	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	15	40	55	P
410444A	IMAGE PROCESSING	PP	100	40	23	40	63	P
410445A	PROB. SOLV. WITH GAMIFICATION	PP	100	40	24	33	57	P
410446	COMPUTER LABORATORY-I	PR	50	20			45	P
410446	COMPUTER LABORATORY-I	OR	50	20			44	P
410447	COMPUTER LABORATORY-II	TW	50	20			41	P
410447	COMPUTER LABORATORY-II	OR	50	20			38	P
410448	PROJECT	TW	50	20			46	P

FIRST SEM TOTAL = 505/750

B120054331 KEKANE PRIYANKA GAHININATH KISNABAI ,71400935M ,PICT ,B120054331

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	15	37	52	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	28	44	72	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	16	36	52	P
410444D	DATA MINING TECH. & APPL.	PP	100	40	25	37	62	P
410445D	MULTIDISCIPLINARY NLP	PP	100	40	20	58	78	P
410446	COMPUTER LABORATORY-I	PR	50	20			43	P
410446	COMPUTER LABORATORY-I	OR	50	20			43	P
410447	COMPUTER LABORATORY-II	TW	50	20			43	P
410447	COMPUTER LABORATORY-II	OR	50	20			40	P
410448	PROJECT	TW	50	20			39	P

FIRST SEM TOTAL = 524/750

B120054332 KESHRI SIMRAN RAJESH KUSUM ,71500246F ,PICT ,B120054332

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	17	46	63	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	27	47	74	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	15	47	62	P
410444C	ADVANCED COMPUTER PROG.	PP	100	40	25	44	69	P
410445A	PROB. SOLV. WITH GAMIFICATION	PP	100	40	27	46	73	P
410446	COMPUTER LABORATORY-I	PR	50	20			43	P
410446	COMPUTER LABORATORY-I	OR	50	20			43	P
410447	COMPUTER LABORATORY-II	TW	50	20			45	P
410447	COMPUTER LABORATORY-II	OR	50	20			39	P
410448	PROJECT	TW	50	20			41	P

FIRST SEM TOTAL = 552/750

BRANCH: 76 B.E. (2012 PAT.)(COMPUTER)

DATE : 02 FEB 2017

CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
 OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120054333 KEVIN KAUL ANJU ,71400936K ,PICT ,B120054333

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	14	32	46	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	09	28	--	F
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	16	29	45	P
410444D	DATA MINING TECH. & APPL.	PP	100	40	18	29	47	P
410445B	PERVASIVE COMPUTING	PP	100	40	14	43	57	P
410446	COMPUTER LABORATORY-I	PR	50	20			42	P
410446	COMPUTER LABORATORY-I	OR	50	20			40	P
410447	COMPUTER LABORATORY-II	TW	50	20			42	P
410447	COMPUTER LABORATORY-II	OR	50	20			35	P
410448	PROJECT	TW	50	20			39	P

FIRST SEM TOTAL = --/750

B120054334 KHANDAVE DEVENDRA SANJAY MAYA ,71400939D ,PICT ,B120054334

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	11	32	43	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	21	33	54	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	16	36	52	P
410444C	ADVANCED COMPUTER PROG.	PP	100	40	22	29	51	P
410445B	PERVASIVE COMPUTING	PP	100	40	14	57	71	P
410446	COMPUTER LABORATORY-I	PR	50	20			39	P
410446	COMPUTER LABORATORY-I	OR	50	20			37	P
410447	COMPUTER LABORATORY-II	TW	50	20			43	P
410447	COMPUTER LABORATORY-II	OR	50	20			35	P
410448	PROJECT	TW	50	20			42	P

FIRST SEM TOTAL = 467/750

B120054335 KHANDEKAR SWAPNIL RAJAN SMITA ,71400940H ,PICT ,B120054335

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	21	38	59	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	25	43	68	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	14	44	58	P
410444C	ADVANCED COMPUTER PROG.	PP	100	40	27	35	62	P
410445D	MULTIDISCIPLINARY NLP	PP	100	40	19	42	61	P
410446	COMPUTER LABORATORY-I	PR	50	20			46	P
410446	COMPUTER LABORATORY-I	OR	50	20			46	P
410447	COMPUTER LABORATORY-II	TW	50	20			44	P
410447	COMPUTER LABORATORY-II	OR	50	20			39	P
410448	PROJECT	TW	50	20			45	P

FIRST SEM TOTAL = 528/750

BRANCH: 76 B.E. (2012 PAT.)(COMPUTER)

DATE : 02 FEB 2017

CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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B120054336 KHATRI SEJAL ANIL SANGEETA ,71400943B ,PICT ,B120054336

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	20	37	57	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	23	46	69	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	15	42	57	P
410444C	ADVANCED COMPUTER PROG.	PP	100	40	27	36	63	P
410445C	EMBEDDED SECURITY	PP	100	40	20	36	56	P
410446	COMPUTER LABORATORY-I	PR	50	20			43	P
410446	COMPUTER LABORATORY-I	OR	50	20			42	P
410447	COMPUTER LABORATORY-II	TW	50	20			43	P
410447	COMPUTER LABORATORY-II	OR	50	20			40	P
410448	PROJECT	TW	50	20			40	P

FIRST SEM TOTAL = 510/750

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B120054337 KHETAN ANJALI RAJESH SUNITA ,71400944L ,PICT ,B120054337

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	13	32	45	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	25	44	69	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	19	40	59	P
410444B	COMP. NET. DESIGN & MODELING	PP	100	40	19	52	71	P
410445B	PERVASIVE COMPUTING	PP	100	40	13	40	53	P
410446	COMPUTER LABORATORY-I	PR	50	20			42	P
410446	COMPUTER LABORATORY-I	OR	50	20			42	P
410447	COMPUTER LABORATORY-II	TW	50	20			41	P
410447	COMPUTER LABORATORY-II	OR	50	20			40	P
410448	PROJECT	TW	50	20			44	P

FIRST SEM TOTAL = 506/750

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B120054338 KHIYANI NISHA HARISHKUMAR SUNITA ,71400947E ,PICT ,B120054338

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	29	43	72	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	26	44	70	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	15	40	55	P
410444B	COMP. NET. DESIGN & MODELING	PP	100	40	27	43	70	P
410445B	PERVASIVE COMPUTING	PP	100	40	16	44	60	P
410446	COMPUTER LABORATORY-I	PR	50	20			45	P
410446	COMPUTER LABORATORY-I	OR	50	20			44	P
410447	COMPUTER LABORATORY-II	TW	50	20			40	P
410447	COMPUTER LABORATORY-II	OR	50	20			41	P
410448	PROJECT	TW	50	20			37	P

FIRST SEM TOTAL = 534/750

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120054340 KINARE MRINMAYI VINAYAK SMITA ,71400951C ,PICT ,B120054340

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	17	41	58	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	28	46	74	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	24	43	67	P
410444D	DATA MINING TECH. & APPL.	PP	100	40	22	45	67	P
410445D	MULTIDISCIPLINARY NLP	PP	100	40	22	53	75	P
410446	COMPUTER LABORATORY-I	PR	50	20			43	P
410446	COMPUTER LABORATORY-I	OR	50	20			42	P
410447	COMPUTER LABORATORY-II	TW	50	20			40	P
410447	COMPUTER LABORATORY-II	OR	50	20			36	P
410448	PROJECT	TW	50	20			44	P

FIRST SEM TOTAL = 546/750

B120054341 KIRTANE NANDAN KIRAN ASHWINI ,71400953K ,PICT ,B120054341

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	16	40	56	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	17	46	63	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	17	34	51	P
410444D	DATA MINING TECH. & APPL.	PP	100	40	14	37	51	P
410445B	PERVASIVE COMPUTING	PP	100	40	15	47	62	P
410446	COMPUTER LABORATORY-I	PR	50	20			37	P
410446	COMPUTER LABORATORY-I	OR	50	20			37	P
410447	COMPUTER LABORATORY-II	TW	50	20			39	P
410447	COMPUTER LABORATORY-II	OR	50	20			36	P
410448	PROJECT	TW	50	20			40	P

FIRST SEM TOTAL = 472/750

B120054342 KIRTE AISHWARYA ULHAS KANCHAN ,71500247D ,PICT ,B120054342

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	14	41	55	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	21	44	65	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	17	39	56	P
410444C	ADVANCED COMPUTER PROG.	PP	100	40	22	37	59	P
410445B	PERVASIVE COMPUTING	PP	100	40	15	55	70	P
410446	COMPUTER LABORATORY-I	PR	50	20			42	P
410446	COMPUTER LABORATORY-I	OR	50	20			42	P
410447	COMPUTER LABORATORY-II	TW	50	20			39	P
410447	COMPUTER LABORATORY-II	OR	50	20			38	P
410448	PROJECT	TW	50	20			39	P

FIRST SEM TOTAL = 505/750

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120054343 KOLHE MUGDHA PRAKASH VAISHALI KOLHE ,71400956D ,PICT ,B120054343

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	11	28	--	F
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	06	20	--	F
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	12	33	45	P
410444C	ADVANCED COMPUTER PROG.	PP	100	40	22	28	50	P
410445D	MULTIDISCIPLINARY NLP	PP	100	40	16	37	53	P
410446	COMPUTER LABORATORY-I	PR	50	20			43	P
410446	COMPUTER LABORATORY-I	OR	50	20			43	P
410447	COMPUTER LABORATORY-II	TW	50	20			43	P
410447	COMPUTER LABORATORY-II	OR	50	20			39	P
410448	PROJECT	TW	50	20			45	P

FIRST SEM TOTAL = --/750

B120054344 KOSHTI HARSHADA MUKUND REKHA ,71400957B ,PICT ,B120054344

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	18	38	56	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	16	37	53	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	14	36	50	P
410444C	ADVANCED COMPUTER PROG.	PP	100	40	23	33	56	P
410445A	PROB. SOLV. WITH GAMIFICATION	PP	100	40	22	36	58	P
410446	COMPUTER LABORATORY-I	PR	50	20			39	P
410446	COMPUTER LABORATORY-I	OR	50	20			38	P
410447	COMPUTER LABORATORY-II	TW	50	20			43	P
410447	COMPUTER LABORATORY-II	OR	50	20			30	P
410448	PROJECT	TW	50	20			40	P

FIRST SEM TOTAL = 463/750

B120054345 KOTHARI SHUBHANKAR AJIT SHUBHANGI ,71400960B ,PICT ,B120054345

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	17	39	56	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	29	48	77	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	16	42	58	P
410444D	DATA MINING TECH. & APPL.	PP	100	40	21	40	61	P
410445D	MULTIDISCIPLINARY NLP	PP	100	40	21	50	71	P
410446	COMPUTER LABORATORY-I	PR	50	20			46	P
410446	COMPUTER LABORATORY-I	OR	50	20			46	P
410447	COMPUTER LABORATORY-II	TW	50	20			43	P
410447	COMPUTER LABORATORY-II	OR	50	20			43	P
410448	PROJECT	TW	50	20			45	P

FIRST SEM TOTAL = 546/750

BRANCH: 76 B.E. (2012 PAT.)(COMPUTER)

DATE : 02 FEB 2017

CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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B120054346 KOTIAN SHIRLEY SYDNEY GRACE ,71400961L ,PICT ,B120054346

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	18	44	62	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	27	45	72	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	18	45	63	P
410444D	DATA MINING TECH. & APPL.	PP	100	40	21	43	64	P
410445B	PERVASIVE COMPUTING	PP	100	40	20	57	77	P
410446	COMPUTER LABORATORY-I	PR	50	20			46	P
410446	COMPUTER LABORATORY-I	OR	50	20			46	P
410447	COMPUTER LABORATORY-II	TW	50	20			46	P
410447	COMPUTER LABORATORY-II	OR	50	20			44	P
410448	PROJECT	TW	50	20			45	P

FIRST SEM TOTAL = 565/750

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B120054347 KULKARNI HRISHIKESH PRASHANT KULKARNI SUSHAMA PRASHANT ,71400964E ,PICT ,B120054347

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	16	40	56	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	25	48	73	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	16	44	60	P
410444D	DATA MINING TECH. & APPL.	PP	100	40	16	42	58	P
410445D	MULTIDISCIPLINARY NLP	PP	100	40	19	48	67	P
410446	COMPUTER LABORATORY-I	PR	50	20			45	P
410446	COMPUTER LABORATORY-I	OR	50	20			44	P
410447	COMPUTER LABORATORY-II	TW	50	20			46	P
410447	COMPUTER LABORATORY-II	OR	50	20			43	P
410448	PROJECT	TW	50	20			43	P

FIRST SEM TOTAL = 535/750

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B120054348 KULKARNI PRANAV LAXMIKANT SNEHA ,71400968H ,PICT ,B120054348

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	19	32	51	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	26	38	64	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	13	32	45	P
410444D	DATA MINING TECH. & APPL.	PP	100	40	20	35	55	P
410445D	MULTIDISCIPLINARY NLP	PP	100	40	19	45	64	P
410446	COMPUTER LABORATORY-I	PR	50	20			43	P
410446	COMPUTER LABORATORY-I	OR	50	20			43	P
410447	COMPUTER LABORATORY-II	TW	50	20			45	P
410447	COMPUTER LABORATORY-II	OR	50	20			40	P
410448	PROJECT	TW	50	20			38	P

FIRST SEM TOTAL = 488/750

BRANCH: 76 B.E. (2012 PAT.)(COMPUTER)

DATE : 02 FEB 2017

CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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B120054349 KULKARNI SHIVAM SHREERAM VRISHALI ,71400971H ,PICT ,B120054349

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	17	36	53	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	24	48	72	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	21	36	57	P
410444D	DATA MINING TECH. & APPL.	PP	100	40	21	34	55	P
410445B	PERVASIVE COMPUTING	PP	100	40	19	43	62	P
410446	COMPUTER LABORATORY-I	PR	50	20			40	P
410446	COMPUTER LABORATORY-I	OR	50	20			38	P
410447	COMPUTER LABORATORY-II	TW	50	20			45	P
410447	COMPUTER LABORATORY-II	OR	50	20			39	P
410448	PROJECT	TW	50	20			37	P

FIRST SEM TOTAL = 498/750

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B120054350 KULKARNI SHREYAS PRASHANT SMITA ,71400972F ,PICT ,B120054350

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	14	29	43	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	17	28	45	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	12	17	--	F
410444C	ADVANCED COMPUTER PROG.	PP	100	40	18	22	--	F
410445B	PERVASIVE COMPUTING	PP	100	40	14	39	53	P
410446	COMPUTER LABORATORY-I	PR	50	20			36	P
410446	COMPUTER LABORATORY-I	OR	50	20			38	P
410447	COMPUTER LABORATORY-II	TW	50	20			40	P
410447	COMPUTER LABORATORY-II	OR	50	20			36	P
410448	PROJECT	TW	50	20			43	P

FIRST SEM TOTAL = --/750

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B120054351 KULKARNI SHRUTI DEEPAK VIDYA ,71500248B ,PICT ,B120054351

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	14	44	58	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	26	54	80	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	18	45	63	P
410444B	COMP. NET. DESIGN & MODELING	PP	100	40	22	58	80	P
410445B	PERVASIVE COMPUTING	PP	100	40	17	50	67	P
410446	COMPUTER LABORATORY-I	PR	50	20			41	P
410446	COMPUTER LABORATORY-I	OR	50	20			38	P
410447	COMPUTER LABORATORY-II	TW	50	20			40	P
410447	COMPUTER LABORATORY-II	OR	50	20			35	P
410448	PROJECT	TW	50	20			42	P

FIRST SEM TOTAL = 544/750

BRANCH: 76 B.E. (2012 PAT.)(COMPUTER)

DATE : 02 FEB 2017

CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120054352 KUMBHAKARNA SWATI MURLIDHAR MANGAL ,71500249L ,PICT ,B120054352

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	12	44	56	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	24	51	75	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	14	28	42	P
410444B	COMP. NET. DESIGN & MODELING	PP	100	40	20	59	79	P
410445B	PERVASIVE COMPUTING	PP	100	40	17	52	69	P
410446	COMPUTER LABORATORY-I	PR	50	20			38	P
410446	COMPUTER LABORATORY-I	OR	50	20			36	P
410447	COMPUTER LABORATORY-II	TW	50	20			42	P
410447	COMPUTER LABORATORY-II	OR	50	20			38	P
410448	PROJECT	TW	50	20			41	P

FIRST SEM TOTAL = 516/750

B120054353 KUMBHAR SWAPNIL VINOD SAVITA ,71400977G ,PICT ,B120054353

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	14	35	49	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	9	35	44	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	12	19	--	F
410444C	ADVANCED COMPUTER PROG.	PP	100	40	18	35	53	P
410445B	PERVASIVE COMPUTING	PP	100	40	14	46	60	P
410446	COMPUTER LABORATORY-I	PR	50	20			42	P
410446	COMPUTER LABORATORY-I	OR	50	20			39	P
410447	COMPUTER LABORATORY-II	TW	50	20			43	P
410447	COMPUTER LABORATORY-II	OR	50	20			39	P
410448	PROJECT	TW	50	20			38	P

FIRST SEM TOTAL = --/750

B120054354 KUSAL SONAL KHUSHAL JANABAI ,71500250D ,PICT ,B120054354

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	18	36	54	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	27	51	78	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	12	39	51	P
410444C	ADVANCED COMPUTER PROG.	PP	100	40	23	38	61	P
410445B	PERVASIVE COMPUTING	PP	100	40	19	49	68	P
410446	COMPUTER LABORATORY-I	PR	50	20			30	P
410446	COMPUTER LABORATORY-I	OR	50	20			30	P
410447	COMPUTER LABORATORY-II	TW	50	20			42	P
410447	COMPUTER LABORATORY-II	OR	50	20			38	P
410448	PROJECT	TW	50	20			43	P

FIRST SEM TOTAL = 495/750

BRANCH: 76 B.E. (2012 PAT.)(COMPUTER)

DATE : 02 FEB 2017

CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120054355 LAHOTI DIPIKA RAJESH JAYSHREE ,71400980G ,PICT ,B120054355

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	17	39	56	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	29	50	79	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	18	43	61	P
410444C	ADVANCED COMPUTER PROG.	PP	100	40	27	36	63	P
410445B	PERVASIVE COMPUTING	PP	100	40	18	45	63	P
410446	COMPUTER LABORATORY-I	PR	50	20			42	P
410446	COMPUTER LABORATORY-I	OR	50	20			43	P
410447	COMPUTER LABORATORY-II	TW	50	20			46	P
410447	COMPUTER LABORATORY-II	OR	50	20			42	P
410448	PROJECT	TW	50	20			43	P

FIRST SEM TOTAL = 538/750

B120054356 LAHOTI YASH VIJAY REKHA ,71400981E ,PICT ,B120054356

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	20	38	58	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	11	47	58	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	17	34	51	P
410444D	DATA MINING TECH. & APPL.	PP	100	40	24	40	64	P
410445A	PROB. SOLV. WITH GAMIFICATION	PP	100	40	23	43	66	P
410446	COMPUTER LABORATORY-I	PR	50	20			40	P
410446	COMPUTER LABORATORY-I	OR	50	20			35	P
410447	COMPUTER LABORATORY-II	TW	50	20			46	P
410447	COMPUTER LABORATORY-II	OR	50	20			42	P
410448	PROJECT	TW	50	20			43	P

FIRST SEM TOTAL = 503/750

B120054357 LANDE KAJAL JANARDHAN MEENA ,71500251B ,PICT ,B120054357

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	10	34	44	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	15	46	61	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	12	42	54	P
410444D	DATA MINING TECH. & APPL.	PP	100	40	22	44	66	P
410445B	PERVASIVE COMPUTING	PP	100	40	17	51	68	P
410446	COMPUTER LABORATORY-I	PR	50	20			10	F
410446	COMPUTER LABORATORY-I	OR	50	20			23	P
410447	COMPUTER LABORATORY-II	TW	50	20			40	P
410447	COMPUTER LABORATORY-II	OR	50	20			36	P
410448	PROJECT	TW	50	20			30	P

FIRST SEM TOTAL = --/750

BRANCH: 76 B.E. (2012 PAT.)(COMPUTER)

DATE : 02 FEB 2017

CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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B120054358 LANDE RUDRA PRADIP UJWALA ,71400983M ,PICT ,B120054358

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	17	41	58	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	24	50	74	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	15	39	54	P
410444D	DATA MINING TECH. & APPL.	PP	100	40	17	39	56	P
410445A	PROB. SOLV. WITH GAMIFICATION	PP	100	40	20	36	56	P
410446	COMPUTER LABORATORY-I	PR	50	20			39	P
410446	COMPUTER LABORATORY-I	OR	50	20			36	P
410447	COMPUTER LABORATORY-II	TW	50	20			47	P
410447	COMPUTER LABORATORY-II	OR	50	20			44	P
410448	PROJECT	TW	50	20			44	P

FIRST SEM TOTAL = 508/750

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B120054359 LOHAR MEDHA RAJENDRA VIDYA ,71500253J ,PICT ,B120054359

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	21	42	63	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	18	54	72	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	18	40	58	P
410444C	ADVANCED COMPUTER PROG.	PP	100	40	24	40	64	P
410445B	PERVASIVE COMPUTING	PP	100	40	20	39	59	P
410446	COMPUTER LABORATORY-I	PR	50	20			40	P
410446	COMPUTER LABORATORY-I	OR	50	20			38	P
410447	COMPUTER LABORATORY-II	TW	50	20			47	P
410447	COMPUTER LABORATORY-II	OR	50	20			44	P
410448	PROJECT	TW	50	20			41	P

FIRST SEM TOTAL = 526/750

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B120054360 MENON KIRTI RAGHUNANDANAN SATI ,71401004K ,PICT ,B120054360

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	17	35	52	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	24	50	74	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	20	41	61	P
410444D	DATA MINING TECH. & APPL.	PP	100	40	22	38	60	P
410445B	PERVASIVE COMPUTING	PP	100	40	18	50	68	P
410446	COMPUTER LABORATORY-I	PR	50	20			36	P
410446	COMPUTER LABORATORY-I	OR	50	20			34	P
410447	COMPUTER LABORATORY-II	TW	50	20			45	P
410447	COMPUTER LABORATORY-II	OR	50	20			41	P
410448	PROJECT	TW	50	20			43	P

FIRST SEM TOTAL = 514/750

BRANCH: 76 B.E. (2012 PAT.)(COMPUTER)

DATE : 02 FEB 2017

CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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B120054361 MIKHIL DESAI KALPANA ,71401006F ,PICT ,B120054361

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	14	32	46	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	14	43	57	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	14	28	42	P
410444A	IMAGE PROCESSING	PP	100	40	16	32	48	P
410445A	PROB. SOLV. WITH GAMIFICATION	PP	100	40	19	38	57	P
410446	COMPUTER LABORATORY-I	PR	50	20			32	P
410446	COMPUTER LABORATORY-I	OR	50	20			30	P
410447	COMPUTER LABORATORY-II	TW	50	20			45	P
410447	COMPUTER LABORATORY-II	OR	50	20			41	P
410448	PROJECT	TW	50	20			42	P

FIRST SEM TOTAL = 440/750

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B120054362 MOKASHI KIRAN SUHAS SHUBHANGI ,71500254G ,PICT ,B120054362

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	18	39	57	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	29	53	82	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	14	30	44	P
410444C	ADVANCED COMPUTER PROG.	PP	100	40	26	34	60	P
410445A	PROB. SOLV. WITH GAMIFICATION	PP	100	40	24	36	60	P
410446	COMPUTER LABORATORY-I	PR	50	20			40	P
410446	COMPUTER LABORATORY-I	OR	50	20			40	P
410447	COMPUTER LABORATORY-II	TW	50	20			45	P
410447	COMPUTER LABORATORY-II	OR	50	20			41	P
410448	PROJECT	TW	50	20			42	P

FIRST SEM TOTAL = 511/750

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B120054363 MORE SHUBHAMKAR SANJAY JAYSHREE ,71401011B ,PICT ,B120054363

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	17	25	--	F
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	23	33	56	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	18	30	48	P
410444D	DATA MINING TECH. & APPL.	PP	100	40	16	34	50	P
410445A	PROB. SOLV. WITH GAMIFICATION	PP	100	40	23	35	58	P
410446	COMPUTER LABORATORY-I	PR	50	20			38	P
410446	COMPUTER LABORATORY-I	OR	50	20			38	P
410447	COMPUTER LABORATORY-II	TW	50	20			41	P
410447	COMPUTER LABORATORY-II	OR	50	20			37	P
410448	PROJECT	TW	50	20			40	P

FIRST SEM TOTAL = --/750

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120054364 MOTE ROHIT CHANDRAKANT SHAILA ,71301089E ,PICT ,B120054364

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	15	31	46	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	10	43	53	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	12	29	41	P
410444A	IMAGE PROCESSING	PP	100	40	17	37	54	P
410445A	PROB. SOLV. WITH GAMIFICATION	PP	100	40	20	40	60	P
410446	COMPUTER LABORATORY-I	PR	50	20			39	P
410446	COMPUTER LABORATORY-I	OR	50	20			40	P
410447	COMPUTER LABORATORY-II	TW	50	20			42	P
410447	COMPUTER LABORATORY-II	OR	50	20			38	P
410448	PROJECT	TW	50	20			41	P

FIRST SEM TOTAL = 454/750

B120054365 MULAY SHUBHAM SHRIKANT SNEHA ,71401015E ,PICT ,B120054365

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	18	34	52	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	23	42	65	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	16	35	51	P
410444D	DATA MINING TECH. & APPL.	PP	100	40	19	39	58	P
410445A	PROB. SOLV. WITH GAMIFICATION	PP	100	40	23	38	61	P
410446	COMPUTER LABORATORY-I	PR	50	20			42	P
410446	COMPUTER LABORATORY-I	OR	50	20			40	P
410447	COMPUTER LABORATORY-II	TW	50	20			42	P
410447	COMPUTER LABORATORY-II	OR	50	20			38	P
410448	PROJECT	TW	50	20			45	P

FIRST SEM TOTAL = 494/750

B120054366 MUNDADA UTSAV SHRIKANT SULOCHANA ,71401017M ,PICT ,B120054366

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	18	39	57	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	29	50	79	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	20	39	59	P
410444A	IMAGE PROCESSING	PP	100	40	27	43	70	P
410445B	PERVASIVE COMPUTING	PP	100	40	18	43	61	P
410446	COMPUTER LABORATORY-I	PR	50	20			42	P
410446	COMPUTER LABORATORY-I	OR	50	20			40	P
410447	COMPUTER LABORATORY-II	TW	50	20			45	P
410447	COMPUTER LABORATORY-II	OR	50	20			39	P
410448	PROJECT	TW	50	20			45	P

FIRST SEM TOTAL = 537/750

BRANCH: 76 B.E. (2012 PAT.)(COMPUTER)

DATE : 02 FEB 2017

CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
 OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120054367 MURTAZA RAJA LULUA ,71401019H ,PICT ,B120054367

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	14	40	54	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	30	55	85	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	21	34	55	P
410444C	ADVANCED COMPUTER PROG.	PP	100	40	27	34	61	P
410445D	MULTIDISCIPLINARY NLP	PP	100	40	25	54	79	P
410446	COMPUTER LABORATORY-I	PR	50	20			AA	F
410446	COMPUTER LABORATORY-I	OR	50	20			AA	F
410447	COMPUTER LABORATORY-II	TW	50	20			46	P
410447	COMPUTER LABORATORY-II	OR	50	20			42	P
410448	PROJECT	TW	50	20			46	P

FIRST SEM TOTAL = --/750

B120054368 NAIK AMIT PANDURANG ASHWINI ,71401021K ,PICT ,B120054368

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	24	43	67	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	27	50	77	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	19	40	59	P
410444C	ADVANCED COMPUTER PROG.	PP	100	40	27	39	66	P
410445A	PROB. SOLV. WITH GAMIFICATION	PP	100	40	24	44	68	P
410446	COMPUTER LABORATORY-I	PR	50	20			41	P
410446	COMPUTER LABORATORY-I	OR	50	20			42	P
410447	COMPUTER LABORATORY-II	TW	50	20			47	P
410447	COMPUTER LABORATORY-II	OR	50	20			43	P
410448	PROJECT	TW	50	20			46	P

FIRST SEM TOTAL = 556/750

B120054369 NAIKADE SUPRIYA BALASAHEB PUSHPALATA ,71500255E ,PICT ,B120054369

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	16	54	70	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	26	52	78	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	19	45	64	P
410444C	ADVANCED COMPUTER PROG.	PP	100	40	27	38	65	P
410445B	PERVASIVE COMPUTING	PP	100	40	18	51	69	P
410446	COMPUTER LABORATORY-I	PR	50	20			12	F
410446	COMPUTER LABORATORY-I	OR	50	20			33	P
410447	COMPUTER LABORATORY-II	TW	50	20			45	P
410447	COMPUTER LABORATORY-II	OR	50	20			41	P
410448	PROJECT	TW	50	20			42	P

FIRST SEM TOTAL = --/750

BRANCH: 76 B.E. (2012 PAT.)(COMPUTER)

DATE : 02 FEB 2017

CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
 OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120054370 NAKATE ABHIJIT DATTATRAYA SHOBHA ,71401026L ,PICT ,B120054370

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	25	48	73	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	19	49	68	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	22	40	62	P
410444D	DATA MINING TECH. & APPL.	PP	100	40	21	40	61	P
410445A	PROB. SOLV. WITH GAMIFICATION	PP	100	40	20	38	58	P
410446	COMPUTER LABORATORY-I	PR	50	20			40	P
410446	COMPUTER LABORATORY-I	OR	50	20			41	P
410447	COMPUTER LABORATORY-II	TW	50	20			45	P
410447	COMPUTER LABORATORY-II	OR	50	20			41	P
410448	PROJECT	TW	50	20			38	P

FIRST SEM TOTAL = 527/750

B120054371 NARKHEDE TANAY SATISH KIRTI ,71401030J ,PICT ,B120054371

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	23	47	70	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	28	52	80	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	20	35	55	P
410444C	ADVANCED COMPUTER PROG.	PP	100	40	25	32	57	P
410445A	PROB. SOLV. WITH GAMIFICATION	PP	100	40	24	40	64	P
410446	COMPUTER LABORATORY-I	PR	50	20			42	P
410446	COMPUTER LABORATORY-I	OR	50	20			44	P
410447	COMPUTER LABORATORY-II	TW	50	20			47	P
410447	COMPUTER LABORATORY-II	OR	50	20			45	P
410448	PROJECT	TW	50	20			46	P

FIRST SEM TOTAL = 550/750

B120054372 NATASHA MENON PREETHA MENON ,71401031G ,PICT ,B120054372

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	20	39	59	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	27	54	81	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	15	37	52	P
410444D	DATA MINING TECH. & APPL.	PP	100	40	28	32	60	P
410445B	PERVASIVE COMPUTING	PP	100	40	16	43	59	P
410446	COMPUTER LABORATORY-I	PR	50	20			40	P
410446	COMPUTER LABORATORY-I	OR	50	20			38	P
410447	COMPUTER LABORATORY-II	TW	50	20			47	P
410447	COMPUTER LABORATORY-II	OR	50	20			43	P
410448	PROJECT	TW	50	20			43	P

FIRST SEM TOTAL = 522/750

BRANCH: 76 B.E. (2012 PAT.)(COMPUTER)

DATE : 02 FEB 2017

CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120054373 NEERAJ VILAS AUTI INDU VILAS AUTI ,71401034M ,PICT ,B120054373

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	14	34	48	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	21	30	51	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	14	33	47	P
410444C	ADVANCED COMPUTER PROG.	PP	100	40	16	30	46	P
410445B	PERVASIVE COMPUTING	PP	100	40	15	42	57	P
410446	COMPUTER LABORATORY-I	PR	50	20			37	P
410446	COMPUTER LABORATORY-I	OR	50	20			35	P
410447	COMPUTER LABORATORY-II	TW	50	20			46	P
410447	COMPUTER LABORATORY-II	OR	50	20			42	P
410448	PROJECT	TW	50	20			42	P

FIRST SEM TOTAL = 451/750

B120054374 NENE AMIT VIVEK VINEETA ,71401035K ,PICT ,B120054374

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	13	40	53	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	23	39	62	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	13	31	44	P
410444D	DATA MINING TECH. & APPL.	PP	100	40	18	32	50	P
410445A	PROB. SOLV. WITH GAMIFICATION	PP	100	40	20	38	58	P
410446	COMPUTER LABORATORY-I	PR	50	20			12	F
410446	COMPUTER LABORATORY-I	OR	50	20			34	P
410447	COMPUTER LABORATORY-II	TW	50	20			46	P
410447	COMPUTER LABORATORY-II	OR	50	20			42	P
410448	PROJECT	TW	50	20			42	P

FIRST SEM TOTAL = --/750

B120054375 NEVE ROHAN MILIND SHWETA ,71401036H ,PICT ,B120054375

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	20	41	61	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	26	37	63	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	19	38	57	P
410444C	ADVANCED COMPUTER PROG.	PP	100	40	22	35	57	P
410445B	PERVASIVE COMPUTING	PP	100	40	17	47	64	P
410446	COMPUTER LABORATORY-I	PR	50	20			38	P
410446	COMPUTER LABORATORY-I	OR	50	20			37	P
410447	COMPUTER LABORATORY-II	TW	50	20			46	P
410447	COMPUTER LABORATORY-II	OR	50	20			42	P
410448	PROJECT	TW	50	20			41	P

FIRST SEM TOTAL = 506/750

BRANCH: 76 B.E. (2012 PAT.)(COMPUTER)

DATE : 02 FEB 2017

CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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B120054376 NIKAM VRUSHALI KIRAN ROHINI ,71500256C ,PICT ,B120054376

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	21	53	74	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	20	40	60	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	19	45	64	P
410444C	ADVANCED COMPUTER PROG.	PP	100	40	25	52	77	P
410445B	PERVASIVE COMPUTING	PP	100	40	20	51	71	P
410446	COMPUTER LABORATORY-I	PR	50	20			30	P
410446	COMPUTER LABORATORY-I	OR	50	20			35	P
410447	COMPUTER LABORATORY-II	TW	50	20			46	P
410447	COMPUTER LABORATORY-II	OR	50	20			42	P
410448	PROJECT	TW	50	20			38	P

FIRST SEM TOTAL = 537/750

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B120054377 NIKUMBH KAMLESH HIRATSING VIMAL ,71401039B ,PICT ,B120054377

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	16	51	67	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	18	39	57	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	18	34	52	P
410444B	COMP. NET. DESIGN & MODELING	PP	100	40	25	53	78	P
410445B	PERVASIVE COMPUTING	PP	100	40	16	47	63	P
410446	COMPUTER LABORATORY-I	PR	50	20			39	P
410446	COMPUTER LABORATORY-I	OR	50	20			30	P
410447	COMPUTER LABORATORY-II	TW	50	20			45	P
410447	COMPUTER LABORATORY-II	OR	50	20			41	P
410448	PROJECT	TW	50	20			41	P

FIRST SEM TOTAL = 513/750

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B120054378 NIMKAR PUSHKAR DEVENDRA VIBHA ,71401042B ,PICT ,B120054378

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	14	41	55	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	20	34	54	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	13	28	41	P
410444D	DATA MINING TECH. & APPL.	PP	100	40	23	32	55	P
410445D	MULTIDISCIPLINARY NLP	PP	100	40	16	45	61	P
410446	COMPUTER LABORATORY-I	PR	50	20			42	P
410446	COMPUTER LABORATORY-I	OR	50	20			36	P
410447	COMPUTER LABORATORY-II	TW	50	20			46	P
410447	COMPUTER LABORATORY-II	OR	50	20			42	P
410448	PROJECT	TW	50	20			45	P

FIRST SEM TOTAL = 477/750

BRANCH: 76 B.E. (2012 PAT.)(COMPUTER)

DATE : 02 FEB 2017

CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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B120054379 PANDE AMBARISH PRASHANT MANJUSHA PANDE ,71401049K ,PICT ,B120054379

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	15	37	52	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	24	40	64	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	19	28	47	P
410444D	DATA MINING TECH. & APPL.	PP	100	40	29	38	67	P
410445D	MULTIDISCIPLINARY NLP	PP	100	40	19	44	63	P
410446	COMPUTER LABORATORY-I	PR	50	20			43	P
410446	COMPUTER LABORATORY-I	OR	50	20			40	P
410447	COMPUTER LABORATORY-II	TW	50	20			47	P
410447	COMPUTER LABORATORY-II	OR	50	20			43	P
410448	PROJECT	TW	50	20			40	P

FIRST SEM TOTAL = 506/750

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B120054380 PANDILWAR SANKET SUNILRAO ANUPAMA ,71401051M ,PICT ,B120054380

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	20	41	61	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	26	40	66	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	23	29	52	P
410444C	ADVANCED COMPUTER PROG.	PP	100	40	25	50	75	P
410445A	PROB. SOLV. WITH GAMIFICATION	PP	100	40	20	33	53	P
410446	COMPUTER LABORATORY-I	PR	50	20			30	P
410446	COMPUTER LABORATORY-I	OR	50	20			28	P
410447	COMPUTER LABORATORY-II	TW	50	20			41	P
410447	COMPUTER LABORATORY-II	OR	50	20			37	P
410448	PROJECT	TW	50	20			43	P

FIRST SEM TOTAL = 486/750

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B120054381 PANGAVHANE PRAGATI SANJAY VANDANA ,71500257M ,PICT ,B120054381

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	15	50	65	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	23	56	79	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	22	49	71	P
410444C	ADVANCED COMPUTER PROG.	PP	100	40	27	48	75	P
410445B	PERVASIVE COMPUTING	PP	100	40	19	52	71	P
410446	COMPUTER LABORATORY-I	PR	50	20			35	P
410446	COMPUTER LABORATORY-I	OR	50	20			38	P
410447	COMPUTER LABORATORY-II	TW	50	20			45	P
410447	COMPUTER LABORATORY-II	OR	50	20			41	P
410448	PROJECT	TW	50	20			44	P

FIRST SEM TOTAL = 564/750

BRANCH: 76 B.E. (2012 PAT.)(COMPUTER)

DATE : 02 FEB 2017

CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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B120054382 PANSE SHUBHANKAR SUNIL KALPANA ,71401053H ,PICT ,B120054382

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	13	39	52	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	27	47	74	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	17	35	52	P
410444D	DATA MINING TECH. & APPL.	PP	100	40	21	39	60	P
410445B	PERVASIVE COMPUTING	PP	100	40	17	40	57	P
410446	COMPUTER LABORATORY-I	PR	50	20			36	P
410446	COMPUTER LABORATORY-I	OR	50	20			38	P
410447	COMPUTER LABORATORY-II	TW	50	20			46	P
410447	COMPUTER LABORATORY-II	OR	50	20			42	P
410448	PROJECT	TW	50	20			43	P

FIRST SEM TOTAL = 500/750

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B120054383 PARAMVIR KARMVIR SINGH SUMAN SINGH ,71401054F ,PICT ,B120054383

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	14	40	54	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	8	33	41	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	12	33	45	P
410444B	COMP. NET. DESIGN & MODELING	PP	100	40	23	37	60	P
410445B	PERVASIVE COMPUTING	PP	100	40	12	28	40	P
410446	COMPUTER LABORATORY-I	PR	50	20			32	P
410446	COMPUTER LABORATORY-I	OR	50	20			25	P
410447	COMPUTER LABORATORY-II	TW	50	20			44	P
410447	COMPUTER LABORATORY-II	OR	50	20			40	P
410448	PROJECT	TW	50	20			45	P

FIRST SEM TOTAL = 426/750

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B120054384 PATEL JAINESH BHUPENDRA HINA ,71401055D ,PICT ,B120054384

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	20	47	67	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	24	47	71	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	20	49	69	P
410444D	DATA MINING TECH. & APPL.	PP	100	40	21	38	59	P
410445A	PROB. SOLV. WITH GAMIFICATION	PP	100	40	24	41	65	P
410446	COMPUTER LABORATORY-I	PR	50	20			38	P
410446	COMPUTER LABORATORY-I	OR	50	20			38	P
410447	COMPUTER LABORATORY-II	TW	50	20			44	P
410447	COMPUTER LABORATORY-II	OR	50	20			40	P
410448	PROJECT	TW	50	20			45	P

FIRST SEM TOTAL = 536/750

FIRST SEM TOTAL = 471/750

BRANCH: 76 B.E. (2012 PAT.)(COMPUTER)

DATE : 02 FEB 2017

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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B120054388 PATIL SAGARIKA ASHOK SUNITA ,71500259H ,PICT ,B120054388

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	21	53	74	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	24	56	80	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	23	42	65	P
410444D	DATA MINING TECH. & APPL.	PP	100	40	25	43	68	P
410445B	PERVASIVE COMPUTING	PP	100	40	18	51	69	P
410446	COMPUTER LABORATORY-I	PR	50	20			38	P
410446	COMPUTER LABORATORY-I	OR	50	20			40	P
410447	COMPUTER LABORATORY-II	TW	50	20			46	P
410447	COMPUTER LABORATORY-II	OR	50	20			42	P
410448	PROJECT	TW	50	20			40	P

FIRST SEM TOTAL = 562/750

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B120054389 PATIL SHARDUL SUHAS JAYASHREE ,71401069D ,PICT ,B120054389

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	15	46	61	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	24	47	71	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	17	32	49	P
410444D	DATA MINING TECH. & APPL.	PP	100	40	24	34	58	P
410445B	PERVASIVE COMPUTING	PP	100	40	17	47	64	P
410446	COMPUTER LABORATORY-I	PR	50	20			38	P
410446	COMPUTER LABORATORY-I	OR	50	20			30	P
410447	COMPUTER LABORATORY-II	TW	50	20			43	P
410447	COMPUTER LABORATORY-II	OR	50	20			39	P
410448	PROJECT	TW	50	20			40	P

FIRST SEM TOTAL = 493/750

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B120054390 PATIL VRUSHALI SUDHAKAR SHUBHANGI ,71401073B ,PICT ,B120054390

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	12	53	65	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	28	57	85	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	23	49	72	P
410444B	COMP. NET. DESIGN & MODELING	PP	100	40	23	60	83	P
410445A	PROB. SOLV. WITH GAMIFICATION	PP	100	40	25	50	75	P
410446	COMPUTER LABORATORY-I	PR	50	20			10	F
410446	COMPUTER LABORATORY-I	OR	50	20			30	P
410447	COMPUTER LABORATORY-II	TW	50	20			46	P
410447	COMPUTER LABORATORY-II	OR	50	20			42	P
410448	PROJECT	TW	50	20			43	P

FIRST SEM TOTAL = --/750

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BRANCH: 76 B.E. (2012 PAT.)(COMPUTER)

DATE : 02 FEB 2017

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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B120054391 PATTHE KAUSTUBH TEJRAM SHOBHA ,71347772F ,PICT ,B120054391

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	20	47	67	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	16	41	57	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	17	37	54	P
410444D	DATA MINING TECH. & APPL.	PP	100	40	14	35	49	P
410445A	PROB. SOLV. WITH GAMIFICATION	PP	100	40	19	32	51	P
410446	COMPUTER LABORATORY-I	PR	50	20			32	P
410446	COMPUTER LABORATORY-I	OR	50	20			30	P
410447	COMPUTER LABORATORY-II	TW	50	20			46	P
410447	COMPUTER LABORATORY-II	OR	50	20			42	P
410448	PROJECT	TW	50	20			38	P

FIRST SEM TOTAL = 466/750

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B120054392 PAWAR KOMAL RAMCHANDRA HIRA ,71401077E ,PICT ,B120054392

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	17	49	66	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	18	36	54	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	16	40	56	P
410444C	ADVANCED COMPUTER PROG.	PP	100	40	20	32	52	P
410445A	PROB. SOLV. WITH GAMIFICATION	PP	100	40	19	33	52	P
410446	COMPUTER LABORATORY-I	PR	50	20			10	F
410446	COMPUTER LABORATORY-I	OR	50	20			40	P
410447	COMPUTER LABORATORY-II	TW	50	20			40	P
410447	COMPUTER LABORATORY-II	OR	50	20			36	P
410448	PROJECT	TW	50	20			40	P

FIRST SEM TOTAL = --/750

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B120054393 PAWAR PRITHVIRAJ HEMANT SANGEETA ,71401080E ,PICT ,B120054393

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	14	50	64	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	25	47	72	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	16	44	60	P
410444D	DATA MINING TECH. & APPL.	PP	100	40	20	35	55	P
410445A	PROB. SOLV. WITH GAMIFICATION	PP	100	40	22	35	57	P
410446	COMPUTER LABORATORY-I	PR	50	20			43	P
410446	COMPUTER LABORATORY-I	OR	50	20			40	P
410447	COMPUTER LABORATORY-II	TW	50	20			45	P
410447	COMPUTER LABORATORY-II	OR	50	20			41	P
410448	PROJECT	TW	50	20			45	P

FIRST SEM TOTAL = 522/750

BRANCH: 76 B.E. (2012 PAT.)(COMPUTER)

DATE : 02 FEB 2017

CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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B120054394 PHATKE SANYUKTA BABASAHEB SANJEEVANI ,71401083K ,PICT ,B120054394

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	22	52	74	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	24	48	72	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	17	34	51	P
410444C	ADVANCED COMPUTER PROG.	PP	100	40	24	47	71	P
410445B	PERVASIVE COMPUTING	PP	100	40	17	50	67	P
410446	COMPUTER LABORATORY-I	PR	50	20			34	P
410446	COMPUTER LABORATORY-I	OR	50	20			35	P
410447	COMPUTER LABORATORY-II	TW	50	20			45	P
410447	COMPUTER LABORATORY-II	OR	50	20			41	P
410448	PROJECT	TW	50	20			43	P

FIRST SEM TOTAL = 533/750

.....

B120054395 PITEKAR SAYALI RAMDAS SANGITA ,71500260M ,PICT ,B120054395

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	10	52	62	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	21	54	75	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	17	45	62	P
410444B	COMP. NET. DESIGN & MODELING	PP	100	40	23	61	84	P
410445B	PERVASIVE COMPUTING	PP	100	40	18	51	69	P
410446	COMPUTER LABORATORY-I	PR	50	20			26	P
410446	COMPUTER LABORATORY-I	OR	50	20			34	P
410447	COMPUTER LABORATORY-II	TW	50	20			46	P
410447	COMPUTER LABORATORY-II	OR	50	20			42	P
410448	PROJECT	TW	50	20			41	P

FIRST SEM TOTAL = 541/750

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B120054396 POOJA VASANT PATIL GEETA PATIL ,71401086D ,PICT ,B120054396

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	18	50	68	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	28	51	79	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	18	41	59	P
410444C	ADVANCED COMPUTER PROG.	PP	100	40	25	49	74	P
410445D	MULTIDISCIPLINARY NLP	PP	100	40	21	52	73	P
410446	COMPUTER LABORATORY-I	PR	50	20			40	P
410446	COMPUTER LABORATORY-I	OR	50	20			40	P
410447	COMPUTER LABORATORY-II	TW	50	20			46	P
410447	COMPUTER LABORATORY-II	OR	50	20			42	P
410448	PROJECT	TW	50	20			44	P

FIRST SEM TOTAL = 565/750

BRANCH: 76 B.E. (2012 PAT.)(COMPUTER)

DATE : 02 FEB 2017

CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
 OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120054397 PRAJAKTA KISHORE RAJHANS JANCY ,71401089J ,PICT ,B120054397

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	17	51	68	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	25	48	73	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	20	41	61	P
410444C	ADVANCED COMPUTER PROG.	PP	100	40	25	57	82	P
410445B	PERVASIVE COMPUTING	PP	100	40	18	53	71	P
410446	COMPUTER LABORATORY-I	PR	50	20			33	P
410446	COMPUTER LABORATORY-I	OR	50	20			38	P
410447	COMPUTER LABORATORY-II	TW	50	20			46	P
410447	COMPUTER LABORATORY-II	OR	50	20			42	P
410448	PROJECT	TW	50	20			44	P

FIRST SEM TOTAL = 558/750

B120054398 PRANALI PRABHAKAR NINAWA BHARTI ,71401090B ,PICT ,B120054398

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	20	52	72	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	26	56	82	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	22	44	66	P
410444C	ADVANCED COMPUTER PROG.	PP	100	40	27	61	88	P
410445B	PERVASIVE COMPUTING	PP	100	40	18	48	66	P
410446	COMPUTER LABORATORY-I	PR	50	20			32	P
410446	COMPUTER LABORATORY-I	OR	50	20			32	P
410447	COMPUTER LABORATORY-II	TW	50	20			46	P
410447	COMPUTER LABORATORY-II	OR	50	20			42	P
410448	PROJECT	TW	50	20			42	P

FIRST SEM TOTAL = 568/750

B120054399 PRANJAL MANOJ BHOR LATA ,71401091L ,PICT ,B120054399

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	15	44	59	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	20	53	73	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	12	46	58	P
410444A	IMAGE PROCESSING	PP	100	40	23	48	71	P
410445A	PROB. SOLV. WITH GAMIFICATION	PP	100	40	24	40	64	P
410446	COMPUTER LABORATORY-I	PR	50	20			42	P
410446	COMPUTER LABORATORY-I	OR	50	20			40	P
410447	COMPUTER LABORATORY-II	TW	50	20			46	P
410447	COMPUTER LABORATORY-II	OR	50	20			42	P
410448	PROJECT	TW	50	20			48	P

FIRST SEM TOTAL = 543/750

BRANCH: 76 B.E. (2012 PAT.)(COMPUTER)

DATE : 02 FEB 2017

CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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B120054400 PRATYUSH GUPTA SEEMA GUPTA ,71401097K ,PICT ,B120054400

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	20	48	68	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	26	47	73	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	22	39	61	P
410444D	DATA MINING TECH. & APPL.	PP	100	40	26	28	54	P
410445D	MULTIDISCIPLINARY NLP	PP	100	40	19	44	63	P
410446	COMPUTER LABORATORY-I	PR	50	20			40	P
410446	COMPUTER LABORATORY-I	OR	50	20			38	P
410447	COMPUTER LABORATORY-II	TW	50	20			46	P
410447	COMPUTER LABORATORY-II	OR	50	20			42	P
410448	PROJECT	TW	50	20			38	P

FIRST SEM TOTAL = 523/750

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B120054401 PRAVIN BASAVRAJ SWAMI SAVITRI BASAVRAJ SWAMI ,71401098H ,PICT ,B120054401

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	18	49	67	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	22	51	73	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	18	36	54	P
410444D	DATA MINING TECH. & APPL.	PP	100	40	18	47	65	P
410445A	PROB. SOLV. WITH GAMIFICATION	PP	100	40	23	43	66	P
410446	COMPUTER LABORATORY-I	PR	50	20			39	P
410446	COMPUTER LABORATORY-I	OR	50	20			38	P
410447	COMPUTER LABORATORY-II	TW	50	20			46	P
410447	COMPUTER LABORATORY-II	OR	50	20			42	P
410448	PROJECT	TW	50	20			36	P

FIRST SEM TOTAL = 526/750

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B120054402 PRIYANKA RAJYASHREE ,71401099F ,PICT ,B120054402

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	13	54	67	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	24	54	78	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	22	42	64	P
410444C	ADVANCED COMPUTER PROG.	PP	100	40	25	56	81	P
410445B	PERVASIVE COMPUTING	PP	100	40	16	51	67	P
410446	COMPUTER LABORATORY-I	PR	50	20			42	P
410446	COMPUTER LABORATORY-I	OR	50	20			38	P
410447	COMPUTER LABORATORY-II	TW	50	20			46	P
410447	COMPUTER LABORATORY-II	OR	50	20			42	P
410448	PROJECT	TW	50	20			43	P

FIRST SEM TOTAL = 568/750

BRANCH: 76 B.E. (2012 PAT.)(COMPUTER)

DATE : 02 FEB 2017

CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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B120054403 PRIYANKA NANABHAU KUWAR PRAMODINI ,71401100C ,PICT ,B120054403

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	19	45	64	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	21	48	69	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	14	31	45	P
410444D	DATA MINING TECH. & APPL.	PP	100	40	17	43	60	P
410445B	PERVASIVE COMPUTING	PP	100	40	18	45	63	P
410446	COMPUTER LABORATORY-I	PR	50	20			35	P
410446	COMPUTER LABORATORY-I	OR	50	20			35	P
410447	COMPUTER LABORATORY-II	TW	50	20			46	P
410447	COMPUTER LABORATORY-II	OR	50	20			42	P
410448	PROJECT	TW	50	20			43	P

FIRST SEM TOTAL = 502/750

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B120054404 RAJPUT BHAGYASHREE RAJENDRA SUVARNA ,71401107L ,PICT ,B120054404

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	12	39	51	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	8	44	52	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	17	32	49	P
410444C	ADVANCED COMPUTER PROG.	PP	100	40	26	44	70	P
410445A	PROB. SOLV. WITH GAMIFICATION	PP	100	40	23	37	60	P
410446	COMPUTER LABORATORY-I	PR	50	20			28	P
410446	COMPUTER LABORATORY-I	OR	50	20			32	P
410447	COMPUTER LABORATORY-II	TW	50	20			45	P
410447	COMPUTER LABORATORY-II	OR	50	20			41	P
410448	PROJECT	TW	50	20			42	P

FIRST SEM TOTAL = 470/750

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B120054405 RATHOD SUJIT INDU ,71401112G ,PICT ,B120054405

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	18	50	68	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	24	45	69	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	18	42	60	P
410444C	ADVANCED COMPUTER PROG.	PP	100	40	23	42	65	P
410445A	PROB. SOLV. WITH GAMIFICATION	PP	100	40	19	42	61	P
410446	COMPUTER LABORATORY-I	PR	50	20			38	P
410446	COMPUTER LABORATORY-I	OR	50	20			35	P
410447	COMPUTER LABORATORY-II	TW	50	20			44	P
410447	COMPUTER LABORATORY-II	OR	50	20			40	P
410448	PROJECT	TW	50	20			43	P

FIRST SEM TOTAL = 523/750

BRANCH: 76 B.E. (2012 PAT.)(COMPUTER)

DATE : 02 FEB 2017

CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
 OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120054406 RISHABH SANJAY SINHA RUPA ,71401117H ,PICT ,B120054406

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	20	51	71	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	25	47	72	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	17	38	55	P
410444A	IMAGE PROCESSING	PP	100	40	25	39	64	P
410445A	PROB. SOLV. WITH GAMIFICATION	PP	100	40	24	36	60	P
410446	COMPUTER LABORATORY-I	PR	50	20			28	P
410446	COMPUTER LABORATORY-I	OR	50	20			36	P
410447	COMPUTER LABORATORY-II	TW	50	20			46	P
410447	COMPUTER LABORATORY-II	OR	50	20			42	P
410448	PROJECT	TW	50	20			40	P

FIRST SEM TOTAL = 514/750

B120054407 RITIKA SINGH SHUBHA ,71401118F ,PICT ,B120054407

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	17	35	52	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	13	41	54	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	18	31	49	P
410444D	DATA MINING TECH. & APPL.	PP	100	40	18	34	52	P
410445C	EMBEDDED SECURITY	PP	100	40	18	40	58	P
410446	COMPUTER LABORATORY-I	PR	50	20			10	F
410446	COMPUTER LABORATORY-I	OR	50	20			30	P
410447	COMPUTER LABORATORY-II	TW	50	20			45	P
410447	COMPUTER LABORATORY-II	OR	50	20			41	P
410448	PROJECT	TW	50	20			40	P

FIRST SEM TOTAL = --/750

B120054408 RITWIK NANDAKUMAR SHARADA ,71401119D ,PICT ,B120054408

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	9	53	62	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	15	43	58	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	15	29	44	P
410444D	DATA MINING TECH. & APPL.	PP	100	40	16	28	44	P
410445A	PROB. SOLV. WITH GAMIFICATION	PP	100	40	23	44	67	P
410446	COMPUTER LABORATORY-I	PR	50	20			28	P
410446	COMPUTER LABORATORY-I	OR	50	20			25	P
410447	COMPUTER LABORATORY-II	TW	50	20			46	P
410447	COMPUTER LABORATORY-II	OR	50	20			42	P
410448	PROJECT	TW	50	20			46	P

FIRST SEM TOTAL = 462/750

BRANCH: 76 B.E. (2012 PAT.)(COMPUTER)

DATE : 02 FEB 2017

CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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B120054409 ROMA KAUL ANnapurna ,71401121F ,PICT ,B120054409

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	25	54	79	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	29	47	76	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	24	38	62	P
410444C	ADVANCED COMPUTER PROG.	PP	100	40	26	39	65	P
410445A	PROB. SOLV. WITH GAMIFICATION	PP	100	40	22	38	60	P
410446	COMPUTER LABORATORY-I	PR	50	20			42	P
410446	COMPUTER LABORATORY-I	OR	50	20			44	P
410447	COMPUTER LABORATORY-II	TW	50	20			45	P
410447	COMPUTER LABORATORY-II	OR	50	20			41	P
410448	PROJECT	TW	50	20			47	P

FIRST SEM TOTAL = 561/750

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B120054410 ROSHAN KUMAR SUNAINA PRASAD ,71401123B ,PICT ,B120054410

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	25	51	76	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	22	55	77	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	14	55	69	P
410444C	ADVANCED COMPUTER PROG.	PP	100	40	27	45	72	P
410445B	PERVASIVE COMPUTING	PP	100	40	17	44	61	P
410446	COMPUTER LABORATORY-I	PR	50	20			38	P
410446	COMPUTER LABORATORY-I	OR	50	20			35	P
410447	COMPUTER LABORATORY-II	TW	50	20			43	P
410447	COMPUTER LABORATORY-II	OR	50	20			39	P
410448	PROJECT	TW	50	20			43	P

FIRST SEM TOTAL = 553/750

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B120054411 SAHIL SHARMA KAMAL ,71401129M ,PICT ,B120054411

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	11	37	48	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	22	28	50	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	13	37	50	P
410444D	DATA MINING TECH. & APPL.	PP	100	40	20	34	54	P
410445B	PERVASIVE COMPUTING	PP	100	40	18	49	67	P
410446	COMPUTER LABORATORY-I	PR	50	20			25	P
410446	COMPUTER LABORATORY-I	OR	50	20			30	P
410447	COMPUTER LABORATORY-II	TW	50	20			40	P
410447	COMPUTER LABORATORY-II	OR	50	20			36	P
410448	PROJECT	TW	50	20			44	P

FIRST SEM TOTAL = 444/750

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120054412 SANGVEKAR CHIRAG GANGADHAR PRIYANKA ,71401136D ,PICT ,B120054412

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	14	36	50	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	24	28	52	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	15	31	46	P
410444A	IMAGE PROCESSING	PP	100	40	19	35	54	P
410445A	PROB. SOLV. WITH GAMIFICATION	PP	100	40	12	38	50	P
410446	COMPUTER LABORATORY-I	PR	50	20			36	P
410446	COMPUTER LABORATORY-I	OR	50	20			34	P
410447	COMPUTER LABORATORY-II	TW	50	20			46	P
410447	COMPUTER LABORATORY-II	OR	50	20			42	P
410448	PROJECT	TW	50	20			42	P

FIRST SEM TOTAL = 452/750

B120054413 SAPKAL SANKET SHRIKRUSHNA RADHA ,71401138L ,PICT ,B120054413

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	23	37	60	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	27	40	67	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	24	45	69	P
410444D	DATA MINING TECH. & APPL.	PP	100	40	21	34	55	P
410445A	PROB. SOLV. WITH GAMIFICATION	PP	100	40	24	57	81	P
410446	COMPUTER LABORATORY-I	PR	50	20			42	P
410446	COMPUTER LABORATORY-I	OR	50	20			35	P
410447	COMPUTER LABORATORY-II	TW	50	20			45	P
410447	COMPUTER LABORATORY-II	OR	50	20			41	P
410448	PROJECT	TW	50	20			46	P

FIRST SEM TOTAL = 541/750

B120054414 SARANGE SAGAR RAJENDRA ROHINI ,71500261K ,PICT ,B120054414

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	25	41	66	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	20	30	50	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	19	39	58	P
410444A	IMAGE PROCESSING	PP	100	40	24	47	71	P
410445A	PROB. SOLV. WITH GAMIFICATION	PP	100	40	23	56	79	P
410446	COMPUTER LABORATORY-I	PR	50	20			40	P
410446	COMPUTER LABORATORY-I	OR	50	20			36	P
410447	COMPUTER LABORATORY-II	TW	50	20			44	P
410447	COMPUTER LABORATORY-II	OR	50	20			40	P
410448	PROJECT	TW	50	20			40	P

FIRST SEM TOTAL = 524/750

BRANCH: 76 B.E. (2012 PAT.)(COMPUTER)

DATE : 02 FEB 2017

CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
 OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120054415 SARODE OMKAR VIJAY ASHWINI ,71401140B ,PICT ,B120054415

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	15	30	45	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	14	22	--	F
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	17	25	--	F
410444B	COMP. NET. DESIGN & MODELING	PP	100	40	15	40	55	P
410445B	PERVASIVE COMPUTING	PP	100	40	14	36	50	P
410446	COMPUTER LABORATORY-I	PR	50	20			34	P
410446	COMPUTER LABORATORY-I	OR	50	20			32	P
410447	COMPUTER LABORATORY-II	TW	50	20			44	P
410447	COMPUTER LABORATORY-II	OR	50	20			40	P
410448	PROJECT	TW	50	20			36	P

FIRST SEM TOTAL = --/750

B120054416 SARSAR SHWETA JEETENDRA SANAJANA ,71401141L ,PICT ,B120054416

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	13	39	52	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	12	28	40	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	15	34	49	P
410444A	IMAGE PROCESSING	PP	100	40	15	18	--	F
410445A	PROB. SOLV. WITH GAMIFICATION	PP	100	40	20	54	74	P
410446	COMPUTER LABORATORY-I	PR	50	20			32	P
410446	COMPUTER LABORATORY-I	OR	50	20			28	P
410447	COMPUTER LABORATORY-II	TW	50	20			46	P
410447	COMPUTER LABORATORY-II	OR	50	20			42	P
410448	PROJECT	TW	50	20			43	P

FIRST SEM TOTAL = --/750

B120054417 SARTHAK MUNSHI KAVITA ,71401142J ,PICT ,B120054417

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	28	39	67	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	21	42	63	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	15	37	52	P
410444A	IMAGE PROCESSING	PP	100	40	16	34	50	P
410445A	PROB. SOLV. WITH GAMIFICATION	PP	100	40	22	36	58	P
410446	COMPUTER LABORATORY-I	PR	50	20			30	P
410446	COMPUTER LABORATORY-I	OR	50	20			36	P
410447	COMPUTER LABORATORY-II	TW	50	20			45	P
410447	COMPUTER LABORATORY-II	OR	50	20			41	P
410448	PROJECT	TW	50	20			45	P

FIRST SEM TOTAL = 487/750

BRANCH: 76 B.E. (2012 PAT.)(COMPUTER)

DATE : 02 FEB 2017

CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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B120054418 SATALKAR PRASAD NEELKANTH NAMRATA ,71401145C ,PICT ,B120054418

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	23	38	61	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	18	31	49	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	12	40	52	P
410444D	DATA MINING TECH. & APPL.	PP	100	40	22	32	54	P
410445D	MULTIDISCIPLINARY NLP	PP	100	40	22	52	74	P
410446	COMPUTER LABORATORY-I	PR	50	20			28	P
410446	COMPUTER LABORATORY-I	OR	50	20			37	P
410447	COMPUTER LABORATORY-II	TW	50	20			45	P
410447	COMPUTER LABORATORY-II	OR	50	20			41	P
410448	PROJECT	TW	50	20			42	P

FIRST SEM TOTAL = 483/750

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B120054419 SAURABH POHNERKAR D VANDANA ,71401148H ,PICT ,B120054419

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	18	41	59	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	29	37	66	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	20	36	56	P
410444D	DATA MINING TECH. & APPL.	PP	100	40	25	40	65	P
410445A	PROB. SOLV. WITH GAMIFICATION	PP	100	40	21	46	67	P
410446	COMPUTER LABORATORY-I	PR	50	20			32	P
410446	COMPUTER LABORATORY-I	OR	50	20			36	P
410447	COMPUTER LABORATORY-II	TW	50	20			46	P
410447	COMPUTER LABORATORY-II	OR	50	20			42	P
410448	PROJECT	TW	50	20			41	P

FIRST SEM TOTAL = 510/750

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B120054420 SAURAV SAGAR POONAM KUMARI ,71301209K ,PICT ,B120054420

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	08	28	--	F
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	04	14	--	F
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	12	19	--	F
410444D	DATA MINING TECH. & APPL.	PP	100	40	12	22	--	F
410445A	PROB. SOLV. WITH GAMIFICATION	PP	100	40	17	30	47	P
410446	COMPUTER LABORATORY-I	PR	50	20			38	P
410446	COMPUTER LABORATORY-I	OR	50	20			35	P
410447	COMPUTER LABORATORY-II	TW	50	20			40	P
410447	COMPUTER LABORATORY-II	OR	50	20			36	P
410448	PROJECT	TW	50	20			37	P

FIRST SEM TOTAL = --/750

BRANCH: 76 B.E. (2012 PAT.)(COMPUTER)

DATE : 02 FEB 2017

CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120054421 SAWANT CHINMAY SHANKAR ANUSAYA ,71401150K ,PICT ,B120054421

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	16	44	60	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	27	38	65	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	20	46	66	P
410444A	IMAGE PROCESSING	PP	100	40	25	44	69	P
410445D	MULTIDISCIPLINARY NLP	PP	100	40	24	53	77	P
410446	COMPUTER LABORATORY-I	PR	50	20			41	P
410446	COMPUTER LABORATORY-I	OR	50	20			40	P
410447	COMPUTER LABORATORY-II	TW	50	20			41	P
410447	COMPUTER LABORATORY-II	OR	50	20			37	P
410448	PROJECT	TW	50	20			44	P

FIRST SEM TOTAL = 540/750

B120054422 SAYALI RAJENDRA PATIL HARSHA PATIL ,71401152F ,PICT ,B120054422

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	16	32	48	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	23	38	61	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	19	31	50	P
410444C	ADVANCED COMPUTER PROG.	PP	100	40	24	41	65	P
410445B	PERVASIVE COMPUTING	PP	100	40	16	39	55	P
410446	COMPUTER LABORATORY-I	PR	50	20			38	P
410446	COMPUTER LABORATORY-I	OR	50	20			38	P
410447	COMPUTER LABORATORY-II	TW	50	20			44	P
410447	COMPUTER LABORATORY-II	OR	50	20			40	P
410448	PROJECT	TW	50	20			44	P

FIRST SEM TOTAL = 483/750

B120054423 SAYYED AYESHA FAZAL RAFIA ,71500262H ,PICT ,B120054423

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	14	36	50	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	19	31	50	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	16	39	55	P
410444D	DATA MINING TECH. & APPL.	PP	100	40	18	35	53	P
410445B	PERVASIVE COMPUTING	PP	100	40	15	35	50	P
410446	COMPUTER LABORATORY-I	PR	50	20			38	P
410446	COMPUTER LABORATORY-I	OR	50	20			36	P
410447	COMPUTER LABORATORY-II	TW	50	20			45	P
410447	COMPUTER LABORATORY-II	OR	50	20			41	P
410448	PROJECT	TW	50	20			42	P

FIRST SEM TOTAL = 460/750

BRANCH: 76 B.E. (2012 PAT.)(COMPUTER)

DATE : 02 FEB 2017

CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
 OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120054424 SHABBIR NURUDDIN BAWAJI KHAIRUN ,71401153D ,PICT ,B120054424

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	19	35	54	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	22	39	61	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	15	41	56	P
410444D	DATA MINING TECH. & APPL.	PP	100	40	15	34	49	P
410445D	MULTIDISCIPLINARY NLP	PP	100	40	26	50	76	P
410446	COMPUTER LABORATORY-I	PR	50	20			40	P
410446	COMPUTER LABORATORY-I	OR	50	20			36	P
410447	COMPUTER LABORATORY-II	TW	50	20			44	P
410447	COMPUTER LABORATORY-II	OR	50	20			40	P
410448	PROJECT	TW	50	20			42	P

FIRST SEM TOTAL = 498/750

B120054425 SHAH ANANDRAJ BHARAT PRIYA ,71401155L ,PICT ,B120054425

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	16	39	55	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	22	32	54	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	19	38	57	P
410444D	DATA MINING TECH. & APPL.	PP	100	40	21	44	65	P
410445B	PERVASIVE COMPUTING	PP	100	40	19	41	60	P
410446	COMPUTER LABORATORY-I	PR	50	20			43	P
410446	COMPUTER LABORATORY-I	OR	50	20			42	P
410447	COMPUTER LABORATORY-II	TW	50	20			45	P
410447	COMPUTER LABORATORY-II	OR	50	20			41	P
410448	PROJECT	TW	50	20			44	P

FIRST SEM TOTAL = 506/750

B120054426 SHAH ANUJ ULHAS ANAGHA ,71401156J ,PICT ,B120054426

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	15	43	58	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	22	34	56	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	14	40	54	P
410444D	DATA MINING TECH. & APPL.	PP	100	40	23	40	63	P
410445D	MULTIDISCIPLINARY NLP	PP	100	40	24	47	71	P
410446	COMPUTER LABORATORY-I	PR	50	20			39	P
410446	COMPUTER LABORATORY-I	OR	50	20			38	P
410447	COMPUTER LABORATORY-II	TW	50	20			46	P
410447	COMPUTER LABORATORY-II	OR	50	20			42	P
410448	PROJECT	TW	50	20			44	P

FIRST SEM TOTAL = 511/750

BRANCH: 76 B.E. (2012 PAT.)(COMPUTER)

DATE : 02 FEB 2017

CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
 OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120054427 SHAH RUSHAB RAKESH ARTI ,71401158E ,PICT ,B120054427

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	19	40	59	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	15	50	65	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	21	44	65	P
410444D	DATA MINING TECH. & APPL.	PP	100	40	13	45	58	P
410445B	PERVASIVE COMPUTING	PP	100	40	19	43	62	P
410446	COMPUTER LABORATORY-I	PR	50	20			42	P
410446	COMPUTER LABORATORY-I	OR	50	20			41	P
410447	COMPUTER LABORATORY-II	TW	50	20			45	P
410447	COMPUTER LABORATORY-II	OR	50	20			41	P
410448	PROJECT	TW	50	20			42	P

FIRST SEM TOTAL = 520/750

B120054428 SHAH SANIKA SANTOSH SWATI ,71401159C ,PICT ,B120054428

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	20	41	61	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	27	30	57	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	16	43	59	P
410444C	ADVANCED COMPUTER PROG.	PP	100	40	26	42	68	P
410445D	MULTIDISCIPLINARY NLP	PP	100	40	24	40	64	P
410446	COMPUTER LABORATORY-I	PR	50	20			43	P
410446	COMPUTER LABORATORY-I	OR	50	20			38	P
410447	COMPUTER LABORATORY-II	TW	50	20			45	P
410447	COMPUTER LABORATORY-II	OR	50	20			41	P
410448	PROJECT	TW	50	20			43	P

FIRST SEM TOTAL = 519/750

B120054429 SHAH SWAPNIL SACHIN KASHMIRA ,71401160G ,PICT ,B120054429

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	23	40	63	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	21	37	58	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	12	47	59	P
410444D	DATA MINING TECH. & APPL.	PP	100	40	20	46	66	P
410445A	PROB. SOLV. WITH GAMIFICATION	PP	100	40	22	44	66	P
410446	COMPUTER LABORATORY-I	PR	50	20			40	P
410446	COMPUTER LABORATORY-I	OR	50	20			35	P
410447	COMPUTER LABORATORY-II	TW	50	20			46	P
410447	COMPUTER LABORATORY-II	OR	50	20			42	P
410448	PROJECT	TW	50	20			45	P

FIRST SEM TOTAL = 520/750

BRANCH: 76 B.E. (2012 PAT.)(COMPUTER)

DATE : 02 FEB 2017

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
 OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120054430 SHAHANE TANMAY SHIVKUMAR VASUNDHARA ,71201062K ,PICT ,B120054430

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	19	30	49	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	25	32	57	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	12	33	45	P
410444A	IMAGE PROCESSING	PP	100	40	09	28	--	F
410445B	PERVASIVE COMPUTING	PP	100	40	14	33	47	P
410446	COMPUTER LABORATORY-I	PR	50	20			AA	F
410446	COMPUTER LABORATORY-I	OR	50	20			AA	F
410447	COMPUTER LABORATORY-II	TW	50	20			AA	F
410447	COMPUTER LABORATORY-II	OR	50	20			AA	F
410448	PROJECT	TW	50	20			37	P

FIRST SEM TOTAL = --/750

TERMWORK FAIL...

B120054431 SHAIKH MARUFA MANSOOR SHAHENAZ ,71500263F ,PICT ,B120054431

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	20	36	56	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	29	37	66	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	16	31	47	P
410444C	ADVANCED COMPUTER PROG.	PP	100	40	25	43	68	P
410445A	PROB. SOLV. WITH GAMIFICATION	PP	100	40	20	43	63	P
410446	COMPUTER LABORATORY-I	PR	50	20			38	P
410446	COMPUTER LABORATORY-I	OR	50	20			36	P
410447	COMPUTER LABORATORY-II	TW	50	20			41	P
410447	COMPUTER LABORATORY-II	OR	50	20			37	P
410448	PROJECT	TW	50	20			40	P

FIRST SEM TOTAL = 492/750

B120054432 SHAIKH MOHD FAIZAAN MOHD ROSHAN SHAMIM PARVIN ,71401162C ,PICT ,B120054432

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	23	34	57	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	17	41	58	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	19	41	60	P
410444D	DATA MINING TECH. & APPL.	PP	100	40	23	44	67	P
410445A	PROB. SOLV. WITH GAMIFICATION	PP	100	40	25	42	67	P
410446	COMPUTER LABORATORY-I	PR	50	20			40	P
410446	COMPUTER LABORATORY-I	OR	50	20			38	P
410447	COMPUTER LABORATORY-II	TW	50	20			44	P
410447	COMPUTER LABORATORY-II	OR	50	20			40	P
410448	PROJECT	TW	50	20			43	P

FIRST SEM TOTAL = 514/750

BRANCH: 76 B.E. (2012 PAT.)(COMPUTER)

DATE : 02 FEB 2017

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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B120054433 SHAIKH ZEBA ILAHI FARAJANA ,71500264D ,PICT ,B120054433

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	21	37	58	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	17	51	68	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	21	32	53	P
410444A	IMAGE PROCESSING	PP	100	40	21	52	73	P
410445C	EMBEDDED SECURITY	PP	100	40	21	42	63	P
410446	COMPUTER LABORATORY-I	PR	50	20			39	P
410446	COMPUTER LABORATORY-I	OR	50	20			34	P
410447	COMPUTER LABORATORY-II	TW	50	20			46	P
410447	COMPUTER LABORATORY-II	OR	50	20			42	P
410448	PROJECT	TW	50	20			41	P

FIRST SEM TOTAL = 517/750

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B120054434 SHETI SUMIT MAHAVIR VINITA ,71401169L ,PICT ,B120054434

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	16	38	54	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	20	41	61	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	13	34	47	P
410444D	DATA MINING TECH. & APPL.	PP	100	40	21	39	60	P
410445D	MULTIDISCIPLINARY NLP	PP	100	40	18	40	58	P
410446	COMPUTER LABORATORY-I	PR	50	20			38	P
410446	COMPUTER LABORATORY-I	OR	50	20			38	P
410447	COMPUTER LABORATORY-II	TW	50	20			40	P
410447	COMPUTER LABORATORY-II	OR	50	20			36	P
410448	PROJECT	TW	50	20			42	P

FIRST SEM TOTAL = 474/750

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B120054435 SHEVAKARI PRAJAKTA MURLIDHAR VAIJAYANTI ,71401170D ,PICT ,B120054435

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	18	41	59	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	25	44	69	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	18	36	54	P
410444C	ADVANCED COMPUTER PROG.	PP	100	40	24	48	72	P
410445B	PERVASIVE COMPUTING	PP	100	40	19	41	60	P
410446	COMPUTER LABORATORY-I	PR	50	20			30	P
410446	COMPUTER LABORATORY-I	OR	50	20			40	P
410447	COMPUTER LABORATORY-II	TW	50	20			45	P
410447	COMPUTER LABORATORY-II	OR	50	20			41	P
410448	PROJECT	TW	50	20			40	P

FIRST SEM TOTAL = 510/750

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120054436 SHINDE CHAITALI MANOHAR MANISHA ,71500265B ,PICT ,B120054436

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	20	42	62	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	26	53	79	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	17	33	50	P
410444C	ADVANCED COMPUTER PROG.	PP	100	40	27	48	75	P
410445B	PERVASIVE COMPUTING	PP	100	40	18	36	54	P
410446	COMPUTER LABORATORY-I	PR	50	20			38	P
410446	COMPUTER LABORATORY-I	OR	50	20			38	P
410447	COMPUTER LABORATORY-II	TW	50	20			45	P
410447	COMPUTER LABORATORY-II	OR	50	20			41	P
410448	PROJECT	TW	50	20			40	P

FIRST SEM TOTAL = 522/750

B120054437 SHINDE PRAJAKTA PRADEEPKUMAR ASHA ,71500266L ,PICT ,B120054437

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	20	37	57	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	16	53	69	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	16	46	62	P
410444B	COMP. NET. DESIGN & MODELING	PP	100	40	22	51	73	P
410445C	EMBEDDED SECURITY	PP	100	40	21	49	70	P
410446	COMPUTER LABORATORY-I	PR	50	20			37	P
410446	COMPUTER LABORATORY-I	OR	50	20			32	P
410447	COMPUTER LABORATORY-II	TW	50	20			40	P
410447	COMPUTER LABORATORY-II	OR	50	20			36	P
410448	PROJECT	TW	50	20			43	P

FIRST SEM TOTAL = 519/750

B120054438 SHINDE PRATIK DNYANESHWAR MINAKSHI ,71401172L ,PICT ,B120054438

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	14	32	46	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	24	34	58	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	15	40	55	P
410444C	ADVANCED COMPUTER PROG.	PP	100	40	23	36	59	P
410445D	MULTIDISCIPLINARY NLP	PP	100	40	14	41	55	P
410446	COMPUTER LABORATORY-I	PR	50	20			40	P
410446	COMPUTER LABORATORY-I	OR	50	20			39	P
410447	COMPUTER LABORATORY-II	TW	50	20			43	P
410447	COMPUTER LABORATORY-II	OR	50	20			39	P
410448	PROJECT	TW	50	20			42	P

FIRST SEM TOTAL = 476/750

BRANCH: 76 B.E. (2012 PAT.)(COMPUTER)

DATE : 02 FEB 2017

CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
 OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120054439 SHINDE SHUBHAM DATTATRAYA NALINI ,71401175E ,PICT ,B120054439

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	18	31	49	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	21	32	53	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	14	23	--	F
410444D	DATA MINING TECH. & APPL.	PP	100	40	9	37	46	P
410445B	PERVASIVE COMPUTING	PP	100	40	11	38	49	P
410446	COMPUTER LABORATORY-I	PR	50	20			30	P
410446	COMPUTER LABORATORY-I	OR	50	20			35	P
410447	COMPUTER LABORATORY-II	TW	50	20			43	P
410447	COMPUTER LABORATORY-II	OR	50	20			39	P
410448	PROJECT	TW	50	20			40	P

FIRST SEM TOTAL = --/750

B120054440 SHINDE SHWETA SANJAY MANISHA ,71500267J ,PICT ,B120054440

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	23	41	64	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	22	30	52	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	17	36	53	P
410444C	ADVANCED COMPUTER PROG.	PP	100	40	24	47	71	P
410445B	PERVASIVE COMPUTING	PP	100	40	15	44	59	P
410446	COMPUTER LABORATORY-I	PR	50	20			38	P
410446	COMPUTER LABORATORY-I	OR	50	20			35	P
410447	COMPUTER LABORATORY-II	TW	50	20			41	P
410447	COMPUTER LABORATORY-II	OR	50	20			37	P
410448	PROJECT	TW	50	20			40	P

FIRST SEM TOTAL = 490/750

B120054441 SHINDE SUJATA CHANDRAKANT DHONDABAI ,71500268G ,PICT ,B120054441

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	17	53	70	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	24	53	77	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	13	46	59	P
410444C	ADVANCED COMPUTER PROG.	PP	100	40	26	51	77	P
410445B	PERVASIVE COMPUTING	PP	100	40	20	46	66	P
410446	COMPUTER LABORATORY-I	PR	50	20			36	P
410446	COMPUTER LABORATORY-I	OR	50	20			34	P
410447	COMPUTER LABORATORY-II	TW	50	20			40	P
410447	COMPUTER LABORATORY-II	OR	50	20			36	P
410448	PROJECT	TW	50	20			44	P

FIRST SEM TOTAL = 539/750

BRANCH: 76 B.E. (2012 PAT.)(COMPUTER)

DATE : 02 FEB 2017

CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
 OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120054442 SHIRBHATE KANCHAN AVINASH JYOTI ,71500269E ,PICT ,B120054442

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	21	41	62	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	15	37	52	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	15	34	49	P
410444B	COMP. NET. DESIGN & MODELING	PP	100	40	19	51	70	P
410445B	PERVASIVE COMPUTING	PP	100	40	18	45	63	P
410446	COMPUTER LABORATORY-I	PR	50	20			25	P
410446	COMPUTER LABORATORY-I	OR	50	20			32	P
410447	COMPUTER LABORATORY-II	TW	50	20			40	P
410447	COMPUTER LABORATORY-II	OR	50	20			36	P
410448	PROJECT	TW	50	20			41	P

FIRST SEM TOTAL = 470/750

B120054443 SHIRGAOKAR ABHISHEK DINESH SANGEETA ,71401179H ,PICT ,B120054443

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	20	38	58	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	28	39	67	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	17	32	49	P
410444D	DATA MINING TECH. & APPL.	PP	100	40	22	38	60	P
410445D	MULTIDISCIPLINARY NLP	PP	100	40	23	47	70	P
410446	COMPUTER LABORATORY-I	PR	50	20			44	P
410446	COMPUTER LABORATORY-I	OR	50	20			46	P
410447	COMPUTER LABORATORY-II	TW	50	20			44	P
410447	COMPUTER LABORATORY-II	OR	50	20			40	P
410448	PROJECT	TW	50	20			44	P

FIRST SEM TOTAL = 522/750

B120054444 SHITAL BAGUL ALKABAI ,71401181K ,PICT ,B120054444

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	23	37	60	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	13	38	51	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	15	28	43	P
410444B	COMP. NET. DESIGN & MODELING	PP	100	40	19	41	60	P
410445B	PERVASIVE COMPUTING	PP	100	40	17	36	53	P
410446	COMPUTER LABORATORY-I	PR	50	20			39	P
410446	COMPUTER LABORATORY-I	OR	50	20			38	P
410447	COMPUTER LABORATORY-II	TW	50	20			41	P
410447	COMPUTER LABORATORY-II	OR	50	20			37	P
410448	PROJECT	TW	50	20			39	P

FIRST SEM TOTAL = 461/750

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120054445 SHIVANI J GUPTA VAISHALI ,71401182H ,PICT ,B120054445

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	15	35	50	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	20	31	51	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	14	28	42	P
410444D	DATA MINING TECH. & APPL.	PP	100	40	17	34	51	P
410445B	PERVASIVE COMPUTING	PP	100	40	13	35	48	P
410446	COMPUTER LABORATORY-I	PR	50	20			38	P
410446	COMPUTER LABORATORY-I	OR	50	20			34	P
410447	COMPUTER LABORATORY-II	TW	50	20			46	P
410447	COMPUTER LABORATORY-II	OR	50	20			42	P
410448	PROJECT	TW	50	20			43	P

FIRST SEM TOTAL = 445/750

B120054446 SHIVANI MAHENDRA SINGH PRAMILA ,71401183F ,PICT ,B120054446

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	16	29	45	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	23	38	61	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	12	39	51	P
410444D	DATA MINING TECH. & APPL.	PP	100	40	22	40	62	P
410445B	PERVASIVE COMPUTING	PP	100	40	14	41	55	P
410446	COMPUTER LABORATORY-I	PR	50	20			39	P
410446	COMPUTER LABORATORY-I	OR	50	20			40	P
410447	COMPUTER LABORATORY-II	TW	50	20			45	P
410447	COMPUTER LABORATORY-II	OR	50	20			41	P
410448	PROJECT	TW	50	20			41	P

FIRST SEM TOTAL = 480/750

B120054447 SHIVGUNDE SAURABH RAVINDRA JAGDEVI ,71401184D ,PICT ,B120054447

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	16	42	58	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	22	40	62	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	17	36	53	P
410444C	ADVANCED COMPUTER PROG.	PP	100	40	24	46	70	P
410445A	PROB. SOLV. WITH GAMIFICATION	PP	100	40	22	37	59	P
410446	COMPUTER LABORATORY-I	PR	50	20			37	P
410446	COMPUTER LABORATORY-I	OR	50	20			35	P
410447	COMPUTER LABORATORY-II	TW	50	20			41	P
410447	COMPUTER LABORATORY-II	OR	50	20			37	P
410448	PROJECT	TW	50	20			44	P

FIRST SEM TOTAL = 496/750

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120054448 SHRADDHA SANJEEV PURNIMA KUMAR ,71401185B ,PICT ,B120054448

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	18	38	56	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	27	32	59	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	19	31	50	P
410444C	ADVANCED COMPUTER PROG.	PP	100	40	21	35	56	P
410445B	PERVASIVE COMPUTING	PP	100	40	14	40	54	P
410446	COMPUTER LABORATORY-I	PR	50	20			41	P
410446	COMPUTER LABORATORY-I	OR	50	20			35	P
410447	COMPUTER LABORATORY-II	TW	50	20			43	P
410447	COMPUTER LABORATORY-II	OR	50	20			39	P
410448	PROJECT	TW	50	20			42	P

FIRST SEM TOTAL = 475/750

B120054449 SHREYAS RAISONI SHOBHA ,71401186L ,PICT ,B120054449

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	25	39	64	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	17	37	54	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	17	28	45	P
410444A	IMAGE PROCESSING	PP	100	40	21	30	51	P
410445A	PROB. SOLV. WITH GAMIFICATION	PP	100	40	20	38	58	P
410446	COMPUTER LABORATORY-I	PR	50	20			38	P
410446	COMPUTER LABORATORY-I	OR	50	20			35	P
410447	COMPUTER LABORATORY-II	TW	50	20			42	P
410447	COMPUTER LABORATORY-II	OR	50	20			38	P
410448	PROJECT	TW	50	20			35	P

FIRST SEM TOTAL = 460/750

B120054450 SHRIYASH PRAVIN INGALE ANURADHA ,71401190J ,PICT ,B120054450

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	20	44	64	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	20	42	62	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	18	39	57	P
410444D	DATA MINING TECH. & APPL.	PP	100	40	16	33	49	P
410445B	PERVASIVE COMPUTING	PP	100	40	17	44	61	P
410446	COMPUTER LABORATORY-I	PR	50	20			38	P
410446	COMPUTER LABORATORY-I	OR	50	20			40	P
410447	COMPUTER LABORATORY-II	TW	50	20			45	P
410447	COMPUTER LABORATORY-II	OR	50	20			41	P
410448	PROJECT	TW	50	20			41	P

FIRST SEM TOTAL = 498/750

BRANCH: 76 B.E. (2012 PAT.)(COMPUTER)

DATE : 02 FEB 2017

CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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B120054451 SHUBHAM DILIP PAMPATTIWAR DEEPA ,71401191G ,PICT ,B120054451

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	24	48	72	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	28	48	76	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	18	42	60	P
410444D	DATA MINING TECH. & APPL.	PP	100	40	23	41	64	P
410445B	PERVASIVE COMPUTING	PP	100	40	18	41	59	P
410446	COMPUTER LABORATORY-I	PR	50	20			30	P
410446	COMPUTER LABORATORY-I	OR	50	20			37	P
410447	COMPUTER LABORATORY-II	TW	50	20			45	P
410447	COMPUTER LABORATORY-II	OR	50	20			41	P
410448	PROJECT	TW	50	20			44	P

FIRST SEM TOTAL = 528/750

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B120054452 SIMRAN SUNIL PATIL RAJASHREE ,71401195K ,PICT ,B120054452

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	17	33	50	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	26	39	65	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	17	37	54	P
410444C	ADVANCED COMPUTER PROG.	PP	100	40	22	37	59	P
410445D	MULTIDISCIPLINARY NLP	PP	100	40	25	43	68	P
410446	COMPUTER LABORATORY-I	PR	50	20			42	P
410446	COMPUTER LABORATORY-I	OR	50	20			40	P
410447	COMPUTER LABORATORY-II	TW	50	20			44	P
410447	COMPUTER LABORATORY-II	OR	50	20			40	P
410448	PROJECT	TW	50	20			42	P

FIRST SEM TOTAL = 504/750

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B120054453 SINGH SHAMLI SUDHIRCHANDRA SUDHA ,71401196H ,PICT ,B120054453

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	14	35	49	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	17	27	--	F
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	12	33	45	P
410444D	DATA MINING TECH. & APPL.	PP	100	40	11	32	43	P
410445D	MULTIDISCIPLINARY NLP	PP	100	40	16	41	57	P
410446	COMPUTER LABORATORY-I	PR	50	20			36	P
410446	COMPUTER LABORATORY-I	OR	50	20			34	P
410447	COMPUTER LABORATORY-II	TW	50	20			44	P
410447	COMPUTER LABORATORY-II	OR	50	20			40	P
410448	PROJECT	TW	50	20			42	P

FIRST SEM TOTAL = --/750

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120054454 SONAR SHUBHADA VIJAY SANJEEVANI ,71401201H ,PICT ,B120054454

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	17	29	46	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	25	42	67	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	14	34	48	P
410444A	IMAGE PROCESSING	PP	100	40	19	36	55	P
410445A	PROB. SOLV. WITH GAMIFICATION	PP	100	40	23	41	64	P
410446	COMPUTER LABORATORY-I	PR	50	20			34	P
410446	COMPUTER LABORATORY-I	OR	50	20			35	P
410447	COMPUTER LABORATORY-II	TW	50	20			46	P
410447	COMPUTER LABORATORY-II	OR	50	20			42	P
410448	PROJECT	TW	50	20			42	P

FIRST SEM TOTAL = 479/750

B120054455 SONAWANE MADHURI NITESH PUSHPALATA ,71401203D ,PICT ,B120054455

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	8	37	45	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	19	29	48	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	15	30	45	P
410444B	COMP. NET. DESIGN & MODELING	PP	100	40	20	45	65	P
410445A	PROB. SOLV. WITH GAMIFICATION	PP	100	40	20	38	58	P
410446	COMPUTER LABORATORY-I	PR	50	20			39	P
410446	COMPUTER LABORATORY-I	OR	50	20			34	P
410447	COMPUTER LABORATORY-II	TW	50	20			42	P
410447	COMPUTER LABORATORY-II	OR	50	20			38	P
410448	PROJECT	TW	50	20			42	P

FIRST SEM TOTAL = 456/750

B120054456 SONAWANE VARUN SHAM RAJSHREE ,71401205L ,PICT ,B120054456

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	15	35	50	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	14	22	--	F
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	14	36	50	P
410444D	DATA MINING TECH. & APPL.	PP	100	40	15	38	53	P
410445A	PROB. SOLV. WITH GAMIFICATION	PP	100	40	21	40	61	P
410446	COMPUTER LABORATORY-I	PR	50	20			38	P
410446	COMPUTER LABORATORY-I	OR	50	20			34	P
410447	COMPUTER LABORATORY-II	TW	50	20			45	P
410447	COMPUTER LABORATORY-II	OR	50	20			41	P
410448	PROJECT	TW	50	20			37	P

FIRST SEM TOTAL = --/750

BRANCH: 76 B.E. (2012 PAT.)(COMPUTER)

DATE : 02 FEB 2017

CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
 OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120054457 SONTAKKE TANAY SHRIPAD MADHURI ,71401206J ,PICT ,B120054457

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	14	32	46	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	16	33	49	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	10	29	--	F
410444D	DATA MINING TECH. & APPL.	PP	100	40	16	30	46	P
410445A	PROB. SOLV. WITH GAMIFICATION	PP	100	40	21	27	--	F
410446	COMPUTER LABORATORY-I	PR	50	20			26	P
410446	COMPUTER LABORATORY-I	OR	50	20			37	P
410447	COMPUTER LABORATORY-II	TW	50	20			46	P
410447	COMPUTER LABORATORY-II	OR	50	20			42	P
410448	PROJECT	TW	50	20			42	P

FIRST SEM TOTAL = --/750

B120054458 SOUMAYA RANJAN SANGEETA ,71401208E ,PICT ,B120054458

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	20	43	63	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	19	43	62	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	24	43	67	P
410444D	DATA MINING TECH. & APPL.	PP	100	40	17	46	63	P
410445A	PROB. SOLV. WITH GAMIFICATION	PP	100	40	21	48	69	P
410446	COMPUTER LABORATORY-I	PR	50	20			39	P
410446	COMPUTER LABORATORY-I	OR	50	20			40	P
410447	COMPUTER LABORATORY-II	TW	50	20			47	P
410447	COMPUTER LABORATORY-II	OR	50	20			44	P
410448	PROJECT	TW	50	20			38	P

FIRST SEM TOTAL = 532/750

B120054459 SUSHANT AMIT MATHUR NANDITA ,71401217D ,PICT ,B120054459

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	21	33	54	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	21	37	58	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	13	35	48	P
410444C	ADVANCED COMPUTER PROG.	PP	100	40	21	40	61	P
410445A	PROB. SOLV. WITH GAMIFICATION	PP	100	40	23	49	72	P
410446	COMPUTER LABORATORY-I	PR	50	20			37	P
410446	COMPUTER LABORATORY-I	OR	50	20			32	P
410447	COMPUTER LABORATORY-II	TW	50	20			42	P
410447	COMPUTER LABORATORY-II	OR	50	20			38	P
410448	PROJECT	TW	50	20			43	P

FIRST SEM TOTAL = 485/750

BRANCH: 76 B.E. (2012 PAT.)(COMPUTER)

DATE : 02 FEB 2017

CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
 OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120054460 SUVIDHI CHAUDHARY SHASHI PRABHA ,71401218B ,PICT ,B120054460

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	23	30	53	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	16	34	50	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	12	30	42	P
410444D	DATA MINING TECH. & APPL.	PP	100	40	15	27	--	F
410445B	PERVASIVE COMPUTING	PP	100	40	15	37	52	P
410446	COMPUTER LABORATORY-I	PR	50	20			36	P
410446	COMPUTER LABORATORY-I	OR	50	20			39	P
410447	COMPUTER LABORATORY-II	TW	50	20			42	P
410447	COMPUTER LABORATORY-II	OR	50	20			38	P
410448	PROJECT	TW	50	20			42	P

FIRST SEM TOTAL = --/750

B120054461 SWATI AGARWAL BABITA ,71401220D ,PICT ,B120054461

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	13	43	56	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	20	45	65	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	22	40	62	P
410444D	DATA MINING TECH. & APPL.	PP	100	40	20	44	64	P
410445A	PROB. SOLV. WITH GAMIFICATION	PP	100	40	18	46	64	P
410446	COMPUTER LABORATORY-I	PR	50	20			44	P
410446	COMPUTER LABORATORY-I	OR	50	20			43	P
410447	COMPUTER LABORATORY-II	TW	50	20			44	P
410447	COMPUTER LABORATORY-II	OR	50	20			40	P
410448	PROJECT	TW	50	20			40	P

FIRST SEM TOTAL = 522/750

B120054462 TADGE APEKSHA ANIL UJWALA ,71500270J ,PICT ,B120054462

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	15	35	50	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	26	54	80	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	18	44	62	P
410444C	ADVANCED COMPUTER PROG.	PP	100	40	27	53	80	P
410445B	PERVASIVE COMPUTING	PP	100	40	20	45	65	P
410446	COMPUTER LABORATORY-I	PR	50	20			40	P
410446	COMPUTER LABORATORY-I	OR	50	20			40	P
410447	COMPUTER LABORATORY-II	TW	50	20			44	P
410447	COMPUTER LABORATORY-II	OR	50	20			40	P
410448	PROJECT	TW	50	20			44	P

FIRST SEM TOTAL = 545/750

BRANCH: 76 B.E. (2012 PAT.)(COMPUTER)

DATE : 02 FEB 2017

CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120054463 TAMBOLI KEDAR SANJEEV NEHA TAMBOLI ,71301268E ,PICT ,B120054463

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	13	23	--	F
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	10	20	--	F
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	12	32	44	P
410444D	DATA MINING TECH. & APPL.	PP	100	40	13	31	44	P
410445A	PROB. SOLV. WITH GAMIFICATION	PP	100	40	22	28	50	P
410446	COMPUTER LABORATORY-I	PR	50	20			25	P
410446	COMPUTER LABORATORY-I	OR	50	20			36	P
410447	COMPUTER LABORATORY-II	TW	50	20			45	P
410447	COMPUTER LABORATORY-II	OR	50	20			41	P
410448	PROJECT	TW	50	20			44	P

FIRST SEM TOTAL = --/750

B120054464 TANMAY SANDEEP MANOLKAR RASHMI ,71401224G ,PICT ,B120054464

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	25	35	60	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	21	44	65	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	16	33	49	P
410444D	DATA MINING TECH. & APPL.	PP	100	40	22	48	70	P
410445B	PERVASIVE COMPUTING	PP	100	40	19	39	58	P
410446	COMPUTER LABORATORY-I	PR	50	20			42	P
410446	COMPUTER LABORATORY-I	OR	50	20			38	P
410447	COMPUTER LABORATORY-II	TW	50	20			45	P
410447	COMPUTER LABORATORY-II	OR	50	20			41	P
410448	PROJECT	TW	50	20			43	P

FIRST SEM TOTAL = 511/750

B120054465 TARU PALLAVI NITIN ARCHANA ,71401229H ,PICT ,B120054465

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	14	36	50	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	18	48	66	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	15	40	55	P
410444C	ADVANCED COMPUTER PROG.	PP	100	40	18	39	57	P
410445B	PERVASIVE COMPUTING	PP	100	40	15	40	55	P
410446	COMPUTER LABORATORY-I	PR	50	20			39	P
410446	COMPUTER LABORATORY-I	OR	50	20			35	P
410447	COMPUTER LABORATORY-II	TW	50	20			45	P
410447	COMPUTER LABORATORY-II	OR	50	20			41	P
410448	PROJECT	TW	50	20			40	P

FIRST SEM TOTAL = 483/750

BRANCH: 76 B.E. (2012 PAT.)(COMPUTER)

DATE : 02 FEB 2017

CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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B120054466 TATIYA PUSHKAR ARUNA ,71401230M ,PICT ,B120054466

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	24	48	72	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	23	38	61	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	22	39	61	P
410444D	DATA MINING TECH. & APPL.	PP	100	40	20	49	69	P
410445D	MULTIDISCIPLINARY NLP	PP	100	40	24	43	67	P
410446	COMPUTER LABORATORY-I	PR	50	20			32	P
410446	COMPUTER LABORATORY-I	OR	50	20			35	P
410447	COMPUTER LABORATORY-II	TW	50	20			44	P
410447	COMPUTER LABORATORY-II	OR	50	20			40	P
410448	PROJECT	TW	50	20			42	P

FIRST SEM TOTAL = 523/750

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B120054467 TAYADE SUSHANT RAMESH NANDINI ,71201094H ,PICT ,B120054467

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	00	AA	--	F
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	AA	AA	--	F
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	15	AA	--	F
410444D	DATA MINING TECH. & APPL.	PP	100	40	AA	AA	--	F
410445A	PROB. SOLV. WITH GAMIFICATION	PP	100	40	AA	AA	--	F
410446	COMPUTER LABORATORY-I	PR	50	20			AA	F
410446	COMPUTER LABORATORY-I	OR	50	20			AA	F
410447	COMPUTER LABORATORY-II	TW	50	20			AA	F
410447	COMPUTER LABORATORY-II	OR	50	20			AA	F
410448	PROJECT	TW	50	20			AA	F

FIRST SEM TOTAL = --/750

TERMWORK FAIL...

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B120054468 TERDALE SHRUTI RAVASAHEB PADMASHRI ,71401231K ,PICT ,B120054468

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	16	46	62	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	28	52	80	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	23	44	67	P
410444D	DATA MINING TECH. & APPL.	PP	100	40	23	56	79	P
410445B	PERVASIVE COMPUTING	PP	100	40	18	47	65	P
410446	COMPUTER LABORATORY-I	PR	50	20			42	P
410446	COMPUTER LABORATORY-I	OR	50	20			38	P
410447	COMPUTER LABORATORY-II	TW	50	20			46	P
410447	COMPUTER LABORATORY-II	OR	50	20			42	P
410448	PROJECT	TW	50	20			41	P

FIRST SEM TOTAL = 562/750

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120054469 THAKUR YASHKUMAR VIVEK JYOTI ,71500271G ,PICT ,B120054469

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	18	29	47	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	24	48	72	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	19	45	64	P
410444C	ADVANCED COMPUTER PROG.	PP	100	40	26	42	68	P
410445B	PERVASIVE COMPUTING	PP	100	40	18	41	59	P
410446	COMPUTER LABORATORY-I	PR	50	20			41	P
410446	COMPUTER LABORATORY-I	OR	50	20			38	P
410447	COMPUTER LABORATORY-II	TW	50	20			44	P
410447	COMPUTER LABORATORY-II	OR	50	20			40	P
410448	PROJECT	TW	50	20			40	P

FIRST SEM TOTAL = 513/750

B120054470 THUBE NEELAM SANTOSH VANDANA ,71401234D ,PICT ,B120054470

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	26	38	64	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	26	40	66	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	12	36	48	P
410444C	ADVANCED COMPUTER PROG.	PP	100	40	19	42	61	P
410445B	PERVASIVE COMPUTING	PP	100	40	13	42	55	P
410446	COMPUTER LABORATORY-I	PR	50	20			40	P
410446	COMPUTER LABORATORY-I	OR	50	20			39	P
410447	COMPUTER LABORATORY-II	TW	50	20			44	P
410447	COMPUTER LABORATORY-II	OR	50	20			40	P
410448	PROJECT	TW	50	20			45	P

FIRST SEM TOTAL = 502/750

B120054471 TIRKHUNDE PRANALI RAMDAS SUREKHA ,71401237J ,PICT ,B120054471

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	18	46	64	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	30	43	73	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	17	36	53	P
410444C	ADVANCED COMPUTER PROG.	PP	100	40	22	39	61	P
410445B	PERVASIVE COMPUTING	PP	100	40	17	44	61	P
410446	COMPUTER LABORATORY-I	PR	50	20			41	P
410446	COMPUTER LABORATORY-I	OR	50	20			39	P
410447	COMPUTER LABORATORY-II	TW	50	20			46	P
410447	COMPUTER LABORATORY-II	OR	50	20			42	P
410448	PROJECT	TW	50	20			42	P

FIRST SEM TOTAL = 522/750

BRANCH: 76 B.E. (2012 PAT.)(COMPUTER)

DATE : 02 FEB 2017

CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
 OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120054472 TOPKAR ANKUSH ANIL VAISHALI ,71401239E ,PICT ,B120054472

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	15	28	43	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	13	28	41	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	12	33	45	P
410444D	DATA MINING TECH. & APPL.	PP	100	40	19	47	66	P
410445B	PERVASIVE COMPUTING	PP	100	40	18	37	55	P
410446	COMPUTER LABORATORY-I	PR	50	20			35	P
410446	COMPUTER LABORATORY-I	OR	50	20			32	P
410447	COMPUTER LABORATORY-II	TW	50	20			42	P
410447	COMPUTER LABORATORY-II	OR	50	20			38	P
410448	PROJECT	TW	50	20			38	P

FIRST SEM TOTAL = 435/750

B120054473 UDIPI KETAKI VISHWANATH SUJATA ,71401242E ,PICT ,B120054473

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	25	46	71	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	22	37	59	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	12	33	45	P
410444A	IMAGE PROCESSING	PP	100	40	20	42	62	P
410445A	PROB. SOLV. WITH GAMIFICATION	PP	100	40	21	57	78	P
410446	COMPUTER LABORATORY-I	PR	50	20			38	P
410446	COMPUTER LABORATORY-I	OR	50	20			37	P
410447	COMPUTER LABORATORY-II	TW	50	20			46	P
410447	COMPUTER LABORATORY-II	OR	50	20			42	P
410448	PROJECT	TW	50	20			41	P

FIRST SEM TOTAL = 519/750

B120054474 UPLAVIKAR PRITISH MILIND SHUBHANGI ,71401245K ,PICT ,B120054474

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	20	28	48	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	25	39	64	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	14	34	48	P
410444D	DATA MINING TECH. & APPL.	PP	100	40	12	42	54	P
410445D	MULTIDISCIPLINARY NLP	PP	100	40	20	39	59	P
410446	COMPUTER LABORATORY-I	PR	50	20			40	P
410446	COMPUTER LABORATORY-I	OR	50	20			40	P
410447	COMPUTER LABORATORY-II	TW	50	20			44	P
410447	COMPUTER LABORATORY-II	OR	50	20			40	P
410448	PROJECT	TW	50	20			43	P

FIRST SEM TOTAL = 480/750

BRANCH: 76 B.E. (2012 PAT.)(COMPUTER)

DATE : 02 FEB 2017

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
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B120054475 VADAJE ROHAN BALIRAM PUSHPA ,71401246H ,PICT ,B120054475

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	22	47	69	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	29	44	73	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	16	37	53	P
410444C	ADVANCED COMPUTER PROG.	PP	100	40	23	38	61	P
410445A	PROB. SOLV. WITH GAMIFICATION	PP	100	40	23	53	76	P
410446	COMPUTER LABORATORY-I	PR	50	20			38	P
410446	COMPUTER LABORATORY-I	OR	50	20			35	P
410447	COMPUTER LABORATORY-II	TW	50	20			41	P
410447	COMPUTER LABORATORY-II	OR	50	20			37	P
410448	PROJECT	TW	50	20			43	P

FIRST SEM TOTAL = 526/750

B120054476 VAHADNE SHIVANI UMESH MANJUSHA ,71401247F ,PICT ,B120054476

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	19	39	58	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	21	40	61	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	19	42	61	P
410444C	ADVANCED COMPUTER PROG.	PP	100	40	27	34	61	P
410445A	PROB. SOLV. WITH GAMIFICATION	PP	100	40	23	57	80	P
410446	COMPUTER LABORATORY-I	PR	50	20			40	P
410446	COMPUTER LABORATORY-I	OR	50	20			39	P
410447	COMPUTER LABORATORY-II	TW	50	20			45	P
410447	COMPUTER LABORATORY-II	OR	50	20			41	P
410448	PROJECT	TW	50	20			37	P

FIRST SEM TOTAL = 523/750

B120054477 VAIDYA SHREYAS SUNIL SHRADDHA ,71401249B ,PICT ,B120054477

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	25	46	71	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	24	45	69	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	16	39	55	P
410444D	DATA MINING TECH. & APPL.	PP	100	40	17	48	65	P
410445D	MULTIDISCIPLINARY NLP	PP	100	40	19	49	68	P
410446	COMPUTER LABORATORY-I	PR	50	20			40	P
410446	COMPUTER LABORATORY-I	OR	50	20			42	P
410447	COMPUTER LABORATORY-II	TW	50	20			44	P
410447	COMPUTER LABORATORY-II	OR	50	20			40	P
410448	PROJECT	TW	50	20			36	P

FIRST SEM TOTAL = 530/750

BRANCH: 76 B.E. (2012 PAT.)(COMPUTER)

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
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B120054478 VARADA SANDEEP HANUMANTE MANISHA ,71401250F ,PICT ,B120054478

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	10	41	51	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	26	39	65	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	15	38	53	P
410444C	ADVANCED COMPUTER PROG.	PP	100	40	22	38	60	P
410445A	PROB. SOLV. WITH GAMIFICATION	PP	100	40	23	53	76	P
410446	COMPUTER LABORATORY-I	PR	50	20			28	P
410446	COMPUTER LABORATORY-I	OR	50	20			38	P
410447	COMPUTER LABORATORY-II	TW	50	20			46	P
410447	COMPUTER LABORATORY-II	OR	50	20			42	P
410448	PROJECT	TW	50	20			44	P

FIRST SEM TOTAL = 503/750

B120054479 VARDAN GANJOO BHARTI BALA GANJOO ,71401251D ,PICT ,B120054479

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	17	30	47	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	13	38	51	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	12	33	45	P
410444D	DATA MINING TECH. & APPL.	PP	100	40	13	47	60	P
410445B	PERVASIVE COMPUTING	PP	100	40	17	36	53	P
410446	COMPUTER LABORATORY-I	PR	50	20			37	P
410446	COMPUTER LABORATORY-I	OR	50	20			35	P
410447	COMPUTER LABORATORY-II	TW	50	20			44	P
410447	COMPUTER LABORATORY-II	OR	50	20			40	P
410448	PROJECT	TW	50	20			38	P

FIRST SEM TOTAL = 450/750

B120054480 VARKHEDE SANKET MURLIDHAR PRATIBHA ,71401252B ,PICT ,B120054480

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	18	51	69	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	24	43	67	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	17	38	55	P
410444B	COMP. NET. DESIGN & MODELING	PP	100	40	25	50	75	P
410445B	PERVASIVE COMPUTING	PP	100	40	14	39	53	P
410446	COMPUTER LABORATORY-I	PR	50	20			42	P
410446	COMPUTER LABORATORY-I	OR	50	20			40	P
410447	COMPUTER LABORATORY-II	TW	50	20			47	P
410447	COMPUTER LABORATORY-II	OR	50	20			43	P
410448	PROJECT	TW	50	20			46	P

FIRST SEM TOTAL = 537/750

BRANCH: 76 B.E. (2012 PAT.)(COMPUTER)

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
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B120054481 VHATKAR UMESH LAXMAN SARUBAI ,71401257C ,PICT ,B120054481

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	14	34	48	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	25	28	53	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	16	30	46	P
410444D	DATA MINING TECH. & APPL.	PP	100	40	14	35	49	P
410445A	PROB. SOLV. WITH GAMIFICATION	PP	100	40	21	53	74	P
410446	COMPUTER LABORATORY-I	PR	50	20			38	P
410446	COMPUTER LABORATORY-I	OR	50	20			38	P
410447	COMPUTER LABORATORY-II	TW	50	20			41	P
410447	COMPUTER LABORATORY-II	OR	50	20			37	P
410448	PROJECT	TW	50	20			41	P

FIRST SEM TOTAL = 465/750

B120054482 VIGHNESH KULKARNI UJWALA ,71401258M ,PICT ,B120054482

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	22	37	59	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	19	38	57	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	15	39	54	P
410444D	DATA MINING TECH. & APPL.	PP	100	40	19	51	70	P
410445A	PROB. SOLV. WITH GAMIFICATION	PP	100	40	21	55	76	P
410446	COMPUTER LABORATORY-I	PR	50	20			35	P
410446	COMPUTER LABORATORY-I	OR	50	20			36	P
410447	COMPUTER LABORATORY-II	TW	50	20			46	P
410447	COMPUTER LABORATORY-II	OR	50	20			42	P
410448	PROJECT	TW	50	20			41	P

FIRST SEM TOTAL = 516/750

B120054483 VISHAL BHANUDAS GHUGE SULOCHANA ,71401261M ,PICT ,B120054483

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	14	28	42	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	26	42	68	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	14	33	47	P
410444C	ADVANCED COMPUTER PROG.	PP	100	40	23	43	66	P
410445A	PROB. SOLV. WITH GAMIFICATION	PP	100	40	20	52	72	P
410446	COMPUTER LABORATORY-I	PR	50	20			40	P
410446	COMPUTER LABORATORY-I	OR	50	20			36	P
410447	COMPUTER LABORATORY-II	TW	50	20			45	P
410447	COMPUTER LABORATORY-II	OR	50	20			41	P
410448	PROJECT	TW	50	20			30	P

FIRST SEM TOTAL = 487/750

BRANCH: 76 B.E. (2012 PAT.)(COMPUTER)

DATE : 02 FEB 2017

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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B120054484 VISHWALI MHASAWADE ROHINI MHASAWADE ,71401263H ,PICT ,B120054484

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	12	36	48	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	19	42	61	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	19	39	58	P
410444D	DATA MINING TECH. & APPL.	PP	100	40	26	48	74	P
410445D	MULTIDISCIPLINARY NLP	PP	100	40	28	45	73	P
410446	COMPUTER LABORATORY-I	PR	50	20			43	P
410446	COMPUTER LABORATORY-I	OR	50	20			43	P
410447	COMPUTER LABORATORY-II	TW	50	20			46	P
410447	COMPUTER LABORATORY-II	OR	50	20			42	P
410448	PROJECT	TW	50	20			41	P

FIRST SEM TOTAL = 529/750

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B120054485 WADHWANI SAYALI DANESHWAR ANITA ,71401265D ,PICT ,B120054485

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	18	38	56	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	23	40	63	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	18	30	48	P
410444C	ADVANCED COMPUTER PROG.	PP	100	40	27	34	61	P
410445A	PROB. SOLV. WITH GAMIFICATION	PP	100	40	24	57	81	P
410446	COMPUTER LABORATORY-I	PR	50	20			38	P
410446	COMPUTER LABORATORY-I	OR	50	20			38	P
410447	COMPUTER LABORATORY-II	TW	50	20			45	P
410447	COMPUTER LABORATORY-II	OR	50	20			41	P
410448	PROJECT	TW	50	20			40	P

FIRST SEM TOTAL = 511/750

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B120054486 WADJE PRAJAKTA PRAKASH KALPANA ,71301299E ,PICT ,B120054486

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	09	13	--	F
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	05	17	--	F
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	13	15	--	F
410444A	IMAGE PROCESSING	PP	100	40	11	15	--	F
410445B	PERVASIVE COMPUTING	PP	100	40	08	10	--	F
410446	COMPUTER LABORATORY-I	PR	50	20			12	F
410446	COMPUTER LABORATORY-I	OR	50	20			32	P
410447	COMPUTER LABORATORY-II	TW	50	20			46	P
410447	COMPUTER LABORATORY-II	OR	50	20			42	P
410448	PROJECT	TW	50	20			35	P

FIRST SEM TOTAL = --/750

BRANCH: 76 B.E. (2012 PAT.)(COMPUTER)

DATE : 02 FEB 2017

CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
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B120054487 WAGH NEERAJ MILIND AMITA ,71401266B ,PICT ,B120054487

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	14	38	52	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	23	39	62	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	18	34	52	P
410444D	DATA MINING TECH. & APPL.	PP	100	40	17	49	66	P
410445D	MULTIDISCIPLINARY NLP	PP	100	40	20	34	54	P
410446	COMPUTER LABORATORY-I	PR	50	20			45	P
410446	COMPUTER LABORATORY-I	OR	50	20			44	P
410447	COMPUTER LABORATORY-II	TW	50	20			45	P
410447	COMPUTER LABORATORY-II	OR	50	20			41	P
410448	PROJECT	TW	50	20			39	P

FIRST SEM TOTAL = 500/750

B120054488 WAGH PAWAN RAMHARI MEERA ,71401267L ,PICT ,B120054488

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	18	45	63	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	26	41	67	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	16	37	53	P
410444C	ADVANCED COMPUTER PROG.	PP	100	40	23	39	62	P
410445A	PROB. SOLV. WITH GAMIFICATION	PP	100	40	22	52	74	P
410446	COMPUTER LABORATORY-I	PR	50	20			37	P
410446	COMPUTER LABORATORY-I	OR	50	20			36	P
410447	COMPUTER LABORATORY-II	TW	50	20			44	P
410447	COMPUTER LABORATORY-II	OR	50	20			40	P
410448	PROJECT	TW	50	20			43	P

FIRST SEM TOTAL = 519/750

B120054489 WAGH SRUSHTI CHANDRAKANT VAISHALI ,71401268J ,PICT ,B120054489

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	18	41	59	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	25	33	58	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	17	38	55	P
410444A	IMAGE PROCESSING	PP	100	40	18	40	58	P
410445C	EMBEDDED SECURITY	PP	100	40	20	36	56	P
410446	COMPUTER LABORATORY-I	PR	50	20			40	P
410446	COMPUTER LABORATORY-I	OR	50	20			39	P
410447	COMPUTER LABORATORY-II	TW	50	20			45	P
410447	COMPUTER LABORATORY-II	OR	50	20			41	P
410448	PROJECT	TW	50	20			43	P

FIRST SEM TOTAL = 494/750

BRANCH: 76 B.E. (2012 PAT.)(COMPUTER)

DATE : 02 FEB 2017

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
 OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120054490 WARUNGASE KSHITIJ BALASAHEB BHARTI ,71401274C ,PICT ,B120054490

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	20	45	65	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	28	44	72	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	18	36	54	P
410444B	COMP. NET. DESIGN & MODELING	PP	100	40	19	44	63	P
410445B	PERVASIVE COMPUTING	PP	100	40	18	39	57	P
410446	COMPUTER LABORATORY-I	PR	50	20			42	P
410446	COMPUTER LABORATORY-I	OR	50	20			36	P
410447	COMPUTER LABORATORY-II	TW	50	20			46	P
410447	COMPUTER LABORATORY-II	OR	50	20			42	P
410448	PROJECT	TW	50	20			44	P

FIRST SEM TOTAL = 521/750

B120054491 WATTAMWAR NACHIKET VIJAY NEHA ,71401275M ,PICT ,B120054491

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	11	29	40	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	12	31	43	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	14	32	46	P
410444B	COMP. NET. DESIGN & MODELING	PP	100	40	21	38	59	P
410445A	PROB. SOLV. WITH GAMIFICATION	PP	100	40	23	54	77	P
410446	COMPUTER LABORATORY-I	PR	50	20			39	P
410446	COMPUTER LABORATORY-I	OR	50	20			41	P
410447	COMPUTER LABORATORY-II	TW	50	20			46	P
410447	COMPUTER LABORATORY-II	OR	50	20			42	P
410448	PROJECT	TW	50	20			41	P

FIRST SEM TOTAL = 474/750

B120054492 YADAV ANKIT RAMKISHAN SERVESH ,71401276K ,PICT ,B120054492

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	16	43	59	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	22	38	60	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	15	37	52	P
410444C	ADVANCED COMPUTER PROG.	PP	100	40	25	34	59	P
410445B	PERVASIVE COMPUTING	PP	100	40	18	33	51	P
410446	COMPUTER LABORATORY-I	PR	50	20			42	P
410446	COMPUTER LABORATORY-I	OR	50	20			42	P
410447	COMPUTER LABORATORY-II	TW	50	20			47	P
410447	COMPUTER LABORATORY-II	OR	50	20			43	P
410448	PROJECT	TW	50	20			40	P

FIRST SEM TOTAL = 495/750

BRANCH: 76 B.E. (2012 PAT.)(COMPUTER)

DATE : 02 FEB 2017

CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
 OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120054493 YAMAJALA SAIKIRAN PARTHASARATHY LAKSHMI ,71401278F ,PICT ,B120054493

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	14	38	52	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	22	37	59	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	15	30	45	P
410444C	ADVANCED COMPUTER PROG.	PP	100	40	21	33	54	P
410445A	PROB. SOLV. WITH GAMIFICATION	PP	100	40	18	46	64	P
410446	COMPUTER LABORATORY-I	PR	50	20			40	P
410446	COMPUTER LABORATORY-I	OR	50	20			38	P
410447	COMPUTER LABORATORY-II	TW	50	20			46	P
410447	COMPUTER LABORATORY-II	OR	50	20			42	P
410448	PROJECT	TW	50	20			44	P

FIRST SEM TOTAL = 484/750

B120054494 YARDI ASHWIN ASHUTOSH ANAGHA ,71401279D ,PICT ,B120054494

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	25	46	71	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	18	37	55	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	15	35	50	P
410444D	DATA MINING TECH. & APPL.	PP	100	40	21	49	70	P
410445A	PROB. SOLV. WITH GAMIFICATION	PP	100	40	20	56	76	P
410446	COMPUTER LABORATORY-I	PR	50	20			40	P
410446	COMPUTER LABORATORY-I	OR	50	20			37	P
410447	COMPUTER LABORATORY-II	TW	50	20			47	P
410447	COMPUTER LABORATORY-II	OR	50	20			43	P
410448	PROJECT	TW	50	20			38	P

FIRST SEM TOTAL = 527/750

B120054495 YASH DHARMAMER SMITA ,71401280H ,PICT ,B120054495

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	24	28	52	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	18	36	54	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	12	35	47	P
410444D	DATA MINING TECH. & APPL.	PP	100	40	14	42	56	P
410445A	PROB. SOLV. WITH GAMIFICATION	PP	100	40	17	52	69	P
410446	COMPUTER LABORATORY-I	PR	50	20			37	P
410446	COMPUTER LABORATORY-I	OR	50	20			36	P
410447	COMPUTER LABORATORY-II	TW	50	20			44	P
410447	COMPUTER LABORATORY-II	OR	50	20			40	P
410448	PROJECT	TW	50	20			39	P

FIRST SEM TOTAL = 474/750

BRANCH: 76 B.E. (2012 PAT.)(COMPUTER)

DATE : 02 FEB 2017

CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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B120054496 YASH NEAL GUPTA POOJA GUPTA ,71401282D ,PICT ,B120054496

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	15	49	64	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	28	48	76	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	19	39	58	P
410444B	COMP. NET. DESIGN & MODELING	PP	100	40	23	51	74	P
410445B	PERVASIVE COMPUTING	PP	100	40	18	40	58	P
410446	COMPUTER LABORATORY-I	PR	50	20			41	P
410446	COMPUTER LABORATORY-I	OR	50	20			42	P
410447	COMPUTER LABORATORY-II	TW	50	20			47	P
410447	COMPUTER LABORATORY-II	OR	50	20			46	P
410448	PROJECT	TW	50	20			44	P

FIRST SEM TOTAL = 550/750

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B120054497 YASH SANDIP KATARIYA ALKA ,71401283B ,PICT ,B120054497

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	22	45	67	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	21	40	61	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	24	36	60	P
410444D	DATA MINING TECH. & APPL.	PP	100	40	24	46	70	P
410445B	PERVASIVE COMPUTING	PP	100	40	19	34	53	P
410446	COMPUTER LABORATORY-I	PR	50	20			42	P
410446	COMPUTER LABORATORY-I	OR	50	20			38	P
410447	COMPUTER LABORATORY-II	TW	50	20			45	P
410447	COMPUTER LABORATORY-II	OR	50	20			41	P
410448	PROJECT	TW	50	20			45	P

FIRST SEM TOTAL = 522/750

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B120054498 YASHGIRI KRISHNAGIRI GOSWAMI BEENA ,71401284L ,PICT ,B120054498

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	24	47	71	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	18	39	57	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	15	38	53	P
410444A	IMAGE PROCESSING	PP	100	40	16	31	47	P
410445A	PROB. SOLV. WITH GAMIFICATION	PP	100	40	21	54	75	P
410446	COMPUTER LABORATORY-I	PR	50	20			35	P
410446	COMPUTER LABORATORY-I	OR	50	20			38	P
410447	COMPUTER LABORATORY-II	TW	50	20			44	P
410447	COMPUTER LABORATORY-II	OR	50	20			40	P
410448	PROJECT	TW	50	20			43	P

FIRST SEM TOTAL = 503/750

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120054499 YEDE MAUSAM RAVINDRA SAPNA ,71401285J ,PICT ,B120054499

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	17	46	63	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	30	43	73	P
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	18	41	59	P
410444D	DATA MINING TECH. & APPL.	PP	100	40	23	52	75	P
410445A	PROB. SOLV. WITH GAMIFICATION	PP	100	40	23	56	79	P
410446	COMPUTER LABORATORY-I	PR	50	20			45	P
410446	COMPUTER LABORATORY-I	OR	50	20			46	P
410447	COMPUTER LABORATORY-II	TW	50	20			47	P
410447	COMPUTER LABORATORY-II	OR	50	20			45	P
410448	PROJECT	TW	50	20			45	P

FIRST SEM TOTAL = 577/750

B120054500 ABNAVE OMKAR RAJENDRA SHOBHA ,71045358C ,PICT ,B120054500

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	14	28	42	P C
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	6	34	40	P C
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	10	30	40	P C
410444A	IMAGE PROCESSING	PP	100	40	9	44	53	P C
410445A	PROB. SOLV. WITH GAMIFICATION	PP	100	40	20	39	59	P C
410446	COMPUTER LABORATORY-I	PR	50	20			30	P C
410446	COMPUTER LABORATORY-I	OR	50	20			30	P C
410447	COMPUTER LABORATORY-II	TW	50	20			36	P C
410447	COMPUTER LABORATORY-II	OR	50	20			37	P C
410448	PROJECT	TW	50	20			36	P C

SEM.:2

410449	SOFT DESIGN METHODO. & TEST.	PP	100	40	8	38	46	P
410450	HIGH PERFORMANCE COMPUTING	PP	100	40	19	35	54	P
410451B	WEB TECHNOLOGY	PP	100	40	12	35	47	P
410452C	MOBILE APPLICATIONS	PP	100	40	14	43	57	P C
410453	COMPUTER LABORATORY-III	PR	50	20			41	P C
410453	COMPUTER LABORATORY-III	OR	50	20			35	P C
410454	COMPUTER LABORATORY-IV	TW	50	20			25	P C
410454	COMPUTER LABORATORY-IV	OR	50	20			37	P C
410455	PROJECT	TW	100	40			90	P C
410455	PROJECT	OR	50	20			45	P C

GRAND TOTAL = 880/1500 , Result : HIGHER SECOND CLASS

B120054501 BANDEWAR AKASH DEVIDAS MANISHA ,71200779C ,PICT ,B120054501

SEM.:1

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	7	33	40	P C
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	10	30\$	40	P C
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	8	32	40	P C
410444D	DATA MINING TECH. & APPL.	PP	100	40	06	23	--	F
410445A	PROB. SOLV. WITH GAMIFICATION	PP	100	40	17	36	53	P C
410446	COMPUTER LABORATORY-I	PR	50	20			25	P C
410446	COMPUTER LABORATORY-I	OR	50	20			22	P C
410447	COMPUTER LABORATORY-II	TW	50	20			24	P C
410447	COMPUTER LABORATORY-II	OR	50	20			21	P C
410448	PROJECT	TW	50	20			42	P C

SEM.:2

410449	SOFT DESIGN METHODO. & TEST.	PP	100	40	10	30	40	P C
410450	HIGH PERFORMANCE COMPUTING	PP	100	40	15	34	49	P
410451C	CLOUD COMPUTING	PP	100	40	12	AA	--	F
410452A	BUSINESS ANALYTIC & INTEL.	PP	100	40	12	AA	--	F
410453	COMPUTER LABORATORY-III	PR	50	20			34	P C
410453	COMPUTER LABORATORY-III	OR	50	20			26	P C
410454	COMPUTER LABORATORY-IV	TW	50	20			35	P C
410454	COMPUTER LABORATORY-IV	OR	50	20			37	P C
410455	PROJECT	TW	100	40			83	P C
410455	PROJECT	OR	50	20			40	P C

GRAND TOTAL = --/1500 , Result : FAILS [\$ 0.1]

BRANCH: 76 B.E. (2012 PAT.)(COMPUTER)

DATE : 02 FEB 2017

CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120054502 KAMBLE PRASHANT RAMESH SANGITA ,71300996K ,PICT ,B120054502

SEM.:1

SEM.:2

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	12	28	40	P	C	410449	SOFT DESIGN METHODO. & TEST.	PP	100	40	14	32	46	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	13	28	41	P	C	410450	HIGH PERFORMANCE COMPUTING	PP	100	40	15	28	43	P C
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	9	32	41	P	C	410451A	MOBILE COMPUTING	PP	100	40	13	28	41	P C
410444D	DATA MINING TECH. & APPL.	PP	100	40	12	42	54	P		410452C	MOBILE APPLICATIONS	PP	100	40	13	32	45	P C
410445A	PROB. SOLV. WITH GAMIFICATION	PP	100	40	14	30	44	P	C	410453	COMPUTER LABORATORY-III	PR	50	20			38	P C
410446	COMPUTER LABORATORY-I	PR	50	20			40	P	C	410453	COMPUTER LABORATORY-III	OR	50	20			33	P C
410446	COMPUTER LABORATORY-I	OR	50	20			31	P	C	410454	COMPUTER LABORATORY-IV	TW	50	20			35	P C
410447	COMPUTER LABORATORY-II	TW	50	20			38	P	C	410454	COMPUTER LABORATORY-IV	OR	50	20			37	P C
410447	COMPUTER LABORATORY-II	OR	50	20			35	P	C	410455	PROJECT	TW	100	40			91	P C
410448	PROJECT	TW	50	20			42	P	C	410455	PROJECT	OR	50	20			41	P C

GRAND TOTAL = 856/1500 , Result : HIGHER SECOND CLASS

B120054505 KHALID RAZA KHAN AYESHA ,70925473D ,PICT ,B120054505

SEM.:1

SEM.:2

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	11	35	46	P	C	410449	SOFT DESIGN METHODO. & TEST.	PP	100	40	03	28	--	F
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	11	29\$	40	P	C	410450	HIGH PERFORMANCE COMPUTING	PP	100	40	18	28	46	P C
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	10	40	50	P	C	410451D	CYBER SECURITY	PP	100	40	11	29\$	40	P C
410444D	DATA MINING TECH. & APPL.	PP	100	40	14	30	44	P	C	410452C	MOBILE APPLICATIONS	PP	100	40	13	41	54	P C
410445A	PROB. SOLV. WITH GAMIFICATION	PP	100	40	13	36	49	P	C	410453	COMPUTER LABORATORY-III	PR	50	20			32	P C
410446	COMPUTER LABORATORY-I	PR	50	20			20\$	P	C	410453	COMPUTER LABORATORY-III	OR	50	20			32	P C
410446	COMPUTER LABORATORY-I	OR	50	20			21	P	C	410454	COMPUTER LABORATORY-IV	TW	50	20			33	P C
410447	COMPUTER LABORATORY-II	TW	50	20			32	P	C	410454	COMPUTER LABORATORY-IV	OR	50	20			35	P C
410447	COMPUTER LABORATORY-II	OR	50	20			32	P	C	410455	PROJECT	TW	100	40			41	P C
410448	PROJECT	TW	50	20			20	P	C	410455	PROJECT	OR	50	20			20	P C

GRAND TOTAL = --/1500 , Result : FAILS [\$ 0.1]

B120054506 NIRMAL ULLAS VRINDA ULLAS ,71301107G ,PICT ,B120054506

SEM.:1

SEM.:2

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	7	39	46	P		410449	SOFT DESIGN METHODO. & TEST.	PP	100	40	8	32\$	40	P
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	6	36	42	P		410450	HIGH PERFORMANCE COMPUTING	PP	100	40	12	33	45	P C
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	9	31	40	P	C	410451A	MOBILE COMPUTING	PP	100	40	14	28\$	42	P C
410444C	ADVANCED COMPUTER PROG.	PP	100	40	10	35	45	P		410452A	BUSINESS ANALYTIC & INTEL.	PP	100	40	08	28	--	F
410445D	MULTIDISCIPLINARY NLP	PP	100	40	7	33\$	40	P		410453	COMPUTER LABORATORY-III	PR	50	20			30	P
410446	COMPUTER LABORATORY-I	PR	50	20			36	P	C	410453	COMPUTER LABORATORY-III	OR	50	20			32	P
410446	COMPUTER LABORATORY-I	OR	50	20			32	P	C	410454	COMPUTER LABORATORY-IV	TW	50	20			40	P
410447	COMPUTER LABORATORY-II	TW	50	20			23	P	C	410454	COMPUTER LABORATORY-IV	OR	50	20			37	P
410447	COMPUTER LABORATORY-II	OR	50	20			20	P	C	410455	PROJECT	TW	100	40			90	P C
410448	PROJECT	TW	50	20			40	P	C	410455	PROJECT	OR	50	20			39	P C

GRAND TOTAL = --/1500 , Result : FAILS [\$ 0.1]

BRANCH: 76 B.E. (2012 PAT.)(COMPUTER)

DATE : 02 FEB 2017

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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B120054507 SANAP KUMUDINI POPAT VINITA ,71301202B ,PICT ,B120054507

SEM.:1

SEM.:2

410441	DESIGN & ANALYSIS OF ALGO.	PP	100	40	6	34	40	P	C	410449	SOFT DESIGN METHODO. & TEST.	PP	100	40	12	29	41	P	C
410442	PRIN. OF MODERN COML. DESIGN	PP	100	40	10	30	40	P	C	410450	HIGH PERFORMANCE COMPUTING	PP	100	40	16	28\$	44	P	C
410443	SMART SYSTEM DESIGN & APPL.	PP	100	40	8	32\$	40	P		410451C	CLOUD COMPUTING	PP	100	40	11	29\$	40	P	C
410444A	IMAGE PROCESSING	PP	100	40	7	34	41	P	C	410452C	MOBILE APPLICATIONS	PP	100	40	9	34	43	P	C
410445B	PERVASIVE COMPUTING	PP	100	40	16	31	47	P	C	410453	COMPUTER LABORATORY-III	PR	50	20			36	P	C
410446	COMPUTER LABORATORY-I	PR	50	20			40	P	C	410453	COMPUTER LABORATORY-III	OR	50	20			37	P	C
410446	COMPUTER LABORATORY-I	OR	50	20			33	P	C	410454	COMPUTER LABORATORY-IV	TW	50	20			42	P	C
410447	COMPUTER LABORATORY-II	TW	50	20			30	P	C	410454	COMPUTER LABORATORY-IV	OR	50	20			35	P	C
410447	COMPUTER LABORATORY-II	OR	50	20			27	P	C	410455	PROJECT	TW	100	40			75	P	C
410448	PROJECT	TW	50	20			27	P	C	410455	PROJECT	OR	50	20			27	P	C

GRAND TOTAL = 785/1500 , Result : SECOND CLASS [\$ 0.1]

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BRANCH: 90 B.E. (2012 PAT.)(INFORMATION TECHNOLOGY)

DATE : 02 FEB 2017

CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120058501 AADITYA SURYAKIRAN WAGHANNA SHALAKA ,71400677H ,PICT ,B120058501

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	12	32	44	P
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	09	20	--	F
414455	MACHINE LEARNING	PP	100	40	06	26	--	F
414456E	CLOUD COMPUTING	PP	100	40	22	32	54	P
414457A	BUSINESS INTELLIGENCE	PP	100	40	15	34	49	P
414458	SOFTWARE LABORATORY -III	TW	50	20			42	P
414458	SOFTWARE LABORATORY -III	OR	50	20			40	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			38	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			40	P
414460	PROJECT PHASE I	TW	50	20			46	P

FIRST SEM TOTAL = --/750

B120058502 AAYUSHI RITA ,71400680H ,PICT ,B120058502

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	19	41	60	P
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	23	44	67	P
414455	MACHINE LEARNING	PP	100	40	20	45	65	P
414456B	USABILITY ENGINEERING	PP	100	40	15	50	65	P
414457A	BUSINESS INTELLIGENCE	PP	100	40	18	43	61	P
414458	SOFTWARE LABORATORY -III	TW	50	20			46	P
414458	SOFTWARE LABORATORY -III	OR	50	20			42	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			40	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			38	P
414460	PROJECT PHASE I	TW	50	20			44	P

FIRST SEM TOTAL = 528/750

B120058503 ABHANGRAO SEJAL SUHAS SUVARNA ,71400683B ,PICT ,B120058503

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	25	44	69	P
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	22	40	62	P
414455	MACHINE LEARNING	PP	100	40	22	46	68	P
414456B	USABILITY ENGINEERING	PP	100	40	23	33	56	P
414457A	BUSINESS INTELLIGENCE	PP	100	40	23	50	73	P
414458	SOFTWARE LABORATORY -III	TW	50	20			46	P
414458	SOFTWARE LABORATORY -III	OR	50	20			42	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			36	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			38	P
414460	PROJECT PHASE I	TW	50	20			41	P

FIRST SEM TOTAL = 531/750

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120058504 ABHINAV SHARAD SHIRORE VIJAYA ,71400684L ,PICT ,B120058504

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	15	35	50	P
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	13	34	47	P
414455	MACHINE LEARNING	PP	100	40	13	41	54	P
414456E	CLOUD COMPUTING	PP	100	40	18	36	54	P
414457A	BUSINESS INTELLIGENCE	PP	100	40	20	33	53	P
414458	SOFTWARE LABORATORY -III	TW	50	20			42	P
414458	SOFTWARE LABORATORY -III	OR	50	20			39	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			43	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			40	P
414460	PROJECT PHASE I	TW	50	20			45	P

FIRST SEM TOTAL = 467/750

B120058505 AGRAWAL NIHAL NARAYANDAS SUNITA ,71400695F ,PICT ,B120058505

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	16	37	53	P
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	13	34	47	P
414455	MACHINE LEARNING	PP	100	40	19	32	51	P
414456B	USABILITY ENGINEERING	PP	100	40	22	39	61	P
414457A	BUSINESS INTELLIGENCE	PP	100	40	16	38	54	P
414458	SOFTWARE LABORATORY -III	TW	50	20			43	P
414458	SOFTWARE LABORATORY -III	OR	50	20			37	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			39	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			38	P
414460	PROJECT PHASE I	TW	50	20			42	P

FIRST SEM TOTAL = 465/750

B120058506 AMIT GUPTA SHAILA GUPTA ,71400709K ,PICT ,B120058506

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	17	38	55	P
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	13	30	43	P
414455	MACHINE LEARNING	PP	100	40	21	30	51	P
414456E	CLOUD COMPUTING	PP	100	40	22	38	60	P
414457A	BUSINESS INTELLIGENCE	PP	100	40	25	40	65	P
414458	SOFTWARE LABORATORY -III	TW	50	20			45	P
414458	SOFTWARE LABORATORY -III	OR	50	20			38	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			41	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			37	P
414460	PROJECT PHASE I	TW	50	20			40	P

FIRST SEM TOTAL = 475/750

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120058507 AMRITKAR SHARAYU PRAKASH KALPANA ,71400712K ,PICT ,B120058507

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	19	44	63	P
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	21	51	72	P
414455	MACHINE LEARNING	PP	100	40	22	28	50	P
414456E	CLOUD COMPUTING	PP	100	40	23	45	68	P
414457A	BUSINESS INTELLIGENCE	PP	100	40	21	39	60	P
414458	SOFTWARE LABORATORY -III	TW	50	20			45	P
414458	SOFTWARE LABORATORY -III	OR	50	20			44	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			35	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			30	P
414460	PROJECT PHASE I	TW	50	20			45	P

FIRST SEM TOTAL = 512/750

B120058508 ANMOL RASTOGI NIDHI RASTOGI ,71400719G ,PICT ,B120058508

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	21	37	58	P
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	19	32	51	P
414455	MACHINE LEARNING	PP	100	40	19	34	53	P
414456E	CLOUD COMPUTING	PP	100	40	20	40	60	P
414457A	BUSINESS INTELLIGENCE	PP	100	40	17	28	45	P
414458	SOFTWARE LABORATORY -III	TW	50	20			42	P
414458	SOFTWARE LABORATORY -III	OR	50	20			38	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			38	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			36	P
414460	PROJECT PHASE I	TW	50	20			44	P

FIRST SEM TOTAL = 465/750

B120058509 ANURAG AGRAWAL PUSHPA ,71400721J ,PICT ,B120058509

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	19	37	56	P
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	19	30	49	P
414455	MACHINE LEARNING	PP	100	40	19	20	--	F
414456E	CLOUD COMPUTING	PP	100	40	17	32	49	P
414457A	BUSINESS INTELLIGENCE	PP	100	40	17	28	45	P
414458	SOFTWARE LABORATORY -III	TW	50	20			43	P
414458	SOFTWARE LABORATORY -III	OR	50	20			40	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			38	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			37	P
414460	PROJECT PHASE I	TW	50	20			45	P

FIRST SEM TOTAL = --/750

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120058510 ANUSHREE GULATI ANSHULA GULATI ,71400722G ,PICT ,B120058510

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	24	44	68	P
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	25	44	69	P
414455	MACHINE LEARNING	PP	100	40	23	44	67	P
414456B	USABILITY ENGINEERING	PP	100	40	23	41	64	P
414457A	BUSINESS INTELLIGENCE	PP	100	40	22	37	59	P
414458	SOFTWARE LABORATORY -III	TW	50	20			46	P
414458	SOFTWARE LABORATORY -III	OR	50	20			45	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			44	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			40	P
414460	PROJECT PHASE I	TW	50	20			43	P

FIRST SEM TOTAL = 545/750

B120058511 ATHARVA KISHOR JAISWAL VANDANA ,71400728F ,PICT ,B120058511

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	21	43	64	P
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	19	33	52	P
414455	MACHINE LEARNING	PP	100	40	19	39	58	P
414456E	CLOUD COMPUTING	PP	100	40	22	42	64	P
414457A	BUSINESS INTELLIGENCE	PP	100	40	20	35	55	P
414458	SOFTWARE LABORATORY -III	TW	50	20			44	P
414458	SOFTWARE LABORATORY -III	OR	50	20			38	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			45	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			40	P
414460	PROJECT PHASE I	TW	50	20			42	P

FIRST SEM TOTAL = 502/750

B120058512 AVINASH SONAWANE SOJARBAI ,71045378H ,PICT ,B120058512

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	8	42	50	P
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	14	30	44	P
414455	MACHINE LEARNING	PP	100	40	23	37	60	P
414456B	USABILITY ENGINEERING	PP	100	40	19	39	58	P
414457A	BUSINESS INTELLIGENCE	PP	100	40	21	31	52	P
414458	SOFTWARE LABORATORY -III	TW	50	20			38	P
414458	SOFTWARE LABORATORY -III	OR	50	20			32	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			35	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			36	P
414460	PROJECT PHASE I	TW	50	20			43	P

FIRST SEM TOTAL = 448/750

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120058513 BAHURSETTIWAR ANTARA SANJAY SADHANA ,71400738C ,PICT ,B120058513

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	21	41	62	P
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	23	37	60	P
414455	MACHINE LEARNING	PP	100	40	26	48	74	P
414456E	CLOUD COMPUTING	PP	100	40	24	48	72	P
414457A	BUSINESS INTELLIGENCE	PP	100	40	21	38	59	P
414458	SOFTWARE LABORATORY -III	TW	50	20			47	P
414458	SOFTWARE LABORATORY -III	OR	50	20			45	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			41	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			37	P
414460	PROJECT PHASE I	TW	50	20			46	P

FIRST SEM TOTAL = 543/750

B120058514 BAMB NAMRATA VINOD SANGITA ,71400746D ,PICT ,B120058514

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	20	53	73	P
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	24	38	62	P
414455	MACHINE LEARNING	PP	100	40	25	50	75	P
414456E	CLOUD COMPUTING	PP	100	40	22	50	72	P
414457A	BUSINESS INTELLIGENCE	PP	100	40	23	34	57	P
414458	SOFTWARE LABORATORY -III	TW	50	20			46	P
414458	SOFTWARE LABORATORY -III	OR	50	20			45	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			44	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			45	P
414460	PROJECT PHASE I	TW	50	20			44	P

FIRST SEM TOTAL = 563/750

B120058515 BARULE SAMEER SANJAY SONIYA ,71400752J ,PICT ,B120058515

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	11	36	47	P
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	20	38	58	P
414455	MACHINE LEARNING	PP	100	40	14	31	45	P
414456E	CLOUD COMPUTING	PP	100	40	13	34	47	P
414457A	BUSINESS INTELLIGENCE	PP	100	40	20	38	58	P
414458	SOFTWARE LABORATORY -III	TW	50	20			42	P
414458	SOFTWARE LABORATORY -III	OR	50	20			38	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			20	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			25	P
414460	PROJECT PHASE I	TW	50	20			40	P

FIRST SEM TOTAL = 420/750

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120058516 BHAGAT PRIYANKA SURYAKANT SHEELPRABHA ,71400760K ,PICT ,B120058516

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	23	57	80	P
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	23	44	67	P
414455	MACHINE LEARNING	PP	100	40	27	37	64	P
414456E	CLOUD COMPUTING	PP	100	40	24	57	81	P
414457A	BUSINESS INTELLIGENCE	PP	100	40	24	47	71	P
414458	SOFTWARE LABORATORY -III	TW	50	20			46	P
414458	SOFTWARE LABORATORY -III	OR	50	20			45	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			39	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			38	P
414460	PROJECT PHASE I	TW	50	20			43	P

FIRST SEM TOTAL = 574/750

B120058517 BHANDARE ASHWIN SHRIKANT PRAJAKTA ,71400763D ,PICT ,B120058517

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	16	36	52	P
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	18	28	46	P
414455	MACHINE LEARNING	PP	100	40	21	27	--	F
414456E	CLOUD COMPUTING	PP	100	40	22	30	52	P
414457A	BUSINESS INTELLIGENCE	PP	100	40	19	32	51	P
414458	SOFTWARE LABORATORY -III	TW	50	20			44	P
414458	SOFTWARE LABORATORY -III	OR	50	20			42	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			30	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			35	P
414460	PROJECT PHASE I	TW	50	20			44	P

FIRST SEM TOTAL = --/750

B120058518 BHANGALE PIYUSH DIGAMBAR MEENAKSHI ,71400764B ,PICT ,B120058518

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	17	39	56	P
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	08	14	--	F
414455	MACHINE LEARNING	PP	100	40	16	23	--	F
414456E	CLOUD COMPUTING	PP	100	40	16	31	47	P
414457A	BUSINESS INTELLIGENCE	PP	100	40	18	28	46	P
414458	SOFTWARE LABORATORY -III	TW	50	20			AA	F
414458	SOFTWARE LABORATORY -III	OR	50	20			AA	F
414459	SOFTWARE LABORATORY -IV	PR	50	20			AA	F
414459	SOFTWARE LABORATORY -IV	OR	50	20			AA	F
414460	PROJECT PHASE I	TW	50	20			38	P

FIRST SEM TOTAL = --/750

TERMWORK FAIL...

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120058519 BHINGARE TEJAL VINAYAK SUCHITA ,71400767G ,PICT ,B120058519

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	14	39	53	P
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	17	33	50	P
414455	MACHINE LEARNING	PP	100	40	24	35	59	P
414456E	CLOUD COMPUTING	PP	100	40	20	40	60	P
414457A	BUSINESS INTELLIGENCE	PP	100	40	22	30	52	P
414458	SOFTWARE LABORATORY -III	TW	50	20			44	P
414458	SOFTWARE LABORATORY -III	OR	50	20			42	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			36	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			39	P
414460	PROJECT PHASE I	TW	50	20			44	P

FIRST SEM TOTAL = 479/750

B120058520 BHINTADE ROHIT RAMDAS SHARADA ,71400768E ,PICT ,B120058520

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	19	46	65	P
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	19	32	51	P
414455	MACHINE LEARNING	PP	100	40	23	35	58	P
414456E	CLOUD COMPUTING	PP	100	40	19	35	54	P
414457A	BUSINESS INTELLIGENCE	PP	100	40	22	34	56	P
414458	SOFTWARE LABORATORY -III	TW	50	20			46	P
414458	SOFTWARE LABORATORY -III	OR	50	20			40	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			40	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			40	P
414460	PROJECT PHASE I	TW	50	20			46	P

FIRST SEM TOTAL = 496/750

B120058521 BHOSALE INDRAJIT MALOJI SADHANA ,71400770G ,PICT ,B120058521

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	15	38	53	P
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	15	21	--	F
414455	MACHINE LEARNING	PP	100	40	13	31	44	P
414456E	CLOUD COMPUTING	PP	100	40	20	48	68	P
414457A	BUSINESS INTELLIGENCE	PP	100	40	17	40	57	P
414458	SOFTWARE LABORATORY -III	TW	50	20			44	P
414458	SOFTWARE LABORATORY -III	OR	50	20			36	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			35	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			36	P
414460	PROJECT PHASE I	TW	50	20			45	P

FIRST SEM TOTAL = --/750

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120058522 BIRAJDAR PRASHANT SOMNATH MAHANANDA ,71400774K ,PICT ,B120058522

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	25	47	72	P
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	23	42	65	P
414455	MACHINE LEARNING	PP	100	40	25	42	67	P
414456E	CLOUD COMPUTING	PP	100	40	21	44	65	P
414457A	BUSINESS INTELLIGENCE	PP	100	40	22	50	72	P
414458	SOFTWARE LABORATORY -III	TW	50	20			43	P
414458	SOFTWARE LABORATORY -III	OR	50	20			30	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			35	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			36	P
414460	PROJECT PHASE I	TW	50	20			43	P

FIRST SEM TOTAL = 528/750

B120058523 CHOKHAR PRITHVIRAJ BALASAHEB VIMAL ,71100770F ,PICT ,B120058523

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	20	29	49	P
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	12	11	--	F
414455	MACHINE LEARNING	PP	100	40	12	17	--	F
414456E	CLOUD COMPUTING	PP	100	40	21	31	52	P
414457A	BUSINESS INTELLIGENCE	PP	100	40	17	28	45	P
414458	SOFTWARE LABORATORY -III	TW	50	20			45	P
414458	SOFTWARE LABORATORY -III	OR	50	20			30	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			25	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			36	P
414460	PROJECT PHASE I	TW	50	20			44	P

FIRST SEM TOTAL = --/750

B120058524 DAKHANE ADITEE DNYANESHWAR SHALINI ,71400795B ,PICT ,B120058524

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	24	45	69	P
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	18	33	51	P
414455	MACHINE LEARNING	PP	100	40	22	38	60	P
414456E	CLOUD COMPUTING	PP	100	40	17	36	53	P
414457A	BUSINESS INTELLIGENCE	PP	100	40	23	40	63	P
414458	SOFTWARE LABORATORY -III	TW	50	20			46	P
414458	SOFTWARE LABORATORY -III	OR	50	20			40	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			42	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			40	P
414460	PROJECT PHASE I	TW	50	20			43	P

FIRST SEM TOTAL = 507/750

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120058525 DARUWALA OBED LAMUEL MANJUSHA ,71400799E ,PICT ,B120058525

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	24	38	62	P
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	22	35	57	P
414455	MACHINE LEARNING	PP	100	40	25	34	59	P
414456B	USABILITY ENGINEERING	PP	100	40	24	47	71	P
414457A	BUSINESS INTELLIGENCE	PP	100	40	23	43	66	P
414458	SOFTWARE LABORATORY -III	TW	50	20			46	P
414458	SOFTWARE LABORATORY -III	OR	50	20			42	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			39	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			38	P
414460	PROJECT PHASE I	TW	50	20			44	P

FIRST SEM TOTAL = 524/750

B120058526 DAS TANISHA TARUNKUMAR MOUSUMI ,71400800B ,PICT ,B120058526

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	22	36	58	P
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	21	30	51	P
414455	MACHINE LEARNING	PP	100	40	14	33	47	P
414456E	CLOUD COMPUTING	PP	100	40	21	33	54	P
414457A	BUSINESS INTELLIGENCE	PP	100	40	18	30	48	P
414458	SOFTWARE LABORATORY -III	TW	50	20			44	P
414458	SOFTWARE LABORATORY -III	OR	50	20			38	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			36	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			39	P
414460	PROJECT PHASE I	TW	50	20			30	P

FIRST SEM TOTAL = 445/750

B120058527 DATAR GAYATRI PRADIP SAVITA ,71400801L ,PICT ,B120058527

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	16	42	58	P
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	12	19	--	F
414455	MACHINE LEARNING	PP	100	40	16	20	--	F
414456E	CLOUD COMPUTING	PP	100	40	14	37	51	P
414457A	BUSINESS INTELLIGENCE	PP	100	40	14	28	42	P
414458	SOFTWARE LABORATORY -III	TW	50	20			40	P
414458	SOFTWARE LABORATORY -III	OR	50	20			32	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			32	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			35	P
414460	PROJECT PHASE I	TW	50	20			30	P

FIRST SEM TOTAL = --/750

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120058528 DATE ANKITA SHARAD MADHAVI ,71500272E ,PICT ,B120058528

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	24	49	73	P
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	19	44	63	P
414455	MACHINE LEARNING	PP	100	40	23	47	70	P
414456E	CLOUD COMPUTING	PP	100	40	24	47	71	P
414457A	BUSINESS INTELLIGENCE	PP	100	40	27	51	78	P
414458	SOFTWARE LABORATORY -III	TW	50	20			45	P
414458	SOFTWARE LABORATORY -III	OR	50	20			46	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			42	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			40	P
414460	PROJECT PHASE I	TW	50	20			44	P

FIRST SEM TOTAL = 572/750

B120058529 DEEPTI GODSE NAYNA ,71400805C ,PICT ,B120058529

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	21	47	68	P
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	18	53	71	P
414455	MACHINE LEARNING	PP	100	40	24	51	75	P
414456E	CLOUD COMPUTING	PP	100	40	24	47	71	P
414457A	BUSINESS INTELLIGENCE	PP	100	40	25	50	75	P
414458	SOFTWARE LABORATORY -III	TW	50	20			45	P
414458	SOFTWARE LABORATORY -III	OR	50	20			42	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			30	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			29	P
414460	PROJECT PHASE I	TW	50	20			40	P

FIRST SEM TOTAL = 546/750

B120058530 DESHMUKH AJAY RATNADEEP VANDANA ,71400807K ,PICT ,B120058530

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	19	43	62	P
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	14	48	62	P
414455	MACHINE LEARNING	PP	100	40	23	40	63	P
414456E	CLOUD COMPUTING	PP	100	40	20	40	60	P
414457A	BUSINESS INTELLIGENCE	PP	100	40	18	32	50	P
414458	SOFTWARE LABORATORY -III	TW	50	20			45	P
414458	SOFTWARE LABORATORY -III	OR	50	20			40	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			45	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			45	P
414460	PROJECT PHASE I	TW	50	20			46	P

FIRST SEM TOTAL = 518/750

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120058531 DESHPANDE SUPREET SHRIPAD PRADNYA ,71400815L ,PICT ,B120058531

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	18	52	70	P
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	20	45	65	P
414455	MACHINE LEARNING	PP	100	40	16	27	--	F
414456E	CLOUD COMPUTING	PP	100	40	22	31	53	P
414457A	BUSINESS INTELLIGENCE	PP	100	40	21	47	68	P
414458	SOFTWARE LABORATORY -III	TW	50	20			40	P
414458	SOFTWARE LABORATORY -III	OR	50	20			35	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			38	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			39	P
414460	PROJECT PHASE I	TW	50	20			45	P

FIRST SEM TOTAL = --/750

B120058532 DIXIT SHITAL BHARAT SULOCHANA ,71500273C ,PICT ,B120058532

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	20	59	79	P
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	19	60	79	P
414455	MACHINE LEARNING	PP	100	40	28	39	67	P
414456E	CLOUD COMPUTING	PP	100	40	25	56	81	P
414457A	BUSINESS INTELLIGENCE	PP	100	40	25	57	82	P
414458	SOFTWARE LABORATORY -III	TW	50	20			45	P
414458	SOFTWARE LABORATORY -III	OR	50	20			45	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			30	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			29	P
414460	PROJECT PHASE I	TW	50	20			41	P

FIRST SEM TOTAL = 578/750

B120058533 DURGUDE SAYALI SUNIL ASHWINI ,71500274M ,PICT ,B120058533

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	25	56	81	P
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	21	53	74	P
414455	MACHINE LEARNING	PP	100	40	24	33	57	P
414456E	CLOUD COMPUTING	PP	100	40	24	49	73	P
414457A	BUSINESS INTELLIGENCE	PP	100	40	27	54	81	P
414458	SOFTWARE LABORATORY -III	TW	50	20			45	P
414458	SOFTWARE LABORATORY -III	OR	50	20			38	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			32	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			35	P
414460	PROJECT PHASE I	TW	50	20			41	P

FIRST SEM TOTAL = 557/750

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120058534 GAIKWAD PIYUSH DEEPAK ANURADHA ,71400836C ,PICT ,B120058534

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	16	35	51	P
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	8	43	51	P
414455	MACHINE LEARNING	PP	100	40	16	29	45	P
414456E	CLOUD COMPUTING	PP	100	40	18	34	52	P
414457A	BUSINESS INTELLIGENCE	PP	100	40	17	41	58	P
414458	SOFTWARE LABORATORY -III	TW	50	20			42	P
414458	SOFTWARE LABORATORY -III	OR	50	20			35	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			35	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			30	P
414460	PROJECT PHASE I	TW	50	20			42	P

FIRST SEM TOTAL = 441/750

B120058535 GANDHI YASH SANJOG MONIKA ,71400840M ,PICT ,B120058535

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	21	38	59	P
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	22	41	63	P
414455	MACHINE LEARNING	PP	100	40	24	43	67	P
414456E	CLOUD COMPUTING	PP	100	40	24	39	63	P
414457A	BUSINESS INTELLIGENCE	PP	100	40	23	50	73	P
414458	SOFTWARE LABORATORY -III	TW	50	20			47	P
414458	SOFTWARE LABORATORY -III	OR	50	20			47	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			47	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			45	P
414460	PROJECT PHASE I	TW	50	20			47	P

FIRST SEM TOTAL = 558/750

B120058536 GHENAND SHWETA SATISH VANDANA ,71500275K ,PICT ,B120058536

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	25	54	79	P
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	23	51	74	P
414455	MACHINE LEARNING	PP	100	40	24	47	71	P
414456E	CLOUD COMPUTING	PP	100	40	21	49	70	P
414457A	BUSINESS INTELLIGENCE	PP	100	40	26	55	81	P
414458	SOFTWARE LABORATORY -III	TW	50	20			46	P
414458	SOFTWARE LABORATORY -III	OR	50	20			44	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			32	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			34	P
414460	PROJECT PHASE I	TW	50	20			43	P

FIRST SEM TOTAL = 574/750

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120058537 GHONGADE KIRAN PRAMOD VAISHALI ,71400847J ,PICT ,B120058537

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	20	46	66	P
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	14	55	69	P
414455	MACHINE LEARNING	PP	100	40	19	35	54	P
414456B	USABILITY ENGINEERING	PP	100	40	18	43	61	P
414457A	BUSINESS INTELLIGENCE	PP	100	40	20	51	71	P
414458	SOFTWARE LABORATORY -III	TW	50	20			41	P
414458	SOFTWARE LABORATORY -III	OR	50	20			38	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			36	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			36	P
414460	PROJECT PHASE I	TW	50	20			38	P

FIRST SEM TOTAL = 510/750

B120058538 GORE AKANKSHA ASHOK SANGITA ,71500276H ,PICT ,B120058538

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	27	52	79	P
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	18	48	66	P
414455	MACHINE LEARNING	PP	100	40	26	37	63	P
414456E	CLOUD COMPUTING	PP	100	40	23	47	70	P
414457A	BUSINESS INTELLIGENCE	PP	100	40	23	52	75	P
414458	SOFTWARE LABORATORY -III	TW	50	20			46	P
414458	SOFTWARE LABORATORY -III	OR	50	20			45	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			34	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			36	P
414460	PROJECT PHASE I	TW	50	20			42	P

FIRST SEM TOTAL = 556/750

B120058539 GUNTOORKAR PARTH LAXMIKANT TRIVENI ,71400863L ,PICT ,B120058539

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	21	42	63	P
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	21	42	63	P
414455	MACHINE LEARNING	PP	100	40	17	38	55	P
414456E	CLOUD COMPUTING	PP	100	40	18	35	53	P
414457A	BUSINESS INTELLIGENCE	PP	100	40	20	50	70	P
414458	SOFTWARE LABORATORY -III	TW	50	20			43	P
414458	SOFTWARE LABORATORY -III	OR	50	20			38	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			36	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			32	P
414460	PROJECT PHASE I	TW	50	20			44	P

FIRST SEM TOTAL = 497/750

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120058540 GUPTA MAYANK ANIL SHUSHMA ,71400865G ,PICT ,B120058540

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	23	39	62	P
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	18	45	63	P
414455	MACHINE LEARNING	PP	100	40	17	31	48	P
414456E	CLOUD COMPUTING	PP	100	40	17	23	--	F
414457A	BUSINESS INTELLIGENCE	PP	100	40	15	45	60	P
414458	SOFTWARE LABORATORY -III	TW	50	20			46	P
414458	SOFTWARE LABORATORY -III	OR	50	20			38	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			40	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			30	P
414460	PROJECT PHASE I	TW	50	20			41	P

FIRST SEM TOTAL = --/750

B120058541 HERKAR SHIVANEE RAVINDRA SANJIVANI ,71400872K ,PICT ,B120058541

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	23	51	74	P
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	20	48	68	P
414455	MACHINE LEARNING	PP	100	40	19	37	56	P
414456E	CLOUD COMPUTING	PP	100	40	21	39	60	P
414457A	BUSINESS INTELLIGENCE	PP	100	40	21	49	70	P
414458	SOFTWARE LABORATORY -III	TW	50	20			45	P
414458	SOFTWARE LABORATORY -III	OR	50	20			37	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			43	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			42	P
414460	PROJECT PHASE I	TW	50	20			42	P

FIRST SEM TOTAL = 537/750

B120058542 IRNAK SWAPNIL ASHOK MEENA ,71350922J ,PICT ,B120058542

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	16	52	68	P
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	5	38	43	P
414455	MACHINE LEARNING	PP	100	40	13	29	42	P
414456E	CLOUD COMPUTING	PP	100	40	21	34	55	P
414457A	BUSINESS INTELLIGENCE	PP	100	40	19	37	56	P
414458	SOFTWARE LABORATORY -III	TW	50	20			38	P
414458	SOFTWARE LABORATORY -III	OR	50	20			30	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			34	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			33	P
414460	PROJECT PHASE I	TW	50	20			41	P

FIRST SEM TOTAL = 440/750

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120058543 JADHAV NISHANT VIJAY REKHA ,71400877L ,PICT ,B120058543

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	25	41	66	P
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	12	44	56	P
414455	MACHINE LEARNING	PP	100	40	22	36	58	P
414456E	CLOUD COMPUTING	PP	100	40	20	43	63	P
414457A	BUSINESS INTELLIGENCE	PP	100	40	16	46	62	P
414458	SOFTWARE LABORATORY -III	TW	50	20			44	P
414458	SOFTWARE LABORATORY -III	OR	50	20			35	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			32	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			34	P
414460	PROJECT PHASE I	TW	50	20			40	P

FIRST SEM TOTAL = 490/750

B120058545 JAGTAP MALVIKA PRAKASH JAGTAP SUNANDA PRAKASH ,71400881J ,PICT ,B120058545

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	20	46	66	P
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	9	38	47	P
414455	MACHINE LEARNING	PP	100	40	24	30	54	P
414456B	USABILITY ENGINEERING	PP	100	40	12	48	60	P
414457A	BUSINESS INTELLIGENCE	PP	100	40	21	45	66	P
414458	SOFTWARE LABORATORY -III	TW	50	20			40	P
414458	SOFTWARE LABORATORY -III	OR	50	20			39	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			32	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			32	P
414460	PROJECT PHASE I	TW	50	20			40	P

FIRST SEM TOTAL = 476/750

B120058546 JAGTAP SEJAL RAMESH ANITA ,71400883E ,PICT ,B120058546

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	23	54	77	P
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	18	46	64	P
414455	MACHINE LEARNING	PP	100	40	25	30	55	P
414456E	CLOUD COMPUTING	PP	100	40	21	51	72	P
414457A	BUSINESS INTELLIGENCE	PP	100	40	22	41	63	P
414458	SOFTWARE LABORATORY -III	TW	50	20			45	P
414458	SOFTWARE LABORATORY -III	OR	50	20			40	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			40	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			38	P
414460	PROJECT PHASE I	TW	50	20			44	P

FIRST SEM TOTAL = 538/750

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120058547 JAIN MANISH SANTOSH NIRMALA ,71400885M ,PICT ,B120058547

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	20	41	61	P
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	22	54	76	P
414455	MACHINE LEARNING	PP	100	40	20	44	64	P
414456B	USABILITY ENGINEERING	PP	100	40	18	48	66	P
414457A	BUSINESS INTELLIGENCE	PP	100	40	21	46	67	P
414458	SOFTWARE LABORATORY -III	TW	50	20			46	P
414458	SOFTWARE LABORATORY -III	OR	50	20			42	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			43	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			40	P
414460	PROJECT PHASE I	TW	50	20			44	P

FIRST SEM TOTAL = 549/750

B120058548 JAJOO NIRAJ VINODKUMAR MANISHA ,71400887H ,PICT ,B120058548

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	21	56	77	P
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	22	54	76	P
414455	MACHINE LEARNING	PP	100	40	28	48	76	P
414456E	CLOUD COMPUTING	PP	100	40	23	44	67	P
414457A	BUSINESS INTELLIGENCE	PP	100	40	23	46	69	P
414458	SOFTWARE LABORATORY -III	TW	50	20			45	P
414458	SOFTWARE LABORATORY -III	OR	50	20			42	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			43	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			41	P
414460	PROJECT PHASE I	TW	50	20			43	P

FIRST SEM TOTAL = 579/750

B120058549 JESSICA ROY RITA ,71400891F ,PICT ,B120058549

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	20	50	70	P
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	18	49	67	P
414455	MACHINE LEARNING	PP	100	40	19	42	61	P
414456E	CLOUD COMPUTING	PP	100	40	21	36	57	P
414457A	BUSINESS INTELLIGENCE	PP	100	40	24	51	75	P
414458	SOFTWARE LABORATORY -III	TW	50	20			43	P
414458	SOFTWARE LABORATORY -III	OR	50	20			35	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			35	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			34	P
414460	PROJECT PHASE I	TW	50	20			42	P

FIRST SEM TOTAL = 519/750

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120058550 JHA PRABHANSHU VINOD KUMAR VIDYA JHA ,71400892D ,PICT ,B120058550

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	20	33	53	P
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	21	45	66	P
414455	MACHINE LEARNING	PP	100	40	17	48	65	P
414456E	CLOUD COMPUTING	PP	100	40	20	30	50	P
414457A	BUSINESS INTELLIGENCE	PP	100	40	18	46	64	P
414458	SOFTWARE LABORATORY -III	TW	50	20			46	P
414458	SOFTWARE LABORATORY -III	OR	50	20			40	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			40	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			34	P
414460	PROJECT PHASE I	TW	50	20			46	P

FIRST SEM TOTAL = 504/750

B120058551 JITIN GEORGE GRACY ,71400893B ,PICT ,B120058551

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	24	41	65	P
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	23	52	75	P
414455	MACHINE LEARNING	PP	100	40	25	54	79	P
414456E	CLOUD COMPUTING	PP	100	40	21	46	67	P
414457A	BUSINESS INTELLIGENCE	PP	100	40	20	53	73	P
414458	SOFTWARE LABORATORY -III	TW	50	20			47	P
414458	SOFTWARE LABORATORY -III	OR	50	20			45	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			40	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			39	P
414460	PROJECT PHASE I	TW	50	20			47	P

FIRST SEM TOTAL = 577/750

B120058552 JOSHI MANOJ MANIKRAO SAVITA ,71400896G ,PICT ,B120058552

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	23	42	65	P
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	16	35	51	P
414455	MACHINE LEARNING	PP	100	40	19	53	72	P
414456E	CLOUD COMPUTING	PP	100	40	19	41	60	P
414457A	BUSINESS INTELLIGENCE	PP	100	40	17	47	64	P
414458	SOFTWARE LABORATORY -III	TW	50	20			40	P
414458	SOFTWARE LABORATORY -III	OR	50	20			38	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			40	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			35	P
414460	PROJECT PHASE I	TW	50	20			44	P

FIRST SEM TOTAL = 509/750

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120058553 KADAM AAKASH EKNATH VANITA ,71400900J ,PICT ,B120058553

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	18	57	75	P
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	23	48	71	P
414455	MACHINE LEARNING	PP	100	40	23	56	79	P
414456B	USABILITY ENGINEERING	PP	100	40	22	48	70	P
414457A	BUSINESS INTELLIGENCE	PP	100	40	25	53	78	P
414458	SOFTWARE LABORATORY -III	TW	50	20			46	P
414458	SOFTWARE LABORATORY -III	OR	50	20			38	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			29	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			28	P
414460	PROJECT PHASE I	TW	50	20			45	P

FIRST SEM TOTAL = 559/750

B120058554 KALE ANIKET CHHABU CHHABU ,71300989G ,PICT ,B120058554

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	25	58	83	P
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	21	54	75	P
414455	MACHINE LEARNING	PP	100	40	25	55	80	P
414456B	USABILITY ENGINEERING	PP	100	40	25	48	73	P
414457A	BUSINESS INTELLIGENCE	PP	100	40	25	53	78	P
414458	SOFTWARE LABORATORY -III	TW	50	20			42	P
414458	SOFTWARE LABORATORY -III	OR	50	20			38	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			28	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			28	P
414460	PROJECT PHASE I	TW	50	20			46	P

FIRST SEM TOTAL = 571/750

B120058555 KAMBLE SANGRAM VISHNU USHA ,71400912B ,PICT ,B120058555

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	15	28	43	P
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	18	31	49	P
414455	MACHINE LEARNING	PP	100	40	15	35	50	P
414456E	CLOUD COMPUTING	PP	100	40	18	26	--	F
414457A	BUSINESS INTELLIGENCE	PP	100	40	16	37	53	P
414458	SOFTWARE LABORATORY -III	TW	50	20			38	P
414458	SOFTWARE LABORATORY -III	OR	50	20			30	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			28	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			26	P
414460	PROJECT PHASE I	TW	50	20			39	P

FIRST SEM TOTAL = --/750

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120058556 KAMBLE SHRADDHA SUNIL SUJATA ,71400913L ,PICT ,B120058556

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	23	52	75	P
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	23	47	70	P
414455	MACHINE LEARNING	PP	100	40	22	56	78	P
414456B	USABILITY ENGINEERING	PP	100	40	18	46	64	P
414457A	BUSINESS INTELLIGENCE	PP	100	40	21	49	70	P
414458	SOFTWARE LABORATORY -III	TW	50	20			44	P
414458	SOFTWARE LABORATORY -III	OR	50	20			35	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			33	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			29	P
414460	PROJECT PHASE I	TW	50	20			44	P

FIRST SEM TOTAL = 542/750

B120058557 KANDEGALE SUSMITA GANPATI SAVITA ,71500277F ,PICT ,B120058557

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	24	51	75	P
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	21	57	78	P
414455	MACHINE LEARNING	PP	100	40	24	60	84	P
414456E	CLOUD COMPUTING	PP	100	40	23	50	73	P
414457A	BUSINESS INTELLIGENCE	PP	100	40	23	57	80	P
414458	SOFTWARE LABORATORY -III	TW	50	20			45	P
414458	SOFTWARE LABORATORY -III	OR	50	20			39	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			32	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			30	P
414460	PROJECT PHASE I	TW	50	20			38	P

FIRST SEM TOTAL = 574/750

B120058558 KAPIL VYAS ANITA ,71400919K ,PICT ,B120058558

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	26	40	66	P
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	22	53	75	P
414455	MACHINE LEARNING	PP	100	40	23	51	74	P
414456E	CLOUD COMPUTING	PP	100	40	20	48	68	P
414457A	BUSINESS INTELLIGENCE	PP	100	40	21	54	75	P
414458	SOFTWARE LABORATORY -III	TW	50	20			46	P
414458	SOFTWARE LABORATORY -III	OR	50	20			42	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			32	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			35	P
414460	PROJECT PHASE I	TW	50	20			44	P

FIRST SEM TOTAL = 557/750

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120058559 KARLE YASH SHAILENDRA PALLAVI ,71400925D ,PICT ,B120058559

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	18	43	61	P
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	21	50	71	P
414455	MACHINE LEARNING	PP	100	40	24	48	72	P
414456E	CLOUD COMPUTING	PP	100	40	21	53	74	P
414457A	BUSINESS INTELLIGENCE	PP	100	40	22	49	71	P
414458	SOFTWARE LABORATORY -III	TW	50	20			46	P
414458	SOFTWARE LABORATORY -III	OR	50	20			44	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			29	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			38	P
414460	PROJECT PHASE I	TW	50	20			44	P

FIRST SEM TOTAL = 550/750

B120058560 KARPE SHRUNOTI KASHINATH SHARMILA ,71400928J ,PICT ,B120058560

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	17	41	58	P
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	26	48	74	P
414455	MACHINE LEARNING	PP	100	40	14	49	63	P
414456B	USABILITY ENGINEERING	PP	100	40	23	50	73	P
414457A	BUSINESS INTELLIGENCE	PP	100	40	21	49	70	P
414458	SOFTWARE LABORATORY -III	TW	50	20			45	P
414458	SOFTWARE LABORATORY -III	OR	50	20			40	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			39	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			30	P
414460	PROJECT PHASE I	TW	50	20			43	P

FIRST SEM TOTAL = 535/750

B120058561 KASABE NIKHIL VINOD KAVITA ,71500278D ,PICT ,B120058561

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	18	39	57	P
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	15	45	60	P
414455	MACHINE LEARNING	PP	100	40	15	45	60	P
414456B	USABILITY ENGINEERING	PP	100	40	16	40	56	P
414457A	BUSINESS INTELLIGENCE	PP	100	40	17	47	64	P
414458	SOFTWARE LABORATORY -III	TW	50	20			42	P
414458	SOFTWARE LABORATORY -III	OR	50	20			30	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			39	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			30	P
414460	PROJECT PHASE I	TW	50	20			45	P

FIRST SEM TOTAL = 483/750

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120058562 KAVYA ANGARA KAVITA ANGARA ,71400934C ,PICT ,B120058562

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	24	43	67	P
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	17	44	61	P
414455	MACHINE LEARNING	PP	100	40	26	54	80	P
414456E	CLOUD COMPUTING	PP	100	40	22	49	71	P
414457A	BUSINESS INTELLIGENCE	PP	100	40	23	55	78	P
414458	SOFTWARE LABORATORY -III	TW	50	20			45	P
414458	SOFTWARE LABORATORY -III	OR	50	20			40	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			42	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			45	P
414460	PROJECT PHASE I	TW	50	20			42	P

FIRST SEM TOTAL = 571/750

B120058563 KHANDELWAL ASHWINI MANOJ JAYASHREE ,71400941F ,PICT ,B120058563

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	24	37	61	P
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	21	45	66	P
414455	MACHINE LEARNING	PP	100	40	20	51	71	P
414456B	USABILITY ENGINEERING	PP	100	40	18	47	65	P
414457A	BUSINESS INTELLIGENCE	PP	100	40	18	51	69	P
414458	SOFTWARE LABORATORY -III	TW	50	20			45	P
414458	SOFTWARE LABORATORY -III	OR	50	20			42	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			39	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			42	P
414460	PROJECT PHASE I	TW	50	20			45	P

FIRST SEM TOTAL = 545/750

B120058564 KHETAN RISHI RAJENDRA SHASHI ,71400945J ,PICT ,B120058564

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	19	41	60	P
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	23	43	66	P
414455	MACHINE LEARNING	PP	100	40	21	59	80	P
414456E	CLOUD COMPUTING	PP	100	40	22	52	74	P
414457A	BUSINESS INTELLIGENCE	PP	100	40	25	58	83	P
414458	SOFTWARE LABORATORY -III	TW	50	20			46	P
414458	SOFTWARE LABORATORY -III	OR	50	20			46	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			46	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			46	P
414460	PROJECT PHASE I	TW	50	20			46	P

FIRST SEM TOTAL = 593/750

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120058565 KHOMANE PRADIP MADHUKAR GANGUBAI ,71500279B ,PICT ,B120058565

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	16	39	55	P
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	12	45	57	P
414455	MACHINE LEARNING	PP	100	40	13	58	71	P
414456B	USABILITY ENGINEERING	PP	100	40	23	41	64	P
414457A	BUSINESS INTELLIGENCE	PP	100	40	24	52	76	P
414458	SOFTWARE LABORATORY -III	TW	50	20			43	P
414458	SOFTWARE LABORATORY -III	OR	50	20			40	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			35	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			38	P
414460	PROJECT PHASE I	TW	50	20			44	P

FIRST SEM TOTAL = 523/750

B120058566 KHULE SHUBHAM RAMCHANDRA BHARATI ,71400950E ,PICT ,B120058566

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	24	42	66	P
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	15	44	59	P
414455	MACHINE LEARNING	PP	100	40	17	45	62	P
414456E	CLOUD COMPUTING	PP	100	40	21	42	63	P
414457A	BUSINESS INTELLIGENCE	PP	100	40	18	43	61	P
414458	SOFTWARE LABORATORY -III	TW	50	20			44	P
414458	SOFTWARE LABORATORY -III	OR	50	20			32	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			29	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			25	P
414460	PROJECT PHASE I	TW	50	20			38	P

FIRST SEM TOTAL = 479/750

B120058567 KOKARE PRIYANKA KRISHNARAO DHANASHREE ,71400954H ,PICT ,B120058567

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	22	37	59	P
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	16	41	57	P
414455	MACHINE LEARNING	PP	100	40	19	50	69	P
414456E	CLOUD COMPUTING	PP	100	40	21	44	65	P
414457A	BUSINESS INTELLIGENCE	PP	100	40	24	52	76	P
414458	SOFTWARE LABORATORY -III	TW	50	20			43	P
414458	SOFTWARE LABORATORY -III	OR	50	20			35	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			26	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			25	P
414460	PROJECT PHASE I	TW	50	20			33	P

FIRST SEM TOTAL = 488/750

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120058568 KOTHARI PIYUSH GOVARDHAN TARA ,71400959J ,PICT ,B120058568

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	13	31	44	P
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	19	39	58	P
414455	MACHINE LEARNING	PP	100	40	12	44	56	P
414456E	CLOUD COMPUTING	PP	100	40	18	41	59	P
414457A	BUSINESS INTELLIGENCE	PP	100	40	20	47	67	P
414458	SOFTWARE LABORATORY -III	TW	50	20			45	P
414458	SOFTWARE LABORATORY -III	OR	50	20			38	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			35	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			36	P
414460	PROJECT PHASE I	TW	50	20			39	P

FIRST SEM TOTAL = 477/750

B120058569 KOTHAWALE PRADIP SAMBHAJI LATA ,71500280F ,PICT ,B120058569

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	21	36	57	P
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	18	41	59	P
414455	MACHINE LEARNING	PP	100	40	23	46	69	P
414456E	CLOUD COMPUTING	PP	100	40	22	40	62	P
414457A	BUSINESS INTELLIGENCE	PP	100	40	22	53	75	P
414458	SOFTWARE LABORATORY -III	TW	50	20			44	P
414458	SOFTWARE LABORATORY -III	OR	50	20			38	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			38	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			40	P
414460	PROJECT PHASE I	TW	50	20			39	P

FIRST SEM TOTAL = 521/750

B120058570 KRISHNA KUMAR UPADHYAYA VINITA ,71400962J ,PICT ,B120058570

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	25	45	70	P
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	17	40	57	P
414455	MACHINE LEARNING	PP	100	40	24	47	71	P
414456E	CLOUD COMPUTING	PP	100	40	24	45	69	P
414457A	BUSINESS INTELLIGENCE	PP	100	40	22	46	68	P
414458	SOFTWARE LABORATORY -III	TW	50	20			40	P
414458	SOFTWARE LABORATORY -III	OR	50	20			35	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			20	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			24	P
414460	PROJECT PHASE I	TW	50	20			31	P

FIRST SEM TOTAL = 485/750

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120058571 KULKARNI MAYANK RAVINDRA MEDHA ,71400966M ,PICT ,B120058571

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	20	45	65	P
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	22	41	63	P
414455	MACHINE LEARNING	PP	100	40	22	57	79	P
414456E	CLOUD COMPUTING	PP	100	40	21	43	64	P
414457A	BUSINESS INTELLIGENCE	PP	100	40	25	51	76	P
414458	SOFTWARE LABORATORY -III	TW	50	20			47	P
414458	SOFTWARE LABORATORY -III	OR	50	20			45	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			39	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			35	P
414460	PROJECT PHASE I	TW	50	20			42	P

FIRST SEM TOTAL = 555/750

B120058572 KULKARNI NISHA ANILRAO SUNITA ,71400967K ,PICT ,B120058572

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	17	35	52	P
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	17	42	59	P
414455	MACHINE LEARNING	PP	100	40	21	44	65	P
414456E	CLOUD COMPUTING	PP	100	40	23	30	53	P
414457A	BUSINESS INTELLIGENCE	PP	100	40	24	47	71	P
414458	SOFTWARE LABORATORY -III	TW	50	20			43	P
414458	SOFTWARE LABORATORY -III	OR	50	20			38	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			37	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			38	P
414460	PROJECT PHASE I	TW	50	20			39	P

FIRST SEM TOTAL = 495/750

B120058573 KULWAL AKASH VIJAYKUMAR SAPNA ,71400975L ,PICT ,B120058573

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	15	32	47	P
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	11	33	44	P
414455	MACHINE LEARNING	PP	100	40	20	41	61	P
414456E	CLOUD COMPUTING	PP	100	40	20	22	--	F
414457A	BUSINESS INTELLIGENCE	PP	100	40	18	29	47	P
414458	SOFTWARE LABORATORY -III	TW	50	20			38	P
414458	SOFTWARE LABORATORY -III	OR	50	20			30	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			20	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			20	P
414460	PROJECT PHASE I	TW	50	20			30	P

FIRST SEM TOTAL = --/750

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120058574 LEENA YASHWANT DEORE SNEHALATA ,71400985H ,PICT ,B120058574

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	27	41	68	P
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	22	43	65	P
414455	MACHINE LEARNING	PP	100	40	22	57	79	P
414456E	CLOUD COMPUTING	PP	100	40	18	42	60	P
414457A	BUSINESS INTELLIGENCE	PP	100	40	20	51	71	P
414458	SOFTWARE LABORATORY -III	TW	50	20			47	P
414458	SOFTWARE LABORATORY -III	OR	50	20			35	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			42	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			39	P
414460	PROJECT PHASE I	TW	50	20			44	P

FIRST SEM TOTAL = 550/750

B120058575 LOLAGE AMRUTA SANJAY BHARATI ,71400987D ,PICT ,B120058575

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	17	35	52	P
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	17	43	60	P
414455	MACHINE LEARNING	PP	100	40	25	55	80	P
414456E	CLOUD COMPUTING	PP	100	40	20	36	56	P
414457A	BUSINESS INTELLIGENCE	PP	100	40	20	39	59	P
414458	SOFTWARE LABORATORY -III	TW	50	20			43	P
414458	SOFTWARE LABORATORY -III	OR	50	20			35	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			36	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			39	P
414460	PROJECT PHASE I	TW	50	20			43	P

FIRST SEM TOTAL = 503/750

B120058576 MALANI ASHISH SATISH ARATHI ,71400993J ,PICT ,B120058576

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	16	31	47	P
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	19	41	60	P
414455	MACHINE LEARNING	PP	100	40	18	38	56	P
414456E	CLOUD COMPUTING	PP	100	40	19	38	57	P
414457A	BUSINESS INTELLIGENCE	PP	100	40	19	46	65	P
414458	SOFTWARE LABORATORY -III	TW	50	20			43	P
414458	SOFTWARE LABORATORY -III	OR	50	20			35	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			39	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			40	P
414460	PROJECT PHASE I	TW	50	20			47	P

FIRST SEM TOTAL = 489/750

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120058577 MALEGAONKAR SIDDHESH SANDEEP KAVITA ,71400994G ,PICT ,B120058577

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	17	41	58	P
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	24	44	68	P
414455	MACHINE LEARNING	PP	100	40	24	57	81	P
414456E	CLOUD COMPUTING	PP	100	40	23	48	71	P
414457A	BUSINESS INTELLIGENCE	PP	100	40	23	46	69	P
414458	SOFTWARE LABORATORY -III	TW	50	20			46	P
414458	SOFTWARE LABORATORY -III	OR	50	20			36	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			46	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			46	P
414460	PROJECT PHASE I	TW	50	20			45	P

FIRST SEM TOTAL = 566/750

B120058578 MALI POOJA SHIVLAL SUNITA ,71400995E ,PICT ,B120058578

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	19	39	58	P
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	13	43	56	P
414455	MACHINE LEARNING	PP	100	40	18	55	73	P
414456B	USABILITY ENGINEERING	PP	100	40	25	44	69	P
414457A	BUSINESS INTELLIGENCE	PP	100	40	18	46	64	P
414458	SOFTWARE LABORATORY -III	TW	50	20			45	P
414458	SOFTWARE LABORATORY -III	OR	50	20			33	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			38	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			39	P
414460	PROJECT PHASE I	TW	50	20			40	P

FIRST SEM TOTAL = 515/750

B120058579 MARKE DARSHANI GAJANAN SUREKHA ,71400998K ,PICT ,B120058579

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	20	37	57	P
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	18	31	49	P
414455	MACHINE LEARNING	PP	100	40	20	46	66	P
414456B	USABILITY ENGINEERING	PP	100	40	17	36	53	P
414457A	BUSINESS INTELLIGENCE	PP	100	40	24	40	64	P
414458	SOFTWARE LABORATORY -III	TW	50	20			45	P
414458	SOFTWARE LABORATORY -III	OR	50	20			41	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			42	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			42	P
414460	PROJECT PHASE I	TW	50	20			40	P

FIRST SEM TOTAL = 499/750

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120058580 MASKE SHUBHAM HIRALAL SANGEETA ,71400999H ,PICT ,B120058580

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	9	40	49	P
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	10	38	48	P
414455	MACHINE LEARNING	PP	100	40	13	32	45	P
414456B	USABILITY ENGINEERING	PP	100	40	21	42	63	P
414457A	BUSINESS INTELLIGENCE	PP	100	40	14	45	59	P
414458	SOFTWARE LABORATORY -III	TW	50	20			37	P
414458	SOFTWARE LABORATORY -III	OR	50	20			29	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			40	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			41	P
414460	PROJECT PHASE I	TW	50	20			40	P

FIRST SEM TOTAL = 451/750

B120058581 MEHTA PRANAV RAJIV RAMA ,71401002C ,PICT ,B120058581

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	16	34	50	P
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	20	29	49	P
414455	MACHINE LEARNING	PP	100	40	19	44	63	P
414456E	CLOUD COMPUTING	PP	100	40	18	42	60	P
414457A	BUSINESS INTELLIGENCE	PP	100	40	20	48	68	P
414458	SOFTWARE LABORATORY -III	TW	50	20			45	P
414458	SOFTWARE LABORATORY -III	OR	50	20			35	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			42	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			43	P
414460	PROJECT PHASE I	TW	50	20			42	P

FIRST SEM TOTAL = 497/750

B120058582 MESHAM PANKAJ ARUN SHOBHA ,71500282B ,PICT ,B120058582

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	15	39	54	P
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	15	42	57	P
414455	MACHINE LEARNING	PP	100	40	15	37	52	P
414456B	USABILITY ENGINEERING	PP	100	40	16	38	54	P
414457A	BUSINESS INTELLIGENCE	PP	100	40	17	41	58	P
414458	SOFTWARE LABORATORY -III	TW	50	20			44	P
414458	SOFTWARE LABORATORY -III	OR	50	20			33	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			40	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			41	P
414460	PROJECT PHASE I	TW	50	20			42	P

FIRST SEM TOTAL = 475/750

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120058583 MOHAMMED AAQUIB ANSARI MOHAMMED YUSUF SUFIYA ,71045526H ,PICT ,B120058583

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	AA	AA	--	F
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	AA	AA	--	F
414455	MACHINE LEARNING	PP	100	40	AA	AA	--	F
414456E	CLOUD COMPUTING	PP	100	40	AA	AA	--	F
414457A	BUSINESS INTELLIGENCE	PP	100	40	AA	AA	--	F
414458	SOFTWARE LABORATORY -III	TW	50	20			AA	F
414458	SOFTWARE LABORATORY -III	OR	50	20			AA	F
414459	SOFTWARE LABORATORY -IV	PR	50	20			AA	F
414459	SOFTWARE LABORATORY -IV	OR	50	20			AA	F
414460	PROJECT PHASE I	TW	50	20			AA	F

FIRST SEM TOTAL = --/750

TERMWORK FAIL...

B120058584 MUKKAWAR SHRUTI PRADIP SUNITA ,71500283L ,PICT ,B120058584

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	23	50	73	P
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	14	43	57	P
414455	MACHINE LEARNING	PP	100	40	28	56	84	P
414456E	CLOUD COMPUTING	PP	100	40	21	44	65	P
414457A	BUSINESS INTELLIGENCE	PP	100	40	24	52	76	P
414458	SOFTWARE LABORATORY -III	TW	50	20			45	P
414458	SOFTWARE LABORATORY -III	OR	50	20			36	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			45	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			46	P
414460	PROJECT PHASE I	TW	50	20			40	P

FIRST SEM TOTAL = 567/750

B120058585 MULE VISHAL GAJANAN SUCHITA ,71401016C ,PICT ,B120058585

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	21	43	64	P
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	12	40	52	P
414455	MACHINE LEARNING	PP	100	40	20	45	65	P
414456E	CLOUD COMPUTING	PP	100	40	20	36	56	P
414457A	BUSINESS INTELLIGENCE	PP	100	40	19	39	58	P
414458	SOFTWARE LABORATORY -III	TW	50	20			40	P
414458	SOFTWARE LABORATORY -III	OR	50	20			22	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			28	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			29	P
414460	PROJECT PHASE I	TW	50	20			25	P

FIRST SEM TOTAL = 439/750

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120058586 NAIK TEJAS SRIRANG SHRUTI ,71401023F ,PICT ,B120058586

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	20	46	66	P
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	19	45	64	P
414455	MACHINE LEARNING	PP	100	40	16	56	72	P
414456E	CLOUD COMPUTING	PP	100	40	18	39	57	P
414457A	BUSINESS INTELLIGENCE	PP	100	40	23	50	73	P
414458	SOFTWARE LABORATORY -III	TW	50	20			46	P
414458	SOFTWARE LABORATORY -III	OR	50	20			35	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			42	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			41	P
414460	PROJECT PHASE I	TW	50	20			45	P

FIRST SEM TOTAL = 541/750

B120058587 NAIR SUSHMITA NAIR PREMA ARUN ,71401025B ,PICT ,B120058587

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	22	48	70	P
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	18	37	55	P
414455	MACHINE LEARNING	PP	100	40	22	46	68	P
414456E	CLOUD COMPUTING	PP	100	40	23	38	61	P
414457A	BUSINESS INTELLIGENCE	PP	100	40	19	40	59	P
414458	SOFTWARE LABORATORY -III	TW	50	20			45	P
414458	SOFTWARE LABORATORY -III	OR	50	20			33	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			39	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			40	P
414460	PROJECT PHASE I	TW	50	20			45	P

FIRST SEM TOTAL = 515/750

B120058588 NALIN VATS LAXMI ,71401027J ,PICT ,B120058588

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	12	41	53	P
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	8	40	48	P
414455	MACHINE LEARNING	PP	100	40	11	52	63	P
414456B	USABILITY ENGINEERING	PP	100	40	16	41	57	P
414457A	BUSINESS INTELLIGENCE	PP	100	40	14	41	55	P
414458	SOFTWARE LABORATORY -III	TW	50	20			40	P
414458	SOFTWARE LABORATORY -III	OR	50	20			33	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			43	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			44	P
414460	PROJECT PHASE I	TW	50	20			42	P

FIRST SEM TOTAL = 478/750

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120058589 NAMAN MANDLIK SHAILJA ,71401028G ,PICT ,B120058589

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	22	54	76	P
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	19	44	63	P
414455	MACHINE LEARNING	PP	100	40	23	59	82	P
414456E	CLOUD COMPUTING	PP	100	40	20	39	59	P
414457A	BUSINESS INTELLIGENCE	PP	100	40	22	52	74	P
414458	SOFTWARE LABORATORY -III	TW	50	20			46	P
414458	SOFTWARE LABORATORY -III	OR	50	20			38	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			46	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			46	P
414460	PROJECT PHASE I	TW	50	20			44	P

FIRST SEM TOTAL = 574/750

B120058590 NARULE SHIVALI BHIMASHANKAR VARSHA ,71500284J ,PICT ,B120058590

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	23	45	68	P
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	23	55	78	P
414455	MACHINE LEARNING	PP	100	40	24	53	77	P
414456E	CLOUD COMPUTING	PP	100	40	22	41	63	P
414457A	BUSINESS INTELLIGENCE	PP	100	40	20	51	71	P
414458	SOFTWARE LABORATORY -III	TW	50	20			45	P
414458	SOFTWARE LABORATORY -III	OR	50	20			34	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			40	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			41	P
414460	PROJECT PHASE I	TW	50	20			40	P

FIRST SEM TOTAL = 557/750

B120058591 NAYYAR RASHMEET KAUR MANMEET SINGH PARAMJEET KAUR ,71401033C ,PICT ,B120058591

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	16	42	58	P
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	13	52	65	P
414455	MACHINE LEARNING	PP	100	40	20	47	67	P
414456E	CLOUD COMPUTING	PP	100	40	22	42	64	P
414457A	BUSINESS INTELLIGENCE	PP	100	40	23	47	70	P
414458	SOFTWARE LABORATORY -III	TW	50	20			45	P
414458	SOFTWARE LABORATORY -III	OR	50	20			32	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			40	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			41	P
414460	PROJECT PHASE I	TW	50	20			44	P

FIRST SEM TOTAL = 526/750

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120058592 NISHA GANDHI NILEEMA GANDHI ,71401043L ,PICT ,B120058592

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	16	40	56	P
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	17	57	74	P
414455	MACHINE LEARNING	PP	100	40	28	54	82	P
414456E	CLOUD COMPUTING	PP	100	40	21	38	59	P
414457A	BUSINESS INTELLIGENCE	PP	100	40	25	52	77	P
414458	SOFTWARE LABORATORY -III	TW	50	20			46	P
414458	SOFTWARE LABORATORY -III	OR	50	20			39	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			43	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			43	P
414460	PROJECT PHASE I	TW	50	20			46	P

FIRST SEM TOTAL = 565/750

B120058593 PADWAL DIPALI BALASO HIRABAI ,71500285G ,PICT ,B120058593

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	22	40	62	P
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	18	61	79	P
414455	MACHINE LEARNING	PP	100	40	22	59	81	P
414456E	CLOUD COMPUTING	PP	100	40	23	41	64	P
414457A	BUSINESS INTELLIGENCE	PP	100	40	23	52	75	P
414458	SOFTWARE LABORATORY -III	TW	50	20			46	P
414458	SOFTWARE LABORATORY -III	OR	50	20			37	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			44	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			45	P
414460	PROJECT PHASE I	TW	50	20			44	P

FIRST SEM TOTAL = 577/750

B120058594 PADWAL RUPALI BALASO HIRABAI ,71500286E ,PICT ,B120058594

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	20	41	61	P
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	14	61	75	P
414455	MACHINE LEARNING	PP	100	40	18	53	71	P
414456E	CLOUD COMPUTING	PP	100	40	21	39	60	P
414457A	BUSINESS INTELLIGENCE	PP	100	40	20	54	74	P
414458	SOFTWARE LABORATORY -III	TW	50	20			47	P
414458	SOFTWARE LABORATORY -III	OR	50	20			36	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			44	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			45	P
414460	PROJECT PHASE I	TW	50	20			44	P

FIRST SEM TOTAL = 557/750

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120058595 PATEL SHOEB SHARIF SHAFATUAT ,71500287C ,PICT ,B120058595

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	14	46	60	P
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	14	41	55	P
414455	MACHINE LEARNING	PP	100	40	17	39	56	P
414456B	USABILITY ENGINEERING	PP	100	40	17	38	55	P
414457A	BUSINESS INTELLIGENCE	PP	100	40	19	38	57	P
414458	SOFTWARE LABORATORY -III	TW	50	20			40	P
414458	SOFTWARE LABORATORY -III	OR	50	20			24	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			30	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			30	P
414460	PROJECT PHASE I	TW	50	20			40	P

FIRST SEM TOTAL = 447/750

B120058596 PATERIA AKASH PRAMODKUMAR VINITA ,71401056B ,PICT ,B120058596

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	21	43	64	P
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	22	65	87	P
414455	MACHINE LEARNING	PP	100	40	17	42	59	P
414456E	CLOUD COMPUTING	PP	100	40	22	38	60	P
414457A	BUSINESS INTELLIGENCE	PP	100	40	20	50	70	P
414458	SOFTWARE LABORATORY -III	TW	50	20			46	P
414458	SOFTWARE LABORATORY -III	OR	50	20			37	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			41	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			42	P
414460	PROJECT PHASE I	TW	50	20			45	P

FIRST SEM TOTAL = 551/750

B120058597 PATIL AKSHAY VIJAY SADHANA ,71401058J ,PICT ,B120058597

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	25	41	66	P
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	19	49	68	P
414455	MACHINE LEARNING	PP	100	40	20	44	64	P
414456E	CLOUD COMPUTING	PP	100	40	22	41	63	P
414457A	BUSINESS INTELLIGENCE	PP	100	40	24	52	76	P
414458	SOFTWARE LABORATORY -III	TW	50	20			41	P
414458	SOFTWARE LABORATORY -III	OR	50	20			23	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			36	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			38	P
414460	PROJECT PHASE I	TW	50	20			30	P

FIRST SEM TOTAL = 505/750

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120058598 PATIL SUNEHA SANJIV ULKA ,71401070H ,PICT ,B120058598

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	18	43	61	P
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	19	43	62	P
414455	MACHINE LEARNING	PP	100	40	17	49	66	P
414456E	CLOUD COMPUTING	PP	100	40	17	29	46	P
414457A	BUSINESS INTELLIGENCE	PP	100	40	19	43	62	P
414458	SOFTWARE LABORATORY -III	TW	50	20			43	P
414458	SOFTWARE LABORATORY -III	OR	50	20			33	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			43	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			44	P
414460	PROJECT PHASE I	TW	50	20			45	P

FIRST SEM TOTAL = 505/750

B120058599 PATIL VIVEK ANIL CHHAYA ,71401072D ,PICT ,B120058599

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	18	44	62	P
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	21	40	61	P
414455	MACHINE LEARNING	PP	100	40	19	41	60	P
414456E	CLOUD COMPUTING	PP	100	40	22	47	69	P
414457A	BUSINESS INTELLIGENCE	PP	100	40	22	55	77	P
414458	SOFTWARE LABORATORY -III	TW	50	20			41	P
414458	SOFTWARE LABORATORY -III	OR	50	20			23	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			43	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			44	P
414460	PROJECT PHASE I	TW	50	20			39	P

FIRST SEM TOTAL = 519/750

B120058600 PATNI NIDHI JITENDRA KAVITA JITENDRA PATNI ,71401075J ,PICT ,B120058600

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	14	43	57	P
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	15	64	79	P
414455	MACHINE LEARNING	PP	100	40	17	59	76	P
414456B	USABILITY ENGINEERING	PP	100	40	20	45	65	P
414457A	BUSINESS INTELLIGENCE	PP	100	40	22	50	72	P
414458	SOFTWARE LABORATORY -III	TW	50	20			44	P
414458	SOFTWARE LABORATORY -III	OR	50	20			35	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			42	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			43	P
414460	PROJECT PHASE I	TW	50	20			37	P

FIRST SEM TOTAL = 550/750

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120058601 PAWAR PRAFFUL SANJAY SHRADHA ,71401079M ,PICT ,B120058601

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	20	37	57	P
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	17	54	71	P
414455	MACHINE LEARNING	PP	100	40	11	48	59	P
414456E	CLOUD COMPUTING	PP	100	40	22	38	60	P
414457A	BUSINESS INTELLIGENCE	PP	100	40	20	53	73	P
414458	SOFTWARE LABORATORY -III	TW	50	20			42	P
414458	SOFTWARE LABORATORY -III	OR	50	20			27	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			40	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			42	P
414460	PROJECT PHASE I	TW	50	20			44	P

FIRST SEM TOTAL = 515/750

B120058602 PAWAR RASIKA JAYPRAKASH SUCHETA ,71401081C ,PICT ,B120058602

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	17	38	55	P
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	15	45	60	P
414455	MACHINE LEARNING	PP	100	40	20	54	74	P
414456E	CLOUD COMPUTING	PP	100	40	19	32	51	P
414457A	BUSINESS INTELLIGENCE	PP	100	40	22	53	75	P
414458	SOFTWARE LABORATORY -III	TW	50	20			43	P
414458	SOFTWARE LABORATORY -III	OR	50	20			35	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			44	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			45	P
414460	PROJECT PHASE I	TW	50	20			43	P

FIRST SEM TOTAL = 525/750

B120058603 PHARATE KOMAL SAMPAT SUNITA ,71401082M ,PICT ,B120058603

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	13	39	52	P
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	20	58	78	P
414455	MACHINE LEARNING	PP	100	40	22	53	75	P
414456E	CLOUD COMPUTING	PP	100	40	24	40	64	P
414457A	BUSINESS INTELLIGENCE	PP	100	40	23	54	77	P
414458	SOFTWARE LABORATORY -III	TW	50	20			46	P
414458	SOFTWARE LABORATORY -III	OR	50	20			34	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			40	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			41	P
414460	PROJECT PHASE I	TW	50	20			45	P

FIRST SEM TOTAL = 552/750

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120058604 PRATIK NARHAR BARHATE SANGITA NARHAR BARHATE ,71401096M ,PICT ,B120058604

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	10	37	47	P
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	22	59	81	P
414455	MACHINE LEARNING	PP	100	40	16	49	65	P
414456B	USABILITY ENGINEERING	PP	100	40	20	33	53	P
414457A	BUSINESS INTELLIGENCE	PP	100	40	18	43	61	P
414458	SOFTWARE LABORATORY -III	TW	50	20			45	P
414458	SOFTWARE LABORATORY -III	OR	50	20			39	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			46	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			46	P
414460	PROJECT PHASE I	TW	50	20			45	P

FIRST SEM TOTAL = 528/750

B120058605 RAINA ASEEM VIJAY SEEMA ,71401104F ,PICT ,B120058605

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	19	41	60	P
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	19	54	73	P
414455	MACHINE LEARNING	PP	100	40	20	49	69	P
414456E	CLOUD COMPUTING	PP	100	40	21	35	56	P
414457A	BUSINESS INTELLIGENCE	PP	100	40	23	55	78	P
414458	SOFTWARE LABORATORY -III	TW	50	20			46	P
414458	SOFTWARE LABORATORY -III	OR	50	20			41	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			46	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			46	P
414460	PROJECT PHASE I	TW	50	20			46	P

FIRST SEM TOTAL = 561/750

B120058606 RAJAT SANJAYAKUMAR TAWARGERI RANJANA ,71401105D ,PICT ,B120058606

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	13	34	47	P
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	18	65	83	P
414455	MACHINE LEARNING	PP	100	40	14	45	59	P
414456E	CLOUD COMPUTING	PP	100	40	18	47	65	P
414457A	BUSINESS INTELLIGENCE	PP	100	40	16	46	62	P
414458	SOFTWARE LABORATORY -III	TW	50	20			45	P
414458	SOFTWARE LABORATORY -III	OR	50	20			37	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			45	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			46	P
414460	PROJECT PHASE I	TW	50	20			42	P

FIRST SEM TOTAL = 531/750

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120058607 RATHI MADHURA MADAN BHAGYASHRI ,71401110L ,PICT ,B120058607

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	22	43	65	P
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	24	58	82	P
414455	MACHINE LEARNING	PP	100	40	18	52	70	P
414456E	CLOUD COMPUTING	PP	100	40	20	45	65	P
414457A	BUSINESS INTELLIGENCE	PP	100	40	24	54	78	P
414458	SOFTWARE LABORATORY -III	TW	50	20			47	P
414458	SOFTWARE LABORATORY -III	OR	50	20			35	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			45	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			45	P
414460	PROJECT PHASE I	TW	50	20			46	P

FIRST SEM TOTAL = 578/750

B120058608 RATHOD SHIVKUMAR GEMA VANITA ,71500288M ,PICT ,B120058608

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	15	37	52	P
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	14	58	72	P
414455	MACHINE LEARNING	PP	100	40	15	38	53	P
414456B	USABILITY ENGINEERING	PP	100	40	23	36	59	P
414457A	BUSINESS INTELLIGENCE	PP	100	40	20	41	61	P
414458	SOFTWARE LABORATORY -III	TW	50	20			43	P
414458	SOFTWARE LABORATORY -III	OR	50	20			28	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			38	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			40	P
414460	PROJECT PHASE I	TW	50	20			41	P

FIRST SEM TOTAL = 487/750

B120058609 RAUT PRATIK ASHOK RAUT SUNITA ASHOK ,71301190E ,PICT ,B120058609

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	16	34	50	P
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	23	22	--	F
414455	MACHINE LEARNING	PP	100	40	16	31	47	P
414456E	CLOUD COMPUTING	PP	100	40	16	31	47	P
414457A	BUSINESS INTELLIGENCE	PP	100	40	12	40	52	P
414458	SOFTWARE LABORATORY -III	TW	50	20			41	P
414458	SOFTWARE LABORATORY -III	OR	50	20			28	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			36	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			40	P
414460	PROJECT PHASE I	TW	50	20			30	P

FIRST SEM TOTAL = --/750

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120058610 RIDHIM RASTOGI SHALINI ,71401115M ,PICT ,B120058610

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	18	38	56	P
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	21	40	61	P
414455	MACHINE LEARNING	PP	100	40	23	61	84	P
414456E	CLOUD COMPUTING	PP	100	40	20	36	56	P
414457A	BUSINESS INTELLIGENCE	PP	100	40	18	51	69	P
414458	SOFTWARE LABORATORY -III	TW	50	20			46	P
414458	SOFTWARE LABORATORY -III	OR	50	20			35	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			35	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			34	P
414460	PROJECT PHASE I	TW	50	20			45	P

FIRST SEM TOTAL = 521/750

B120058611 ROHAN VIJAY DONGARE ROHINI ,71401120H ,PICT ,B120058611

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	10	15	--	F
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	12	24	--	F
414455	MACHINE LEARNING	PP	100	40	15	14	--	F
414456E	CLOUD COMPUTING	PP	100	40	12	18	--	F
414457A	BUSINESS INTELLIGENCE	PP	100	40	13	29	42	P
414458	SOFTWARE LABORATORY -III	TW	50	20			41	P
414458	SOFTWARE LABORATORY -III	OR	50	20			26	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			40	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			42	P
414460	PROJECT PHASE I	TW	50	20			32	P

FIRST SEM TOTAL = --/750

B120058612 RUKADIKAR ADITYA RAJENDRA JAYASHREE ,71401124L ,PICT ,B120058612

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	18	35	53	P
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	15	27	--	F
414455	MACHINE LEARNING	PP	100	40	11	33	44	P
414456E	CLOUD COMPUTING	PP	100	40	14	29	43	P
414457A	BUSINESS INTELLIGENCE	PP	100	40	18	41	59	P
414458	SOFTWARE LABORATORY -III	TW	50	20			41	P
414458	SOFTWARE LABORATORY -III	OR	50	20			35	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			44	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			45	P
414460	PROJECT PHASE I	TW	50	20			40	P

FIRST SEM TOTAL = --/750

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120058613 SABLE ADITYA KUMAR SHALINI ,71401126G ,PICT ,B120058613

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	13	32	45	P
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	14	18	--	F
414455	MACHINE LEARNING	PP	100	40	16	35	51	P
414456E	CLOUD COMPUTING	PP	100	40	15	36	51	P
414457A	BUSINESS INTELLIGENCE	PP	100	40	14	46	60	P
414458	SOFTWARE LABORATORY -III	TW	50	20			40	P
414458	SOFTWARE LABORATORY -III	OR	50	20			36	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			37	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			38	P
414460	PROJECT PHASE I	TW	50	20			34	P

FIRST SEM TOTAL = --/750

B120058614 SALVE AKSHAY KRISHNA MAYA ,71401133K ,PICT ,B120058614

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	12	38	50	P
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	15	23	--	F
414455	MACHINE LEARNING	PP	100	40	10	19	--	F
414456E	CLOUD COMPUTING	PP	100	40	18	43	61	P
414457A	BUSINESS INTELLIGENCE	PP	100	40	18	47	65	P
414458	SOFTWARE LABORATORY -III	TW	50	20			36	P
414458	SOFTWARE LABORATORY -III	OR	50	20			34	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			35	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			38	P
414460	PROJECT PHASE I	TW	50	20			25	P

FIRST SEM TOTAL = --/750

B120058615 SANCHETI SNEHA JITENDRA KALPANA ,71500289K ,PICT ,B120058615

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	24	47	71	P
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	22	69	91	P
414455	MACHINE LEARNING	PP	100	40	24	61	85	P
414456E	CLOUD COMPUTING	PP	100	40	21	46	67	P
414457A	BUSINESS INTELLIGENCE	PP	100	40	23	57	80	P
414458	SOFTWARE LABORATORY -III	TW	50	20			46	P
414458	SOFTWARE LABORATORY -III	OR	50	20			41	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			42	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			43	P
414460	PROJECT PHASE I	TW	50	20			40	P

FIRST SEM TOTAL = 606/750

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120058616 SANTANI RAHUL GURMUKHDAS GANGA ,71401137B ,PICT ,B120058616

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	24	40	64	P
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	25	51	76	P
414455	MACHINE LEARNING	PP	100	40	22	46	68	P
414456E	CLOUD COMPUTING	PP	100	40	20	40	60	P
414457A	BUSINESS INTELLIGENCE	PP	100	40	21	50	71	P
414458	SOFTWARE LABORATORY -III	TW	50	20			43	P
414458	SOFTWARE LABORATORY -III	OR	50	20			31	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			42	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			43	P
414460	PROJECT PHASE I	TW	50	20			40	P

FIRST SEM TOTAL = 538/750

B120058617 SARODE NITESH RAJENDRA SANGITA ,71401139J ,PICT ,B120058617

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	16	35	51	P
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	20	49	69	P
414455	MACHINE LEARNING	PP	100	40	12	39	51	P
414456E	CLOUD COMPUTING	PP	100	40	17	35	52	P
414457A	BUSINESS INTELLIGENCE	PP	100	40	20	46	66	P
414458	SOFTWARE LABORATORY -III	TW	50	20			42	P
414458	SOFTWARE LABORATORY -III	OR	50	20			31	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			46	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			46	P
414460	PROJECT PHASE I	TW	50	20			43	P

FIRST SEM TOTAL = 497/750

B120058618 SARVANAN G JAMUNA ,71401143G ,PICT ,B120058618

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	26	41	67	P
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	24	67	91	P
414455	MACHINE LEARNING	PP	100	40	18	58	76	P
414456E	CLOUD COMPUTING	PP	100	40	24	41	65	P
414457A	BUSINESS INTELLIGENCE	PP	100	40	23	49	72	P
414458	SOFTWARE LABORATORY -III	TW	50	20			47	P
414458	SOFTWARE LABORATORY -III	OR	50	20			42	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			42	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			43	P
414460	PROJECT PHASE I	TW	50	20			45	P

FIRST SEM TOTAL = 590/750

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120058619 SAUMYA PANDEY ABHA ,71401147K ,PICT ,B120058619

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	16	39	55	P
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	15	56	71	P
414455	MACHINE LEARNING	PP	100	40	13	62	75	P
414456B	USABILITY ENGINEERING	PP	100	40	17	42	59	P
414457A	BUSINESS INTELLIGENCE	PP	100	40	21	57	78	P
414458	SOFTWARE LABORATORY -III	TW	50	20			43	P
414458	SOFTWARE LABORATORY -III	OR	50	20			37	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			38	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			39	P
414460	PROJECT PHASE I	TW	50	20			39	P

FIRST SEM TOTAL = 534/750

B120058620 SAVAJI RAGHAV RAJESH SHILPA ,71401149F ,PICT ,B120058620

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	19	33	52	P
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	14	49	63	P
414455	MACHINE LEARNING	PP	100	40	17	45	62	P
414456E	CLOUD COMPUTING	PP	100	40	17	34	51	P
414457A	BUSINESS INTELLIGENCE	PP	100	40	23	49	72	P
414458	SOFTWARE LABORATORY -III	TW	50	20			42	P
414458	SOFTWARE LABORATORY -III	OR	50	20			33	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			43	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			44	P
414460	PROJECT PHASE I	TW	50	20			39	P

FIRST SEM TOTAL = 501/750

B120058621 SAWANT PRITAM PRABHAKAR SUNITA ,71401151H ,PICT ,B120058621

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	16	39	55	P
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	16	40	56	P
414455	MACHINE LEARNING	PP	100	40	11	37	48	P
414456E	CLOUD COMPUTING	PP	100	40	16	34	50	P
414457A	BUSINESS INTELLIGENCE	PP	100	40	21	48	69	P
414458	SOFTWARE LABORATORY -III	TW	50	20			42	P
414458	SOFTWARE LABORATORY -III	OR	50	20			31	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			38	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			39	P
414460	PROJECT PHASE I	TW	50	20			42	P

FIRST SEM TOTAL = 470/750

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120058622 SAYERAM ESWAR SUBASHINI E ,71301213H ,PICT ,B120058622

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	23	44	67	P
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	21	43	64	P
414455	MACHINE LEARNING	PP	100	40	21	41	62	P
414456B	USABILITY ENGINEERING	PP	100	40	25	42	67	P
414457A	BUSINESS INTELLIGENCE	PP	100	40	23	57	80	P
414458	SOFTWARE LABORATORY -III	TW	50	20			43	P
414458	SOFTWARE LABORATORY -III	OR	50	20			36	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			40	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			41	P
414460	PROJECT PHASE I	TW	50	20			45	P

FIRST SEM TOTAL = 545/750

B120058623 SHAFI ALIMUHAMMAD HAKIMUDDIN AMTULLAH ,71401154B ,PICT ,B120058623

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	14	32	46	P
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	15	36	51	P
414455	MACHINE LEARNING	PP	100	40	9	42	51	P
414456B	USABILITY ENGINEERING	PP	100	40	15	31	46	P
414457A	BUSINESS INTELLIGENCE	PP	100	40	16	47	63	P
414458	SOFTWARE LABORATORY -III	TW	50	20			45	P
414458	SOFTWARE LABORATORY -III	OR	50	20			34	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			42	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			43	P
414460	PROJECT PHASE I	TW	50	20			39	P

FIRST SEM TOTAL = 460/750

B120058624 SHAH MAANAV ROOPAM GOPIKA ,71500281D ,PICT ,B120058624

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	28	54	82	P
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	23	49	72	P
414455	MACHINE LEARNING	PP	100	40	23	49	72	P
414456E	CLOUD COMPUTING	PP	100	40	24	45	69	P
414457A	BUSINESS INTELLIGENCE	PP	100	40	25	58	83	P
414458	SOFTWARE LABORATORY -III	TW	50	20			47	P
414458	SOFTWARE LABORATORY -III	OR	50	20			42	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			45	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			46	P
414460	PROJECT PHASE I	TW	50	20			47	P

FIRST SEM TOTAL = 605/750

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120058625 SHAIKH AIHAAB AHMED ABOOBAKER SADIQUE YASMEEN ,71401161E ,PICT ,B120058625

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	13	21	--	F
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	16	35	51	P
414455	MACHINE LEARNING	PP	100	40	11	31	42	P
414456B	USABILITY ENGINEERING	PP	100	40	19	32	51	P
414457A	BUSINESS INTELLIGENCE	PP	100	40	10	57	67	P
414458	SOFTWARE LABORATORY -III	TW	50	20			41	P
414458	SOFTWARE LABORATORY -III	OR	50	20			28	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			35	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			37	P
414460	PROJECT PHASE I	TW	50	20			41	P

FIRST SEM TOTAL = --/750

B120058626 SHAIKH MUJAHID QAYYUM SULTANA ,71500291M ,PICT ,B120058626

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	21	35	56	P
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	13	46	59	P
414455	MACHINE LEARNING	PP	100	40	14	34	48	P
414456E	CLOUD COMPUTING	PP	100	40	19	34	53	P
414457A	BUSINESS INTELLIGENCE	PP	100	40	21	51	72	P
414458	SOFTWARE LABORATORY -III	TW	50	20			42	P
414458	SOFTWARE LABORATORY -III	OR	50	20			30	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			40	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			42	P
414460	PROJECT PHASE I	TW	50	20			40	P

FIRST SEM TOTAL = 482/750

B120058627 SHELKE GAURI ARUN SAVITA ,71401165H ,PICT ,B120058627

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	18	34	52	P
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	16	37	53	P
414455	MACHINE LEARNING	PP	100	40	14	42	56	P
414456B	USABILITY ENGINEERING	PP	100	40	23	33	56	P
414457A	BUSINESS INTELLIGENCE	PP	100	40	20	52	72	P
414458	SOFTWARE LABORATORY -III	TW	50	20			44	P
414458	SOFTWARE LABORATORY -III	OR	50	20			38	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			42	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			43	P
414460	PROJECT PHASE I	TW	50	20			40	P

FIRST SEM TOTAL = 496/750

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120058628 SHETH GAURANG NITIN ARATI ,71401167D ,PICT ,B120058628

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	17	38	55	P
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	21	36	57	P
414455	MACHINE LEARNING	PP	100	40	13	30	43	P
414456B	USABILITY ENGINEERING	PP	100	40	20	35	55	P
414457A	BUSINESS INTELLIGENCE	PP	100	40	22	57	79	P
414458	SOFTWARE LABORATORY -III	TW	50	20			45	P
414458	SOFTWARE LABORATORY -III	OR	50	20			38	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			39	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			40	P
414460	PROJECT PHASE I	TW	50	20			40	P

FIRST SEM TOTAL = 491/750

B120058629 SHETH SMIT HARESH KETANA ,71401168B ,PICT ,B120058629

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	27	39	66	P
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	21	40	61	P
414455	MACHINE LEARNING	PP	100	40	22	39	61	P
414456E	CLOUD COMPUTING	PP	100	40	23	28	51	P
414457A	BUSINESS INTELLIGENCE	PP	100	40	15	54	69	P
414458	SOFTWARE LABORATORY -III	TW	50	20			45	P
414458	SOFTWARE LABORATORY -III	OR	50	20			39	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			42	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			43	P
414460	PROJECT PHASE I	TW	50	20			46	P

FIRST SEM TOTAL = 523/750

B120058630 SHINDE AKASH DHARMA SHAMAL ,71500292K ,PICT ,B120058630

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	23	42	65	P
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	19	44	63	P
414455	MACHINE LEARNING	PP	100	40	18	44	62	P
414456B	USABILITY ENGINEERING	PP	100	40	22	43	65	P
414457A	BUSINESS INTELLIGENCE	PP	100	40	25	55	80	P
414458	SOFTWARE LABORATORY -III	TW	50	20			40	P
414458	SOFTWARE LABORATORY -III	OR	50	20			30	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			32	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			34	P
414460	PROJECT PHASE I	TW	50	20			37	P

FIRST SEM TOTAL = 508/750

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120058631 SHINDE LINA BHAUSAHEB SUNANDA ,71500293H ,PICT ,B120058631

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	21	38	59	P
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	21	39	60	P
414455	MACHINE LEARNING	PP	100	40	14	42	56	P
414456E	CLOUD COMPUTING	PP	100	40	19	37	56	P
414457A	BUSINESS INTELLIGENCE	PP	100	40	21	53	74	P
414458	SOFTWARE LABORATORY -III	TW	50	20			41	P
414458	SOFTWARE LABORATORY -III	OR	50	20			35	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			33	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			34	P
414460	PROJECT PHASE I	TW	50	20			40	P

FIRST SEM TOTAL = 488/750

B120058632 SHINDE PUSHKARAJ SUHAS MADHURI ,71401173J ,PICT ,B120058632

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	15	32	47	P
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	22	30	52	P
414455	MACHINE LEARNING	PP	100	40	14	37	51	P
414456B	USABILITY ENGINEERING	PP	100	40	21	35	56	P
414457A	BUSINESS INTELLIGENCE	PP	100	40	13	50	63	P
414458	SOFTWARE LABORATORY -III	TW	50	20			42	P
414458	SOFTWARE LABORATORY -III	OR	50	20			37	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			32	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			34	P
414460	PROJECT PHASE I	TW	50	20			27	P

FIRST SEM TOTAL = 441/750

B120058633 SHINDE SACHIN VISHNU SHAILA ,71401174G ,PICT ,B120058633

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	19	39	58	P
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	21	38	59	P
414455	MACHINE LEARNING	PP	100	40	21	39	60	P
414456B	USABILITY ENGINEERING	PP	100	40	25	36	61	P
414457A	BUSINESS INTELLIGENCE	PP	100	40	19	43	62	P
414458	SOFTWARE LABORATORY -III	TW	50	20			47	P
414458	SOFTWARE LABORATORY -III	OR	50	20			34	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			46	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			46	P
414460	PROJECT PHASE I	TW	50	20			46	P

FIRST SEM TOTAL = 519/750

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120058634 SHIROLE ADITYA DEVENDRA NIRUPAMA ,71401180M ,PICT ,B120058634

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	19	18	--	F
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	11	31	42	P
414455	MACHINE LEARNING	PP	100	40	05	02	--	F
414456B	USABILITY ENGINEERING	PP	100	40	18	21	--	F
414457A	BUSINESS INTELLIGENCE	PP	100	40	07	30	--	F
414458	SOFTWARE LABORATORY -III	TW	50	20			43	P
414458	SOFTWARE LABORATORY -III	OR	50	20			34	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			42	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			43	P
414460	PROJECT PHASE I	TW	50	20			43	P

FIRST SEM TOTAL = --/750

B120058635 SHWETA NARAIN SINGH ALKA ,71401193C ,PICT ,B120058635

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	25	44	69	P
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	17	39	56	P
414455	MACHINE LEARNING	PP	100	40	26	47	73	P
414456B	USABILITY ENGINEERING	PP	100	40	22	44	66	P
414457A	BUSINESS INTELLIGENCE	PP	100	40	22	51	73	P
414458	SOFTWARE LABORATORY -III	TW	50	20			45	P
414458	SOFTWARE LABORATORY -III	OR	50	20			41	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			40	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			42	P
414460	PROJECT PHASE I	TW	50	20			43	P

FIRST SEM TOTAL = 548/750

B120058636 SOMWANSHI SHRUTI SHASHIKANT SANGITA ,71401198D ,PICT ,B120058636

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	20	47	67	P
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	22	48	70	P
414455	MACHINE LEARNING	PP	100	40	21	44	65	P
414456B	USABILITY ENGINEERING	PP	100	40	25	44	69	P
414457A	BUSINESS INTELLIGENCE	PP	100	40	26	60	86	P
414458	SOFTWARE LABORATORY -III	TW	50	20			45	P
414458	SOFTWARE LABORATORY -III	OR	50	20			40	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			37	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			38	P
414460	PROJECT PHASE I	TW	50	20			43	P

FIRST SEM TOTAL = 560/750

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120058637 SONALI SURAJIT DASGUPTA MITHU ,71401199B ,PICT ,B120058637

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	25	48	73	P
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	16	46	62	P
414455	MACHINE LEARNING	PP	100	40	22	44	66	P
414456E	CLOUD COMPUTING	PP	100	40	22	38	60	P
414457A	BUSINESS INTELLIGENCE	PP	100	40	19	44	63	P
414458	SOFTWARE LABORATORY -III	TW	50	20			47	P
414458	SOFTWARE LABORATORY -III	OR	50	20			41	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			44	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			45	P
414460	PROJECT PHASE I	TW	50	20			47	P

FIRST SEM TOTAL = 548/750

B120058638 SONAWANE RASHMI ARUN SUNITA ,71500294F ,PICT ,B120058638

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	19	41	60	P
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	14	47	61	P
414455	MACHINE LEARNING	PP	100	40	19	44	63	P
414456E	CLOUD COMPUTING	PP	100	40	19	38	57	P
414457A	BUSINESS INTELLIGENCE	PP	100	40	21	53	74	P
414458	SOFTWARE LABORATORY -III	TW	50	20			42	P
414458	SOFTWARE LABORATORY -III	OR	50	20			35	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			43	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			44	P
414460	PROJECT PHASE I	TW	50	20			43	P

FIRST SEM TOTAL = 522/750

B120058639 SULE SHUBHAM NANDKUMAR MEERA ,71401213M ,PICT ,B120058639

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	16	28	44	P
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	12	42	54	P
414455	MACHINE LEARNING	PP	100	40	16	37	53	P
414456E	CLOUD COMPUTING	PP	100	40	23	42	65	P
414457A	BUSINESS INTELLIGENCE	PP	100	40	20	46	66	P
414458	SOFTWARE LABORATORY -III	TW	50	20			38	P
414458	SOFTWARE LABORATORY -III	OR	50	20			34	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			25	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			27	P
414460	PROJECT PHASE I	TW	50	20			42	P

FIRST SEM TOTAL = 448/750

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120058640 TAMBOLI MOEEJ NUSRATJAHAN ,71401222L ,PICT ,B120058640

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	17	39	56	P
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	21	39	60	P
414455	MACHINE LEARNING	PP	100	40	16	38	54	P
414456B	USABILITY ENGINEERING	PP	100	40	16	34	50	P
414457A	BUSINESS INTELLIGENCE	PP	100	40	19	46	65	P
414458	SOFTWARE LABORATORY -III	TW	50	20			43	P
414458	SOFTWARE LABORATORY -III	OR	50	20			38	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			45	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			46	P
414460	PROJECT PHASE I	TW	50	20			39	P

FIRST SEM TOTAL = 496/750

B120058641 TANAY MITTAL SUSHMA ,71401223J ,PICT ,B120058641

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	21	43	64	P
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	16	44	60	P
414455	MACHINE LEARNING	PP	100	40	14	44	58	P
414456B	USABILITY ENGINEERING	PP	100	40	19	36	55	P
414457A	BUSINESS INTELLIGENCE	PP	100	40	24	50	74	P
414458	SOFTWARE LABORATORY -III	TW	50	20			42	P
414458	SOFTWARE LABORATORY -III	OR	50	20			36	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			46	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			47	P
414460	PROJECT PHASE I	TW	50	20			38	P

FIRST SEM TOTAL = 520/750

B120058643 TAPDIYA RENU PRAVINKUMAR SMITA ,71401228K ,PICT ,B120058643

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	21	34	55	P
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	16	40	56	P
414455	MACHINE LEARNING	PP	100	40	19	43	62	P
414456E	CLOUD COMPUTING	PP	100	40	20	38	58	P
414457A	BUSINESS INTELLIGENCE	PP	100	40	27	57	84	P
414458	SOFTWARE LABORATORY -III	TW	50	20			44	P
414458	SOFTWARE LABORATORY -III	OR	50	20			39	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			44	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			45	P
414460	PROJECT PHASE I	TW	50	20			38	P

FIRST SEM TOTAL = 525/750

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120058645 TILEKAR AARYA SHEKHAAR SUVARNA ,71401235B ,PICT ,B120058645

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	19	43	62	P
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	22	39	61	P
414455	MACHINE LEARNING	PP	100	40	14	41	55	P
414456E	CLOUD COMPUTING	PP	100	40	20	35	55	P
414457A	BUSINESS INTELLIGENCE	PP	100	40	19	57	76	P
414458	SOFTWARE LABORATORY -III	TW	50	20			43	P
414458	SOFTWARE LABORATORY -III	OR	50	20			39	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			44	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			45	P
414460	PROJECT PHASE I	TW	50	20			42	P

FIRST SEM TOTAL = 522/750

B120058646 TUSHAR METHWANI MONICA ,71401241G ,PICT ,B120058646

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	14	35	49	P
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	19	37	56	P
414455	MACHINE LEARNING	PP	100	40	14	26	--	F
414456E	CLOUD COMPUTING	PP	100	40	20	34	54	P
414457A	BUSINESS INTELLIGENCE	PP	100	40	18	44	62	P
414458	SOFTWARE LABORATORY -III	TW	50	20			47	P
414458	SOFTWARE LABORATORY -III	OR	50	20			41	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			42	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			43	P
414460	PROJECT PHASE I	TW	50	20			45	P

FIRST SEM TOTAL = --/750

B120058647 WAGHELA NILAY RAJENDRA CHARUSHILA ,71401269G ,PICT ,B120058647

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	14	35	49	P
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	20	41	61	P
414455	MACHINE LEARNING	PP	100	40	13	31	44	P
414456B	USABILITY ENGINEERING	PP	100	40	15	31	46	P
414457A	BUSINESS INTELLIGENCE	PP	100	40	14	39	53	P
414458	SOFTWARE LABORATORY -III	TW	50	20			42	P
414458	SOFTWARE LABORATORY -III	OR	50	20			32	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			35	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			36	P
414460	PROJECT PHASE I	TW	50	20			31	P

FIRST SEM TOTAL = 429/750

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120058648 WAKCHAURE KAVITA DHANANJAY MANISHA ,71401271J ,PICT ,B120058648

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	25	40	65	P
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	18	48	66	P
414455	MACHINE LEARNING	PP	100	40	18	47	65	P
414456E	CLOUD COMPUTING	PP	100	40	20	36	56	P
414457A	BUSINESS INTELLIGENCE	PP	100	40	23	57	80	P
414458	SOFTWARE LABORATORY -III	TW	50	20			46	P
414458	SOFTWARE LABORATORY -III	OR	50	20			43	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			42	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			43	P
414460	PROJECT PHASE I	TW	50	20			41	P

FIRST SEM TOTAL = 547/750

B120058649 WANI PIYUSH DAGADU ANITA ,71401272G ,PICT ,B120058649

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	27	34	61	P
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	19	48	67	P
414455	MACHINE LEARNING	PP	100	40	21	40	61	P
414456E	CLOUD COMPUTING	PP	100	40	20	32	52	P
414457A	BUSINESS INTELLIGENCE	PP	100	40	21	53	74	P
414458	SOFTWARE LABORATORY -III	TW	50	20			45	P
414458	SOFTWARE LABORATORY -III	OR	50	20			40	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			43	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			44	P
414460	PROJECT PHASE I	TW	50	20			46	P

FIRST SEM TOTAL = 533/750

B120058650 YASH MUNDRA NEELU ,71401281F ,PICT ,B120058650

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	14	31	45	P
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	AA	AA	--	F
414455	MACHINE LEARNING	PP	100	40	12	30	42	P
414456E	CLOUD COMPUTING	PP	100	40	18	28	46	P
414457A	BUSINESS INTELLIGENCE	PP	100	40	8	47	55	P
414458	SOFTWARE LABORATORY -III	TW	50	20			41	P
414458	SOFTWARE LABORATORY -III	OR	50	20			35	P
414459	SOFTWARE LABORATORY -IV	PR	50	20			45	P
414459	SOFTWARE LABORATORY -IV	OR	50	20			46	P
414460	PROJECT PHASE I	TW	50	20			44	P

FIRST SEM TOTAL = --/750

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120058651 AJINKYA BINWADE RITU BINWADE ,71300765G ,PICT ,B120058651

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	12	35	47	P	C
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	5	35	40	P	C
414455	MACHINE LEARNING	PP	100	40	7	33\$	40	P	C
414456E	CLOUD COMPUTING	PP	100	40	10	30\$	40	P	
414457A	BUSINESS INTELLIGENCE	PP	100	40	10	34	44	P	C
414458	SOFTWARE LABORATORY -III	TW	50	20			35	P	C
414458	SOFTWARE LABORATORY -III	OR	50	20			31	P	C
414459	SOFTWARE LABORATORY -IV	PR	50	20			33	P	C
414459	SOFTWARE LABORATORY -IV	OR	50	20			35	P	C
414460	PROJECT PHASE I	TW	50	20			30	P	C

SEM.:2

414461	DISTRIBUTED SYSTEM	PP	100	40	13	34	47	P	C
414462	ADVANCED DATABASE	PP	100	40	12	39	51	P	C
414463B	ADVANCED GRAPHICS AND ANIM.	PP	100	40	6	40	46	P	C
414463B	ADVANCED GRAPHICS AND ANIM.	TW	25	10			21	P	C
414463B	ADVANCED GRAPHICS AND ANIM.	OR	25	10			21	P	C
414464D	INTERNET OF THING	PP	100	40	20	49	69	P	C
414465	SOFTWARE LABORATORY-V	TW	25	10			19	P	C
414465	SOFTWARE LABORATORY-V	PR	25	10			17	P	C
414466	SOFTWARE LABORATORY-VI	PR	50	20			38	P	C
414466	SOFTWARE LABORATORY-VI	OR	50	20			39	P	C
414467	PROJECT WORK	TW	50	20			35	P	C
414467	PROJECT WORK	OR	100	40			85	P	C

GRAND TOTAL = 863/1500 , Result : HIGHER SECOND CLASS [\$ 0.1]

B120058653 DAHAWAD SUNIL NAMDEO VANITA ,71241757F ,PICT ,B120058653

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	8	37	45	P	
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	9	48	57	P	
414455	MACHINE LEARNING	PP	100	40	10	24	--	F	
414456E	CLOUD COMPUTING	PP	100	40	15	32	47	P	
414457D	GEO INFORMATICS SYSTEMS	PP	100	40	AA	37	--	F	
414458	SOFTWARE LABORATORY -III	TW	50	20			41	P	
414458	SOFTWARE LABORATORY -III	OR	50	20			32	P	
414459	SOFTWARE LABORATORY -IV	PR	50	20			27	P	
414459	SOFTWARE LABORATORY -IV	OR	50	20			28	P	
414460	PROJECT PHASE I	TW	50	20			41	P	

SEM.:2

GRAND TOTAL = --/750 , Result :

B120058654 DHANAWATE PRANOTI PRAKASH PUSHPA ,71300877G ,PICT ,B120058654

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	8	32\$	40	P	C
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	10	38	48	P	C
414455	MACHINE LEARNING	PP	100	40	11	29	40	P	C
414456B	USABILITY ENGINEERING	PP	100	40	13	36	49	P	C
414457A	BUSINESS INTELLIGENCE	PP	100	40	9	40	49	P	C
414458	SOFTWARE LABORATORY -III	TW	50	20			39	P	C
414458	SOFTWARE LABORATORY -III	OR	50	20			31	P	C
414459	SOFTWARE LABORATORY -IV	PR	50	20			35	P	C
414459	SOFTWARE LABORATORY -IV	OR	50	20			30	P	C
414460	PROJECT PHASE I	TW	50	20			42	P	C

SEM.:2

414461	DISTRIBUTED SYSTEM	PP	100	40	14	29	43	P	C
414462	ADVANCED DATABASE	PP	100	40	13	29	42	P	C
414463B	ADVANCED GRAPHICS AND ANIM.	PP	100	40	7	38	45	P	
414463B	ADVANCED GRAPHICS AND ANIM.	TW	25	10			22	P	C
414463B	ADVANCED GRAPHICS AND ANIM.	OR	25	10			20	P	C
414464C	GREEN IT-PRINCIPLES AND PRAC	PP	100	40	15	34	49	P	C
414465	SOFTWARE LABORATORY-V	TW	25	10			20	P	C
414465	SOFTWARE LABORATORY-V	PR	25	10			18	P	C
414466	SOFTWARE LABORATORY-VI	PR	50	20			34	P	C
414466	SOFTWARE LABORATORY-VI	OR	50	20			32	P	C
414467	PROJECT WORK	TW	50	20			46	P	C
414467	PROJECT WORK	OR	100	40			94	P	C

GRAND TOTAL = 868/1500 , Result : HIGHER SECOND CLASS [\$ 0.1]

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120058655 KAILAS JAHANGIR PAWARA THAWALI ,71100842G ,PICT ,B120058655

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	13	46	59	P	C
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	7	33	40	P	C
414455	MACHINE LEARNING	PP	100	40	9	31	40	P	C
414456B	USABILITY ENGINEERING	PP	100	40	9	43	52	P	C
414457D	GEO INFORMATICS SYSTEMS	PP	100	40	19	46	65	P	
414458	SOFTWARE LABORATORY -III	TW	50	20			36	P	C
414458	SOFTWARE LABORATORY -III	OR	50	20			30	P	C
414459	SOFTWARE LABORATORY -IV	PR	50	20			25	P	C
414459	SOFTWARE LABORATORY -IV	OR	50	20			23	P	C
414460	PROJECT PHASE I	TW	50	20			38	P	C

SEM.:2

414461	DISTRIBUTED SYSTEM	PP	100	40	15	37	52	P	
414462	ADVANCED DATABASE	PP	100	40	21	39	60	P	C
414463A	MOBILE COMPUTING	PP	100	40	13	34	47	P	
414463A	MOBILE COMPUTING	TW	25	10			21	P	C
414463A	MOBILE COMPUTING	OR	25	10			22	P	C
414464C	GREEN IT-PRINCIPLES AND PRAC	PP	100	40	16	37	53	P	
414465	SOFTWARE LABORATORY-V	TW	25	10			20	P	C
414465	SOFTWARE LABORATORY-V	PR	25	10			16	P	C
414466	SOFTWARE LABORATORY-VI	PR	50	20			34	P	C
414466	SOFTWARE LABORATORY-VI	OR	50	20			36	P	C
414467	PROJECT WORK	TW	50	20			43	P	C
414467	PROJECT WORK	OR	100	40			91	P	C

GRAND TOTAL = 903/1500 , Result : FIRST CLASS

B120058657 MUNOT SHRENIK SUMTILAL SADHANA ,71100903B ,PICT ,B120058657

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	11	29	40	P	C
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	8	38	46	P	C
414455	MACHINE LEARNING	PP	100	40	20	28	48	P	C
414456B	USABILITY ENGINEERING	PP	100	40	8	37	45	P	C
414457D	GEO INFORMATICS SYSTEMS	PP	100	40	11	30	41	P	C
414458	SOFTWARE LABORATORY -III	TW	50	20			40	P	C
414458	SOFTWARE LABORATORY -III	OR	50	20			38	P	C
414459	SOFTWARE LABORATORY -IV	PR	50	20			38	P	C
414459	SOFTWARE LABORATORY -IV	OR	50	20			40	P	C
414460	PROJECT PHASE I	TW	50	20			42	P	C

SEM.:2

414461	DISTRIBUTED SYSTEM	PP	100	40	12	28\$	40	P	
414462	ADVANCED DATABASE	PP	100	40	13	28	41	P	
414463A	MOBILE COMPUTING	PP	100	40	9	31	40	P	C
414463A	MOBILE COMPUTING	TW	25	10			20	P	C
414463A	MOBILE COMPUTING	OR	25	10			21	P	C
414464C	GREEN IT-PRINCIPLES AND PRAC	PP	100	40	14	29	43	P	C
414465	SOFTWARE LABORATORY-V	TW	25	10			23	P	C
414465	SOFTWARE LABORATORY-V	PR	25	10			19	P	C
414466	SOFTWARE LABORATORY-VI	PR	50	20			35	P	C
414466	SOFTWARE LABORATORY-VI	OR	50	20			35	P	C
414467	PROJECT WORK	TW	50	20			46	P	C
414467	PROJECT WORK	OR	100	40			92	P	C

GRAND TOTAL = 873/1500 , Result : HIGHER SECOND CLASS [\$ 0.1]

B120058658 NAKHALE BHUSHAN DILIP RAJKANYA ,71301096H ,PICT ,B120058658

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	9	42	51	P	C
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	7	38	45	P	C
414455	MACHINE LEARNING	PP	100	40	6	34	40	P	C
414456B	USABILITY ENGINEERING	PP	100	40	4	39	43	P	C
414457A	BUSINESS INTELLIGENCE	PP	100	40	5	35	40	P	C
414458	SOFTWARE LABORATORY -III	TW	50	20			38	P	C
414458	SOFTWARE LABORATORY -III	OR	50	20			35	P	C
414459	SOFTWARE LABORATORY -IV	PR	50	20			40	P	C
414459	SOFTWARE LABORATORY -IV	OR	50	20			25	P	C
414460	PROJECT PHASE I	TW	50	20			43	P	C

SEM.:2

414461	DISTRIBUTED SYSTEM	PP	100	40	9	33	42	P	C
414462	ADVANCED DATABASE	PP	100	40	16	39	55	P	
414463C	INFO. STORAGE & RETRIEVAL	PP	100	40	14	40	54	P	C
414463C	INFO. STORAGE & RETRIEVAL	TW	25	10			19	P	C
414463C	INFO. STORAGE & RETRIEVAL	OR	25	10			19	P	C
414464D	INTERNET OF THING	PP	100	40	14	33	47	P	C
414465	SOFTWARE LABORATORY-V	TW	25	10			20	P	C
414465	SOFTWARE LABORATORY-V	PR	25	10			19	P	C
414466	SOFTWARE LABORATORY-VI	PR	50	20			37	P	C
414466	SOFTWARE LABORATORY-VI	OR	50	20			35	P	C

414467	PROJECT WORK	TW	50	20	42	P	C
414467	PROJECT WORK	OR	100	40	85	P	C

GRAND TOTAL = 874/1500 , Result : HIGHER SECOND CLASS

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120058659 RUPANAWAR AKSHAY VINAYAK PRAMODINI ,71301196D ,PICT ,B120058659

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	15	40	55	P	C
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	12	48	60	P	C
414455	MACHINE LEARNING	PP	100	40	11	29\$	40	P	C
414456E	CLOUD COMPUTING	PP	100	40	13	30	43	P	C
414457D	GEO INFORMATICS SYSTEMS	PP	100	40	17	31	48	P	C
414458	SOFTWARE LABORATORY -III	TW	50	20			39	P	C
414458	SOFTWARE LABORATORY -III	OR	50	20			38	P	C
414459	SOFTWARE LABORATORY -IV	PR	50	20			35	P	C
414459	SOFTWARE LABORATORY -IV	OR	50	20			32	P	C
414460	PROJECT PHASE I	TW	50	20			39	P	C

SEM.:2

414461	DISTRIBUTED SYSTEM	PP	100	40	13	28	41	P	C
414462	ADVANCED DATABASE	PP	100	40	18	34	52	P	
414463C	INFO. STORAGE & RETRIEVAL	PP	100	40	15	31	46	P	C
414463C	INFO. STORAGE & RETRIEVAL	TW	25	10			20	P	C
414463C	INFO. STORAGE & RETRIEVAL	OR	25	10			20	P	C
414464C	GREEN IT-PRINCIPLES AND PRAC	PP	100	40	19	30	49	P	
414465	SOFTWARE LABORATORY-V	TW	25	10			21	P	C
414465	SOFTWARE LABORATORY-V	PR	25	10			19	P	C
414466	SOFTWARE LABORATORY-VI	PR	50	20			35	P	C
414466	SOFTWARE LABORATORY-VI	OR	50	20			36	P	C
414467	PROJECT WORK	TW	50	20			41	P	C
414467	PROJECT WORK	OR	100	40			86	P	C

GRAND TOTAL = 895/1500 , Result : HIGHER SECOND CLASS [\$ 0.1]

B120058660 SHALVI RANI ANNAPURNA SETH ,71301220L ,PICT ,B120058660

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	13	28	41	P	C
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	10	42	52	P	C
414455	MACHINE LEARNING	PP	100	40	7	29#	36	P	
414456B	USABILITY ENGINEERING	PP	100	40	10	46	56	P	C
414457D	GEO INFORMATICS SYSTEMS	PP	100	40	14	34	48	P	C
414458	SOFTWARE LABORATORY -III	TW	50	20			38	P	C
414458	SOFTWARE LABORATORY -III	OR	50	20			35	P	C
414459	SOFTWARE LABORATORY -IV	PR	50	20			30	P	C
414459	SOFTWARE LABORATORY -IV	OR	50	20			29	P	C
414460	PROJECT PHASE I	TW	50	20			30	P	C

SEM.:2

414461	DISTRIBUTED SYSTEM	PP	100	40	11	33	44	P	C
414462	ADVANCED DATABASE	PP	100	40	17	40	57	P	C
414463C	INFO. STORAGE & RETRIEVAL	PP	100	40	16	38	54	P	C
414463C	INFO. STORAGE & RETRIEVAL	TW	25	10			20	P	C
414463C	INFO. STORAGE & RETRIEVAL	OR	25	10			20	P	C
414464C	GREEN IT-PRINCIPLES AND PRAC	PP	100	40	9	32	41	P	
414465	SOFTWARE LABORATORY-V	TW	25	10			19	P	C
414465	SOFTWARE LABORATORY-V	PR	25	10			18	P	C
414466	SOFTWARE LABORATORY-VI	PR	50	20			30	P	C
414466	SOFTWARE LABORATORY-VI	OR	50	20			32	P	C
414467	PROJECT WORK	TW	50	20			35	P	C
414467	PROJECT WORK	OR	100	40			85	P	C

GRAND TOTAL = 850/1500 , Result : HIGHER SECOND CLASS [# 0.4]

B120058661 SHRAVAN OZA MADHURI OZA ,71301239M ,PICT ,B120058661

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	19	34	53	P	C
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	12	32	44	P	C
414455	MACHINE LEARNING	PP	100	40	15	40	55	P	C
414456B	USABILITY ENGINEERING	PP	100	40	14	39	53	P	C
414457A	BUSINESS INTELLIGENCE	PP	100	40	14	38	52	P	C
414458	SOFTWARE LABORATORY -III	TW	50	20			39	P	C
414458	SOFTWARE LABORATORY -III	OR	50	20			36	P	C
414459	SOFTWARE LABORATORY -IV	PR	50	20			38	P	C
414459	SOFTWARE LABORATORY -IV	OR	50	20			32	P	C
414460	PROJECT PHASE I	TW	50	20			40	P	C

SEM.:2

414461	DISTRIBUTED SYSTEM	PP	100	40	18	40	58	P	C
414462	ADVANCED DATABASE	PP	100	40	19	38	57	P	C
414463C	INFO. STORAGE & RETRIEVAL	PP	100	40	15	37	52	P	C
414463C	INFO. STORAGE & RETRIEVAL	TW	25	10			21	P	C
414463C	INFO. STORAGE & RETRIEVAL	OR	25	10			22	P	C
414464C	GREEN IT-PRINCIPLES AND PRAC	PP	100	40	18	39	57	P	
414465	SOFTWARE LABORATORY-V	TW	25	10			20	P	C
414465	SOFTWARE LABORATORY-V	PR	25	10			19	P	C
414466	SOFTWARE LABORATORY-VI	PR	50	20			42	P	C
414466	SOFTWARE LABORATORY-VI	OR	50	20			40	P	C

414467	PROJECT WORK	TW	50	20	44	P	C
414467	PROJECT WORK	OR	100	40	90	P	C

GRAND TOTAL = 964/1500 , Result : FIRST CLASS

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120058662 TEJASH KUMAR

RITA

,71045642F

,PICT

,B120058662

SEM.:1

414453	INFORMATION & CYBER SECURITY	PP	100	40	11	34	45	P	C
414454	SOFTWARE MODELING AND DESIGN	PP	100	40	6	39	45	P	C
414455	MACHINE LEARNING	PP	100	40	9	31	40	P	C
414456B	USABILITY ENGINEERING	PP	100	40	8	37	45	P	C
414457A	BUSINESS INTELLIGENCE	PP	100	40	7	33	40	P	C
414458	SOFTWARE LABORATORY -III	TW	50	20			32	P	C
414458	SOFTWARE LABORATORY -III	OR	50	20			30	P	C
414459	SOFTWARE LABORATORY -IV	PR	50	20			25	P	C
414459	SOFTWARE LABORATORY -IV	OR	50	20			29	P	C
414460	PROJECT PHASE I	TW	50	20			25	P	C

SEM.:2

414461	DISTRIBUTED SYSTEM	PP	100	40	13	28	41	P	C
414462	ADVANCED DATABASE	PP	100	40	11	31	42	P	C
414463E	ADVANCED COMPUTER NETWORKS	PP	100	40	7	41	48	P	C
414463E	ADVANCED COMPUTER NETWORKS	TW	25	10			16	P	C
414463E	ADVANCED COMPUTER NETWORKS	OR	25	10			16	P	C
414464C	GREEN IT-PRINCIPLES AND PRAC	PP	100	40	8	33	41	P	
414465	SOFTWARE LABORATORY-V	TW	25	10			18	P	C
414465	SOFTWARE LABORATORY-V	PR	25	10			18	P	C
414466	SOFTWARE LABORATORY-VI	PR	50	20			25	P	C
414466	SOFTWARE LABORATORY-VI	OR	50	20			25	P	C
414467	PROJECT WORK	TW	50	20			38	P	C
414467	PROJECT WORK	OR	100	40			75	P	C

GRAND TOTAL = 759/1500 , Result : SECOND CLASS [\$ 0.1]