

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8053001	AAKASH SHINDE				MAMTA		, 71100709J			PICT		S8053001
01. SIGNAL AND SYSTEMS	PP	100	40	40	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	72	P	
02. SIGNAL AND SYSTEMS	OR	50	20	20	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	23	P	
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	40	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	40	P	
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	37	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	20	P	
05. NETWORK ANALYSIS	PP	100	40	41	P C	15. ELECTROMAGNETIC	PP	100	40	40	P	
06. DIGITAL LOGIC DESIGN	PP	100	40	42	P C	16. ELECTROMAGNETIC	TW	25	10	20	P	
07. DIGITAL LOGIC DESIGN	PR	50	20	21	P C	17. DATA STRUCTURES	PP	100	40	54	P	
08. POWER DEVICES AND MACHINES	PP	100	40	40	P C	18. DATA STRUCTURES	PR	50	20	35	P	
09. NETWORK AND POWER LAB.	TW	50	20	36	P C	19. COMMUNICATION THEORY	PP	100	40	40	P	
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	33	P C	20. COMMUNICATION THEORY	OR	50	20	22	P	
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	42	P	

GRAND TOTAL = 758/1500, RESULT: SECOND CLASS

ORDN. 1 MARKS :

S8053002	AASHIQ HANEEF AHMED				RASHEEDA		, 71106382G			PICT		S8053002
01. SIGNAL AND SYSTEMS	PP	100	40	85	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	88	P	
02. SIGNAL AND SYSTEMS	OR	50	20	43	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	24	P	
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	63	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	63	P	
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	40	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	42	P	
05. NETWORK ANALYSIS	PP	100	40	67	P C	15. ELECTROMAGNETIC	PP	100	40	79	P	
06. DIGITAL LOGIC DESIGN	PP	100	40	84	P C	16. ELECTROMAGNETIC	TW	25	10	24	P	
07. DIGITAL LOGIC DESIGN	PR	50	20	28	P C	17. DATA STRUCTURES	PP	100	40	65	P	
08. POWER DEVICES AND MACHINES	PP	100	40	50	P C	18. DATA STRUCTURES	PR	50	20	48	P	
09. NETWORK AND POWER LAB.	TW	50	20	43	P C	19. COMMUNICATION THEORY	PP	100	40	73	P	
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	43	P C	20. COMMUNICATION THEORY	OR	50	20	37	P	
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	42	P	

GRAND TOTAL = 1131/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

S8053003	ABHISHEK KUMAR				KIRAN DEVI		, 71100714E			PICT		S8053003
01. SIGNAL AND SYSTEMS	PP	100	40	61	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	83	P	
02. SIGNAL AND SYSTEMS	OR	50	20	32	P	12. ENGINEERING MATHEMATICS III	TW	25	10	17	P	
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	55	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	48	P	
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	39	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	30	P	
05. NETWORK ANALYSIS	PP	100	40	43	P C	15. ELECTROMAGNETIC	PP	100	40	60	P	
06. DIGITAL LOGIC DESIGN	PP	100	40	64	P C	16. ELECTROMAGNETIC	TW	25	10	15	P	
07. DIGITAL LOGIC DESIGN	PR	50	20	29	P C	17. DATA STRUCTURES	PP	100	40	53	P	
08. POWER DEVICES AND MACHINES	PP	100	40	55	P C	18. DATA STRUCTURES	PR	50	20	38	P	
09. NETWORK AND POWER LAB.	TW	50	20	29	P C	19. COMMUNICATION THEORY	PP	100	40	52	P	
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	30	P C	20. COMMUNICATION THEORY	OR	50	20	27	P	
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	34	P	

GRAND TOTAL = 894+06/1500, RESULT: FIRST CLASS [0.2]

ORDN. 1 MARKS :

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S8053004	ABHISHEK SHARMA				KANTA				, 71100715C				PICT		S8053004
01. SIGNAL AND SYSTEMS	PP	100	40	72	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	80	P				
02. SIGNAL AND SYSTEMS	OR	50	20	28	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	18	P				
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	53	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	52	P				
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	30	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	36	P				
05. NETWORK ANALYSIS	PP	100	40	59	P C	15. ELECTROMAGNETIC	PP	100	40	76	P				
06. DIGITAL LOGIC DESIGN	PP	100	40	70	P C	16. ELECTROMAGNETIC	TW	25	10	16	P				
07. DIGITAL LOGIC DESIGN	PR	50	20	23	P C	17. DATA STRUCTURES	PP	100	40	68	P				
08. POWER DEVICES AND MACHINES	PP	100	40	47	P C	18. DATA STRUCTURES	PR	50	20	44	P				
09. NETWORK AND POWER LAB.	TW	50	20	36	P C	19. COMMUNICATION THEORY	PP	100	40	44	P				
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	40	P C	20. COMMUNICATION THEORY	OR	50	20	32	P				
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	37	P				

GRAND TOTAL = 961/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

S8053005	ABIZAR HUSSAIN				NASEEM				, 71100716M				PICT		S8053005
01. SIGNAL AND SYSTEMS	PP	100	40	50	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	81	P				
02. SIGNAL AND SYSTEMS	OR	50	20	33	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	21	P				
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	59	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	31#	P				
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	39	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	30	P				
05. NETWORK ANALYSIS	PP	100	40	59	P C	15. ELECTROMAGNETIC	PP	100	40	64	P				
06. DIGITAL LOGIC DESIGN	PP	100	40	57	P C	16. ELECTROMAGNETIC	TW	25	10	19	P				
07. DIGITAL LOGIC DESIGN	PR	50	20	37	P C	17. DATA STRUCTURES	PP	100	40	58	P				
08. POWER DEVICES AND MACHINES	PP	100	40	42	P C	18. DATA STRUCTURES	PR	50	20	44	P				
09. NETWORK AND POWER LAB.	TW	50	20	41	P C	19. COMMUNICATION THEORY	PP	100	40	56	P				
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	37	P C	20. COMMUNICATION THEORY	OR	50	20	35	P				
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	36	P				

GRAND TOTAL = 929/1500, RESULT: FIRST CLASS # [0.4]

ORDN. 1 MARKS :

S8053006	ADITYA KALIA				NEELAM				, 71100718H				PICT		S8053006
01. SIGNAL AND SYSTEMS	PP	100	40	40	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	40	P				
02. SIGNAL AND SYSTEMS	OR	50	20	22	P	12. ENGINEERING MATHEMATICS III	TW	25	10	14	P				
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	43	P	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	29	F				
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	20	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	05	F				
05. NETWORK ANALYSIS	PP	100	40	40	P	15. ELECTROMAGNETIC	PP	100	40	47	P				
06. DIGITAL LOGIC DESIGN	PP	100	40	42	P C	16. ELECTROMAGNETIC	TW	25	10	15	P				
07. DIGITAL LOGIC DESIGN	PR	50	20	20	P C	17. DATA STRUCTURES	PP	100	40	40	P				
08. POWER DEVICES AND MACHINES	PP	100	40	40	P C	18. DATA STRUCTURES	PR	50	20	38	P				
09. NETWORK AND POWER LAB.	TW	50	20	25	P C	19. COMMUNICATION THEORY	PP	100	40	40	P				
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	29	P C	20. COMMUNICATION THEORY	OR	50	20	15	F				
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	34	P				

GRAND TOTAL = 638/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

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S8053007	AGRAWAL PRANAT ADITYA			ASHA			, 71100721H			PICT		S8053007
01. SIGNAL AND SYSTEMS	PP	100	40	47	P C		11. ENGINEERING MATHEMATICS III	PP	100	40	25	F
02. SIGNAL AND SYSTEMS	OR	50	20	28	P		12. ENGINEERING MATHEMATICS III	TW	25	10	17	P
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	40	P		13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	23	F
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	20	P C		14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	23	P
05. NETWORK ANALYSIS	PP	100	40	40	P C		15. ELECTROMAGNETIC	PP	100	40	40	P
06. DIGITAL LOGIC DESIGN	PP	100	40	40	P C		16. ELECTROMAGNETIC	TW	25	10	17	P
07. DIGITAL LOGIC DESIGN	PR	50	20	30	P C		17. DATA STRUCTURES	PP	100	40	45	P
08. POWER DEVICES AND MACHINES	PP	100	40	40	P		18. DATA STRUCTURES	PR	50	20	37	P
09. NETWORK AND POWER LAB.	TW	50	20	33	P C		19. COMMUNICATION THEORY	PP	100	40	31	F
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	30	P C		20. COMMUNICATION THEORY	OR	50	20	21	P
							21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	36	P

GRAND TOTAL = 663/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

S8053008	AIKAL SANKET KABIRNATH			SANGEETHA			, 71100723D			PICT		S8053008
01. SIGNAL AND SYSTEMS	PP	100	40	51	P C		11. ENGINEERING MATHEMATICS III	PP	100	40	78	P
02. SIGNAL AND SYSTEMS	OR	50	20	42	P C		12. ENGINEERING MATHEMATICS III	TW	25	10	17	P
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	41	P		13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	41	P
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	36	P C		14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	36	P
05. NETWORK ANALYSIS	PP	100	40	53	P C		15. ELECTROMAGNETIC	PP	100	40	45	P
06. DIGITAL LOGIC DESIGN	PP	100	40	67	P C		16. ELECTROMAGNETIC	TW	25	10	17	P
07. DIGITAL LOGIC DESIGN	PR	50	20	30	P		17. DATA STRUCTURES	PP	100	40	56	P
08. POWER DEVICES AND MACHINES	PP	100	40	46	P C		18. DATA STRUCTURES	PR	50	20	36	P
09. NETWORK AND POWER LAB.	TW	50	20	33	P C		19. COMMUNICATION THEORY	PP	100	40	47	P
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	35	P C		20. COMMUNICATION THEORY	OR	50	20	25	P
							21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	38	P

GRAND TOTAL = 870/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

S8053009	ALI ASGER MODI			ZEENAT			, 71100724B			PICT		S8053009
01. SIGNAL AND SYSTEMS	PP	100	40	41	P C		11. ENGINEERING MATHEMATICS III	PP	100	40	40	P
02. SIGNAL AND SYSTEMS	OR	50	20	22	P C		12. ENGINEERING MATHEMATICS III	TW	25	10	14	P
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	40	P		13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	20	F
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	30	P C		14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	25	P
05. NETWORK ANALYSIS	PP	100	40	32	F		15. ELECTROMAGNETIC	PP	100	40	15	F
06. DIGITAL LOGIC DESIGN	PP	100	40	42	P C		16. ELECTROMAGNETIC	TW	25	10	15	P
07. DIGITAL LOGIC DESIGN	PR	50	20	25	P C		17. DATA STRUCTURES	PP	100	40	54	P
08. POWER DEVICES AND MACHINES	PP	100	40	43	P		18. DATA STRUCTURES	PR	50	20	37	P
09. NETWORK AND POWER LAB.	TW	50	20	34	P C		19. COMMUNICATION THEORY	PP	100	40	40	P
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	30	P C		20. COMMUNICATION THEORY	OR	50	20	25	P
							21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	30	P

GRAND TOTAL = 654/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

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S8053010	AMBURE GANESH VITHALRAO				ANITA				, 71100726J				PICT		S8053010
01. SIGNAL AND SYSTEMS	PP	100	40	57	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	74	P				
02. SIGNAL AND SYSTEMS	OR	50	20	38	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	12	P				
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	75	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	53	P				
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	26	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	34	P				
05. NETWORK ANALYSIS	PP	100	40	55	P C	15. ELECTROMAGNETIC	PP	100	40	53	P				
06. DIGITAL LOGIC DESIGN	PP	100	40	53	P C	16. ELECTROMAGNETIC	TW	25	10	13	P				
07. DIGITAL LOGIC DESIGN	PR	50	20	27	P C	17. DATA STRUCTURES	PP	100	40	53	P				
08. POWER DEVICES AND MACHINES	PP	100	40	40	P C	18. DATA STRUCTURES	PR	50	20	36	P				
09. NETWORK AND POWER LAB.	TW	50	20	24	P C	19. COMMUNICATION THEORY	PP	100	40	68	P				
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	31	P C	20. COMMUNICATION THEORY	OR	50	20	25	P				
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	33	P				

GRAND TOTAL = 880/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

S8053011	ANUPAM BHATTACHARJEE				MOUSUMI				, 71100731E				PICT		S8053011
01. SIGNAL AND SYSTEMS	PP	100	40	64	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	83	P				
02. SIGNAL AND SYSTEMS	OR	50	20	42	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	17	P				
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	61	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	40	P				
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	43	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	44	P				
05. NETWORK ANALYSIS	PP	100	40	49	P C	15. ELECTROMAGNETIC	PP	100	40	68	P				
06. DIGITAL LOGIC DESIGN	PP	100	40	70	P C	16. ELECTROMAGNETIC	TW	25	10	17	P				
07. DIGITAL LOGIC DESIGN	PR	50	20	39	P C	17. DATA STRUCTURES	PP	100	40	61	P				
08. POWER DEVICES AND MACHINES	PP	100	40	56	P C	18. DATA STRUCTURES	PR	50	20	46	P				
09. NETWORK AND POWER LAB.	TW	50	20	40	P C	19. COMMUNICATION THEORY	PP	100	40	49	P				
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	39	P C	20. COMMUNICATION THEORY	OR	50	20	37	P				
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	31	P				

GRAND TOTAL = 996/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

S8053012	BAGALE PRASHANT MUTYAPPA				MAHADEVI				, 71236306J				PICT		S8053012
01. SIGNAL AND SYSTEMS	PP	100	40	40	P	11. ENGINEERING MATHEMATICS III	PP	100	40	23	F				
02. SIGNAL AND SYSTEMS	OR	50	20	34	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	18	P				
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	60	P	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	63	P				
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	20	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	30	P				
05. NETWORK ANALYSIS	PP	100	40	40	P	15. ELECTROMAGNETIC	PP	100	40	24	F				
06. DIGITAL LOGIC DESIGN	PP	100	40	40	P C	16. ELECTROMAGNETIC	TW	25	10	18	P				
07. DIGITAL LOGIC DESIGN	PR	50	20	23	P C	17. DATA STRUCTURES	PP	100	40	60	P				
08. POWER DEVICES AND MACHINES	PP	100	40	43	P C	18. DATA STRUCTURES	PR	50	20	39	P				
09. NETWORK AND POWER LAB.	TW	50	20	38	P C	19. COMMUNICATION THEORY	PP	100	40	47	P				
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	41	P C	20. COMMUNICATION THEORY	OR	50	20	28	P				
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	39	P				

GRAND TOTAL = 768/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
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S8053013	BAGALE PRITESH NAMDEV				JIJABAI		, 71100737D			PICT		S8053013
01. SIGNAL AND SYSTEMS	PP	100	40	52	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	77	P	
02. SIGNAL AND SYSTEMS	OR	50	20	38	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	18	P	
03. SOLID STATES DEVICES AND CIRCUITS	SPP	100	40	52	P	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	40	P	
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	31	P	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	38	P	
05. NETWORK ANALYSIS	PP	100	40	49	P C	15. ELECTROMAGNETIC	PP	100	40	57	P	
06. DIGITAL LOGIC DESIGN	PP	100	40	76	P C	16. ELECTROMAGNETIC	TW	25	10	19	P	
07. DIGITAL LOGIC DESIGN	PR	50	20	29	P C	17. DATA STRUCTURES	PP	100	40	45	P	
08. POWER DEVICES AND MACHINES	PP	100	40	43	P	18. DATA STRUCTURES	PR	50	20	39	P	
09. NETWORK AND POWER LAB.	TW	50	20	32	P C	19. COMMUNICATION THEORY	PP	100	40	49	P	
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	36	P C	20. COMMUNICATION THEORY	OR	50	20	28	P	
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	39	P	

GRAND TOTAL = 887/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

S8053014	BAGUL KAUSTUBH HITENDRA				JAYSHRI		, 71100738B			PICT		S8053014
01. SIGNAL AND SYSTEMS	PP	100	40	58	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	96	P	
02. SIGNAL AND SYSTEMS	OR	50	20	36	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	22	P	
03. SOLID STATES DEVICES AND CIRCUITS	SPP	100	40	77	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	49	P	
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	44	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	38	P	
05. NETWORK ANALYSIS	PP	100	40	56	P C	15. ELECTROMAGNETIC	PP	100	40	65	P	
06. DIGITAL LOGIC DESIGN	PP	100	40	69	P C	16. ELECTROMAGNETIC	TW	25	10	22	P	
07. DIGITAL LOGIC DESIGN	PR	50	20	30	P C	17. DATA STRUCTURES	PP	100	40	59	P	
08. POWER DEVICES AND MACHINES	PP	100	40	63	P C	18. DATA STRUCTURES	PR	50	20	48	P	
09. NETWORK AND POWER LAB.	TW	50	20	41	P C	19. COMMUNICATION THEORY	PP	100	40	53	P	
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	42	P C	20. COMMUNICATION THEORY	OR	50	20	33	P	
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	42	P	

GRAND TOTAL = 1043/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

S8053015	BHOSALE ABHISHEK BHAUSAHEB				MIRA		, 71100750M			PICT		S8053015
01. SIGNAL AND SYSTEMS	PP	100	40	40	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	43	P	
02. SIGNAL AND SYSTEMS	OR	50	20	34	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	17	P	
03. SOLID STATES DEVICES AND CIRCUITS	SPP	100	40	45	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	45	P	
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	42	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	35	P	
05. NETWORK ANALYSIS	PP	100	40	47	P C	15. ELECTROMAGNETIC	PP	100	40	51	P	
06. DIGITAL LOGIC DESIGN	PP	100	40	40	P C	16. ELECTROMAGNETIC	TW	25	10	18	P	
07. DIGITAL LOGIC DESIGN	PR	50	20	27	P C	17. DATA STRUCTURES	PP	100	40	53	P	
08. POWER DEVICES AND MACHINES	PP	100	40	43	P C	18. DATA STRUCTURES	PR	50	20	34	P	
09. NETWORK AND POWER LAB.	TW	50	20	33	P C	19. COMMUNICATION THEORY	PP	100	40	45	P	
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	36	P C	20. COMMUNICATION THEORY	OR	50	20	24	P	
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	39	P	

GRAND TOTAL = 791/1500, RESULT: SECOND CLASS

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8053016	BHUTADA GOPAL MADHUSUDAN				BASANTI				, 71100752H				PICT		S8053016
01. SIGNAL AND SYSTEMS	PP	100	40	65	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	96	P				
02. SIGNAL AND SYSTEMS	OR	50	20	39	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	22	P				
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	64	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	60	P				
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	43	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	25	P				
05. NETWORK ANALYSIS	PP	100	40	67	P C	15. ELECTROMAGNETIC	PP	100	40	63	P				
06. DIGITAL LOGIC DESIGN	PP	100	40	79	P C	16. ELECTROMAGNETIC	TW	25	10	22	P				
07. DIGITAL LOGIC DESIGN	PR	50	20	30	P C	17. DATA STRUCTURES	PP	100	40	65	P				
08. POWER DEVICES AND MACHINES	PP	100	40	61	P C	18. DATA STRUCTURES	PR	50	20	31	P				
09. NETWORK AND POWER LAB.	TW	50	20	40	P C	19. COMMUNICATION THEORY	PP	100	40	72	P				
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	40	P C	20. COMMUNICATION THEORY	OR	50	20	30	P				
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	42	P				

GRAND TOTAL = 1056/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

S8053017	BORSE SWATI NANA				KALPANA				, 71100754D				PICT		S8053017
01. SIGNAL AND SYSTEMS	PP	100	40	23	F	11. ENGINEERING MATHEMATICS III	PP	100	40	40	P				
02. SIGNAL AND SYSTEMS	OR	50	20	38	P	12. ENGINEERING MATHEMATICS III	TW	25	10	18	P				
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	18	F	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	22	F				
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	23	P	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	20	P				
05. NETWORK ANALYSIS	PP	100	40	29	F	15. ELECTROMAGNETIC	PP	100	40	09	F				
06. DIGITAL LOGIC DESIGN	PP	100	40	28	F	16. ELECTROMAGNETIC	TW	25	10	17	P				
07. DIGITAL LOGIC DESIGN	PR	50	20	20	P	17. DATA STRUCTURES	PP	100	40	45	P				
08. POWER DEVICES AND MACHINES	PP	100	40	40	P	18. DATA STRUCTURES	PR	50	20	32	P				
09. NETWORK AND POWER LAB.	TW	50	20	27	P C	19. COMMUNICATION THEORY	PP	100	40	27	F				
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	32	P C	20. COMMUNICATION THEORY	OR	50	20	15	F				
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	33	P				

GRAND TOTAL = 556/1500, RESULT: FAILS

ORDN. 1 MARKS :

S8053018	BUDHWANT UMA SHYAMRAO				NANDINI				, 71100757J				PICT		S8053018
01. SIGNAL AND SYSTEMS	PP	100	40	41	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	25	F				
02. SIGNAL AND SYSTEMS	OR	50	20	27	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	16	P				
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	45	P	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	40	P				
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	28	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	28	P				
05. NETWORK ANALYSIS	PP	100	40	40	P C	15. ELECTROMAGNETIC	PP	100	40	40	P				
06. DIGITAL LOGIC DESIGN	PP	100	40	43	P C	16. ELECTROMAGNETIC	TW	25	10	17	P				
07. DIGITAL LOGIC DESIGN	PR	50	20	28	P C	17. DATA STRUCTURES	PP	100	40	51	P				
08. POWER DEVICES AND MACHINES	PP	100	40	41	P	18. DATA STRUCTURES	PR	50	20	33	P				
09. NETWORK AND POWER LAB.	TW	50	20	38	P C	19. COMMUNICATION THEORY	PP	100	40	31	F				
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	34	P C	20. COMMUNICATION THEORY	OR	50	20	25	P				
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	32	P				

GRAND TOTAL = 703/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8053019	BUTALA KARAN SANDESH				NANDINI		, 71100759E			PICT		S8053019
01. SIGNAL AND SYSTEMS	PP	100	40	79	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	64	P	
02. SIGNAL AND SYSTEMS	OR	50	20	44	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	21	P	
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	50	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	50	P	
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	26	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	25	P	
05. NETWORK ANALYSIS	PP	100	40	52	P C	15. ELECTROMAGNETIC	PP	100	40	73	P	
06. DIGITAL LOGIC DESIGN	PP	100	40	57	P C	16. ELECTROMAGNETIC	TW	25	10	21	P	
07. DIGITAL LOGIC DESIGN	PR	50	20	20	P C	17. DATA STRUCTURES	PP	100	40	59	P	
08. POWER DEVICES AND MACHINES	PP	100	40	40	P C	18. DATA STRUCTURES	PR	50	20	47	P	
09. NETWORK AND POWER LAB.	TW	50	20	32	P C	19. COMMUNICATION THEORY	PP	100	40	53	P	
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	35	P C	20. COMMUNICATION THEORY	OR	50	20	30	P	
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	40	P	

GRAND TOTAL = 918/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

S8053020	CHANDAK ANKIT PANDURANGJI				MAYA		, 71100760J			PICT		S8053020
01. SIGNAL AND SYSTEMS	PP	100	40	45	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	26	F	
02. SIGNAL AND SYSTEMS	OR	50	20	20	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	18	P	
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	48	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	18	F	
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	40	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	36	P	
05. NETWORK ANALYSIS	PP	100	40	12	F	15. ELECTROMAGNETIC	PP	100	40	13	F	
06. DIGITAL LOGIC DESIGN	PP	100	40	60	P C	16. ELECTROMAGNETIC	TW	25	10	18	P	
07. DIGITAL LOGIC DESIGN	PR	50	20	35	P C	17. DATA STRUCTURES	PP	100	40	43	P	
08. POWER DEVICES AND MACHINES	PP	100	40	28	F	18. DATA STRUCTURES	PR	50	20	25	P	
09. NETWORK AND POWER LAB.	TW	50	20	37	P C	19. COMMUNICATION THEORY	PP	100	40	45	P	
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	35	P C	20. COMMUNICATION THEORY	OR	50	20	21	P	
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	37	P	

GRAND TOTAL = 660/1500, RESULT: FAILS

ORDN. 1 MARKS :

S8053021	CHANDAK RASIKA RAMGOPAL				ALAKNANDA		, 71236307G			PICT		S8053021
01. SIGNAL AND SYSTEMS	PP	100	40	42	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	51	P	
02. SIGNAL AND SYSTEMS	OR	50	20	34	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	22	P	
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	40	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	42	P	
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	30	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	25	P	
05. NETWORK ANALYSIS	PP	100	40	40	P C	15. ELECTROMAGNETIC	PP	100	40	57	P	
06. DIGITAL LOGIC DESIGN	PP	100	40	50	P C	16. ELECTROMAGNETIC	TW	25	10	19	P	
07. DIGITAL LOGIC DESIGN	PR	50	20	28	P C	17. DATA STRUCTURES	PP	100	40	57	P	
08. POWER DEVICES AND MACHINES	PP	100	40	40	P C	18. DATA STRUCTURES	PR	50	20	34	P	
09. NETWORK AND POWER LAB.	TW	50	20	42	P C	19. COMMUNICATION THEORY	PP	100	40	48	P	
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	42	P C	20. COMMUNICATION THEORY	OR	50	20	30	P	
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	41	P	

GRAND TOTAL = 814/1500, RESULT: SECOND CLASS

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8053022	CHANDAN PANCHLORIA				SUMITRADEVI		, 71100761G			PICT		S8053022
01. SIGNAL AND SYSTEMS	PP	100	40	40	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	59	P	
02. SIGNAL AND SYSTEMS	OR	50	20	30	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	19	P	
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	40	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	30	F	
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	30	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	15	F	
05. NETWORK ANALYSIS	PP	100	40	40	P	15. ELECTROMAGNETIC	PP	100	40	31	F	
06. DIGITAL LOGIC DESIGN	PP	100	40	45	P C	16. ELECTROMAGNETIC	TW	25	10	20	P	
07. DIGITAL LOGIC DESIGN	PR	50	20	22	P C	17. DATA STRUCTURES	PP	100	40	57	P	
08. POWER DEVICES AND MACHINES	PP	100	40	40	P C	18. DATA STRUCTURES	PR	50	20	36	P	
09. NETWORK AND POWER LAB.	TW	50	20	39	P C	19. COMMUNICATION THEORY	PP	100	40	40	P	
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	39	P C	20. COMMUNICATION THEORY	OR	50	20	30	P	
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	38	P	

GRAND TOTAL = 740/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

S8053023	CHAVARE PRANAV NITIN				NITISHA		, 71100765K			PICT		S8053023
01. SIGNAL AND SYSTEMS	PP	100	40	71	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	86	P	
02. SIGNAL AND SYSTEMS	OR	50	20	42	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	23	P	
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	61	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	63	P	
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	44	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	40	P	
05. NETWORK ANALYSIS	PP	100	40	44	P C	15. ELECTROMAGNETIC	PP	100	40	48	P	
06. DIGITAL LOGIC DESIGN	PP	100	40	67	P C	16. ELECTROMAGNETIC	TW	25	10	22	P	
07. DIGITAL LOGIC DESIGN	PR	50	20	35	P C	17. DATA STRUCTURES	PP	100	40	63	P	
08. POWER DEVICES AND MACHINES	PP	100	40	57	P C	18. DATA STRUCTURES	PR	50	20	46	P	
09. NETWORK AND POWER LAB.	TW	50	20	43	P C	19. COMMUNICATION THEORY	PP	100	40	40	P	
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	41	P C	20. COMMUNICATION THEORY	OR	50	20	42	P	
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	42	P	

GRAND TOTAL = 1020/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

S8053024	CHAVHAN YOGESH LAXMAN				ASHA		, 71100766H			PICT		S8053024
01. SIGNAL AND SYSTEMS	PP	100	40	70	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	87	P	
02. SIGNAL AND SYSTEMS	OR	50	20	39	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	20	P	
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	54	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	50	P	
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	36	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	38	P	
05. NETWORK ANALYSIS	PP	100	40	52	P C	15. ELECTROMAGNETIC	PP	100	40	52	P	
06. DIGITAL LOGIC DESIGN	PP	100	40	63	P C	16. ELECTROMAGNETIC	TW	25	10	21	P	
07. DIGITAL LOGIC DESIGN	PR	50	20	30	P C	17. DATA STRUCTURES	PP	100	40	63	P	
08. POWER DEVICES AND MACHINES	PP	100	40	43	P C	18. DATA STRUCTURES	PR	50	20	38	P	
09. NETWORK AND POWER LAB.	TW	50	20	38	P C	19. COMMUNICATION THEORY	PP	100	40	46	P	
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	42	P C	20. COMMUNICATION THEORY	OR	50	20	25	P	
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	41	P	

GRAND TOTAL = 948/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8053025	CHHATRE JAI VILAS			HEMA		, 71100767F			PICT		S8053025
01. SIGNAL AND SYSTEMS	PP	100	40	60	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	74	P
02. SIGNAL AND SYSTEMS	OR	50	20	39	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	23	P
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	60	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	40	P
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	40	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	36	P
05. NETWORK ANALYSIS	PP	100	40	50	P C	15. ELECTROMAGNETIC	PP	100	40	65	P
06. DIGITAL LOGIC DESIGN	PP	100	40	46	P C	16. ELECTROMAGNETIC	TW	25	10	21	P
07. DIGITAL LOGIC DESIGN	PR	50	20	37	P C	17. DATA STRUCTURES	PP	100	40	53	P
08. POWER DEVICES AND MACHINES	PP	100	40	40	P C	18. DATA STRUCTURES	PR	50	20	47	P
09. NETWORK AND POWER LAB.	TW	50	20	42	P C	19. COMMUNICATION THEORY	PP	100	40	43	P
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	42	P C	20. COMMUNICATION THEORY	OR	50	20	25	P
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	42	P

GRAND TOTAL = 925/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

S8053026	CHIDRAWAR DNYANESHWARI GIRISH			GEETA		, 71100768D			PICT		S8053026
01. SIGNAL AND SYSTEMS	PP	100	40	64	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	94	P
02. SIGNAL AND SYSTEMS	OR	50	20	33	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	23	P
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	65	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	46	P
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	35	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	41	P
05. NETWORK ANALYSIS	PP	100	40	65	P C	15. ELECTROMAGNETIC	PP	100	40	60	P
06. DIGITAL LOGIC DESIGN	PP	100	40	75	P C	16. ELECTROMAGNETIC	TW	25	10	22	P
07. DIGITAL LOGIC DESIGN	PR	50	20	30	P C	17. DATA STRUCTURES	PP	100	40	65	P
08. POWER DEVICES AND MACHINES	PP	100	40	64	P C	18. DATA STRUCTURES	PR	50	20	48	P
09. NETWORK AND POWER LAB.	TW	50	20	41	P C	19. COMMUNICATION THEORY	PP	100	40	54	P
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	40	P C	20. COMMUNICATION THEORY	OR	50	20	29	P
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	42	P

GRAND TOTAL = 1036/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

S8053027	CHITALE JANHAVI GOVIND			VARSHA		, 71100769B			PICT		S8053027
01. SIGNAL AND SYSTEMS	PP	100	40	52	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	84	P
02. SIGNAL AND SYSTEMS	OR	50	20	38	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	22	P
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	50	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	43	P
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	38	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	37	P
05. NETWORK ANALYSIS	PP	100	40	40	P C	15. ELECTROMAGNETIC	PP	100	40	40	P
06. DIGITAL LOGIC DESIGN	PP	100	40	73	P C	16. ELECTROMAGNETIC	TW	25	10	20	P
07. DIGITAL LOGIC DESIGN	PR	50	20	42	P C	17. DATA STRUCTURES	PP	100	40	56	P
08. POWER DEVICES AND MACHINES	PP	100	40	41	P C	18. DATA STRUCTURES	PR	50	20	35	P
09. NETWORK AND POWER LAB.	TW	50	20	42	P C	19. COMMUNICATION THEORY	PP	100	40	40	P
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	41	P C	20. COMMUNICATION THEORY	OR	50	20	34	P
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	40	P

GRAND TOTAL = 908/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8053028	DESHMUKH AMRUTA VILAS				ANJALI			, 71100779K				PICT		S8053028
01. SIGNAL AND SYSTEMS	PP	100	40	60	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	64	P			
02. SIGNAL AND SYSTEMS	OR	50	20	32	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	21	P			
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	67	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	49	P			
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	38	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	38	P			
05. NETWORK ANALYSIS	PP	100	40	54	P C	15. ELECTROMAGNETIC	PP	100	40	77	P			
06. DIGITAL LOGIC DESIGN	PP	100	40	74	P C	16. ELECTROMAGNETIC	TW	25	10	18	P			
07. DIGITAL LOGIC DESIGN	PR	50	20	28	P C	17. DATA STRUCTURES	PP	100	40	57	P			
08. POWER DEVICES AND MACHINES	PP	100	40	65	P C	18. DATA STRUCTURES	PR	50	20	47	P			
09. NETWORK AND POWER LAB.	TW	50	20	36	P C	19. COMMUNICATION THEORY	PP	100	40	58	P			
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	41	P C	20. COMMUNICATION THEORY	OR	50	20	40	P			
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	39	P			

GRAND TOTAL = 1003/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

S8053029	DESHMUKH ASHUTOSH VILASRAO					SUNITA	, 71100780C	,		PICT	, S8053029			
01. SIGNAL AND SYSTEMS	PP	100	40	49	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	54	P			
02. SIGNAL AND SYSTEMS	OR	50	20	22	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	11	P			
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	41	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	32	F			
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	24	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	05	F			
05. NETWORK ANALYSIS	PP	100	40	46	P C	15. ELECTROMAGNETIC	PP	100	40	31	F			
06. DIGITAL LOGIC DESIGN	PP	100	40	40	P C	16. ELECTROMAGNETIC	TW	25	10	11	P			
07. DIGITAL LOGIC DESIGN	PR	50	20	27	P	17. DATA STRUCTURES	PP	100	40	AA	F			
08. POWER DEVICES AND MACHINES	PP	100	40	40	P C	18. DATA STRUCTURES	PR	50	20	05	F			
09. NETWORK AND POWER LAB.	TW	50	20	29	P C	19. COMMUNICATION THEORY	PP	100	40	40	P			
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	25	P C	20. COMMUNICATION THEORY	OR	50	20	20	P			
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	24	P			

GRAND TOTAL = 576/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

S8053030	DESHPANDE ADITI VIDHYADHAR					ALKA	, 71100781M				,	PICT				,	S8053030
01. SIGNAL AND SYSTEMS	PP	100	40	45	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	49	P						
02. SIGNAL AND SYSTEMS	OR	50	20	20	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	16	P						
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	40	P	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	40	P						
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	30	P	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	32	P						
05. NETWORK ANALYSIS	PP	100	40	40	P C	15. ELECTROMAGNETIC	PP	100	40	40	P						
06. DIGITAL LOGIC DESIGN	PP	100	40	54	P C	16. ELECTROMAGNETIC	TW	25	10	18	P						
07. DIGITAL LOGIC DESIGN	PR	50	20	21	P C	17. DATA STRUCTURES	PP	100	40	52	P						
08. POWER DEVICES AND MACHINES	PP	100	40	40	P C	18. DATA STRUCTURES	PR	50	20	41	P						
09. NETWORK AND POWER LAB.	TW	50	20	31	P C	19. COMMUNICATION THEORY	PP	100	40	51	P						
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	37	P C	20. COMMUNICATION THEORY	OR	50	20	21	P						
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	39	P						

GRAND TOTAL = 757/1500, RESULT: SECOND CLASS

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8053031	DHANDAR RUCHITA PANDURANG					VIJAYA		, 71236308E		, PICT		, S8053031		
01. SIGNAL AND SYSTEMS	PP	100	40	40	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	64	P			
02. SIGNAL AND SYSTEMS	OR	50	20	24	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	22	P			
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	40	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	40	P			
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	33	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	34	P			
05. NETWORK ANALYSIS	PP	100	40	40	P C	15. ELECTROMAGNETIC	PP	100	40	52	P			
06. DIGITAL LOGIC DESIGN	PP	100	40	65	P C	16. ELECTROMAGNETIC	TW	25	10	19	P			
07. DIGITAL LOGIC DESIGN	PR	50	20	22	P	17. DATA STRUCTURES	PP	100	40	55	P			
08. POWER DEVICES AND MACHINES	PP	100	40	48	P C	18. DATA STRUCTURES	PR	50	20	42	P			
09. NETWORK AND POWER LAB.	TW	50	20	42	P C	19. COMMUNICATION THEORY	PP	100	40	44	P			
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	41	P C	20. COMMUNICATION THEORY	OR	50	20	26	P			
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	41	P			

GRAND TOTAL = 834/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

S8053032	DHANE AKSHAY MUKUNDRAO					SHARADA					, 71236309C					, PICT					, S8053032				
01. SIGNAL AND SYSTEMS	PP	100	40	46	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	77	P														
02. SIGNAL AND SYSTEMS	OR	50	20	23	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	22	P														
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	60	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	42	P														
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	34	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	40	P														
05. NETWORK ANALYSIS	PP	100	40	40	P C	15. ELECTROMAGNETIC	PP	100	40	42	P														
06. DIGITAL LOGIC DESIGN	PP	100	40	62	P C	16. ELECTROMAGNETIC	TW	25	10	18	P														
07. DIGITAL LOGIC DESIGN	PR	50	20	30	P C	17. DATA STRUCTURES	PP	100	40	61	P														
08. POWER DEVICES AND MACHINES	PP	100	40	49	P C	18. DATA STRUCTURES	PR	50	20	39	P														
09. NETWORK AND POWER LAB.	TW	50	20	40	P C	19. COMMUNICATION THEORY	PP	100	40	54	P														
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	39	P C	20. COMMUNICATION THEORY	OR	50	20	25	P														
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	42	P														

GRAND TOTAL = 885/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

S8053033	DHODI SHRADDHA MADHU					ARUNA		, 71045415F		,	PICT		, S8053033	
01. SIGNAL AND SYSTEMS	PP	100	40	49	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	40	P			
02. SIGNAL AND SYSTEMS	OR	50	20	25	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	18	P			
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	23	F	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	23	F			
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	24	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	27	P			
05. NETWORK ANALYSIS	PP	100	40	29	F	15. ELECTROMAGNETIC	PP	100	40	14	F			
06. DIGITAL LOGIC DESIGN	PP	100	40	46	P C	16. ELECTROMAGNETIC	TW	25	10	16	P			
07. DIGITAL LOGIC DESIGN	PR	50	20	22	P C	17. DATA STRUCTURES	PP	100	40	40	P			
08. POWER DEVICES AND MACHINES	PP	100	40	30	F	18. DATA STRUCTURES	PR	50	20	39	P			
09. NETWORK AND POWER LAB.	TW	50	20	20	P C	19. COMMUNICATION THEORY	PP	100	40	40	P			
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	25	P C	20. COMMUNICATION THEORY	OR	50	20	12	F			
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	32	P			

GRAND TOTAL = 594/1500, RESULT: FAILS

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8053034	DHOLE NILAKSHI AJAY				CHHAYA		, 71100786B			PICT		S8053034
01. SIGNAL AND SYSTEMS	PP	100	40	58	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	71	P	
02. SIGNAL AND SYSTEMS	OR	50	20	28	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	22	P	
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	64	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	43	P	
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	32	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	28	P	
05. NETWORK ANALYSIS	PP	100	40	67	P C	15. ELECTROMAGNETIC	PP	100	40	64	P	
06. DIGITAL LOGIC DESIGN	PP	100	40	64	P C	16. ELECTROMAGNETIC	TW	25	10	19	P	
07. DIGITAL LOGIC DESIGN	PR	50	20	27	P C	17. DATA STRUCTURES	PP	100	40	61	P	
08. POWER DEVICES AND MACHINES	PP	100	40	57	P C	18. DATA STRUCTURES	PR	50	20	46	P	
09. NETWORK AND POWER LAB.	TW	50	20	42	P C	19. COMMUNICATION THEORY	PP	100	40	50	P	
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	38	P C	20. COMMUNICATION THEORY	OR	50	20	30	P	
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	38	P	

GRAND TOTAL = 949/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

S8053035	DOLAS LUBDHA RAJU				SUNITA		, 71236310G			PICT		S8053035
01. SIGNAL AND SYSTEMS	PP	100	40	40	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	25	F	
02. SIGNAL AND SYSTEMS	OR	50	20	22	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	14	P	
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	41	P	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	40	P	
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	21	P	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	20	P	
05. NETWORK ANALYSIS	PP	100	40	31	F	15. ELECTROMAGNETIC	PP	100	40	22	F	
06. DIGITAL LOGIC DESIGN	PP	100	40	40	P	16. ELECTROMAGNETIC	TW	25	10	14	P	
07. DIGITAL LOGIC DESIGN	PR	50	20	28	P	17. DATA STRUCTURES	PP	100	40	40	P	
08. POWER DEVICES AND MACHINES	PP	100	40	40	P	18. DATA STRUCTURES	PR	50	20	36	P	
09. NETWORK AND POWER LAB.	TW	50	20	32	P C	19. COMMUNICATION THEORY	PP	100	40	29	F	
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	36	P C	20. COMMUNICATION THEORY	OR	50	20	29	P	
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	30	P	

GRAND TOTAL = 630/1500, RESULT: FAILS

ORDN. 1 MARKS :

S8053036	DONGARE MANOJ JAYSING				SHOBHA		, 71236311E			PICT		S8053036
01. SIGNAL AND SYSTEMS	PP	100	40	40	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	26	F	
02. SIGNAL AND SYSTEMS	OR	50	20	32	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	21	P	
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	56	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	58	P	
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	35	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	32	P	
05. NETWORK ANALYSIS	PP	100	40	40	P C	15. ELECTROMAGNETIC	PP	100	40	40	P	
06. DIGITAL LOGIC DESIGN	PP	100	40	61	P C	16. ELECTROMAGNETIC	TW	25	10	18	P	
07. DIGITAL LOGIC DESIGN	PR	50	20	21	P C	17. DATA STRUCTURES	PP	100	40	57	P	
08. POWER DEVICES AND MACHINES	PP	100	40	62	P C	18. DATA STRUCTURES	PR	50	20	38	P	
09. NETWORK AND POWER LAB.	TW	50	20	40	P C	19. COMMUNICATION THEORY	PP	100	40	57	P	
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	40	P C	20. COMMUNICATION THEORY	OR	50	20	35	P	
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	41	P	

GRAND TOTAL = 850/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8053037	GAIKWAD GAYATRI DILIP				DIPALI		, 71100792G			PICT		S8053037
01. SIGNAL AND SYSTEMS	PP	100	40	23	F		11. ENGINEERING MATHEMATICS III	PP	100	40	40	P
02. SIGNAL AND SYSTEMS	OR	50	20	29	P C		12. ENGINEERING MATHEMATICS III	TW	25	10	18	P
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	22	F		13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	20	F
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	25	P C		14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	25	P
05. NETWORK ANALYSIS	PP	100	40	19	F		15. ELECTROMAGNETIC	PP	100	40	26	F
06. DIGITAL LOGIC DESIGN	PP	100	40	47	P C		16. ELECTROMAGNETIC	TW	25	10	18	P
07. DIGITAL LOGIC DESIGN	PR	50	20	20	P C		17. DATA STRUCTURES	PP	100	40	32	F
08. POWER DEVICES AND MACHINES	PP	100	40	11	F		18. DATA STRUCTURES	PR	50	20	41	P
09. NETWORK AND POWER LAB.	TW	50	20	30	P C		19. COMMUNICATION THEORY	PP	100	40	40	P
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	31	P C		20. COMMUNICATION THEORY	OR	50	20	22	P
							21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	36	P

GRAND TOTAL = 575/1500, RESULT: FAILS

ORDN. 1 MARKS :

S8053038	GAIKWAD PRATIKSHA JAGANNATH				ANITA		, 71100794C			PICT		S8053038
01. SIGNAL AND SYSTEMS	PP	100	40	42	P C		11. ENGINEERING MATHEMATICS III	PP	100	40	64	P
02. SIGNAL AND SYSTEMS	OR	50	20	30	P C		12. ENGINEERING MATHEMATICS III	TW	25	10	18	P
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	49	P C		13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	43	P
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	40	P C		14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	22	P
05. NETWORK ANALYSIS	PP	100	40	41	P C		15. ELECTROMAGNETIC	PP	100	40	52	P
06. DIGITAL LOGIC DESIGN	PP	100	40	56	P C		16. ELECTROMAGNETIC	TW	25	10	18	P
07. DIGITAL LOGIC DESIGN	PR	50	20	28	P		17. DATA STRUCTURES	PP	100	40	56	P
08. POWER DEVICES AND MACHINES	PP	100	40	53	P		18. DATA STRUCTURES	PR	50	20	46	P
09. NETWORK AND POWER LAB.	TW	50	20	41	P C		19. COMMUNICATION THEORY	PP	100	40	40	P
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	36	P C		20. COMMUNICATION THEORY	OR	50	20	25	P
							21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	36	P

GRAND TOTAL = 836/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

S8053039	GAVADE SACHIN SHIVAJI				SNEHAL		, 71100798F			PICT		S8053039
01. SIGNAL AND SYSTEMS	PP	100	40	60	P C		11. ENGINEERING MATHEMATICS III	PP	100	40	41	P
02. SIGNAL AND SYSTEMS	OR	50	20	26	P C		12. ENGINEERING MATHEMATICS III	TW	25	10	19	P
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	49	P		13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	53	P
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	26	P C		14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	25	P
05. NETWORK ANALYSIS	PP	100	40	43	P C		15. ELECTROMAGNETIC	PP	100	40	40	P
06. DIGITAL LOGIC DESIGN	PP	100	40	41	P C		16. ELECTROMAGNETIC	TW	25	10	19	P
07. DIGITAL LOGIC DESIGN	PR	50	20	25	P		17. DATA STRUCTURES	PP	100	40	47	P
08. POWER DEVICES AND MACHINES	PP	100	40	40	P C		18. DATA STRUCTURES	PR	50	20	37	P
09. NETWORK AND POWER LAB.	TW	50	20	30	P C		19. COMMUNICATION THEORY	PP	100	40	40	P
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	31	P C		20. COMMUNICATION THEORY	OR	50	20	30	P
							21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	39	P

GRAND TOTAL = 761/1500, RESULT: SECOND CLASS

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8053040	GENGAJE PRAJAKTA SHANTARAM				SUREKHA		, 71236312C			PICT		S8053040
01. SIGNAL AND SYSTEMS	PP	100	40	49	P		11. ENGINEERING MATHEMATICS III	PP	100	40	17	F
02. SIGNAL AND SYSTEMS	OR	50	20	25	P C		12. ENGINEERING MATHEMATICS III	TW	25	10	21	P
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	40	P		13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	31	F
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	25	P C		14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	22	P
05. NETWORK ANALYSIS	PP	100	40	30	F		15. ELECTROMAGNETIC	PP	100	40	27	F
06. DIGITAL LOGIC DESIGN	PP	100	40	40	P C		16. ELECTROMAGNETIC	TW	25	10	17	P
07. DIGITAL LOGIC DESIGN	PR	50	20	22	P		17. DATA STRUCTURES	PP	100	40	31	F
08. POWER DEVICES AND MACHINES	PP	100	40	48	P		18. DATA STRUCTURES	PR	50	20	38	P
09. NETWORK AND POWER LAB.	TW	50	20	40	P C		19. COMMUNICATION THEORY	PP	100	40	45	P
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	40	P C		20. COMMUNICATION THEORY	OR	50	20	23	P
							21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	40	P

GRAND TOTAL = 671/1500, RESULT: FAILS

ORDN. 1 MARKS :

S8053041	GHARDE SAGAR BHIMRAO				SHARDA		, 71236313M			PICT		S8053041
01. SIGNAL AND SYSTEMS	PP	100	40	48	P C		11. ENGINEERING MATHEMATICS III	PP	100	40	41	P
02. SIGNAL AND SYSTEMS	OR	50	20	34	P C		12. ENGINEERING MATHEMATICS III	TW	25	10	19	P
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	40	P C		13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	40	P
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	25	P C		14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	23	P
05. NETWORK ANALYSIS	PP	100	40	40	P C		15. ELECTROMAGNETIC	PP	100	40	42	P
06. DIGITAL LOGIC DESIGN	PP	100	40	57	P C		16. ELECTROMAGNETIC	TW	25	10	17	P
07. DIGITAL LOGIC DESIGN	PR	50	20	20	P C		17. DATA STRUCTURES	PP	100	40	59	P
08. POWER DEVICES AND MACHINES	PP	100	40	40	P C		18. DATA STRUCTURES	PR	50	20	35	P
09. NETWORK AND POWER LAB.	TW	50	20	33	P C		19. COMMUNICATION THEORY	PP	100	40	40	P
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	37	P C		20. COMMUNICATION THEORY	OR	50	20	12	F
							21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	40	P

GRAND TOTAL = 742/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

S8053042	GIRI POOJA RAVINDRA				LATA		, 71100805B			PICT		S8053042
01. SIGNAL AND SYSTEMS	PP	100	40	64	P C		11. ENGINEERING MATHEMATICS III	PP	100	40	63	P
02. SIGNAL AND SYSTEMS	OR	50	20	41	P C		12. ENGINEERING MATHEMATICS III	TW	25	10	21	P
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	42	P C		13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	53	P
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	34	P C		14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	24	P
05. NETWORK ANALYSIS	PP	100	40	51	P C		15. ELECTROMAGNETIC	PP	100	40	42	P
06. DIGITAL LOGIC DESIGN	PP	100	40	55	P C		16. ELECTROMAGNETIC	TW	25	10	21	P
07. DIGITAL LOGIC DESIGN	PR	50	20	37	P C		17. DATA STRUCTURES	PP	100	40	67	P
08. POWER DEVICES AND MACHINES	PP	100	40	40	P C		18. DATA STRUCTURES	PR	50	20	43	P
09. NETWORK AND POWER LAB.	TW	50	20	39	P C		19. COMMUNICATION THEORY	PP	100	40	40	P
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	37	P C		20. COMMUNICATION THEORY	OR	50	20	27	P
							21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	41	P

GRAND TOTAL = 882/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8053043	GOKHALE SIDDHARTH ADESH					SUJATA		, 71100808G		,	PICT		,	S8053043	
01. SIGNAL AND SYSTEMS	PP	100	40	73	P	C	11. ENGINEERING MATHEMATICS III	PP	100	40	71	P			
02. SIGNAL AND SYSTEMS	OR	50	20	46	P	C	12. ENGINEERING MATHEMATICS III	TW	25	10	22	P			
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	70	P	C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	60	P			
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	46	P	C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	44	P			
05. NETWORK ANALYSIS	PP	100	40	64	P	C	15. ELECTROMAGNETIC	PP	100	40	65	P			
06. DIGITAL LOGIC DESIGN	PP	100	40	84	P	C	16. ELECTROMAGNETIC	TW	25	10	22	P			
07. DIGITAL LOGIC DESIGN	PR	50	20	48	P	C	17. DATA STRUCTURES	PP	100	40	69	P			
08. POWER DEVICES AND MACHINES	PP	100	40	56	P	C	18. DATA STRUCTURES	PR	50	20	48	P			
09. NETWORK AND POWER LAB.	TW	50	20	44	P	C	19. COMMUNICATION THEORY	PP	100	40	55	P			
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	43	P	C	20. COMMUNICATION THEORY	OR	50	20	47	P			
							21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	45	P			

GRAND TOTAL = 1122/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

S8053044	HARSHIKA THUSU				ANITA		, 71100817F		,		PICT		, S8053044	
01. SIGNAL AND SYSTEMS	PP	100	40	53	P	C	11. ENGINEERING MATHEMATICS III	PP	100	40	56	P		
02. SIGNAL AND SYSTEMS	OR	50	20	42	P	C	12. ENGINEERING MATHEMATICS III	TW	25	10	22	P		
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	64	P	C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	62	P		
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	37	P	C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	37	P		
05. NETWORK ANALYSIS	PP	100	40	54	P	C	15. ELECTROMAGNETIC	PP	100	40	77	P		
06. DIGITAL LOGIC DESIGN	PP	100	40	58	P	C	16. ELECTROMAGNETIC	TW	25	10	22	P		
07. DIGITAL LOGIC DESIGN	PR	50	20	21	P	C	17. DATA STRUCTURES	PP	100	40	75	P		
08. POWER DEVICES AND MACHINES	PP	100	40	47	P	C	18. DATA STRUCTURES	PR	50	20	48	P		
09. NETWORK AND POWER LAB.	TW	50	20	42	P	C	19. COMMUNICATION THEORY	PP	100	40	60	P		
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	43	P	C	20. COMMUNICATION THEORY	OR	50	20	30	P		
							21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	43	P		

GRAND TOTAL = 993/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

S8053045	HIMANSHU JAIN					RACHANA					, 71100821D					, PICT					, S8053045				
01. SIGNAL AND SYSTEMS	PP	100	40	57	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	40	P														
02. SIGNAL AND SYSTEMS	OR	50	20	36	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	12	P														
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	28	F	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	40	P														
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	20	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	35	P														
05. NETWORK ANALYSIS	PP	100	40	40	P C	15. ELECTROMAGNETIC	PP	100	40	67	P														
06. DIGITAL LOGIC DESIGN	PP	100	40	47	P C	16. ELECTROMAGNETIC	TW	25	10	15	P														
07. DIGITAL LOGIC DESIGN	PR	50	20	25	P	17. DATA STRUCTURES	PP	100	40	40	P														
08. POWER DEVICES AND MACHINES	PP	100	40	40	P	18. DATA STRUCTURES	PR	50	20	33	P														
09. NETWORK AND POWER LAB.	TW	50	20	29	P C	19. COMMUNICATION THEORY	PP	100	40	40	P														
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	24	P C	20. COMMUNICATION THEORY	OR	50	20	23	P														
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	26	P														

GRAND TOTAL = 717/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8053046	JAGTAP MANJIRI ARVIND				SHUBHANGI			, 71100825G			PICT		S8053046
01. SIGNAL AND SYSTEMS	PP	100	40	40	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	15	F		
02. SIGNAL AND SYSTEMS	OR	50	20	30	P	12. ENGINEERING MATHEMATICS III	TW	25	10	15	P		
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	40	P	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	40	P		
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	25	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	25	P		
05. NETWORK ANALYSIS	PP	100	40	29	F	15. ELECTROMAGNETIC	PP	100	40	16	F		
06. DIGITAL LOGIC DESIGN	PP	100	40	40	P C	16. ELECTROMAGNETIC	TW	25	10	16	P		
07. DIGITAL LOGIC DESIGN	PR	50	20	21	P	17. DATA STRUCTURES	PP	100	40	40	P		
08. POWER DEVICES AND MACHINES	PP	100	40	40	P C	18. DATA STRUCTURES	PR	50	20	32	P		
09. NETWORK AND POWER LAB.	TW	50	20	33	P C	19. COMMUNICATION THEORY	PP	100	40	40	P		
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	33	P C	20. COMMUNICATION THEORY	OR	50	20	22	P		
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	38	P		

GRAND TOTAL = 630/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

S8053047	JAIN SAJAL ABHAYKUMAR				SUNITA			, 71100827C			PICT		S8053047
01. SIGNAL AND SYSTEMS	PP	100	40	40	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	41	P		
02. SIGNAL AND SYSTEMS	OR	50	20	28	P	12. ENGINEERING MATHEMATICS III	TW	25	10	16	P		
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	40	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	23	F		
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	30	P	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	22	P		
05. NETWORK ANALYSIS	PP	100	40	50	P C	15. ELECTROMAGNETIC	PP	100	40	27	F		
06. DIGITAL LOGIC DESIGN	PP	100	40	40	P C	16. ELECTROMAGNETIC	TW	25	10	15	P		
07. DIGITAL LOGIC DESIGN	PR	50	20	25	P C	17. DATA STRUCTURES	PP	100	40	46	P		
08. POWER DEVICES AND MACHINES	PP	100	40	45	P	18. DATA STRUCTURES	PR	50	20	32	P		
09. NETWORK AND POWER LAB.	TW	50	20	34	P C	19. COMMUNICATION THEORY	PP	100	40	40	P		
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	31	P C	20. COMMUNICATION THEORY	OR	50	20	23	P		
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	30	P		

GRAND TOTAL = 678/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

S8053048	JEDGULE UMASHANKAR RADHAKISAN				ANITA			, 71100831M			PICT		S8053048
01. SIGNAL AND SYSTEMS	PP	100	40	53	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	67	P		
02. SIGNAL AND SYSTEMS	OR	50	20	39	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	15	P		
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	41	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	64	P		
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	29	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	40	P		
05. NETWORK ANALYSIS	PP	100	40	40	P C	15. ELECTROMAGNETIC	PP	100	40	45	P		
06. DIGITAL LOGIC DESIGN	PP	100	40	57	P C	16. ELECTROMAGNETIC	TW	25	10	16	P		
07. DIGITAL LOGIC DESIGN	PR	50	20	23	P C	17. DATA STRUCTURES	PP	100	40	64	P		
08. POWER DEVICES AND MACHINES	PP	100	40	48	P C	18. DATA STRUCTURES	PR	50	20	47	P		
09. NETWORK AND POWER LAB.	TW	50	20	33	P C	19. COMMUNICATION THEORY	PP	100	40	51	P		
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	37	P C	20. COMMUNICATION THEORY	OR	50	20	25	P		
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	36	P		

GRAND TOTAL = 870/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8053049	JOSHI APOORV VIJAY				ANAGHA				, 71100835D				PICT		, S8053049
01. SIGNAL AND SYSTEMS	PP	100	40	69	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	76	P				
02. SIGNAL AND SYSTEMS	OR	50	20	27	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	22	P				
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	54	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	50	P				
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	37	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	43	P				
05. NETWORK ANALYSIS	PP	100	40	40	P C	15. ELECTROMAGNETIC	PP	100	40	51	P				
06. DIGITAL LOGIC DESIGN	PP	100	40	83	P C	16. ELECTROMAGNETIC	TW	25	10	20	P				
07. DIGITAL LOGIC DESIGN	PR	50	20	37	P C	17. DATA STRUCTURES	PP	100	40	56	P				
08. POWER DEVICES AND MACHINES	PP	100	40	49	P C	18. DATA STRUCTURES	PR	50	20	47	P				
09. NETWORK AND POWER LAB.	TW	50	20	44	P C	19. COMMUNICATION THEORY	PP	100	40	58	P				
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	42	P C	20. COMMUNICATION THEORY	OR	50	20	30	P				
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	41	P				

GRAND TOTAL = 976/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

S8053050	JOSHI CHAITRALI SANJAY				VARSHA				, 71100836B				PICT		, S8053050
01. SIGNAL AND SYSTEMS	PP	100	40	57	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	71	P				
02. SIGNAL AND SYSTEMS	OR	50	20	36	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	21	P				
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	40	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	63	P				
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	40	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	42	P				
05. NETWORK ANALYSIS	PP	100	40	60	P C	15. ELECTROMAGNETIC	PP	100	40	59	P				
06. DIGITAL LOGIC DESIGN	PP	100	40	43	P C	16. ELECTROMAGNETIC	TW	25	10	22	P				
07. DIGITAL LOGIC DESIGN	PR	50	20	30	P C	17. DATA STRUCTURES	PP	100	40	63	P				
08. POWER DEVICES AND MACHINES	PP	100	40	55	P C	18. DATA STRUCTURES	PR	50	20	46	P				
09. NETWORK AND POWER LAB.	TW	50	20	40	P C	19. COMMUNICATION THEORY	PP	100	40	65	P				
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	41	P C	20. COMMUNICATION THEORY	OR	50	20	42	P				
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	41	P				

GRAND TOTAL = 977/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

S8053051	KADAM SHRIKANT DEVIDASRAO				SANJIVANI				, 71100840L				PICT		, S8053051
01. SIGNAL AND SYSTEMS	PP	100	40	49	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	40	P				
02. SIGNAL AND SYSTEMS	OR	50	20	35	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	19	P				
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	40	P	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	40	P				
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	25	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	32	P				
05. NETWORK ANALYSIS	PP	100	40	40	P C	15. ELECTROMAGNETIC	PP	100	40	40	P				
06. DIGITAL LOGIC DESIGN	PP	100	40	67	P C	16. ELECTROMAGNETIC	TW	25	10	15	P				
07. DIGITAL LOGIC DESIGN	PR	50	20	28	P C	17. DATA STRUCTURES	PP	100	40	44	P				
08. POWER DEVICES AND MACHINES	PP	100	40	45	P	18. DATA STRUCTURES	PR	50	20	46	P				
09. NETWORK AND POWER LAB.	TW	50	20	31	P C	19. COMMUNICATION THEORY	PP	100	40	40	P				
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	27	P C	20. COMMUNICATION THEORY	OR	50	20	20	P				
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	34	P				

GRAND TOTAL = 757/1500, RESULT: SECOND CLASS

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8053052	KAMBLE AMRUTA GORAKH				NANDA		, 71100844C			PICT		S8053052
01. SIGNAL AND SYSTEMS	PP	100	40	70	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	78	P	
02. SIGNAL AND SYSTEMS	OR	50	20	35	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	19	P	
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	43	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	56	P	
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	35	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	26	P	
05. NETWORK ANALYSIS	PP	100	40	49	P C	15. ELECTROMAGNETIC	PP	100	40	50	P	
06. DIGITAL LOGIC DESIGN	PP	100	40	71	P C	16. ELECTROMAGNETIC	TW	25	10	18	P	
07. DIGITAL LOGIC DESIGN	PR	50	20	25	P C	17. DATA STRUCTURES	PP	100	40	46	P	
08. POWER DEVICES AND MACHINES	PP	100	40	50	P C	18. DATA STRUCTURES	PR	50	20	45	P	
09. NETWORK AND POWER LAB.	TW	50	20	40	P C	19. COMMUNICATION THEORY	PP	100	40	53	P	
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	40	P C	20. COMMUNICATION THEORY	OR	50	20	30	P	
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	34	P	

GRAND TOTAL = 913/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

S8053053	KAMBLE DASHARATH SATAPPA				KAMAL		, 71236314K			PICT		S8053053
01. SIGNAL AND SYSTEMS	PP	100	40	40	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	40	P	
02. SIGNAL AND SYSTEMS	OR	50	20	33	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	19	P	
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	49	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	44	P	
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	27	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	27	P	
05. NETWORK ANALYSIS	PP	100	40	40	P C	15. ELECTROMAGNETIC	PP	100	40	58	P	
06. DIGITAL LOGIC DESIGN	PP	100	40	44	P C	16. ELECTROMAGNETIC	TW	25	10	15	P	
07. DIGITAL LOGIC DESIGN	PR	50	20	27	P C	17. DATA STRUCTURES	PP	100	40	45	P	
08. POWER DEVICES AND MACHINES	PP	100	40	46	P C	18. DATA STRUCTURES	PR	50	20	39	P	
09. NETWORK AND POWER LAB.	TW	50	20	38	P C	19. COMMUNICATION THEORY	PP	100	40	57	P	
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	38	P C	20. COMMUNICATION THEORY	OR	50	20	29	P	
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	34	P	

GRAND TOTAL = 789/1500, RESULT: SECOND CLASS

ORDN. 1 MARKS :

S8053054	KAMBLE PRATIK RAVINDRA				SUMAN		, 71100845M			PICT		S8053054
01. SIGNAL AND SYSTEMS	PP	100	40	40	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	57	P	
02. SIGNAL AND SYSTEMS	OR	50	20	34	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	14	P	
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	40	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	48	P	
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	26	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	32	P	
05. NETWORK ANALYSIS	PP	100	40	31	F	15. ELECTROMAGNETIC	PP	100	40	30	F	
06. DIGITAL LOGIC DESIGN	PP	100	40	54	P C	16. ELECTROMAGNETIC	TW	25	10	12	P	
07. DIGITAL LOGIC DESIGN	PR	50	20	29	P C	17. DATA STRUCTURES	PP	100	40	52	P	
08. POWER DEVICES AND MACHINES	PP	100	40	40	P	18. DATA STRUCTURES	PR	50	20	30	P	
09. NETWORK AND POWER LAB.	TW	50	20	34	P C	19. COMMUNICATION THEORY	PP	100	40	61	P	
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	30	P C	20. COMMUNICATION THEORY	OR	50	20	25	P	
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	27	P	

GRAND TOTAL = 746/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8053055	KAMTHE SNEHAL SUNIL			ALKA			, 71236315H			PICT		S8053055
01. SIGNAL AND SYSTEMS	PP	100	40	68	P	C	11. ENGINEERING MATHEMATICS III	PP	100	40	51	P
02. SIGNAL AND SYSTEMS	OR	50	20	38	P	C	12. ENGINEERING MATHEMATICS III	TW	25	10	22	P
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	57	P	C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	51	P
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	38	P	C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	27	P
05. NETWORK ANALYSIS	PP	100	40	43	P	C	15. ELECTROMAGNETIC	PP	100	40	56	P
06. DIGITAL LOGIC DESIGN	PP	100	40	56	P	C	16. ELECTROMAGNETIC	TW	25	10	18	P
07. DIGITAL LOGIC DESIGN	PR	50	20	27	P	C	17. DATA STRUCTURES	PP	100	40	61	P
08. POWER DEVICES AND MACHINES	PP	100	40	41	P	C	18. DATA STRUCTURES	PR	50	20	38	P
09. NETWORK AND POWER LAB.	TW	50	20	42	P	C	19. COMMUNICATION THEORY	PP	100	40	55	P
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	41	P	C	20. COMMUNICATION THEORY	OR	50	20	30	P
							21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	42	P

GRAND TOTAL = 902/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

S8053056	KANSARA KRUTIKA SANJAY			JIGISHA			, 71100847H			PICT		S8053056
01. SIGNAL AND SYSTEMS	PP	100	40	68	P	C	11. ENGINEERING MATHEMATICS III	PP	100	40	73	P
02. SIGNAL AND SYSTEMS	OR	50	20	38	P	C	12. ENGINEERING MATHEMATICS III	TW	25	10	23	P
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	41	P	C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	49	P
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	43	P	C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	43	P
05. NETWORK ANALYSIS	PP	100	40	65	P	C	15. ELECTROMAGNETIC	PP	100	40	62	P
06. DIGITAL LOGIC DESIGN	PP	100	40	61	P	C	16. ELECTROMAGNETIC	TW	25	10	23	P
07. DIGITAL LOGIC DESIGN	PR	50	20	42	P	C	17. DATA STRUCTURES	PP	100	40	74	P
08. POWER DEVICES AND MACHINES	PP	100	40	49	P	C	18. DATA STRUCTURES	PR	50	20	44	P
09. NETWORK AND POWER LAB.	TW	50	20	44	P	C	19. COMMUNICATION THEORY	PP	100	40	53	P
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	44	P	C	20. COMMUNICATION THEORY	OR	50	20	39	P
							21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	44	P

GRAND TOTAL = 1022/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

S8053057	KANUNGO KAPIL RAMESH			KRISHNA			, 71100849D			PICT		S8053057
01. SIGNAL AND SYSTEMS	PP	100	40	52	P	C	11. ENGINEERING MATHEMATICS III	PP	100	40	67	P
02. SIGNAL AND SYSTEMS	OR	50	20	44	P	C	12. ENGINEERING MATHEMATICS III	TW	25	10	19	P
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	50	P	C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	46	P
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	27	P	C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	30	P
05. NETWORK ANALYSIS	PP	100	40	40	P	C	15. ELECTROMAGNETIC	PP	100	40	60	P
06. DIGITAL LOGIC DESIGN	PP	100	40	48	P	C	16. ELECTROMAGNETIC	TW	25	10	17	P
07. DIGITAL LOGIC DESIGN	PR	50	20	27	P	C	17. DATA STRUCTURES	PP	100	40	62	P
08. POWER DEVICES AND MACHINES	PP	100	40	48	P	C	18. DATA STRUCTURES	PR	50	20	47	P
09. NETWORK AND POWER LAB.	TW	50	20	40	P	C	19. COMMUNICATION THEORY	PP	100	40	46	P
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	39	P	C	20. COMMUNICATION THEORY	OR	50	20	32	P
							21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	40	P

GRAND TOTAL = 881/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8053058	KASHID SONALI BHARAT			KALPANA		, 71236316F			PICT		S8053058
01. SIGNAL AND SYSTEMS	PP	100	40	40	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	61	P
02. SIGNAL AND SYSTEMS	OR	50	20	36	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	19	P
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	48	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	49	P
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	42	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	40	P
05. NETWORK ANALYSIS	PP	100	40	48	P C	15. ELECTROMAGNETIC	PP	100	40	49	P
06. DIGITAL LOGIC DESIGN	PP	100	40	66	P C	16. ELECTROMAGNETIC	TW	25	10	19	P
07. DIGITAL LOGIC DESIGN	PR	50	20	37	P C	17. DATA STRUCTURES	PP	100	40	67	P
08. POWER DEVICES AND MACHINES	PP	100	40	41	P C	18. DATA STRUCTURES	PR	50	20	46	P
09. NETWORK AND POWER LAB.	TW	50	20	41	P C	19. COMMUNICATION THEORY	PP	100	40	47	P
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	42	P C	20. COMMUNICATION THEORY	OR	50	20	32	P
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	40	P

GRAND TOTAL = 910/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

S8053059	KESARKAR ONKAR PANDURANG			ANITA		, 71236317D			PICT		S8053059
01. SIGNAL AND SYSTEMS	PP	100	40	52	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	40	P
02. SIGNAL AND SYSTEMS	OR	50	20	34	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	20	P
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	46	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	64	P
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	32	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	39	P
05. NETWORK ANALYSIS	PP	100	40	53	P C	15. ELECTROMAGNETIC	PP	100	40	51	P
06. DIGITAL LOGIC DESIGN	PP	100	40	69	P C	16. ELECTROMAGNETIC	TW	25	10	19	P
07. DIGITAL LOGIC DESIGN	PR	50	20	25	P C	17. DATA STRUCTURES	PP	100	40	65	P
08. POWER DEVICES AND MACHINES	PP	100	40	56	P C	18. DATA STRUCTURES	PR	50	20	35	P
09. NETWORK AND POWER LAB.	TW	50	20	42	P C	19. COMMUNICATION THEORY	PP	100	40	52	P
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	42	P C	20. COMMUNICATION THEORY	OR	50	20	36	P
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	40	P

GRAND TOTAL = 912/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

S8053060	KHADATKAR NIRAJ MANOHAR			TARA		, 71100855J			PICT		S8053060
01. SIGNAL AND SYSTEMS	PP	100	40	62	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	64	P
02. SIGNAL AND SYSTEMS	OR	50	20	42	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	23	P
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	55	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	48	P
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	33	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	36	P
05. NETWORK ANALYSIS	PP	100	40	71	P C	15. ELECTROMAGNETIC	PP	100	40	70	P
06. DIGITAL LOGIC DESIGN	PP	100	40	79	P C	16. ELECTROMAGNETIC	TW	25	10	20	P
07. DIGITAL LOGIC DESIGN	PR	50	20	28	P C	17. DATA STRUCTURES	PP	100	40	65	P
08. POWER DEVICES AND MACHINES	PP	100	40	63	P C	18. DATA STRUCTURES	PR	50	20	43	P
09. NETWORK AND POWER LAB.	TW	50	20	37	P C	19. COMMUNICATION THEORY	PP	100	40	56	P
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	41	P C	20. COMMUNICATION THEORY	OR	50	20	37	P
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	42	P

GRAND TOTAL = 1015/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8053061	KHAMBE DIGVIJAY RAJARAM			MANJUSHA		, 71100859M			PICT		S8053061
01. SIGNAL AND SYSTEMS	PP	100	40	41	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	66	P
02. SIGNAL AND SYSTEMS	OR	50	20	20	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	16	P
03. SOLID STATES DEVICES AND CIRCUITS	SPP	100	40	40	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	40	P
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	35	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	40	P
05. NETWORK ANALYSIS	PP	100	40	56	P	15. ELECTROMAGNETIC	PP	100	40	56	P
06. DIGITAL LOGIC DESIGN	PP	100	40	40	P C	16. ELECTROMAGNETIC	TW	25	10	13	P
07. DIGITAL LOGIC DESIGN	PR	50	20	25	P C	17. DATA STRUCTURES	PP	100	40	62	P
08. POWER DEVICES AND MACHINES	PP	100	40	43	P C	18. DATA STRUCTURES	PR	50	20	43	P
09. NETWORK AND POWER LAB.	TW	50	20	33	P C	19. COMMUNICATION THEORY	PP	100	40	62	P
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	31	P C	20. COMMUNICATION THEORY	OR	50	20	21	P
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	30	P

GRAND TOTAL = 813/1500, RESULT: SECOND CLASS

ORDN. 1 MARKS :

S8053062	KHANDELWAL DIVYANG KAMALKISHOR			SANGEETA		, 71100863K			PICT		S8053062
01. SIGNAL AND SYSTEMS	PP	100	40	43	P C						
02. SIGNAL AND SYSTEMS	OR	50	20	20	P C						
03. SOLID STATES DEVICES AND CIRCUITS	SPP	100	40	AA	F						
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	AA	F						
05. NETWORK ANALYSIS	PP	100	40	10	F						
06. DIGITAL LOGIC DESIGN	PP	100	40	AA	F						
07. DIGITAL LOGIC DESIGN	PR	50	20	29	P C						
08. POWER DEVICES AND MACHINES	PP	100	40	16	F						
09. NETWORK AND POWER LAB.	TW	50	20	21	P C						
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	23	P C						

FIRST TERM TOTAL = 162/750.

ORDN. 1 MARKS :

S8053064	KOLI VIJAYA VYANKATRAO			SEETA		, 71100866D			PICT		S8053064
01. SIGNAL AND SYSTEMS	PP	100	40	40	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	06	F
02. SIGNAL AND SYSTEMS	OR	50	20	22	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	17	P
03. SOLID STATES DEVICES AND CIRCUITS	SPP	100	40	24	F	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	40	P
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	20	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	05	F
05. NETWORK ANALYSIS	PP	100	40	17	F	15. ELECTROMAGNETIC	PP	100	40	31	F
06. DIGITAL LOGIC DESIGN	PP	100	40	15	F	16. ELECTROMAGNETIC	TW	25	10	18	P
07. DIGITAL LOGIC DESIGN	PR	50	20	30	P C	17. DATA STRUCTURES	PP	100	40	43	P
08. POWER DEVICES AND MACHINES	PP	100	40	31	F	18. DATA STRUCTURES	PR	50	20	32	P
09. NETWORK AND POWER LAB.	TW	50	20	27	P C	19. COMMUNICATION THEORY	PP	100	40	20	F
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	30	P C	20. COMMUNICATION THEORY	OR	50	20	12	F
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	32	P

GRAND TOTAL = 512/1500, RESULT: FAILS

RESULT RESERVED FOR BKL

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8053065	KRISHNAN SANTOSH KUMAR				RADHIKA				, 71100867B				PICT		, S8053065
01. SIGNAL AND SYSTEMS	PP	100	40	58	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	54	P				
02. SIGNAL AND SYSTEMS	OR	50	20	41	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	23	P				
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	56	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	52	P				
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	34	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	35	P				
05. NETWORK ANALYSIS	PP	100	40	48	P C	15. ELECTROMAGNETIC	PP	100	40	66	P				
06. DIGITAL LOGIC DESIGN	PP	100	40	71	P C	16. ELECTROMAGNETIC	TW	25	10	21	P				
07. DIGITAL LOGIC DESIGN	PR	50	20	27	P C	17. DATA STRUCTURES	PP	100	40	55	P				
08. POWER DEVICES AND MACHINES	PP	100	40	55	P C	18. DATA STRUCTURES	PR	50	20	47	P				
09. NETWORK AND POWER LAB.	TW	50	20	41	P C	19. COMMUNICATION THEORY	PP	100	40	66	P				
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	43	P C	20. COMMUNICATION THEORY	OR	50	20	22	P				
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	43	P				

GRAND TOTAL = 958/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

S8053066	KULKARNI KRANTI MADHAV				MADHURI				, 71100871L				PICT		, S8053066
01. SIGNAL AND SYSTEMS	PP	100	40	44	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	74	P				
02. SIGNAL AND SYSTEMS	OR	50	20	28	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	19	P				
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	45	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	50	P				
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	32	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	34	P				
05. NETWORK ANALYSIS	PP	100	40	40	P C	15. ELECTROMAGNETIC	PP	100	40	53	P				
06. DIGITAL LOGIC DESIGN	PP	100	40	64	P C	16. ELECTROMAGNETIC	TW	25	10	19	P				
07. DIGITAL LOGIC DESIGN	PR	50	20	32	P	17. DATA STRUCTURES	PP	100	40	58	P				
08. POWER DEVICES AND MACHINES	PP	100	40	45	P C	18. DATA STRUCTURES	PR	50	20	46	P				
09. NETWORK AND POWER LAB.	TW	50	20	38	P C	19. COMMUNICATION THEORY	PP	100	40	52	P				
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	36	P C	20. COMMUNICATION THEORY	OR	50	20	29	P				
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	34	P				

GRAND TOTAL = 872/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

S8053067	KUNAL AGARWAL				POONAM DEVI				, 71100875C				PICT		, S8053067
01. SIGNAL AND SYSTEMS	PP	100	40	52	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	69	P				
02. SIGNAL AND SYSTEMS	OR	50	20	30	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	23	P				
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	51	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	51	P				
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	35	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	39	P				
05. NETWORK ANALYSIS	PP	100	40	48	P C	15. ELECTROMAGNETIC	PP	100	40	79	P				
06. DIGITAL LOGIC DESIGN	PP	100	40	57	P C	16. ELECTROMAGNETIC	TW	25	10	19	P				
07. DIGITAL LOGIC DESIGN	PR	50	20	39	P C	17. DATA STRUCTURES	PP	100	40	60	P				
08. POWER DEVICES AND MACHINES	PP	100	40	45	P C	18. DATA STRUCTURES	PR	50	20	48	P				
09. NETWORK AND POWER LAB.	TW	50	20	36	P C	19. COMMUNICATION THEORY	PP	100	40	58	P				
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	36	P C	20. COMMUNICATION THEORY	OR	50	20	30	P				
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	38	P				

GRAND TOTAL = 943/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8053068	KURHADE RUSHIKESH SAMBHAJI				SANGITA		, 71100876M			PICT		S8053068
01. SIGNAL AND SYSTEMS	PP	100	40	59	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	88	P	
02. SIGNAL AND SYSTEMS	OR	50	20	38	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	23	P	
03. SOLID STATES DEVICES AND CIRCUITS	SPP	100	40	51	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	53	P	
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	44	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	40	P	
05. NETWORK ANALYSIS	PP	100	40	63	P C	15. ELECTROMAGNETIC	PP	100	40	80	P	
06. DIGITAL LOGIC DESIGN	PP	100	40	73	P C	16. ELECTROMAGNETIC	TW	25	10	23	P	
07. DIGITAL LOGIC DESIGN	PR	50	20	30	P C	17. DATA STRUCTURES	PP	100	40	55	P	
08. POWER DEVICES AND MACHINES	PP	100	40	53	P C	18. DATA STRUCTURES	PR	50	20	47	P	
09. NETWORK AND POWER LAB.	TW	50	20	43	P C	19. COMMUNICATION THEORY	PP	100	40	62	P	
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	44	P C	20. COMMUNICATION THEORY	OR	50	20	34	P	
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	44	P	

GRAND TOTAL = 1047/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

S8053069	KUSH VARMA				PARVEEN		, 71100877K			PICT		S8053069
01. SIGNAL AND SYSTEMS	PP	100	40	64	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	69	P	
02. SIGNAL AND SYSTEMS	OR	50	20	39	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	22	P	
03. SOLID STATES DEVICES AND CIRCUITS	SPP	100	40	72	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	60	P	
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	43	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	30	P	
05. NETWORK ANALYSIS	PP	100	40	48	P C	15. ELECTROMAGNETIC	PP	100	40	57	P	
06. DIGITAL LOGIC DESIGN	PP	100	40	75	P C	16. ELECTROMAGNETIC	TW	25	10	19	P	
07. DIGITAL LOGIC DESIGN	PR	50	20	45	P C	17. DATA STRUCTURES	PP	100	40	73	P	
08. POWER DEVICES AND MACHINES	PP	100	40	71	P C	18. DATA STRUCTURES	PR	50	20	48	P	
09. NETWORK AND POWER LAB.	TW	50	20	41	P C	19. COMMUNICATION THEORY	PP	100	40	54	P	
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	40	P C	20. COMMUNICATION THEORY	OR	50	20	37	P	
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	42	P	

GRAND TOTAL = 1049/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

S8053070	M SUBRAMANIAN				M KOHILA		, 71100885L			PICT		S8053070
01. SIGNAL AND SYSTEMS	PP	100	40	43	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	52	P	
02. SIGNAL AND SYSTEMS	OR	50	20	43	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	20	P	
03. SOLID STATES DEVICES AND CIRCUITS	SPP	100	40	51	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	51	P	
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	42	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	35	P	
05. NETWORK ANALYSIS	PP	100	40	56	P C	15. ELECTROMAGNETIC	PP	100	40	72	P	
06. DIGITAL LOGIC DESIGN	PP	100	40	66	P C	16. ELECTROMAGNETIC	TW	25	10	16	P	
07. DIGITAL LOGIC DESIGN	PR	50	20	35	P C	17. DATA STRUCTURES	PP	100	40	61	P	
08. POWER DEVICES AND MACHINES	PP	100	40	50	P C	18. DATA STRUCTURES	PR	50	20	44	P	
09. NETWORK AND POWER LAB.	TW	50	20	40	P C	19. COMMUNICATION THEORY	PP	100	40	45	P	
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	39	P C	20. COMMUNICATION THEORY	OR	50	20	33	P	
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	39	P	

GRAND TOTAL = 933+05/1500, RESULT: FIRST CLASS[0.163]

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8053071	MAHAJAN PRATEEK PARAG				PREETI		, 71100888E			PICT		, S8053071
01. SIGNAL AND SYSTEMS	PP	100	40	80	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	85	P	
02. SIGNAL AND SYSTEMS	OR	50	20	36	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	23	P	
03. SOLID STATES DEVICES AND CIRCUITS	SPP	100	40	40	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	43	P	
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	36	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	37	P	
05. NETWORK ANALYSIS	PP	100	40	55	P C	15. ELECTROMAGNETIC	PP	100	40	72	P	
06. DIGITAL LOGIC DESIGN	PP	100	40	59	P C	16. ELECTROMAGNETIC	TW	25	10	22	P	
07. DIGITAL LOGIC DESIGN	PR	50	20	32	P C	17. DATA STRUCTURES	PP	100	40	71	P	
08. POWER DEVICES AND MACHINES	PP	100	40	46	P C	18. DATA STRUCTURES	PR	50	20	48	P	
09. NETWORK AND POWER LAB.	TW	50	20	38	P C	19. COMMUNICATION THEORY	PP	100	40	62	P	
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	43	P C	20. COMMUNICATION THEORY	OR	50	20	35	P	
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	39	P	

GRAND TOTAL = 1002/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

S8053072	MAHANGARE BHAGYASHRI BABASAHEB				SUREKHA		, 71236318B			PICT		, S8053072
01. SIGNAL AND SYSTEMS	PP	100	40	41	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	40	P	
02. SIGNAL AND SYSTEMS	OR	50	20	29	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	22	P	
03. SOLID STATES DEVICES AND CIRCUITS	SPP	100	40	42	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	30#	P	
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	30	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	25	P	
05. NETWORK ANALYSIS	PP	100	40	41	P	15. ELECTROMAGNETIC	PP	100	40	40	P	
06. DIGITAL LOGIC DESIGN	PP	100	40	57	P C	16. ELECTROMAGNETIC	TW	25	10	17	P	
07. DIGITAL LOGIC DESIGN	PR	50	20	29	P C	17. DATA STRUCTURES	PP	100	40	48	P	
08. POWER DEVICES AND MACHINES	PP	100	40	42	P C	18. DATA STRUCTURES	PR	50	20	33	P	
09. NETWORK AND POWER LAB.	TW	50	20	40	P C	19. COMMUNICATION THEORY	PP	100	40	50	P	
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	37	P C	20. COMMUNICATION THEORY	OR	50	20	38	P	
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	42	P	

GRAND TOTAL = 773/1500, RESULT: SECOND CLASS # [0.4]

ORDN. 1 MARKS :

S8053073	MALI RAKESH SAKHARAM				REKHA		, 71100889C			PICT		, S8053073
01. SIGNAL AND SYSTEMS	PP	100	40	63	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	89	P	
02. SIGNAL AND SYSTEMS	OR	50	20	42	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	22	P	
03. SOLID STATES DEVICES AND CIRCUITS	SPP	100	40	53	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	51	P	
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	22	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	28	P	
05. NETWORK ANALYSIS	PP	100	40	59	P C	15. ELECTROMAGNETIC	PP	100	40	64	P	
06. DIGITAL LOGIC DESIGN	PP	100	40	55	P C	16. ELECTROMAGNETIC	TW	25	10	21	P	
07. DIGITAL LOGIC DESIGN	PR	50	20	21	P C	17. DATA STRUCTURES	PP	100	40	72	P	
08. POWER DEVICES AND MACHINES	PP	100	40	49	P C	18. DATA STRUCTURES	PR	50	20	47	P	
09. NETWORK AND POWER LAB.	TW	50	20	34	P C	19. COMMUNICATION THEORY	PP	100	40	55	P	
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	38	P C	20. COMMUNICATION THEORY	OR	50	20	30	P	
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	37	P	

GRAND TOTAL = 952/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8053074	METHIKAR PRIYANKA JITENDRA			SUSHMA		, 71100895H			PICT		S8053074
01. SIGNAL AND SYSTEMS	PP	100	40	80	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	86	P
02. SIGNAL AND SYSTEMS	OR	50	20	36	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	21	P
03. SOLID STATES DEVICES AND CIRCUITS	SPP	100	40	77	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	62	P
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	44	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	42	P
05. NETWORK ANALYSIS	PP	100	40	66	P C	15. ELECTROMAGNETIC	PP	100	40	61	P
06. DIGITAL LOGIC DESIGN	PP	100	40	76	P C	16. ELECTROMAGNETIC	TW	25	10	18	P
07. DIGITAL LOGIC DESIGN	PR	50	20	22	P C	17. DATA STRUCTURES	PP	100	40	82	P
08. POWER DEVICES AND MACHINES	PP	100	40	52	P C	18. DATA STRUCTURES	PR	50	20	48	P
09. NETWORK AND POWER LAB.	TW	50	20	42	P C	19. COMMUNICATION THEORY	PP	100	40	69	P
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	42	P C	20. COMMUNICATION THEORY	OR	50	20	33	P
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	39	P

GRAND TOTAL = 1098/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

S8053075	MOHITE SHEETAL CHANDRAKANT			SAVITA		, 71236319L			PICT		S8053075
01. SIGNAL AND SYSTEMS	PP	100	40	64	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	93	P
02. SIGNAL AND SYSTEMS	OR	50	20	42	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	21	P
03. SOLID STATES DEVICES AND CIRCUITS	SPP	100	40	68	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	61	P
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	32	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	35	P
05. NETWORK ANALYSIS	PP	100	40	67	P C	15. ELECTROMAGNETIC	PP	100	40	70	P
06. DIGITAL LOGIC DESIGN	PP	100	40	77	P C	16. ELECTROMAGNETIC	TW	25	10	20	P
07. DIGITAL LOGIC DESIGN	PR	50	20	32	P C	17. DATA STRUCTURES	PP	100	40	74	P
08. POWER DEVICES AND MACHINES	PP	100	40	51	P C	18. DATA STRUCTURES	PR	50	20	44	P
09. NETWORK AND POWER LAB.	TW	50	20	40	P C	19. COMMUNICATION THEORY	PP	100	40	64	P
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	40	P C	20. COMMUNICATION THEORY	OR	50	20	34	P
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	38	P

GRAND TOTAL = 1067/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

S8053076	MORE ANKUR SURESHRAO			LAXMI		, 71100900H			PICT		S8053076
01. SIGNAL AND SYSTEMS	PP	100	40	40	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	44	P
02. SIGNAL AND SYSTEMS	OR	50	20	26	P	12. ENGINEERING MATHEMATICS III	TW	25	10	12	P
03. SOLID STATES DEVICES AND CIRCUITS	SPP	100	40	11	F	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	12	F
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	20	P	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	08	F
05. NETWORK ANALYSIS	PP	100	40	23	F	15. ELECTROMAGNETIC	PP	100	40	18	F
06. DIGITAL LOGIC DESIGN	PP	100	40	19	F	16. ELECTROMAGNETIC	TW	25	10	11	P
07. DIGITAL LOGIC DESIGN	PR	50	20	25	P C	17. DATA STRUCTURES	PP	100	40	17	F
08. POWER DEVICES AND MACHINES	PP	100	40	20	F	18. DATA STRUCTURES	PR	50	20	32	P
09. NETWORK AND POWER LAB.	TW	50	20	24	P C	19. COMMUNICATION THEORY	PP	100	40	19	F
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	27	P C	20. COMMUNICATION THEORY	OR	50	20	05	F
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	23	P

GRAND TOTAL = 436/1500, RESULT: FAILS

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8053077	MUSALE VIVEK KISANRAO			INDUBAI		, 71236320D			PICT		S8053077
01. SIGNAL AND SYSTEMS	PP	100	40	55	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	43	P
02. SIGNAL AND SYSTEMS	OR	50	20	28	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	18	P
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	56	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	40	P
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	33	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	38	P
05. NETWORK ANALYSIS	PP	100	40	40	P	15. ELECTROMAGNETIC	PP	100	40	40	P
06. DIGITAL LOGIC DESIGN	PP	100	40	60	P C	16. ELECTROMAGNETIC	TW	25	10	19	P
07. DIGITAL LOGIC DESIGN	PR	50	20	28	P C	17. DATA STRUCTURES	PP	100	40	53	P
08. POWER DEVICES AND MACHINES	PP	100	40	40	P C	18. DATA STRUCTURES	PR	50	20	32	P
09. NETWORK AND POWER LAB.	TW	50	20	36	P C	19. COMMUNICATION THEORY	PP	100	40	46	P
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	39	P C	20. COMMUNICATION THEORY	OR	50	20	30	P
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	38	P

GRAND TOTAL = 812/1500, RESULT: SECOND CLASS

ORDN. 1 MARKS :

S8053078	NADEKAR SANJIV CHANDRAKANT			ALKA		, 71236321B			PICT		S8053078
01. SIGNAL AND SYSTEMS	PP	100	40	44	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	40	P
02. SIGNAL AND SYSTEMS	OR	50	20	22	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	17	P
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	40	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	41	P
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	33	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	38	P
05. NETWORK ANALYSIS	PP	100	40	40	P C	15. ELECTROMAGNETIC	PP	100	40	28	F
06. DIGITAL LOGIC DESIGN	PP	100	40	59	P C	16. ELECTROMAGNETIC	TW	25	10	16	P
07. DIGITAL LOGIC DESIGN	PR	50	20	30	P C	17. DATA STRUCTURES	PP	100	40	65	P
08. POWER DEVICES AND MACHINES	PP	100	40	40	P C	18. DATA STRUCTURES	PR	50	20	42	P
09. NETWORK AND POWER LAB.	TW	50	20	37	P C	19. COMMUNICATION THEORY	PP	100	40	65	P
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	38	P C	20. COMMUNICATION THEORY	OR	50	20	29	P
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	37	P

GRAND TOTAL = 801/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

S8053079	NAIK DIGJA ANKUSH			VASANTI		, 71100906G			PICT		S8053079
01. SIGNAL AND SYSTEMS	PP	100	40	72	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	82	P
02. SIGNAL AND SYSTEMS	OR	50	20	35	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	22	P
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	55	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	48	P
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	44	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	30	P
05. NETWORK ANALYSIS	PP	100	40	58	P C	15. ELECTROMAGNETIC	PP	100	40	47	P
06. DIGITAL LOGIC DESIGN	PP	100	40	58	P C	16. ELECTROMAGNETIC	TW	25	10	18	P
07. DIGITAL LOGIC DESIGN	PR	50	20	22	P C	17. DATA STRUCTURES	PP	100	40	83	P
08. POWER DEVICES AND MACHINES	PP	100	40	43	P C	18. DATA STRUCTURES	PR	50	20	46	P
09. NETWORK AND POWER LAB.	TW	50	20	42	P C	19. COMMUNICATION THEORY	PP	100	40	62	P
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	41	P C	20. COMMUNICATION THEORY	OR	50	20	35	P
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	38	P

GRAND TOTAL = 981/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8053080	NAKHATE ANIKET BHAGWANRAO					ANITA					, 71100909M	,		PICT		S8053080
01. SIGNAL AND SYSTEMS	PP	100	40	67	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	74	P					
02. SIGNAL AND SYSTEMS	OR	50	20	44	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	23	P					
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	48	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	42	P					
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	35	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	31	P					
05. NETWORK ANALYSIS	PP	100	40	56	P C	15. ELECTROMAGNETIC	PP	100	40	46	P					
06. DIGITAL LOGIC DESIGN	PP	100	40	43	P C	16. ELECTROMAGNETIC	TW	25	10	22	P					
07. DIGITAL LOGIC DESIGN	PR	50	20	25	P C	17. DATA STRUCTURES	PP	100	40	56	P					
08. POWER DEVICES AND MACHINES	PP	100	40	40	P C	18. DATA STRUCTURES	PR	50	20	44	P					
09. NETWORK AND POWER LAB.	TW	50	20	36	P C	19. COMMUNICATION THEORY	PP	100	40	54	P					
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	36	P C	20. COMMUNICATION THEORY	OR	50	20	29	P					
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	43	P					

GRAND TOTAL = 894+06/1500, RESULT: FIRST CLASS [0.2]

ORDN. 1 MARKS :

S8053081	NANDNAWARE AKASH JAGNNATH					DRAUPADA					, 71100910E	,		PICT		S8053081
01. SIGNAL AND SYSTEMS	PP	100	40	15	F	11. ENGINEERING MATHEMATICS III	PP	100	40	40	P					
02. SIGNAL AND SYSTEMS	OR	50	20	26	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	11	P					
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	40	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	08	F					
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	25	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	05	F					
05. NETWORK ANALYSIS	PP	100	40	11	F	15. ELECTROMAGNETIC	PP	100	40	12	F					
06. DIGITAL LOGIC DESIGN	PP	100	40	49	P C	16. ELECTROMAGNETIC	TW	25	10	10	P					
07. DIGITAL LOGIC DESIGN	PR	50	20	05	F	17. DATA STRUCTURES	PP	100	40	40	P					
08. POWER DEVICES AND MACHINES	PP	100	40	22	F	18. DATA STRUCTURES	PR	50	20	35	P					
09. NETWORK AND POWER LAB.	TW	50	20	30	P C	19. COMMUNICATION THEORY	PP	100	40	27	F					
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	28	P C	20. COMMUNICATION THEORY	OR	50	20	30	P					
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	25	P					

GRAND TOTAL = 494/1500, RESULT: FAILS

ORDN. 1 MARKS :

S8053082	NARKAR PRAJAKTA SUHAS					SUPRIYA					, 71100914H					, PICT					, S8053082				
01. SIGNAL AND SYSTEMS	PP	100	40	78	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	93	P														
02. SIGNAL AND SYSTEMS	OR	50	20	38	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	24	P														
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	74	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	53	P														
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	43	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	42	P														
05. NETWORK ANALYSIS	PP	100	40	83	P C	15. ELECTROMAGNETIC	PP	100	40	66	P														
06. DIGITAL LOGIC DESIGN	PP	100	40	55	P C	16. ELECTROMAGNETIC	TW	25	10	24	P														
07. DIGITAL LOGIC DESIGN	PR	50	20	43	P C	17. DATA STRUCTURES	PP	100	40	76	P														
08. POWER DEVICES AND MACHINES	PP	100	40	59	P C	18. DATA STRUCTURES	PR	50	20	48	P														
09. NETWORK AND POWER LAB.	TW	50	20	45	P C	19. COMMUNICATION THEORY	PP	100	40	63	P														
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	46	P C	20. COMMUNICATION THEORY	OR	50	20	42	P														
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	45	P														

GRAND TOTAL = 1140/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8053083	NIKITA AVINASH ANGRE			CHITRA		, 71100917B			PICT		S8053083
01. SIGNAL AND SYSTEMS	PP	100	40	40	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	72	P
02. SIGNAL AND SYSTEMS	OR	50	20	42	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	18	P
03. SOLID STATES DEVICES AND CIRCUITS	SPP	100	40	42	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	30	F
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	32	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	22	P
05. NETWORK ANALYSIS	PP	100	40	66	P C	15. ELECTROMAGNETIC	PP	100	40	22	F
06. DIGITAL LOGIC DESIGN	PP	100	40	45	P C	16. ELECTROMAGNETIC	TW	25	10	15	P
07. DIGITAL LOGIC DESIGN	PR	50	20	34	P C	17. DATA STRUCTURES	PP	100	40	53	P
08. POWER DEVICES AND MACHINES	PP	100	40	40	P	18. DATA STRUCTURES	PR	50	20	42	P
09. NETWORK AND POWER LAB.	TW	50	20	36	P C	19. COMMUNICATION THEORY	PP	100	40	40	P
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	33	P C	20. COMMUNICATION THEORY	OR	50	20	40	P
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	31	P

GRAND TOTAL = 795/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

S8053084	PADGILWAR GAURAV VISHWAMBHAR			SAVITA		, 71100920B			PICT		S8053084
01. SIGNAL AND SYSTEMS	PP	100	40	49	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	80	P
02. SIGNAL AND SYSTEMS	OR	50	20	36	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	18	P
03. SOLID STATES DEVICES AND CIRCUITS	SPP	100	40	59	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	40	P
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	40	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	41	P
05. NETWORK ANALYSIS	PP	100	40	57	P C	15. ELECTROMAGNETIC	PP	100	40	49	P
06. DIGITAL LOGIC DESIGN	PP	100	40	75	P C	16. ELECTROMAGNETIC	TW	25	10	15	P
07. DIGITAL LOGIC DESIGN	PR	50	20	30	P C	17. DATA STRUCTURES	PP	100	40	64	P
08. POWER DEVICES AND MACHINES	PP	100	40	45	P C	18. DATA STRUCTURES	PR	50	20	45	P
09. NETWORK AND POWER LAB.	TW	50	20	36	P C	19. COMMUNICATION THEORY	PP	100	40	48	P
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	35	P C	20. COMMUNICATION THEORY	OR	50	20	40	P
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	35	P

GRAND TOTAL = 937/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

S8053085	PADHYE ABHINAV ANANT			ANURADHA		, 71236322L			PICT		S8053085
01. SIGNAL AND SYSTEMS	PP	100	40	65	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	49	P
02. SIGNAL AND SYSTEMS	OR	50	20	39	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	20	P
03. SOLID STATES DEVICES AND CIRCUITS	SPP	100	40	63	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	56	P
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	36	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	40	P
05. NETWORK ANALYSIS	PP	100	40	55	P C	15. ELECTROMAGNETIC	PP	100	40	45	P
06. DIGITAL LOGIC DESIGN	PP	100	40	71	P C	16. ELECTROMAGNETIC	TW	25	10	19	P
07. DIGITAL LOGIC DESIGN	PR	50	20	33	P C	17. DATA STRUCTURES	PP	100	40	65	P
08. POWER DEVICES AND MACHINES	PP	100	40	48	P C	18. DATA STRUCTURES	PR	50	20	43	P
09. NETWORK AND POWER LAB.	TW	50	20	44	P C	19. COMMUNICATION THEORY	PP	100	40	57	P
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	43	P C	20. COMMUNICATION THEORY	OR	50	20	45	P
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	40	P

GRAND TOTAL = 976/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8053086	PADOLE NEHA SUNIL				NIRMALA		, 71100921L			PICT		S8053086
01. SIGNAL AND SYSTEMS	PP	100	40	75	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	96	P	
02. SIGNAL AND SYSTEMS	OR	50	20	30	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	22	P	
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	64	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	50	P	
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	40	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	38	P	
05. NETWORK ANALYSIS	PP	100	40	40	P C	15. ELECTROMAGNETIC	PP	100	40	44	P	
06. DIGITAL LOGIC DESIGN	PP	100	40	82	P C	16. ELECTROMAGNETIC	TW	25	10	22	P	
07. DIGITAL LOGIC DESIGN	PR	50	20	35	P C	17. DATA STRUCTURES	PP	100	40	81	P	
08. POWER DEVICES AND MACHINES	PP	100	40	40	P C	18. DATA STRUCTURES	PR	50	20	46	P	
09. NETWORK AND POWER LAB.	TW	50	20	39	P C	19. COMMUNICATION THEORY	PP	100	40	62	P	
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	38	P C	20. COMMUNICATION THEORY	OR	50	20	42	P	
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	41	P	

GRAND TOTAL = 1027/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

S8053087	PAGARIYA BHUSHAN RAMESHCHAND				MANJUSHRI		, 71100923G			PICT		S8053087
01. SIGNAL AND SYSTEMS	PP	100	40	66	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	91	P	
02. SIGNAL AND SYSTEMS	OR	50	20	38	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	22	P	
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	71	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	52	P	
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	39	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	42	P	
05. NETWORK ANALYSIS	PP	100	40	78	P C	15. ELECTROMAGNETIC	PP	100	40	56	P	
06. DIGITAL LOGIC DESIGN	PP	100	40	91	P C	16. ELECTROMAGNETIC	TW	25	10	21	P	
07. DIGITAL LOGIC DESIGN	PR	50	20	42	P C	17. DATA STRUCTURES	PP	100	40	81	P	
08. POWER DEVICES AND MACHINES	PP	100	40	64	P C	18. DATA STRUCTURES	PR	50	20	48	P	
09. NETWORK AND POWER LAB.	TW	50	20	42	P C	19. COMMUNICATION THEORY	PP	100	40	66	P	
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	42	P C	20. COMMUNICATION THEORY	OR	50	20	43	P	
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	42	P	

GRAND TOTAL = 1137/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

S8053088	PANDEY JUHI SANJAY				SUNITA		, 71100927K			PICT		S8053088
01. SIGNAL AND SYSTEMS	PP	100	40	75	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	91	P	
02. SIGNAL AND SYSTEMS	OR	50	20	45	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	24	P	
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	53	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	48	P	
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	45	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	45	P	
05. NETWORK ANALYSIS	PP	100	40	85	P C	15. ELECTROMAGNETIC	PP	100	40	71	P	
06. DIGITAL LOGIC DESIGN	PP	100	40	73	P C	16. ELECTROMAGNETIC	TW	25	10	24	P	
07. DIGITAL LOGIC DESIGN	PR	50	20	44	P C	17. DATA STRUCTURES	PP	100	40	61	P	
08. POWER DEVICES AND MACHINES	PP	100	40	59	P C	18. DATA STRUCTURES	PR	50	20	48	P	
09. NETWORK AND POWER LAB.	TW	50	20	47	P C	19. COMMUNICATION THEORY	PP	100	40	74	P	
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	47	P C	20. COMMUNICATION THEORY	OR	50	20	40	P	
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	46	P	

GRAND TOTAL = 1145/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8053089	PANDILWAR RAMESHWAR KISHORE					SUJATA					, 71100928H				PICT		S8053089
01. SIGNAL AND SYSTEMS	PP	100	40	55	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	46	P						
02. SIGNAL AND SYSTEMS	OR	50	20	32	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	14	P						
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	40	P	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	32	F						
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	28	P	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	20	P						
05. NETWORK ANALYSIS	PP	100	40	26	F	15. ELECTROMAGNETIC	PP	100	40	41	P						
06. DIGITAL LOGIC DESIGN	PP	100	40	58	P C	16. ELECTROMAGNETIC	TW	25	10	12	P						
07. DIGITAL LOGIC DESIGN	PR	50	20	25	P C	17. DATA STRUCTURES	PP	100	40	48	P						
08. POWER DEVICES AND MACHINES	PP	100	40	40	P C	18. DATA STRUCTURES	PR	50	20	31	P						
09. NETWORK AND POWER LAB.	TW	50	20	27	P C	19. COMMUNICATION THEORY	PP	100	40	50	P						
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	25	P C	20. COMMUNICATION THEORY	OR	50	20	30	P						
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	24	P						

GRAND TOTAL = 704/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

S8053090	PARDESHI SAGAR RAJKUMAR				USHA			, 71100931H			PICT		S8053090
01. SIGNAL AND SYSTEMS	PP	100	40	66	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	66	P		
02. SIGNAL AND SYSTEMS	OR	50	20	39	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	20	P		
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	55	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	40	P		
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	37	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	35	P		
05. NETWORK ANALYSIS	PP	100	40	54	P C	15. ELECTROMAGNETIC	PP	100	40	55	P		
06. DIGITAL LOGIC DESIGN	PP	100	40	47	P C	16. ELECTROMAGNETIC	TW	25	10	17	P		
07. DIGITAL LOGIC DESIGN	PR	50	20	42	P C	17. DATA STRUCTURES	PP	100	40	73	P		
08. POWER DEVICES AND MACHINES	PP	100	40	41	P C	18. DATA STRUCTURES	PR	50	20	46	P		
09. NETWORK AND POWER LAB.	TW	50	20	43	P C	19. COMMUNICATION THEORY	PP	100	40	63	P		
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	43	P C	20. COMMUNICATION THEORY	OR	50	20	38	P		
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	34	P		

GRAND TOTAL = 954/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

S8053091	PATHAK SWANAND SUDHIR					SONALI					, 71100936J	,		PICT		S8053091	
01. SIGNAL AND SYSTEMS	PP	100	40	66	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	87	P						
02. SIGNAL AND SYSTEMS	OR	50	20	37	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	23	P						
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	49	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	40	P						
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	35	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	32	P						
05. NETWORK ANALYSIS	PP	100	40	40	P C	15. ELECTROMAGNETIC	PP	100	40	61	P						
06. DIGITAL LOGIC DESIGN	PP	100	40	67	P C	16. ELECTROMAGNETIC	TW	25	10	20	P						
07. DIGITAL LOGIC DESIGN	PR	50	20	24	P	17. DATA STRUCTURES	PP	100	40	67	P						
08. POWER DEVICES AND MACHINES	PP	100	40	40	P C	18. DATA STRUCTURES	PR	50	20	48	P						
09. NETWORK AND POWER LAB.	TW	50	20	40	P C	19. COMMUNICATION THEORY	PP	100	40	43	P						
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	40	P C	20. COMMUNICATION THEORY	OR	50	20	35	P						
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	44	P						

GRAND TOTAL = 938/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8053092	PATIL AMOL HIRAMAN				LATA		, 71100937G			PICT		S8053092
01. SIGNAL AND SYSTEMS	PP	100	40	44	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	47	P	
02. SIGNAL AND SYSTEMS	OR	50	20	29	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	21	P	
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	40	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	40	P	
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	27	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	30	P	
05. NETWORK ANALYSIS	PP	100	40	44	P	15. ELECTROMAGNETIC	PP	100	40	40	P	
06. DIGITAL LOGIC DESIGN	PP	100	40	43	P C	16. ELECTROMAGNETIC	TW	25	10	20	P	
07. DIGITAL LOGIC DESIGN	PR	50	20	30	P C	17. DATA STRUCTURES	PP	100	40	70	P	
08. POWER DEVICES AND MACHINES	PP	100	40	41	P C	18. DATA STRUCTURES	PR	50	20	43	P	
09. NETWORK AND POWER LAB.	TW	50	20	39	P C	19. COMMUNICATION THEORY	PP	100	40	51	P	
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	37	P C	20. COMMUNICATION THEORY	OR	50	20	35	P	
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	37	P	

GRAND TOTAL = 808/1500, RESULT: SECOND CLASS

ORDN. 1 MARKS :

S8053093	PATIL ASHISH ISHWAR				KUSUM		, 71100939C			PICT		S8053093
01. SIGNAL AND SYSTEMS	PP	100	40	70	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	67	P	
02. SIGNAL AND SYSTEMS	OR	50	20	26	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	20	P	
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	52	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	43	P	
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	30	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	35	P	
05. NETWORK ANALYSIS	PP	100	40	49	P C	15. ELECTROMAGNETIC	PP	100	40	40	P	
06. DIGITAL LOGIC DESIGN	PP	100	40	72	P C	16. ELECTROMAGNETIC	TW	25	10	16	P	
07. DIGITAL LOGIC DESIGN	PR	50	20	30	P C	17. DATA STRUCTURES	PP	100	40	59	P	
08. POWER DEVICES AND MACHINES	PP	100	40	40	P C	18. DATA STRUCTURES	PR	50	20	40	P	
09. NETWORK AND POWER LAB.	TW	50	20	39	P C	19. COMMUNICATION THEORY	PP	100	40	57	P	
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	37	P C	20. COMMUNICATION THEORY	OR	50	20	38	P	
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	38	P	

GRAND TOTAL = 898+02/1500, RESULT: FIRST CLASS [0.2]

ORDN. 1 MARKS :

S8053094	PATIL PRADNYA RAJENDRA				SNEHAL		, 71236323J			PICT		S8053094
01. SIGNAL AND SYSTEMS	PP	100	40	65	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	69	P	
02. SIGNAL AND SYSTEMS	OR	50	20	32	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	20	P	
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	54	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	40	P	
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	38	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	35	P	
05. NETWORK ANALYSIS	PP	100	40	49	P C	15. ELECTROMAGNETIC	PP	100	40	46	P	
06. DIGITAL LOGIC DESIGN	PP	100	40	66	P C	16. ELECTROMAGNETIC	TW	25	10	19	P	
07. DIGITAL LOGIC DESIGN	PR	50	20	34	P C	17. DATA STRUCTURES	PP	100	40	71	P	
08. POWER DEVICES AND MACHINES	PP	100	40	44	P C	18. DATA STRUCTURES	PR	50	20	42	P	
09. NETWORK AND POWER LAB.	TW	50	20	42	P C	19. COMMUNICATION THEORY	PP	100	40	59	P	
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	43	P C	20. COMMUNICATION THEORY	OR	50	20	35	P	
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	39	P	

GRAND TOTAL = 942/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8053095	PATIL PUJA VIJAY				MIRA		, 71100942C			PICT		S8053095
01. SIGNAL AND SYSTEMS	PP	100	40	48	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	69	P	
02. SIGNAL AND SYSTEMS	OR	50	20	25	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	16	P	
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	40	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	45	P	
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	32	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	33	P	
05. NETWORK ANALYSIS	PP	100	40	56	P	15. ELECTROMAGNETIC	PP	100	40	45	P	
06. DIGITAL LOGIC DESIGN	PP	100	40	49	P C	16. ELECTROMAGNETIC	TW	25	10	15	P	
07. DIGITAL LOGIC DESIGN	PR	50	20	25	P C	17. DATA STRUCTURES	PP	100	40	64	P	
08. POWER DEVICES AND MACHINES	PP	100	40	40	P C	18. DATA STRUCTURES	PR	50	20	33	P	
09. NETWORK AND POWER LAB.	TW	50	20	32	P C	19. COMMUNICATION THEORY	PP	100	40	53	P	
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	33	P C	20. COMMUNICATION THEORY	OR	50	20	30	P	
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	31	P	

GRAND TOTAL = 814/1500, RESULT: SECOND CLASS

ORDN. 1 MARKS :

S8053096	PATIL SHIVRATNA BALASAHEB				SHEELA		, 71236324G			PICT		S8053096
01. SIGNAL AND SYSTEMS	PP	100	40	54	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	57	P	
02. SIGNAL AND SYSTEMS	OR	50	20	30	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	16	P	
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	49	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	61	P	
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	39	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	30	P	
05. NETWORK ANALYSIS	PP	100	40	44	P C	15. ELECTROMAGNETIC	PP	100	40	40	P	
06. DIGITAL LOGIC DESIGN	PP	100	40	68	P C	16. ELECTROMAGNETIC	TW	25	10	16	P	
07. DIGITAL LOGIC DESIGN	PR	50	20	24	P C	17. DATA STRUCTURES	PP	100	40	56	P	
08. POWER DEVICES AND MACHINES	PP	100	40	40	P C	18. DATA STRUCTURES	PR	50	20	34	P	
09. NETWORK AND POWER LAB.	TW	50	20	30	P C	19. COMMUNICATION THEORY	PP	100	40	64	P	
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	32	P C	20. COMMUNICATION THEORY	OR	50	20	30	P	
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	36	P	

GRAND TOTAL = 850/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

S8053097	PAVAN TENKALE				SHIVKANTA		, 71100946F			PICT		S8053097
01. SIGNAL AND SYSTEMS	PP	100	40	54	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	61	P	
02. SIGNAL AND SYSTEMS	OR	50	20	32	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	17	P	
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	48	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	13	F	
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	35	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	AA	F	
05. NETWORK ANALYSIS	PP	100	40	42	P C	15. ELECTROMAGNETIC	PP	100	40	31	F	
06. DIGITAL LOGIC DESIGN	PP	100	40	46	P C	16. ELECTROMAGNETIC	TW	25	10	13	P	
07. DIGITAL LOGIC DESIGN	PR	50	20	23	P C	17. DATA STRUCTURES	PP	100	40	43	P	
08. POWER DEVICES AND MACHINES	PP	100	40	40	P C	18. DATA STRUCTURES	PR	50	20	25	P	
09. NETWORK AND POWER LAB.	TW	50	20	38	P C	19. COMMUNICATION THEORY	PP	100	40	53	P	
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	32	P C	20. COMMUNICATION THEORY	OR	50	20	23	P	
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	30	P	

GRAND TOTAL = 699/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8053098	PAWAR AJAY BALNATH				SUNITA		, 71100948B			PICT		S8053098
01. SIGNAL AND SYSTEMS	PP	100	40	65	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	83	P	
02. SIGNAL AND SYSTEMS	OR	50	20	42	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	22	P	
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	56	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	48	P	
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	39	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	30	P	
05. NETWORK ANALYSIS	PP	100	40	57	P C	15. ELECTROMAGNETIC	PP	100	40	40	P	
06. DIGITAL LOGIC DESIGN	PP	100	40	63	P C	16. ELECTROMAGNETIC	TW	25	10	22	P	
07. DIGITAL LOGIC DESIGN	PR	50	20	38	P C	17. DATA STRUCTURES	PP	100	40	67	P	
08. POWER DEVICES AND MACHINES	PP	100	40	52	P C	18. DATA STRUCTURES	PR	50	20	46	P	
09. NETWORK AND POWER LAB.	TW	50	20	40	P C	19. COMMUNICATION THEORY	PP	100	40	56	P	
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	41	P C	20. COMMUNICATION THEORY	OR	50	20	30	P	
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	41	P	

GRAND TOTAL = 978/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

S8053099	PAWAR ASHWINI SUDHAKAR				VANDANA		, 71236325E			PICT		S8053099
01. SIGNAL AND SYSTEMS	PP	100	40	53	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	62	P	
02. SIGNAL AND SYSTEMS	OR	50	20	32	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	22	P	
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	54	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	40	P	
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	34	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	38	P	
05. NETWORK ANALYSIS	PP	100	40	40	P C	15. ELECTROMAGNETIC	PP	100	40	52	P	
06. DIGITAL LOGIC DESIGN	PP	100	40	46	P C	16. ELECTROMAGNETIC	TW	25	10	17	P	
07. DIGITAL LOGIC DESIGN	PR	50	20	30	P C	17. DATA STRUCTURES	PP	100	40	59	P	
08. POWER DEVICES AND MACHINES	PP	100	40	41	P C	18. DATA STRUCTURES	PR	50	20	36	P	
09. NETWORK AND POWER LAB.	TW	50	20	39	P C	19. COMMUNICATION THEORY	PP	100	40	56	P	
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	39	P C	20. COMMUNICATION THEORY	OR	50	20	40	P	
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	41	P	

GRAND TOTAL = 871/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

S8053100	PHAPALE MONIKA DILIP				ALKA		, 71100955E			PICT		S8053100
01. SIGNAL AND SYSTEMS	PP	100	40	53	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	65	P	
02. SIGNAL AND SYSTEMS	OR	50	20	34	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	19	P	
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	42	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	24	F	
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	33	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	35	P	
05. NETWORK ANALYSIS	PP	100	40	40	P C	15. ELECTROMAGNETIC	PP	100	40	40	P	
06. DIGITAL LOGIC DESIGN	PP	100	40	48	P C	16. ELECTROMAGNETIC	TW	25	10	20	P	
07. DIGITAL LOGIC DESIGN	PR	50	20	26	P C	17. DATA STRUCTURES	PP	100	40	63	P	
08. POWER DEVICES AND MACHINES	PP	100	40	56	P	18. DATA STRUCTURES	PR	50	20	35	P	
09. NETWORK AND POWER LAB.	TW	50	20	40	P C	19. COMMUNICATION THEORY	PP	100	40	53	P	
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	37	P C	20. COMMUNICATION THEORY	OR	50	20	30	P	
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	40	P	

GRAND TOTAL = 833/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8053101	PRAGATI VASANT SHAH				VARSHA				, 71100958K				PICT		, S8053101
01. SIGNAL AND SYSTEMS	PP	100	40	59	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	77	P				
02. SIGNAL AND SYSTEMS	OR	50	20	37	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	21	P				
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	50	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	40	P				
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	28	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	28	P				
05. NETWORK ANALYSIS	PP	100	40	55	P C	15. ELECTROMAGNETIC	PP	100	40	52	P				
06. DIGITAL LOGIC DESIGN	PP	100	40	54	P C	16. ELECTROMAGNETIC	TW	25	10	20	P				
07. DIGITAL LOGIC DESIGN	PR	50	20	39	P	17. DATA STRUCTURES	PP	100	40	58	P				
08. POWER DEVICES AND MACHINES	PP	100	40	40	P C	18. DATA STRUCTURES	PR	50	20	48	P				
09. NETWORK AND POWER LAB.	TW	50	20	41	P C	19. COMMUNICATION THEORY	PP	100	40	64	P				
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	41	P C	20. COMMUNICATION THEORY	OR	50	20	40	P				
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	39	P				

GRAND TOTAL = 931/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

S8053102	PRAMOD KUMAR VERMA				SUMAN DEVI				, 71100959H				PICT		, S8053102
01. SIGNAL AND SYSTEMS	PP	100	40	58	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	91	P				
02. SIGNAL AND SYSTEMS	OR	50	20	38	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	21	P				
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	50	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	45	P				
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	35	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	42	P				
05. NETWORK ANALYSIS	PP	100	40	65	P C	15. ELECTROMAGNETIC	PP	100	40	71	P				
06. DIGITAL LOGIC DESIGN	PP	100	40	60	P C	16. ELECTROMAGNETIC	TW	25	10	18	P				
07. DIGITAL LOGIC DESIGN	PR	50	20	28	P	17. DATA STRUCTURES	PP	100	40	74	P				
08. POWER DEVICES AND MACHINES	PP	100	40	51	P C	18. DATA STRUCTURES	PR	50	20	44	P				
09. NETWORK AND POWER LAB.	TW	50	20	42	P C	19. COMMUNICATION THEORY	PP	100	40	63	P				
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	39	P C	20. COMMUNICATION THEORY	OR	50	20	39	P				
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	40	P				

GRAND TOTAL = 1014/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

S8053103	PRAYAG SHREYAS NAGNATH				JAYASHRI				, 71100960M				PICT		, S8053103
01. SIGNAL AND SYSTEMS	PP	100	40	58	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	73	P				
02. SIGNAL AND SYSTEMS	OR	50	20	34	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	19	P				
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	61	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	43	P				
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	33	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	32	P				
05. NETWORK ANALYSIS	PP	100	40	51	P C	15. ELECTROMAGNETIC	PP	100	40	40	P				
06. DIGITAL LOGIC DESIGN	PP	100	40	57	P C	16. ELECTROMAGNETIC	TW	25	10	15	P				
07. DIGITAL LOGIC DESIGN	PR	50	20	34	P C	17. DATA STRUCTURES	PP	100	40	63	P				
08. POWER DEVICES AND MACHINES	PP	100	40	46	P C	18. DATA STRUCTURES	PR	50	20	46	P				
09. NETWORK AND POWER LAB.	TW	50	20	35	P C	19. COMMUNICATION THEORY	PP	100	40	57	P				
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	38	P C	20. COMMUNICATION THEORY	OR	50	20	38	P				
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	30	P				

GRAND TOTAL = 903+05/1500, RESULT: FIRST CLASS[0.163]

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8053104	PURANDARE ADWAIT PRASHANT				MONIKA				, 71100961K				PICT		S8053104
01. SIGNAL AND SYSTEMS	PP	100	40	80	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	95	P				
02. SIGNAL AND SYSTEMS	OR	50	20	46	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	24	P				
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	64	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	40	P				
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	45	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	45	P				
05. NETWORK ANALYSIS	PP	100	40	72	P C	15. ELECTROMAGNETIC	PP	100	40	65	P				
06. DIGITAL LOGIC DESIGN	PP	100	40	76	P C	16. ELECTROMAGNETIC	TW	25	10	22	P				
07. DIGITAL LOGIC DESIGN	PR	50	20	38	P C	17. DATA STRUCTURES	PP	100	40	86	P				
08. POWER DEVICES AND MACHINES	PP	100	40	55	P C	18. DATA STRUCTURES	PR	50	20	48	P				
09. NETWORK AND POWER LAB.	TW	50	20	46	P C	19. COMMUNICATION THEORY	PP	100	40	72	P				
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	45	P C	20. COMMUNICATION THEORY	OR	50	20	43	P				
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	47	P				

GRAND TOTAL = 1154/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

S8053105	RATHOD AKASH NURSING				NALINI				, 71100970J				PICT		S8053105
01. SIGNAL AND SYSTEMS	PP	100	40	40	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	56	P				
02. SIGNAL AND SYSTEMS	OR	50	20	38	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	17	P				
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	42	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	28	F				
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	29	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	35	P				
05. NETWORK ANALYSIS	PP	100	40	42	P C	15. ELECTROMAGNETIC	PP	100	40	28	F				
06. DIGITAL LOGIC DESIGN	PP	100	40	64	P C	16. ELECTROMAGNETIC	TW	25	10	16	P				
07. DIGITAL LOGIC DESIGN	PR	50	20	39	P C	17. DATA STRUCTURES	PP	100	40	42	P				
08. POWER DEVICES AND MACHINES	PP	100	40	40	P C	18. DATA STRUCTURES	PR	50	20	45	P				
09. NETWORK AND POWER LAB.	TW	50	20	42	P C	19. COMMUNICATION THEORY	PP	100	40	44	P				
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	39	P C	20. COMMUNICATION THEORY	OR	50	20	28	P				
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	36	P				

GRAND TOTAL = 790/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

S8053106	RATHOD VIJAY HIRAMAN				KANTA				, 71236326C				PICT		S8053106
01. SIGNAL AND SYSTEMS	PP	100	40	41	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	40	P				
02. SIGNAL AND SYSTEMS	OR	50	20	34	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	19	P				
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	40	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	57	P				
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	34	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	35	P				
05. NETWORK ANALYSIS	PP	100	40	48	P	15. ELECTROMAGNETIC	PP	100	40	46	P				
06. DIGITAL LOGIC DESIGN	PP	100	40	62	P C	16. ELECTROMAGNETIC	TW	25	10	15	P				
07. DIGITAL LOGIC DESIGN	PR	50	20	24	P C	17. DATA STRUCTURES	PP	100	40	57	P				
08. POWER DEVICES AND MACHINES	PP	100	40	45	P C	18. DATA STRUCTURES	PR	50	20	36	P				
09. NETWORK AND POWER LAB.	TW	50	20	37	P C	19. COMMUNICATION THEORY	PP	100	40	47	P				
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	37	P C	20. COMMUNICATION THEORY	OR	50	20	42	P				
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	38	P				

GRAND TOTAL = 834/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8053107	RAUT CHAITANYA SADASHIV			CHHAYA		, 71100972E			PICT		S8053107
01. SIGNAL AND SYSTEMS	PP	100	40	42	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	71	P
02. SIGNAL AND SYSTEMS	OR	50	20	39	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	20	P
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	55	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	66	P
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	32	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	34	P
05. NETWORK ANALYSIS	PP	100	40	61	P C	15. ELECTROMAGNETIC	PP	100	40	63	P
06. DIGITAL LOGIC DESIGN	PP	100	40	67	P C	16. ELECTROMAGNETIC	TW	25	10	15	P
07. DIGITAL LOGIC DESIGN	PR	50	20	38	P C	17. DATA STRUCTURES	PP	100	40	54	P
08. POWER DEVICES AND MACHINES	PP	100	40	61	P C	18. DATA STRUCTURES	PR	50	20	46	P
09. NETWORK AND POWER LAB.	TW	50	20	29	P C	19. COMMUNICATION THEORY	PP	100	40	57	P
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	36	P C	20. COMMUNICATION THEORY	OR	50	20	41	P
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	35	P

GRAND TOTAL = 962/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

S8053108	ROHAN KHANNA			NANCY		, 71100976H			PICT		S8053108
01. SIGNAL AND SYSTEMS	PP	100	40	32	F	11. ENGINEERING MATHEMATICS III	PP	100	40	06	F
02. SIGNAL AND SYSTEMS	OR	50	20	32	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	12	P
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	28	F	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	18	F
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	33	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	07	F
05. NETWORK ANALYSIS	PP	100	40	40	P C	15. ELECTROMAGNETIC	PP	100	40	00	F
06. DIGITAL LOGIC DESIGN	PP	100	40	47	P C	16. ELECTROMAGNETIC	TW	25	10	13	P
07. DIGITAL LOGIC DESIGN	PR	50	20	34	P C	17. DATA STRUCTURES	PP	100	40	61	P
08. POWER DEVICES AND MACHINES	PP	100	40	30	F	18. DATA STRUCTURES	PR	50	20	44	P
09. NETWORK AND POWER LAB.	TW	50	20	33	P C	19. COMMUNICATION THEORY	PP	100	40	06	F
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	30	P C	20. COMMUNICATION THEORY	OR	50	20	39	P
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	28	P

GRAND TOTAL = 573/1500, RESULT: FAILS

ORDN. 1 MARKS :

S8053109	ROKADE VINITA SATISHKUMAR			VIBHAVARI		, 71100977F			PICT		S8053109
01. SIGNAL AND SYSTEMS	PP	100	40	40	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	26	F
02. SIGNAL AND SYSTEMS	OR	50	20	30	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	19	P
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	41	P	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	40	P
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	20	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	30	P
05. NETWORK ANALYSIS	PP	100	40	42	P	15. ELECTROMAGNETIC	PP	100	40	46	P
06. DIGITAL LOGIC DESIGN	PP	100	40	48	P C	16. ELECTROMAGNETIC	TW	25	10	18	P
07. DIGITAL LOGIC DESIGN	PR	50	20	24	P C	17. DATA STRUCTURES	PP	100	40	56	P
08. POWER DEVICES AND MACHINES	PP	100	40	40	P C	18. DATA STRUCTURES	PR	50	20	43	P
09. NETWORK AND POWER LAB.	TW	50	20	26	P C	19. COMMUNICATION THEORY	PP	100	40	45	P
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	30	P C	20. COMMUNICATION THEORY	OR	50	20	40	P
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	34	P

GRAND TOTAL = 738/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8053110	RUDRAWAR SOURABH ASHWIN				SHOBHA				, 71100979B				PICT		, S8053110
01. SIGNAL AND SYSTEMS	PP	100	40	59	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	47	P				
02. SIGNAL AND SYSTEMS	OR	50	20	35	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	14	P				
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	49	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	52	P				
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	31	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	28	P				
05. NETWORK ANALYSIS	PP	100	40	40	P C	15. ELECTROMAGNETIC	PP	100	40	58	P				
06. DIGITAL LOGIC DESIGN	PP	100	40	59	P C	16. ELECTROMAGNETIC	TW	25	10	14	P				
07. DIGITAL LOGIC DESIGN	PR	50	20	24	P C	17. DATA STRUCTURES	PP	100	40	55	P				
08. POWER DEVICES AND MACHINES	PP	100	40	48	P C	18. DATA STRUCTURES	PR	50	20	42	P				
09. NETWORK AND POWER LAB.	TW	50	20	35	P C	19. COMMUNICATION THEORY	PP	100	40	53	P				
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	33	P C	20. COMMUNICATION THEORY	OR	50	20	29	P				
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	32	P				

GRAND TOTAL = 837/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

S8053111	SALUNKE SAYALI RAJENDRA				SANGEETA				, 71100984J				PICT		, S8053111
01. SIGNAL AND SYSTEMS	PP	100	40	60	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	79	P				
02. SIGNAL AND SYSTEMS	OR	50	20	40	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	22	P				
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	57	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	63	P				
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	38	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	30	P				
05. NETWORK ANALYSIS	PP	100	40	60	P C	15. ELECTROMAGNETIC	PP	100	40	78	P				
06. DIGITAL LOGIC DESIGN	PP	100	40	60	P C	16. ELECTROMAGNETIC	TW	25	10	20	P				
07. DIGITAL LOGIC DESIGN	PR	50	20	38	P C	17. DATA STRUCTURES	PP	100	40	67	P				
08. POWER DEVICES AND MACHINES	PP	100	40	64	P C	18. DATA STRUCTURES	PR	50	20	42	P				
09. NETWORK AND POWER LAB.	TW	50	20	32	P C	19. COMMUNICATION THEORY	PP	100	40	63	P				
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	34	P C	20. COMMUNICATION THEORY	OR	50	20	34	P				
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	38	P				

GRAND TOTAL = 1019/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

S8053112	SALUNKHE MITALI KISHOR				BHARATI				, 71236327M				PICT		, S8053112
01. SIGNAL AND SYSTEMS	PP	100	40	56	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	61	P				
02. SIGNAL AND SYSTEMS	OR	50	20	24	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	21	P				
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	42	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	57	P				
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	36	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	20	P				
05. NETWORK ANALYSIS	PP	100	40	42	P C	15. ELECTROMAGNETIC	PP	100	40	42	P				
06. DIGITAL LOGIC DESIGN	PP	100	40	60	P C	16. ELECTROMAGNETIC	TW	25	10	16	P				
07. DIGITAL LOGIC DESIGN	PR	50	20	26	P C	17. DATA STRUCTURES	PP	100	40	70	P				
08. POWER DEVICES AND MACHINES	PP	100	40	46	P C	18. DATA STRUCTURES	PR	50	20	35	P				
09. NETWORK AND POWER LAB.	TW	50	20	41	P C	19. COMMUNICATION THEORY	PP	100	40	51	P				
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	40	P C	20. COMMUNICATION THEORY	OR	50	20	34	P				
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	40	P				

GRAND TOTAL = 860/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8053113	SARODE BHUSHAN CHANDRAKANT			ALKA		, 71236328K			PICT		S8053113
01. SIGNAL AND SYSTEMS	PP	100	40	48	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	22	F
02. SIGNAL AND SYSTEMS	OR	50	20	37	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	20	P
03. SOLID STATES DEVICES AND CIRCUITS	SPP	100	40	40	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	63	P
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	35	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	38	P
05. NETWORK ANALYSIS	PP	100	40	40	P C	15. ELECTROMAGNETIC	PP	100	40	40	P
06. DIGITAL LOGIC DESIGN	PP	100	40	69	P C	16. ELECTROMAGNETIC	TW	25	10	19	P
07. DIGITAL LOGIC DESIGN	PR	50	20	22	P C	17. DATA STRUCTURES	PP	100	40	67	P
08. POWER DEVICES AND MACHINES	PP	100	40	54	P C	18. DATA STRUCTURES	PR	50	20	45	P
09. NETWORK AND POWER LAB.	TW	50	20	41	P C	19. COMMUNICATION THEORY	PP	100	40	70	P
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	42	P C	20. COMMUNICATION THEORY	OR	50	20	35	P
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	38	P

GRAND TOTAL = 885/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

S8053114	SARWAR ABHIJIT ASHOK			MEERA		, 71100988M			PICT		S8053114
01. SIGNAL AND SYSTEMS	PP	100	40	40	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	27	F
02. SIGNAL AND SYSTEMS	OR	50	20	20	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	12	P
03. SOLID STATES DEVICES AND CIRCUITS	SPP	100	40	46	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	45	P
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	33	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	30	P
05. NETWORK ANALYSIS	PP	100	40	40	P C	15. ELECTROMAGNETIC	PP	100	40	25	F
06. DIGITAL LOGIC DESIGN	PP	100	40	52	P C	16. ELECTROMAGNETIC	TW	25	10	12	P
07. DIGITAL LOGIC DESIGN	PR	50	20	32	P C	17. DATA STRUCTURES	PP	100	40	51	P
08. POWER DEVICES AND MACHINES	PP	100	40	40	P C	18. DATA STRUCTURES	PR	50	20	34	P
09. NETWORK AND POWER LAB.	TW	50	20	39	P C	19. COMMUNICATION THEORY	PP	100	40	46	P
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	35	P C	20. COMMUNICATION THEORY	OR	50	20	30	P
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	30	P

GRAND TOTAL = 719/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

S8053115	SHAHA SANKET MANOJKUMAR			NEETA		, 71100997L			PICT		S8053115
01. SIGNAL AND SYSTEMS	PP	100	40	51	P C						
02. SIGNAL AND SYSTEMS	OR	50	20	24	P C						
03. SOLID STATES DEVICES AND CIRCUITS	SPP	100	40	40	P C						
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	22	P C						
05. NETWORK ANALYSIS	PP	100	40	58	P						
06. DIGITAL LOGIC DESIGN	PP	100	40	48	P C						
07. DIGITAL LOGIC DESIGN	PR	50	20	15	F						
08. POWER DEVICES AND MACHINES	PP	100	40	40	P C						
09. NETWORK AND POWER LAB.	TW	50	20	20	P C						
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	22	P C						

FIRST TERM TOTAL = 340/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8053116	SHAIKH MAAZ FAYYAZ				SHAHEEN		, 71100998J			PICT		S8053116
01. SIGNAL AND SYSTEMS	PP	100	40	67	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	76	P	
02. SIGNAL AND SYSTEMS	OR	50	20	33	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	23	P	
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	65	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	67	P	
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	35	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	37	P	
05. NETWORK ANALYSIS	PP	100	40	53	P C	15. ELECTROMAGNETIC	PP	100	40	63	P	
06. DIGITAL LOGIC DESIGN	PP	100	40	64	P C	16. ELECTROMAGNETIC	TW	25	10	19	P	
07. DIGITAL LOGIC DESIGN	PR	50	20	33	P C	17. DATA STRUCTURES	PP	100	40	56	P	
08. POWER DEVICES AND MACHINES	PP	100	40	56	P C	18. DATA STRUCTURES	PR	50	20	45	P	
09. NETWORK AND POWER LAB.	TW	50	20	42	P C	19. COMMUNICATION THEORY	PP	100	40	63	P	
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	38	P C	20. COMMUNICATION THEORY	OR	50	20	40	P	
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	42	P	

GRAND TOTAL = 1017/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

S8053117	SHELKE ISHWAR ANKUSH				MUKTA		, 71101001D			PICT		S8053117
01. SIGNAL AND SYSTEMS	PP	100	40	65	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	84	P	
02. SIGNAL AND SYSTEMS	OR	50	20	35	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	22	P	
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	60	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	69	P	
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	38	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	35	P	
05. NETWORK ANALYSIS	PP	100	40	76	P C	15. ELECTROMAGNETIC	PP	100	40	86	P	
06. DIGITAL LOGIC DESIGN	PP	100	40	76	P C	16. ELECTROMAGNETIC	TW	25	10	22	P	
07. DIGITAL LOGIC DESIGN	PR	50	20	38	P C	17. DATA STRUCTURES	PP	100	40	71	P	
08. POWER DEVICES AND MACHINES	PP	100	40	66	P C	18. DATA STRUCTURES	PR	50	20	48	P	
09. NETWORK AND POWER LAB.	TW	50	20	39	P C	19. COMMUNICATION THEORY	PP	100	40	70	P	
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	37	P C	20. COMMUNICATION THEORY	OR	50	20	36	P	
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	43	P	

GRAND TOTAL = 1116/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

S8053118	SHIKHA SUMAN SINHA				SAVITA		, 71101004J			PICT		S8053118
01. SIGNAL AND SYSTEMS	PP	100	40	42	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	24	F	
02. SIGNAL AND SYSTEMS	OR	50	20	33	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	19	P	
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	40	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	47	P	
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	25	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	20	P	
05. NETWORK ANALYSIS	PP	100	40	40	P C	15. ELECTROMAGNETIC	PP	100	40	28	F	
06. DIGITAL LOGIC DESIGN	PP	100	40	61	P C	16. ELECTROMAGNETIC	TW	25	10	17	P	
07. DIGITAL LOGIC DESIGN	PR	50	20	24	P	17. DATA STRUCTURES	PP	100	40	61	P	
08. POWER DEVICES AND MACHINES	PP	100	40	40	P C	18. DATA STRUCTURES	PR	50	20	40	P	
09. NETWORK AND POWER LAB.	TW	50	20	39	P C	19. COMMUNICATION THEORY	PP	100	40	51	P	
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	35	P C	20. COMMUNICATION THEORY	OR	50	20	24	P	
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	36	P	

GRAND TOTAL = 746/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8053119	SHITRE SOURABH DNYANESHWAR				SNEHA				, 71101011M				PICT		, S8053119
01.	SIGNAL AND SYSTEMS	PP	100	40	55	P	C		11.	ENGINEERING MATHEMATICS III	PP	100	40	57	P
02.	SIGNAL AND SYSTEMS	OR	50	20	37	P	C		12.	ENGINEERING MATHEMATICS III	TW	25	10	22	P
03.	SOLID STATES DEVICES AND CIRCUITS	SPP	100	40	60	P	C		13.	INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	45	P
04.	SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	38	P	C		14.	INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	35	P
05.	NETWORK ANALYSIS	PP	100	40	46	P	C		15.	ELECTROMAGNETIC	PP	100	40	58	P
06.	DIGITAL LOGIC DESIGN	PP	100	40	51	P	C		16.	ELECTROMAGNETIC	TW	25	10	22	P
07.	DIGITAL LOGIC DESIGN	PR	50	20	42	P	C		17.	DATA STRUCTURES	PP	100	40	68	P
08.	POWER DEVICES AND MACHINES	PP	100	40	42	P	C		18.	DATA STRUCTURES	PR	50	20	48	P
09.	NETWORK AND POWER LAB.	TW	50	20	43	P	C		19.	COMMUNICATION THEORY	PP	100	40	58	P
10.	ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	40	P	C		20.	COMMUNICATION THEORY	OR	50	20	39	P
									21.	CIRCUIT SIMULATION AND TOOLS	TW	50	20	43	P

GRAND TOTAL = 949/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

S8053120	SHRADDHA AGRAWAL				REKHA				, 71101013H				PICT		, S8053120
01.	SIGNAL AND SYSTEMS	PP	100	40	46	P	C		11.	ENGINEERING MATHEMATICS III	PP	100	40	65	P
02.	SIGNAL AND SYSTEMS	OR	50	20	36	P	C		12.	ENGINEERING MATHEMATICS III	TW	25	10	17	P
03.	SOLID STATES DEVICES AND CIRCUITS	SPP	100	40	61	P	C		13.	INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	61	P
04.	SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	37	P	C		14.	INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	30	P
05.	NETWORK ANALYSIS	PP	100	40	58	P	C		15.	ELECTROMAGNETIC	PP	100	40	73	P
06.	DIGITAL LOGIC DESIGN	PP	100	40	68	P	C		16.	ELECTROMAGNETIC	TW	25	10	16	P
07.	DIGITAL LOGIC DESIGN	PR	50	20	46	P	C		17.	DATA STRUCTURES	PP	100	40	60	P
08.	POWER DEVICES AND MACHINES	PP	100	40	46	P	C		18.	DATA STRUCTURES	PR	50	20	35	P
09.	NETWORK AND POWER LAB.	TW	50	20	39	P	C		19.	COMMUNICATION THEORY	PP	100	40	62	P
10.	ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	36	P	C		20.	COMMUNICATION THEORY	OR	50	20	39	P
									21.	CIRCUIT SIMULATION AND TOOLS	TW	50	20	35	P

GRAND TOTAL = 966/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

S8053121	SIDDAMAL GANESH UDDANAPPA				PREMA				, 71101014F				PICT		, S8053121
01.	SIGNAL AND SYSTEMS	PP	100	40	51	P	C		11.	ENGINEERING MATHEMATICS III	PP	100	40	67	P
02.	SIGNAL AND SYSTEMS	OR	50	20	38	P	C		12.	ENGINEERING MATHEMATICS III	TW	25	10	22	P
03.	SOLID STATES DEVICES AND CIRCUITS	SPP	100	40	40	P	C		13.	INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	52	P
04.	SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	30	P	C		14.	INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	36	P
05.	NETWORK ANALYSIS	PP	100	40	49	P	C		15.	ELECTROMAGNETIC	PP	100	40	48	P
06.	DIGITAL LOGIC DESIGN	PP	100	40	40	P	C		16.	ELECTROMAGNETIC	TW	25	10	20	P
07.	DIGITAL LOGIC DESIGN	PR	50	20	29	P			17.	DATA STRUCTURES	PP	100	40	53	P
08.	POWER DEVICES AND MACHINES	PP	100	40	40	P	C		18.	DATA STRUCTURES	PR	50	20	44	P
09.	NETWORK AND POWER LAB.	TW	50	20	35	P	C		19.	COMMUNICATION THEORY	PP	100	40	49	P
10.	ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	37	P	C		20.	COMMUNICATION THEORY	OR	50	20	37	P
									21.	CIRCUIT SIMULATION AND TOOLS	TW	50	20	40	P

GRAND TOTAL = 857/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8053122	SIDDHANT KHURANA				SAROJ		, 71101016B			PICT		S8053122
01. SIGNAL AND SYSTEMS	PP	100	40	43	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	44	P	
02. SIGNAL AND SYSTEMS	OR	50	20	36	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	17	P	
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	40	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	15	F	
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	34	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	20	P	
05. NETWORK ANALYSIS	PP	100	40	40	P C	15. ELECTROMAGNETIC	PP	100	40	22	F	
06. DIGITAL LOGIC DESIGN	PP	100	40	40	P C	16. ELECTROMAGNETIC	TW	25	10	14	P	
07. DIGITAL LOGIC DESIGN	PR	50	20	25	P	17. DATA STRUCTURES	PP	100	40	53	P	
08. POWER DEVICES AND MACHINES	PP	100	40	40	P	18. DATA STRUCTURES	PR	50	20	46	P	
09. NETWORK AND POWER LAB.	TW	50	20	38	P C	19. COMMUNICATION THEORY	PP	100	40	40	P	
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	35	P C	20. COMMUNICATION THEORY	OR	50	20	AA	F	
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	33	P	

GRAND TOTAL = 675/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

S8053123	SONAWANE ANUJA GIRIDHAR				SUNITA		, 71101019G			PICT		S8053123
01. SIGNAL AND SYSTEMS	PP	100	40	46	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	42	P	
02. SIGNAL AND SYSTEMS	OR	50	20	40	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	22	P	
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	43	P	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	44	P	
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	22	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	22	P	
05. NETWORK ANALYSIS	PP	100	40	40	P C	15. ELECTROMAGNETIC	PP	100	40	40	P	
06. DIGITAL LOGIC DESIGN	PP	100	40	51	P C	16. ELECTROMAGNETIC	TW	25	10	19	P	
07. DIGITAL LOGIC DESIGN	PR	50	20	30	P C	17. DATA STRUCTURES	PP	100	40	55	P	
08. POWER DEVICES AND MACHINES	PP	100	40	45	P C	18. DATA STRUCTURES	PR	50	20	42	P	
09. NETWORK AND POWER LAB.	TW	50	20	35	P C	19. COMMUNICATION THEORY	PP	100	40	51	P	
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	37	P C	20. COMMUNICATION THEORY	OR	50	20	37	P	
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	38	P	

GRAND TOTAL = 801/1500, RESULT: SECOND CLASS

ORDN. 1 MARKS :

S8053124	SONOO KUMAR				GAURADEVI		, 71101020L			PICT		S8053124
01. SIGNAL AND SYSTEMS	PP	100	40	65	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	70	P	
02. SIGNAL AND SYSTEMS	OR	50	20	30	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	15	P	
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	53	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	53	P	
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	28	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	20	P	
05. NETWORK ANALYSIS	PP	100	40	63	P C	15. ELECTROMAGNETIC	PP	100	40	63	P	
06. DIGITAL LOGIC DESIGN	PP	100	40	69	P C	16. ELECTROMAGNETIC	TW	25	10	14	P	
07. DIGITAL LOGIC DESIGN	PR	50	20	32	P C	17. DATA STRUCTURES	PP	100	40	59	P	
08. POWER DEVICES AND MACHINES	PP	100	40	52	P C	18. DATA STRUCTURES	PR	50	20	34	P	
09. NETWORK AND POWER LAB.	TW	50	20	34	P C	19. COMMUNICATION THEORY	PP	100	40	50	P	
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	30	P C	20. COMMUNICATION THEORY	OR	50	20	35	P	
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	30	P	

GRAND TOTAL = 899+01/1500, RESULT: FIRST CLASS [0.2]

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8053125	SONTAKKE BASVESHVAR RAOSAHEB			SEETABAI		, 71101021J			PICT		S8053125
01. SIGNAL AND SYSTEMS	PP	100	40	47	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	79	P
02. SIGNAL AND SYSTEMS	OR	50	20	33	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	19	P
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	57	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	56	P
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	30	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	20	P
05. NETWORK ANALYSIS	PP	100	40	50	P C	15. ELECTROMAGNETIC	PP	100	40	51	P
06. DIGITAL LOGIC DESIGN	PP	100	40	44	P C	16. ELECTROMAGNETIC	TW	25	10	15	P
07. DIGITAL LOGIC DESIGN	PR	50	20	30	P C	17. DATA STRUCTURES	PP	100	40	50	P
08. POWER DEVICES AND MACHINES	PP	100	40	40	P C	18. DATA STRUCTURES	PR	50	20	37	P
09. NETWORK AND POWER LAB.	TW	50	20	31	P C	19. COMMUNICATION THEORY	PP	100	40	42	P
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	36	P C	20. COMMUNICATION THEORY	OR	50	20	26	P
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	31	P

GRAND TOTAL = 824+05/1500, RESULT: HIGHER SECOND CLASS [0.163]

ORDN. 1 MARKS :

S8053126	SUMIT SINGH			USHA		, 71101023E			PICT		S8053126
01. SIGNAL AND SYSTEMS	PP	100	40	63	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	70	P
02. SIGNAL AND SYSTEMS	OR	50	20	42	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	21	P
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	74	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	76	P
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	42	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	32	P
05. NETWORK ANALYSIS	PP	100	40	75	P C	15. ELECTROMAGNETIC	PP	100	40	70	P
06. DIGITAL LOGIC DESIGN	PP	100	40	73	P C	16. ELECTROMAGNETIC	TW	25	10	19	P
07. DIGITAL LOGIC DESIGN	PR	50	20	24	P C	17. DATA STRUCTURES	PP	100	40	71	P
08. POWER DEVICES AND MACHINES	PP	100	40	76	P C	18. DATA STRUCTURES	PR	50	20	42	P
09. NETWORK AND POWER LAB.	TW	50	20	42	P C	19. COMMUNICATION THEORY	PP	100	40	69	P
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	39	P C	20. COMMUNICATION THEORY	OR	50	20	39	P
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	42	P

GRAND TOTAL = 1101/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

S8053127	TANK HIMANSHU SHARAD			GEETA		, 71101025M			PICT		S8053127
01. SIGNAL AND SYSTEMS	PP	100	40	63	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	42	P
02. SIGNAL AND SYSTEMS	OR	50	20	40	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	16	P
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	61	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	45	P
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	40	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	10	F
05. NETWORK ANALYSIS	PP	100	40	47	P C	15. ELECTROMAGNETIC	PP	100	40	52	P
06. DIGITAL LOGIC DESIGN	PP	100	40	70	P C	16. ELECTROMAGNETIC	TW	25	10	14	P
07. DIGITAL LOGIC DESIGN	PR	50	20	25	P C	17. DATA STRUCTURES	PP	100	40	57	P
08. POWER DEVICES AND MACHINES	PP	100	40	40	P C	18. DATA STRUCTURES	PR	50	20	44	P
09. NETWORK AND POWER LAB.	TW	50	20	33	P C	19. COMMUNICATION THEORY	PP	100	40	44	P
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	35	P C	20. COMMUNICATION THEORY	OR	50	20	35	P
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	30	P

GRAND TOTAL = 843/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8053128	TANNA VATSAL AJAY					VAISHALI					, 71236329H	,		PICT		S8053128	
01. SIGNAL AND SYSTEMS	PP	100	40	40	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	14	F						
02. SIGNAL AND SYSTEMS	OR	50	20	30	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	16	P						
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	46	P	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	40	P						
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	39	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	27	P						
05. NETWORK ANALYSIS	PP	100	40	43	P C	15. ELECTROMAGNETIC	PP	100	40	19	F						
06. DIGITAL LOGIC DESIGN	PP	100	40	53	P C	16. ELECTROMAGNETIC	TW	25	10	13	P						
07. DIGITAL LOGIC DESIGN	PR	50	20	23	P C	17. DATA STRUCTURES	PP	100	40	54	P						
08. POWER DEVICES AND MACHINES	PP	100	40	44	P C	18. DATA STRUCTURES	PR	50	20	33	P						
09. NETWORK AND POWER LAB.	TW	50	20	37	P C	19. COMMUNICATION THEORY	PP	100	40	48	P						
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	36	P C	20. COMMUNICATION THEORY	OR	50	20	40	P						
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	32	P						

GRAND TOTAL = 727/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

S8053129	TANYA SINGHAL				SUSHMA		, 71101026K		,	PICT	, S8053129			
01. SIGNAL AND SYSTEMS	PP	100	40	56	P	C	11. ENGINEERING MATHEMATICS III	PP	100	40	81	P		
02. SIGNAL AND SYSTEMS	OR	50	20	35	P	C	12. ENGINEERING MATHEMATICS III	TW	25	10	19	P		
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	48	P	C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	61	P		
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	20	P	C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	35	P		
05. NETWORK ANALYSIS	PP	100	40	62	P	C	15. ELECTROMAGNETIC	PP	100	40	58	P		
06. DIGITAL LOGIC DESIGN	PP	100	40	63	P	C	16. ELECTROMAGNETIC	TW	25	10	19	P		
07. DIGITAL LOGIC DESIGN	PR	50	20	30	P	C	17. DATA STRUCTURES	PP	100	40	62	P		
08. POWER DEVICES AND MACHINES	PP	100	40	43	P	C	18. DATA STRUCTURES	PR	50	20	42	P		
09. NETWORK AND POWER LAB.	TW	50	20	32	P	C	19. COMMUNICATION THEORY	PP	100	40	61	P		
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	36	P	C	20. COMMUNICATION THEORY	OR	50	20	28	P		
							21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	35	P		

GRAND TOTAL = 926/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

S8053130	TAWATE PRIYA GIRISH	SNEHAPRABHA					, 71236330M	,	PICT	, S8053130				
01. SIGNAL AND SYSTEMS	PP	100	40	46	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	81	P			
02. SIGNAL AND SYSTEMS	OR	50	20	37	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	23	P			
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	60	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	75	P			
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	40	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	40	P			
05. NETWORK ANALYSIS	PP	100	40	64	P C	15. ELECTROMAGNETIC	PP	100	40	65	P			
06. DIGITAL LOGIC DESIGN	PP	100	40	70	P C	16. ELECTROMAGNETIC	TW	25	10	20	P			
07. DIGITAL LOGIC DESIGN	PR	50	20	34	P C	17. DATA STRUCTURES	PP	100	40	79	P			
08. POWER DEVICES AND MACHINES	PP	100	40	63	P C	18. DATA STRUCTURES	PR	50	20	48	P			
09. NETWORK AND POWER LAB.	TW	50	20	43	P C	19. COMMUNICATION THEORY	PP	100	40	75	P			
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	43	P C	20. COMMUNICATION THEORY	OR	50	20	34	P			
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	44	P			

GRAND TOTAL = 1084/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8053131	TERKAR PRASAD VITTHALRAO			ASHA		, 71101030H			PICT		S8053131
01. SIGNAL AND SYSTEMS	PP	100	40	49	P	11. ENGINEERING MATHEMATICS III	PP	100	40	40	P
02. SIGNAL AND SYSTEMS	OR	50	20	20	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	12	P
03. SOLID STATES DEVICES AND CIRCUITS	SPP	100	40	22	F	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	43	P
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	20	P	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	06	F
05. NETWORK ANALYSIS	PP	100	40	19	F	15. ELECTROMAGNETIC	PP	100	40	16	F
06. DIGITAL LOGIC DESIGN	PP	100	40	43	P C	16. ELECTROMAGNETIC	TW	25	10	12	P
07. DIGITAL LOGIC DESIGN	PR	50	20	25	P C	17. DATA STRUCTURES	PP	100	40	40	P
08. POWER DEVICES AND MACHINES	PP	100	40	25	F	18. DATA STRUCTURES	PR	50	20	42	P
09. NETWORK AND POWER LAB.	TW	50	20	33	P C	19. COMMUNICATION THEORY	PP	100	40	40	P
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	28	P C	20. COMMUNICATION THEORY	OR	50	20	33	P
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	27	P

GRAND TOTAL = 595/1500, RESULT: FAILS

ORDN. 1 MARKS :

S8053132	THAKARE APURV VIJAY			MEENA		, 71101031F			PICT		S8053132
01. SIGNAL AND SYSTEMS	PP	100	40	31	F	11. ENGINEERING MATHEMATICS III	PP	100	40	20	F
02. SIGNAL AND SYSTEMS	OR	50	20	26	P	12. ENGINEERING MATHEMATICS III	TW	25	10	11	P
03. SOLID STATES DEVICES AND CIRCUITS	SPP	100	40	28	F	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	40	P
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	21	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	05	F
05. NETWORK ANALYSIS	PP	100	40	21	F	15. ELECTROMAGNETIC	PP	100	40	12	F
06. DIGITAL LOGIC DESIGN	PP	100	40	40	P C	16. ELECTROMAGNETIC	TW	25	10	11	P
07. DIGITAL LOGIC DESIGN	PR	50	20	20	P	17. DATA STRUCTURES	PP	100	40	46	P
08. POWER DEVICES AND MACHINES	PP	100	40	40	P	18. DATA STRUCTURES	PR	50	20	33	P
09. NETWORK AND POWER LAB.	TW	50	20	20	P C	19. COMMUNICATION THEORY	PP	100	40	28	F
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	22	P C	20. COMMUNICATION THEORY	OR	50	20	26	P
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	23	P

GRAND TOTAL = 524/1500, RESULT: FAILS

ORDN. 1 MARKS :

S8053133	TILAK AMEY CHANDRASHEKHAR			APARNA		, 71101036G			PICT		S8053133
01. SIGNAL AND SYSTEMS	PP	100	40	55	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	79	P
02. SIGNAL AND SYSTEMS	OR	50	20	38	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	24	P
03. SOLID STATES DEVICES AND CIRCUITS	SPP	100	40	57	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	65	P
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	39	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	38	P
05. NETWORK ANALYSIS	PP	100	40	60	P C	15. ELECTROMAGNETIC	PP	100	40	61	P
06. DIGITAL LOGIC DESIGN	PP	100	40	74	P C	16. ELECTROMAGNETIC	TW	25	10	21	P
07. DIGITAL LOGIC DESIGN	PR	50	20	40	P C	17. DATA STRUCTURES	PP	100	40	60	P
08. POWER DEVICES AND MACHINES	PP	100	40	47	P C	18. DATA STRUCTURES	PR	50	20	48	P
09. NETWORK AND POWER LAB.	TW	50	20	44	P C	19. COMMUNICATION THEORY	PP	100	40	63	P
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	44	P C	20. COMMUNICATION THEORY	OR	50	20	40	P
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	47	P

GRAND TOTAL = 1044/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8053134	TIWARI BHAVYA YOGESH				NEHA		, 71101037E			PICT		S8053134
01. SIGNAL AND SYSTEMS	PP	100	40	51	P C		11. ENGINEERING MATHEMATICS III	PP	100	40	75	P
02. SIGNAL AND SYSTEMS	OR	50	20	37	P C		12. ENGINEERING MATHEMATICS III	TW	25	10	14	P
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	55	P C		13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	65	P
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	35	P		14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	36	P
05. NETWORK ANALYSIS	PP	100	40	52	P C		15. ELECTROMAGNETIC	PP	100	40	72	P
06. DIGITAL LOGIC DESIGN	PP	100	40	62	P C		16. ELECTROMAGNETIC	TW	25	10	15	P
07. DIGITAL LOGIC DESIGN	PR	50	20	39	P C		17. DATA STRUCTURES	PP	100	40	52	P
08. POWER DEVICES AND MACHINES	PP	100	40	51	P C		18. DATA STRUCTURES	PR	50	20	45	P
09. NETWORK AND POWER LAB.	TW	50	20	30	P C		19. COMMUNICATION THEORY	PP	100	40	53	P
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	36	P C		20. COMMUNICATION THEORY	OR	50	20	28	P
							21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	37	P

GRAND TOTAL = 940/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

S8053135	TIWARI VIPUL TARUN				PADMA		, 71101038C			PICT		S8053135
01. SIGNAL AND SYSTEMS	PP	100	40	40	P C		11. ENGINEERING MATHEMATICS III	PP	100	40	40	P
02. SIGNAL AND SYSTEMS	OR	50	20	33	P C		12. ENGINEERING MATHEMATICS III	TW	25	10	14	P
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	40	P C		13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	40	P
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	30	P C		14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	06	F
05. NETWORK ANALYSIS	PP	100	40	51	P C		15. ELECTROMAGNETIC	PP	100	40	21	F
06. DIGITAL LOGIC DESIGN	PP	100	40	60	P C		16. ELECTROMAGNETIC	TW	25	10	13	P
07. DIGITAL LOGIC DESIGN	PR	50	20	22	P C		17. DATA STRUCTURES	PP	100	40	56	P
08. POWER DEVICES AND MACHINES	PP	100	40	44	P C		18. DATA STRUCTURES	PR	50	20	32	P
09. NETWORK AND POWER LAB.	TW	50	20	25	P C		19. COMMUNICATION THEORY	PP	100	40	42	P
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	30	P C		20. COMMUNICATION THEORY	OR	50	20	35	P
							21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	25	P

GRAND TOTAL = 699/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

S8053136	VAIBHAV RAMDAS BORUDE				MANDAKINI		, 71101043K			PICT		S8053136
01. SIGNAL AND SYSTEMS	PP	100	40	71	P C		11. ENGINEERING MATHEMATICS III	PP	100	40	66	P
02. SIGNAL AND SYSTEMS	OR	50	20	41	P C		12. ENGINEERING MATHEMATICS III	TW	25	10	16	P
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	58	P C		13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	63	P
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	39	P C		14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	38	P
05. NETWORK ANALYSIS	PP	100	40	70	P C		15. ELECTROMAGNETIC	PP	100	40	77	P
06. DIGITAL LOGIC DESIGN	PP	100	40	62	P C		16. ELECTROMAGNETIC	TW	25	10	15	P
07. DIGITAL LOGIC DESIGN	PR	50	20	40	P C		17. DATA STRUCTURES	PP	100	40	67	P
08. POWER DEVICES AND MACHINES	PP	100	40	61	P C		18. DATA STRUCTURES	PR	50	20	46	P
09. NETWORK AND POWER LAB.	TW	50	20	39	P C		19. COMMUNICATION THEORY	PP	100	40	61	P
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	35	P C		20. COMMUNICATION THEORY	OR	50	20	35	P
							21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	35	P

GRAND TOTAL = 1035/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8053137	VAIDYA SANDIP MILIND				SANGEETA		, 71101044H			PICT		S8053137
01. SIGNAL AND SYSTEMS	PP	100	40	51	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	56	P	
02. SIGNAL AND SYSTEMS	OR	50	20	41	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	23	P	
03. SOLID STATES DEVICES AND CIRCUITS	SPP	100	40	52	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	62	P	
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	39	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	40	P	
05. NETWORK ANALYSIS	PP	100	40	55	P C	15. ELECTROMAGNETIC	PP	100	40	32#	P	
06. DIGITAL LOGIC DESIGN	PP	100	40	66	P C	16. ELECTROMAGNETIC	TW	25	10	21	P	
07. DIGITAL LOGIC DESIGN	PR	50	20	28	P	17. DATA STRUCTURES	PP	100	40	59	P	
08. POWER DEVICES AND MACHINES	PP	100	40	52	P C	18. DATA STRUCTURES	PR	50	20	47	P	
09. NETWORK AND POWER LAB.	TW	50	20	34	P C	19. COMMUNICATION THEORY	PP	100	40	52	P	
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	39	P C	20. COMMUNICATION THEORY	OR	50	20	35	P	
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	43	P	

GRAND TOTAL = 927/1500, RESULT: FIRST CLASS # [0.4]

ORDN. 1 MARKS :

S8053138	VARADE RUSHIKESH DILIP				CHHAYA		, 71101045F			PICT		S8053138
01. SIGNAL AND SYSTEMS	PP	100	40	42	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	27	F	
02. SIGNAL AND SYSTEMS	OR	50	20	30	P	12. ENGINEERING MATHEMATICS III	TW	25	10	12	P	
03. SOLID STATES DEVICES AND CIRCUITS	SPP	100	40	49	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	51	P	
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	36	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	33	P	
05. NETWORK ANALYSIS	PP	100	40	30	F	15. ELECTROMAGNETIC	PP	100	40	22	F	
06. DIGITAL LOGIC DESIGN	PP	100	40	53	P C	16. ELECTROMAGNETIC	TW	25	10	11	P	
07. DIGITAL LOGIC DESIGN	PR	50	20	32	P C	17. DATA STRUCTURES	PP	100	40	41	P	
08. POWER DEVICES AND MACHINES	PP	100	40	46	P C	18. DATA STRUCTURES	PR	50	20	32	P	
09. NETWORK AND POWER LAB.	TW	50	20	28	P C	19. COMMUNICATION THEORY	PP	100	40	40	P	
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	27	P C	20. COMMUNICATION THEORY	OR	50	20	28	P	
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	25	P	

GRAND TOTAL = 695/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

S8053139	VASAGADEKAR SWATI PRADEEP				SUPRIYA		, 71236331K			PICT		S8053139
01. SIGNAL AND SYSTEMS	PP	100	40	81	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	77	P	
02. SIGNAL AND SYSTEMS	OR	50	20	35	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	22	P	
03. SOLID STATES DEVICES AND CIRCUITS	SPP	100	40	61	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	73	P	
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	40	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	39	P	
05. NETWORK ANALYSIS	PP	100	40	64	P C	15. ELECTROMAGNETIC	PP	100	40	62	P	
06. DIGITAL LOGIC DESIGN	PP	100	40	65	P C	16. ELECTROMAGNETIC	TW	25	10	18	P	
07. DIGITAL LOGIC DESIGN	PR	50	20	38	P C	17. DATA STRUCTURES	PP	100	40	77	P	
08. POWER DEVICES AND MACHINES	PP	100	40	49	P C	18. DATA STRUCTURES	PR	50	20	44	P	
09. NETWORK AND POWER LAB.	TW	50	20	43	P C	19. COMMUNICATION THEORY	PP	100	40	56	P	
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	43	P C	20. COMMUNICATION THEORY	OR	50	20	28	P	
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	42	P	

GRAND TOTAL = 1057/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8053140	VIDHATE TRUPTI SOPAN				VARSHA			, 71101048L			PICT		S8053140
01. SIGNAL AND SYSTEMS	PP	100	40	40	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	48	P		
02. SIGNAL AND SYSTEMS	OR	50	20	37	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	22	P		
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	42	P	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	50	P		
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	38	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	40	P		
05. NETWORK ANALYSIS	PP	100	40	48	P C	15. ELECTROMAGNETIC	PP	100	40	43	P		
06. DIGITAL LOGIC DESIGN	PP	100	40	67	P C	16. ELECTROMAGNETIC	TW	25	10	19	P		
07. DIGITAL LOGIC DESIGN	PR	50	20	35	P C	17. DATA STRUCTURES	PP	100	40	48	P		
08. POWER DEVICES AND MACHINES	PP	100	40	40	P C	18. DATA STRUCTURES	PR	50	20	35	P		
09. NETWORK AND POWER LAB.	TW	50	20	29	P C	19. COMMUNICATION THEORY	PP	100	40	56	P		
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	37	P C	20. COMMUNICATION THEORY	OR	50	20	35	P		
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	39	P		

GRAND TOTAL = 848/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

S8053141	WANI ANIKET ARUN				BHARTI			, 71101055C			PICT		S8053141
01. SIGNAL AND SYSTEMS	PP	100	40	80	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	74	P		
02. SIGNAL AND SYSTEMS	OR	50	20	37	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	16	P		
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	56	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	69	P		
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	37	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	38	P		
05. NETWORK ANALYSIS	PP	100	40	61	P C	15. ELECTROMAGNETIC	PP	100	40	80	P		
06. DIGITAL LOGIC DESIGN	PP	100	40	76	P C	16. ELECTROMAGNETIC	TW	25	10	15	P		
07. DIGITAL LOGIC DESIGN	PR	50	20	36	P C	17. DATA STRUCTURES	PP	100	40	62	P		
08. POWER DEVICES AND MACHINES	PP	100	40	62	P C	18. DATA STRUCTURES	PR	50	20	34	P		
09. NETWORK AND POWER LAB.	TW	50	20	39	P C	19. COMMUNICATION THEORY	PP	100	40	49	P		
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	37	P C	20. COMMUNICATION THEORY	OR	50	20	37	P		
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	36	P		

GRAND TOTAL = 1031/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

S8053142	WANKHEDE ROHIT NITIN				UMA			, 71101057K			PICT		S8053142
01. SIGNAL AND SYSTEMS	PP	100	40	89	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	63	P		
02. SIGNAL AND SYSTEMS	OR	50	20	38	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	22	P		
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	69	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	75	P		
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	30	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	35	P		
05. NETWORK ANALYSIS	PP	100	40	58	P C	15. ELECTROMAGNETIC	PP	100	40	78	P		
06. DIGITAL LOGIC DESIGN	PP	100	40	74	P C	16. ELECTROMAGNETIC	TW	25	10	18	P		
07. DIGITAL LOGIC DESIGN	PR	50	20	37	P C	17. DATA STRUCTURES	PP	100	40	66	P		
08. POWER DEVICES AND MACHINES	PP	100	40	58	P C	18. DATA STRUCTURES	PR	50	20	48	P		
09. NETWORK AND POWER LAB.	TW	50	20	35	P C	19. COMMUNICATION THEORY	PP	100	40	53	P		
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	38	P C	20. COMMUNICATION THEORY	OR	50	20	40	P		
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	38	P		

GRAND TOTAL = 1062/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8053143	YAWALKAR SIDDHESH SHRIPAD				SANDHYA				, 71101061H				PICT		S8053143
01.	SIGNAL AND SYSTEMS	PP	100	40	76	P	C		11.	ENGINEERING MATHEMATICS III	PP	100	40	80	P
02.	SIGNAL AND SYSTEMS	OR	50	20	33	P	C		12.	ENGINEERING MATHEMATICS III	TW	25	10	22	P
03.	SOLID STATES DEVICES AND CIRCUITS	SPP	100	40	68	P	C		13.	INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	72	P
04.	SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	38	P	C		14.	INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	36	P
05.	NETWORK ANALYSIS	PP	100	40	60	P	C		15.	ELECTROMAGNETIC	PP	100	40	68	P
06.	DIGITAL LOGIC DESIGN	PP	100	40	71	P	C		16.	ELECTROMAGNETIC	TW	25	10	19	P
07.	DIGITAL LOGIC DESIGN	PR	50	20	35	P	C		17.	DATA STRUCTURES	PP	100	40	62	P
08.	POWER DEVICES AND MACHINES	PP	100	40	40	P	C		18.	DATA STRUCTURES	PR	50	20	47	P
09.	NETWORK AND POWER LAB.	TW	50	20	40	P	C		19.	COMMUNICATION THEORY	PP	100	40	46	P
10.	ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	39	P	C		20.	COMMUNICATION THEORY	OR	50	20	40	P
									21.	CIRCUIT SIMULATION AND TOOLS	TW	50	20	41	P

GRAND TOTAL = 1033/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

S8053144	YEILE PRAJKT PRAMOD				JYOTI				, 71236332H				PICT		S8053144
01.	SIGNAL AND SYSTEMS	PP	100	40	41	P			11.	ENGINEERING MATHEMATICS III	PP	100	40	18	F
02.	SIGNAL AND SYSTEMS	OR	50	20	23	P	C		12.	ENGINEERING MATHEMATICS III	TW	25	10	15	P
03.	SOLID STATES DEVICES AND CIRCUITS	SPP	100	40	29	F			13.	INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	40	P
04.	SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	26	P	C		14.	INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	20	P
05.	NETWORK ANALYSIS	PP	100	40	27	F			15.	ELECTROMAGNETIC	PP	100	40	15	F
06.	DIGITAL LOGIC DESIGN	PP	100	40	31	F			16.	ELECTROMAGNETIC	TW	25	10	12	P
07.	DIGITAL LOGIC DESIGN	PR	50	20	30	P	C		17.	DATA STRUCTURES	PP	100	40	40	P
08.	POWER DEVICES AND MACHINES	PP	100	40	41	P			18.	DATA STRUCTURES	PR	50	20	35	P
09.	NETWORK AND POWER LAB.	TW	50	20	35	P	C		19.	COMMUNICATION THEORY	PP	100	40	17	F
10.	ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	29	P	C		20.	COMMUNICATION THEORY	OR	50	20	28	P
									21.	CIRCUIT SIMULATION AND TOOLS	TW	50	20	31	P

GRAND TOTAL = 583/1500, RESULT: FAILS

ORDN. 1 MARKS :

S8053146	BHANDALKAR VISHAL MAHADEO				LAXMI				, 70801362H				PICT		S8053146
01.	SIGNAL AND SYSTEMS	PP	100	40	AA	F									
02.	SIGNAL AND SYSTEMS	OR	50	20	AA	F									
03.	SOLID STATES DEVICES AND CIRCUITS	SPP	100	40	AA	F									
04.	SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	AA	F									
05.	NETWORK ANALYSIS	PP	100	40	AA	F									
06.	DIGITAL LOGIC DESIGN	PP	100	40	AA	F									
07.	DIGITAL LOGIC DESIGN	PR	50	20	AA	F									
08.	POWER DEVICES AND MACHINES	PP	100	40	AA	F									
09.	NETWORK AND POWER LAB.	TW	50	20	24	P	C								
10.	ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	20	P	C								

FIRST TERM TOTAL = 44/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8053148	ISHTA PRAKASH BHAGVAN					KAMAL		, 70925442D			PICT		S8053148
01. SIGNAL AND SYSTEMS	PP	100	40	40	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	40	P C		
02. SIGNAL AND SYSTEMS	OR	50	20	24	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	18	P C		
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	40	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	40	P C		
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	20	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	25	P C		
05. NETWORK ANALYSIS	PP	100	40	46	P C	15. ELECTROMAGNETIC	PP	100	40	57	P C		
06. DIGITAL LOGIC DESIGN	PP	100	40	48	P	16. ELECTROMAGNETIC	TW	25	10	10	P C		
07. DIGITAL LOGIC DESIGN	PR	50	20	24	P C	17. DATA STRUCTURES	PP	100	40	46	P C		
08. POWER DEVICES AND MACHINES	PP	100	40	30#	P	18. DATA STRUCTURES	PR	50	20	43	P C		
09. NETWORK AND POWER LAB.	TW	50	20	32	P C	19. COMMUNICATION THEORY	PP	100	40	40	P C		
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	35	P C	20. COMMUNICATION THEORY	OR	50	20	22	P C		
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	26	P C		

GRAND TOTAL = 706/1500, RESULT: PASS CLASS # [0.4]

ORDN. 1 MARKS :

S8053151	MORE VIKAS CHANDRABHAN				SUMAN		, 71045527F				PICT		S8053151
01. SIGNAL AND SYSTEMS	PP	100	40	44	P C		11. ENGINEERING MATHEMATICS III	PP	100	40	40	P C	
02. SIGNAL AND SYSTEMS	OR	50	20	22	P C		12. ENGINEERING MATHEMATICS III	TW	25	10	15	P C	
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	40	P C		13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	42	P C	
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	21	P C		14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	20	P C	
05. NETWORK ANALYSIS	PP	100	40	40	P C		15. ELECTROMAGNETIC	PP	100	40	27	F	
06. DIGITAL LOGIC DESIGN	PP	100	40	40	P C		16. ELECTROMAGNETIC	TW	25	10	17	P C	
07. DIGITAL LOGIC DESIGN	PR	50	20	25	P C		17. DATA STRUCTURES	PP	100	40	47	P	
08. POWER DEVICES AND MACHINES	PP	100	40	42	P C		18. DATA STRUCTURES	PR	50	20	32	P C	
09. NETWORK AND POWER LAB.	TW	50	20	39	P C		19. COMMUNICATION THEORY	PP	100	40	40	P C	
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	38	P C		20. COMMUNICATION THEORY	OR	50	20	29	P C	
							21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	33	P C	

GRAND TOTAL = 693/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

S8053153	NERLEKAR SHWETA PRAKASH	SUGANDHA					, 71045546B	,	PICT	, S8053153				
01. SIGNAL AND SYSTEMS	PP	100	40	46	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	66	P C			
02. SIGNAL AND SYSTEMS	OR	50	20	22	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	21	P C			
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	40	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	51	P C			
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	25	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	35	P C			
05. NETWORK ANALYSIS	PP	100	40	40	P C	15. ELECTROMAGNETIC	PP	100	40	49	P			
06. DIGITAL LOGIC DESIGN	PP	100	40	65	P C	16. ELECTROMAGNETIC	TW	25	10	19	P C			
07. DIGITAL LOGIC DESIGN	PR	50	20	25	P C	17. DATA STRUCTURES	PP	100	40	50	P C			
08. POWER DEVICES AND MACHINES	PP	100	40	40	P C	18. DATA STRUCTURES	PR	50	20	36	P C			
09. NETWORK AND POWER LAB.	TW	50	20	40	P C	19. COMMUNICATION THEORY	PP	100	40	56	P C			
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	41	P C	20. COMMUNICATION THEORY	OR	50	20	27	P C			
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	37	P C			

GRAND TOTAL = 831/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8053155	NIKAM JEEVAN SHANKAR				SANGITA		, 71045547L		, PICT		, S8053155			
01. SIGNAL AND SYSTEMS	PP	100	40	72	P	11. ENGINEERING MATHEMATICS III	PP	100	40	40	P			
02. SIGNAL AND SYSTEMS	OR	50	20	20	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	23	P C			
03. SOLID STATES DEVICES AND CIRCUITS	SPP	100	40	40	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	43	P C			
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	20	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	25	P C			
05. NETWORK ANALYSIS	PP	100	40	47	P	15. ELECTROMAGNETIC	PP	100	40	41	P			
06. DIGITAL LOGIC DESIGN	PP	100	40	40	P C	16. ELECTROMAGNETIC	TW	25	10	15	P C			
07. DIGITAL LOGIC DESIGN	PR	50	20	29	P C	17. DATA STRUCTURES	PP	100	40	40	P C			
08. POWER DEVICES AND MACHINES	PP	100	40	40	P C	18. DATA STRUCTURES	PR	50	20	28	P C			
09. NETWORK AND POWER LAB.	TW	50	20	37	P C	19. COMMUNICATION THEORY	PP	100	40	40	P C			
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	39	P C	20. COMMUNICATION THEORY	OR	50	20	23	P C			
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	37	P C			

GRAND TOTAL = 739/1500, RESULT: PASS CLASS

ORDN. 1 MARKS :

S8053156	PAWAR SACHIN VINAYAKRAO					SAVITA	, 70925557J				, PICT				, S8053156			
01. SIGNAL AND SYSTEMS	PP	100	40	AA	F	11. ENGINEERING MATHEMATICS III	PP	100	40	AA	F							
02. SIGNAL AND SYSTEMS	OR	50	20	22	P	12. ENGINEERING MATHEMATICS III	TW	25	10	15	P C							
03. SOLID STATES DEVICES AND CIRCUITS	SPP	100	40	AA	F	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	AA	F							
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	20	P	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	40	P C							
05. NETWORK ANALYSIS	PP	100	40	AA	F	15. ELECTROMAGNETIC	PP	100	40	AA	F							
06. DIGITAL LOGIC DESIGN	PP	100	40	AA	F	16. ELECTROMAGNETIC	TW	25	10	18	P C							
07. DIGITAL LOGIC DESIGN	PR	50	20	AA	F	17. DATA STRUCTURES	PP	100	40	AA	F							
08. POWER DEVICES AND MACHINES	PP	100	40	AA	F	18. DATA STRUCTURES	PR	50	20	34	P							
09. NETWORK AND POWER LAB.	TW	50	20	33	P C	19. COMMUNICATION THEORY	PP	100	40	AA	F							
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	20	P C	20. COMMUNICATION THEORY	OR	50	20	21	P C							
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	32	P C							

GRAND TOTAL = 255/1500, RESULT: FAILS

ORDN. 1 MARKS :

RESULT RESERVED FOR BKL

S8053158	PAWAR VISHAL ALIAS DIGVIJAY					VIJAYANTI	, 71070203F				, PICT				, S8053158			
01. SIGNAL AND SYSTEMS	PP	100	40	40	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	AA	F							
02. SIGNAL AND SYSTEMS	OR	50	20	20	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	14	P C							
03. SOLID STATES DEVICES AND CIRCUITS	SPP	100	40	50	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	59	P C							
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	20	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	20	P C							
05. NETWORK ANALYSIS	PP	100	40	AA	F	15. ELECTROMAGNETIC	PP	100	40	40	P C							
06. DIGITAL LOGIC DESIGN	PP	100	40	AA	F	16. ELECTROMAGNETIC	TW	25	10	12	P C							
07. DIGITAL LOGIC DESIGN	PR	50	20	20	P C	17. DATA STRUCTURES	PP	100	40	40	P C							
08. POWER DEVICES AND MACHINES	PP	100	40	56	P C	18. DATA STRUCTURES	PR	50	20	28	P C							
09. NETWORK AND POWER LAB.	TW	50	20	34	P C	19. COMMUNICATION THEORY	PP	100	40	53	P C							
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	30	P C	20. COMMUNICATION THEORY	OR	50	20	24	P C							
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	37	P C							

GRAND TOTAL = 597/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8053159	PITRODA UTSAV RAJENDRA				KAILASH				, 71129936G				PICT		S8053159
01. SIGNAL AND SYSTEMS	PP	100	40	40	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	22	F				
02. SIGNAL AND SYSTEMS	OR	50	20	21	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	18	P C				
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	49	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	51	P C				
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	28	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	30	P C				
05. NETWORK ANALYSIS	PP	100	40	40	P	15. ELECTROMAGNETIC	PP	100	40	40	P C				
06. DIGITAL LOGIC DESIGN	PP	100	40	40	P C	16. ELECTROMAGNETIC	TW	25	10	16	P C				
07. DIGITAL LOGIC DESIGN	PR	50	20	24	P C	17. DATA STRUCTURES	PP	100	40	43	P C				
08. POWER DEVICES AND MACHINES	PP	100	40	48	P C	18. DATA STRUCTURES	PR	50	20	45	P C				
09. NETWORK AND POWER LAB.	TW	50	20	41	P C	19. COMMUNICATION THEORY	PP	100	40	40	P C				
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	38	P C	20. COMMUNICATION THEORY	OR	50	20	20	P C				
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	42	P C				

GRAND TOTAL = 736/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

S8053162	SONAWANE NIKHIL BHIMRAO				SUNANDA				, 71045632J				PICT		S8053162
01. SIGNAL AND SYSTEMS	PP	100	40	20	F	11. ENGINEERING MATHEMATICS III	PP	100	40	16	F				
02. SIGNAL AND SYSTEMS	OR	50	20	24	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	10	P C				
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	13	F	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	14	F				
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	AA	F	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	AA	F				
05. NETWORK ANALYSIS	PP	100	40	09	F	15. ELECTROMAGNETIC	PP	100	40	07	F				
06. DIGITAL LOGIC DESIGN	PP	100	40	40	P C	16. ELECTROMAGNETIC	TW	25	10	10	P C				
07. DIGITAL LOGIC DESIGN	PR	50	20	30	P C	17. DATA STRUCTURES	PP	100	40	30	F				
08. POWER DEVICES AND MACHINES	PP	100	40	40	P C	18. DATA STRUCTURES	PR	50	20	AA	F				
09. NETWORK AND POWER LAB.	TW	50	20	27	P C	19. COMMUNICATION THEORY	PP	100	40	10	F				
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	30	P C	20. COMMUNICATION THEORY	OR	50	20	AA	F				
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	20	P C				

GRAND TOTAL = 350/1500, RESULT: FAILS

ORDN. 1 MARKS :

S8053163	SONU SINGH				USHA				, 71045633G				PICT		S8053163
01. SIGNAL AND SYSTEMS	PP	100	40	40	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	40	P C				
02. SIGNAL AND SYSTEMS	OR	50	20	32	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	17	P C				
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	40	P	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	43	P C				
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	20	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	34	P C				
05. NETWORK ANALYSIS	PP	100	40	40	P	15. ELECTROMAGNETIC	PP	100	40	40	P				
06. DIGITAL LOGIC DESIGN	PP	100	40	40	P C	16. ELECTROMAGNETIC	TW	25	10	16	P C				
07. DIGITAL LOGIC DESIGN	PR	50	20	29	P C	17. DATA STRUCTURES	PP	100	40	41	P C				
08. POWER DEVICES AND MACHINES	PP	100	40	30#	P	18. DATA STRUCTURES	PR	50	20	35	P C				
09. NETWORK AND POWER LAB.	TW	50	20	35	P C	19. COMMUNICATION THEORY	PP	100	40	40	P C				
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	33	P C	20. COMMUNICATION THEORY	OR	50	20	29	P C				
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	35	P C				

GRAND TOTAL = 709/1500, RESULT: PASS CLASS # [0.4]

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8053164	THOKALE PRIYANKA MADHAV				MANDAKINI		, 70925636B			PICT		S8053164
01. SIGNAL AND SYSTEMS	PP	100	40	53	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	40	P C	
02. SIGNAL AND SYSTEMS	OR	50	20	32	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	18	P C	
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	AA	F	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	AA	F	
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	29	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	AA	F	
05. NETWORK ANALYSIS	PP	100	40	AA	F	15. ELECTROMAGNETIC	PP	100	40	AA	F	
06. DIGITAL LOGIC DESIGN	PP	100	40	43	P C	16. ELECTROMAGNETIC	TW	25	10	13	P C	
07. DIGITAL LOGIC DESIGN	PR	50	20	20	P C	17. DATA STRUCTURES	PP	100	40	50	P C	
08. POWER DEVICES AND MACHINES	PP	100	40	AA	F	18. DATA STRUCTURES	PR	50	20	AA	F	
09. NETWORK AND POWER LAB.	TW	50	20	32	P C	19. COMMUNICATION THEORY	PP	100	40	AA	F	
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	32	P C	20. COMMUNICATION THEORY	OR	50	20	AA	F	
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	41	P C	

GRAND TOTAL = 403/1500, RESULT: FAILS

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8054201	AAMIR AHMED ABOOBAKAR SADIQUE				YASMEEN				, 71100710B				PICT		S8054201
01. DISCRETE STRUCTURES	PP	100	40	45	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	67	P				
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	53	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	46	P				
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	66	P C	13. DATA STRUCTURES	PP	100	40	63	P				
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	62	P C	14. COMPUTER GRAPHICS	PP	100	40	60	P				
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	48	P C	15. COMPUTER ORGANIZATION	PP	100	40	55	P				
06. PROGRAMMING LABORATORY	TW	25	10	19	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	30	P				
07. PROGRAMMING LABORATORY	PR	50	20	28	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	34	P				
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	18	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	37	P				
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	25	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	23	P				
10. SOFT SKILLS	TW	50	20	32	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	36	P				
						21. DATA STRUCTURES LABORATORY	PR	50	20	38	P				

GRAND TOTAL = 885/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

S8054202	ABHISHEK KUMAR SAURAV				SUMAN				, 71045357E				PICT		S8054202
01. DISCRETE STRUCTURES	PP	100	40	40	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	40	P				
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	32	F	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	16	F				
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	43	P	13. DATA STRUCTURES	PP	100	40	43	P				
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	41	P	14. COMPUTER GRAPHICS	PP	100	40	30	F				
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	41	P	15. COMPUTER ORGANIZATION	PP	100	40	30	F				
06. PROGRAMMING LABORATORY	TW	25	10	10	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	38	P				
07. PROGRAMMING LABORATORY	PR	50	20	06	F	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	14	F				
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	10	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	22	P				
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	05	F	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	AA	F				
10. SOFT SKILLS	TW	50	20	20	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	20	P				
						21. DATA STRUCTURES LABORATORY	PR	50	20	10	F				

GRAND TOTAL = 511/1500, RESULT: FAILS

RESULT RESERVED FOR BKLG

ORDN. 1 MARKS :

S8054203	AMAN TEWARY				SUNANDA				, 71100725L				PICT		S8054203
01. DISCRETE STRUCTURES	PP	100	40	29	F	11. ENGINEERING MATHEMATICS III	PP	100	40	14	F				
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	AA	F	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	AA	F				
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	AA	F	13. DATA STRUCTURES	PP	100	40	18	F				
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	AA	F	14. COMPUTER GRAPHICS	PP	100	40	18	F				
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	40	P C	15. COMPUTER ORGANIZATION	PP	100	40	AA	F				
06. PROGRAMMING LABORATORY	TW	25	10	17	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	30	P				
07. PROGRAMMING LABORATORY	PR	50	20	AA	F	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	25	P				
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	17	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	21	P				
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	34	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	21	P				
10. SOFT SKILLS	TW	50	20	33	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	22	P				
						21. DATA STRUCTURES LABORATORY	PR	50	20	AA	F				

GRAND TOTAL = 339/1500, RESULT: FAILS

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8054204	AMRUTKAR SNEHAL RAJENDRA			JYOTI		, 71100728E			PICT		S8054204
01. DISCRETE STRUCTURES	PP	100	40	51	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	62	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	62	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	46	P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	65	P C	13. DATA STRUCTURES	PP	100	40	71	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	74	P C	14. COMPUTER GRAPHICS	PP	100	40	64	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	58	P C	15. COMPUTER ORGANIZATION	PP	100	40	71	P
06. PROGRAMMING LABORATORY	TW	25	10	22	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	38	P
07. PROGRAMMING LABORATORY	PR	50	20	40	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	31	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	19	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	41	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	38	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	32	P
10. SOFT SKILLS	TW	50	20	40	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	41	P
						21. DATA STRUCTURES LABORATORY	PR	50	20	40	P

GRAND TOTAL = 1006/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

S8054205	AVHAD PRIYA SANTOSH			MADHURI		, 71100735H			PICT		S8054205
01. DISCRETE STRUCTURES	PP	100	40	75	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	92	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	61	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	57	P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	75	P C	13. DATA STRUCTURES	PP	100	40	72	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	72	P C	14. COMPUTER GRAPHICS	PP	100	40	67	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	60	P C	15. COMPUTER ORGANIZATION	PP	100	40	63	P
06. PROGRAMMING LABORATORY	TW	25	10	20	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	40	P
07. PROGRAMMING LABORATORY	PR	50	20	41	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	42	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	17	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	40	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	37	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	30	P
10. SOFT SKILLS	TW	50	20	32	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	36	P
						21. DATA STRUCTURES LABORATORY	PR	50	20	41	P

GRAND TOTAL = 1070/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

S8054206	BALI SONIYA NARAYAN			SHOBHA		, 71100740D			PICT		S8054206
01. DISCRETE STRUCTURES	PP	100	40	58	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	21	F
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	42	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	40	P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	57	P C	13. DATA STRUCTURES	PP	100	40	50	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	40	P C	14. COMPUTER GRAPHICS	PP	100	40	55	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	50	P C	15. COMPUTER ORGANIZATION	PP	100	40	58	P
06. PROGRAMMING LABORATORY	TW	25	10	18	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	33	P
07. PROGRAMMING LABORATORY	PR	50	20	35	P	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	32	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	18	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	35	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	25	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	25	P
10. SOFT SKILLS	TW	50	20	38	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	32	P
						21. DATA STRUCTURES LABORATORY	PR	50	20	32	P

GRAND TOTAL = 794/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8054207	BALIWANT SOURABH SANJAY				KALPANA			, 71100741B			PICT		S8054207
01. DISCRETE STRUCTURES	PP	100	40	40	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	40	P		
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	40	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	43	P		
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	44	P C	13. DATA STRUCTURES	PP	100	40	54	P		
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	63	P C	14. COMPUTER GRAPHICS	PP	100	40	56	P		
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	58	P C	15. COMPUTER ORGANIZATION	PP	100	40	48	P		
06. PROGRAMMING LABORATORY	TW	25	10	21	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	33	P		
07. PROGRAMMING LABORATORY	PR	50	20	33	P	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	32	P		
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	17	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	30	P		
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	30	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	06	F		
10. SOFT SKILLS	TW	50	20	32	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	30	P		
						21. DATA STRUCTURES LABORATORY	PR	50	20	27	P		

GRAND TOTAL = 777/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

S8054208	BAPAT RUTUGANDHA SUNIL				ANAGHA			, 71239286G			PICT		S8054208
01. DISCRETE STRUCTURES	PP	100	40	45	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	27	F		
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	54	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	46	P		
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	50	P C	13. DATA STRUCTURES	PP	100	40	71	P		
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	54	P C	14. COMPUTER GRAPHICS	PP	100	40	59	P		
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	61	P C	15. COMPUTER ORGANIZATION	PP	100	40	61	P		
06. PROGRAMMING LABORATORY	TW	25	10	19	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	38	P		
07. PROGRAMMING LABORATORY	PR	50	20	10	F	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	36	P		
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	22	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	44	P		
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	32	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	07	F		
10. SOFT SKILLS	TW	50	20	40	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	37	P		
						21. DATA STRUCTURES LABORATORY	PR	50	20	24	P		

GRAND TOTAL = 837/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

S8054209	BAPAT SWARNADEE SANDEEP				RIJUTA			, 71100742L			PICT		S8054209
01. DISCRETE STRUCTURES	PP	100	40	53	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	66	P		
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	58	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	57	P		
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	65	P C	13. DATA STRUCTURES	PP	100	40	75	P		
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	65	P C	14. COMPUTER GRAPHICS	PP	100	40	56	P		
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	60	P C	15. COMPUTER ORGANIZATION	PP	100	40	54	P		
06. PROGRAMMING LABORATORY	TW	25	10	23	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	41	P		
07. PROGRAMMING LABORATORY	PR	50	20	31	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	40	P		
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	20	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	42	P		
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	35	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	42	P		
10. SOFT SKILLS	TW	50	20	40	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	43	P		
						21. DATA STRUCTURES LABORATORY	PR	50	20	34	P		

GRAND TOTAL = 1000/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8054210	BARDE KAJAL NARAYAN				SULOCHANA		, 71239287E			PICT		S8054210
01. DISCRETE STRUCTURES	PP	100	40	43	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	43	P	
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	59	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	51	P	
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	70	P C	13. DATA STRUCTURES	PP	100	40	70	P	
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	69	P C	14. COMPUTER GRAPHICS	PP	100	40	50	P	
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	53	P C	15. COMPUTER ORGANIZATION	PP	100	40	64	P	
06. PROGRAMMING LABORATORY	TW	25	10	23	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	45	P	
07. PROGRAMMING LABORATORY	PR	50	20	44	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	44	P	
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	21	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	46	P	
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	42	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	39	P	
10. SOFT SKILLS	TW	50	20	43	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	45	P	
						21. DATA STRUCTURES LABORATORY	PR	50	20	44	P	

GRAND TOTAL = 1008/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

S8054211	BARDE KOMAL NARAYAN				SULOCHANA		, 71239288C			PICT		S8054211
01. DISCRETE STRUCTURES	PP	100	40	42	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	43	P	
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	71	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	51	P	
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	69	P C	13. DATA STRUCTURES	PP	100	40	70	P	
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	68	P C	14. COMPUTER GRAPHICS	PP	100	40	57	P	
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	57	P C	15. COMPUTER ORGANIZATION	PP	100	40	63	P	
06. PROGRAMMING LABORATORY	TW	25	10	23	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	45	P	
07. PROGRAMMING LABORATORY	PR	50	20	42	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	41	P	
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	20	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	44	P	
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	42	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	25	P	
10. SOFT SKILLS	TW	50	20	43	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	42	P	
						21. DATA STRUCTURES LABORATORY	PR	50	20	38	P	

GRAND TOTAL = 996/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

S8054212	BASAPURE SHRAVYA RAMESH				VIJAYA		, 71100743J			PICT		S8054212
01. DISCRETE STRUCTURES	PP	100	40	26	F	11. ENGINEERING MATHEMATICS III	PP	100	40	04	F	
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	15	F	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	03	F	
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	13	F	13. DATA STRUCTURES	PP	100	40	22	F	
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	18	F	14. COMPUTER GRAPHICS	PP	100	40	23	F	
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	23	F	15. COMPUTER ORGANIZATION	PP	100	40	14	F	
06. PROGRAMMING LABORATORY	TW	25	10	21	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	32	P	
07. PROGRAMMING LABORATORY	PR	50	20	17	F	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	10	F	
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	18	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	21	P	
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	25	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	04	F	
10. SOFT SKILLS	TW	50	20	36	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	26	P	
						21. DATA STRUCTURES LABORATORY	PR	50	20	05	F	

GRAND TOTAL = 376/1500, RESULT: FAILS

RESULT RESERVED FOR BKLG

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8054213	BAWALE DEVEN ABHAY				SANDHYA			, 71100745E			PICT		S8054213
01. DISCRETE STRUCTURES	PP	100	40	42	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	40	P		
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	47	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	40	P		
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	42	P C	13. DATA STRUCTURES	PP	100	40	63	P		
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	40	P C	14. COMPUTER GRAPHICS	PP	100	40	50	P		
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	46	P	15. COMPUTER ORGANIZATION	PP	100	40	42	P		
06. PROGRAMMING LABORATORY	TW	25	10	21	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	45	P		
07. PROGRAMMING LABORATORY	PR	50	20	36	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	40	P		
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	22	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	42	P		
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	38	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	29	P		
10. SOFT SKILLS	TW	50	20	42	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	42	P		
						21. DATA STRUCTURES LABORATORY	PR	50	20	32	P		

GRAND TOTAL = 841/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

S8054214	BELOTE AMOL PRABHAKAR				GANGUBAI			, 71100746C			PICT		S8054214
01. DISCRETE STRUCTURES	PP	100	40	57	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	40	P		
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	44	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	49	P		
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	49	P C	13. DATA STRUCTURES	PP	100	40	62	P		
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	59	P C	14. COMPUTER GRAPHICS	PP	100	40	57	P		
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	51	P C	15. COMPUTER ORGANIZATION	PP	100	40	67	P		
06. PROGRAMMING LABORATORY	TW	25	10	17	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	35	P		
07. PROGRAMMING LABORATORY	PR	50	20	28	P	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	38	P		
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	18	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	32	P		
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	21	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	39	P		
10. SOFT SKILLS	TW	50	20	34	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	31	P		
						21. DATA STRUCTURES LABORATORY	PR	50	20	28	P		

GRAND TOTAL = 856/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

S8054215	BHADKE ASHUTOSH CHANDRAKANT				SUNANDA			, 71239289M			PICT		S8054215
01. DISCRETE STRUCTURES	PP	100	40	60	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	52	P		
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	60	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	60	P		
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	72	P C	13. DATA STRUCTURES	PP	100	40	74	P		
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	74	P C	14. COMPUTER GRAPHICS	PP	100	40	53	P		
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	54	P C	15. COMPUTER ORGANIZATION	PP	100	40	71	P		
06. PROGRAMMING LABORATORY	TW	25	10	21	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	37	P		
07. PROGRAMMING LABORATORY	PR	50	20	43	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	42	P		
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	19	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	45	P		
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	35	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	42	P		
10. SOFT SKILLS	TW	50	20	34	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	39	P		
						21. DATA STRUCTURES LABORATORY	PR	50	20	30	P		

GRAND TOTAL = 1017/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8054216	BHANDARI RUHI BABU				SMITA		, 71239290E			PICT		S8054216
01. DISCRETE STRUCTURES	PP	100	40	41	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	25	F	
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	57	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	49	P	
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	40	P C	13. DATA STRUCTURES	PP	100	40	72	P	
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	49	P C	14. COMPUTER GRAPHICS	PP	100	40	57	P	
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	58	P C	15. COMPUTER ORGANIZATION	PP	100	40	54	P	
06. PROGRAMMING LABORATORY	TW	25	10	17	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	39	P	
07. PROGRAMMING LABORATORY	PR	50	20	20	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	42	P	
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	18	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	41	P	
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	33	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	10	F	
10. SOFT SKILLS	TW	50	20	31	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	33	P	
						21. DATA STRUCTURES LABORATORY	PR	50	20	30	P	

GRAND TOTAL = 816/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

S8054217	BHANDARI YASHASHRI VASANT				RANJANA		, 71239291C			PICT		S8054217
01. DISCRETE STRUCTURES	PP	100	40	43	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	40	P	
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	47	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	55	P	
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	48	P C	13. DATA STRUCTURES	PP	100	40	75	P	
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	56	P C	14. COMPUTER GRAPHICS	PP	100	40	59	P	
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	44	P C	15. COMPUTER ORGANIZATION	PP	100	40	65	P	
06. PROGRAMMING LABORATORY	TW	25	10	19	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	36	P	
07. PROGRAMMING LABORATORY	PR	50	20	20	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	43	P	
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	21	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	43	P	
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	35	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	23	P	
10. SOFT SKILLS	TW	50	20	37	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	37	P	
						21. DATA STRUCTURES LABORATORY	PR	50	20	22	P	

GRAND TOTAL = 868/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

S8054218	BHOIR RUPALI LAXMAN				VANITA		, 71045390G			PICT		S8054218
01. DISCRETE STRUCTURES	PP	100	40	42	P	11. ENGINEERING MATHEMATICS III	PP	100	40	40	P	
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	40	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	26	F	
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	24	F	13. DATA STRUCTURES	PP	100	40	49	P	
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	31	F	14. COMPUTER GRAPHICS	PP	100	40	42	P	
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	40	P C	15. COMPUTER ORGANIZATION	PP	100	40	23	F	
06. PROGRAMMING LABORATORY	TW	25	10	19	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	43	P	
07. PROGRAMMING LABORATORY	PR	50	20	24	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	23	P	
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	18	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	39	P	
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	27	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	21	P	
10. SOFT SKILLS	TW	50	20	34	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	38	P	
						21. DATA STRUCTURES LABORATORY	PR	50	20	10	F	

GRAND TOTAL = 653/1500, RESULT: FAILS

RESULT RESERVED FOR BKLG

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8054219	BHURKE SHWETA MILIND				MADHURA			, 71100751K			PICT		S8054219
01. DISCRETE STRUCTURES	PP	100	40	66	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	78	P		
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	52	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	44	P		
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	60	P C	13. DATA STRUCTURES	PP	100	40	69	P		
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	65	P C	14. COMPUTER GRAPHICS	PP	100	40	55	P		
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	60	P C	15. COMPUTER ORGANIZATION	PP	100	40	59	P		
06. PROGRAMMING LABORATORY	TW	25	10	22	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	44	P		
07. PROGRAMMING LABORATORY	PR	50	20	21	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	40	P		
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	22	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	45	P		
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	38	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	43	P		
10. SOFT SKILLS	TW	50	20	43	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	43	P		
						21. DATA STRUCTURES LABORATORY	PR	50	20	30	P		

GRAND TOTAL = 999/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

S8054221	CHAUDHAR VISHAL SHESHRAO				SAVITA			, 71100762E			PICT		S8054221
01. DISCRETE STRUCTURES	PP	100	40	45	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	40	P		
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	41	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	42	P		
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	47	P C	13. DATA STRUCTURES	PP	100	40	63	P		
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	48	P C	14. COMPUTER GRAPHICS	PP	100	40	52	P		
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	47	P C	15. COMPUTER ORGANIZATION	PP	100	40	54	P		
06. PROGRAMMING LABORATORY	TW	25	10	17	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	29	P		
07. PROGRAMMING LABORATORY	PR	50	20	21	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	16	F		
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	15	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	30	P		
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	31	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	11	F		
10. SOFT SKILLS	TW	50	20	29	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	30	P		
						21. DATA STRUCTURES LABORATORY	PR	50	20	28	P		

GRAND TOTAL = 736/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

S8054222	CHAUDHARI POOJA DHANRAJ				DNYNESHWARI			, 71100763C			PICT		S8054222
01. DISCRETE STRUCTURES	PP	100	40	40	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	85	P		
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	52	P	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	53	P		
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	62	P C	13. DATA STRUCTURES	PP	100	40	71	P		
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	75	P C	14. COMPUTER GRAPHICS	PP	100	40	63	P		
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	59	P C	15. COMPUTER ORGANIZATION	PP	100	40	65	P		
06. PROGRAMMING LABORATORY	TW	25	10	19	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	42	P		
07. PROGRAMMING LABORATORY	PR	50	20	21	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	30	P		
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	21	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	39	P		
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	33	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	27	P		
10. SOFT SKILLS	TW	50	20	38	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	40	P		
						21. DATA STRUCTURES LABORATORY	PR	50	20	35	P		

GRAND TOTAL = 970/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8054223	CHAVAN KARISHMA SANJAY				SHARADA			, 71239292M			PICT		S8054223
01. DISCRETE STRUCTURES	PP	100	40	40	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	50	P		
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	63	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	57	P		
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	60	P C	13. DATA STRUCTURES	PP	100	40	74	P		
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	67	P C	14. COMPUTER GRAPHICS	PP	100	40	47	P		
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	60	P C	15. COMPUTER ORGANIZATION	PP	100	40	65	P		
06. PROGRAMMING LABORATORY	TW	25	10	21	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	39	P		
07. PROGRAMMING LABORATORY	PR	50	20	38	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	38	P		
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	19	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	43	P		
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	35	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	35	P		
10. SOFT SKILLS	TW	50	20	40	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	42	P		
						21. DATA STRUCTURES LABORATORY	PR	50	20	40	P		

GRAND TOTAL = 973/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

S8054224	CHIYA IFFA IMTIAZ				FAMIDA			, 71239293K			PICT		S8054224
01. DISCRETE STRUCTURES	PP	100	40	40	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	65	P		
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	42	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	54	P		
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	60	P C	13. DATA STRUCTURES	PP	100	40	74	P		
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	77	P C	14. COMPUTER GRAPHICS	PP	100	40	49	P		
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	58	P C	15. COMPUTER ORGANIZATION	PP	100	40	52	P		
06. PROGRAMMING LABORATORY	TW	25	10	22	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	41	P		
07. PROGRAMMING LABORATORY	PR	50	20	43	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	43	P		
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	18	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	44	P		
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	41	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	30	P		
10. SOFT SKILLS	TW	50	20	35	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	45	P		
						21. DATA STRUCTURES LABORATORY	PR	50	20	41	P		

GRAND TOTAL = 974/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

S8054225	DAMLE RUCHA JAYANT				VANDANA			, 71100771D			PICT		S8054225
01. DISCRETE STRUCTURES	PP	100	40	64	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	79	P		
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	60	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	55	P		
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	66	P C	13. DATA STRUCTURES	PP	100	40	71	P		
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	74	P C	14. COMPUTER GRAPHICS	PP	100	40	58	P		
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	56	P C	15. COMPUTER ORGANIZATION	PP	100	40	73	P		
06. PROGRAMMING LABORATORY	TW	25	10	22	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	38	P		
07. PROGRAMMING LABORATORY	PR	50	20	38	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	40	P		
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	21	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	37	P		
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	42	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	43	P		
10. SOFT SKILLS	TW	50	20	38	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	37	P		
						21. DATA STRUCTURES LABORATORY	PR	50	20	37	P		

GRAND TOTAL = 1049/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8054226	DAMLE VISHAKHA RAVINDRA				VIJAYANTI		, 71100772B			PICT		S8054226
01. DISCRETE STRUCTURES	PP	100	40	74	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	95	P	
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	64	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	60	P	
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	76	P C	13. DATA STRUCTURES	PP	100	40	75	P	
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	69	P C	14. COMPUTER GRAPHICS	PP	100	40	61	P	
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	59	P C	15. COMPUTER ORGANIZATION	PP	100	40	72	P	
06. PROGRAMMING LABORATORY	TW	25	10	20	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	41	P	
07. PROGRAMMING LABORATORY	PR	50	20	45	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	45	P	
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	18	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	39	P	
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	38	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	40	P	
10. SOFT SKILLS	TW	50	20	37	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	36	P	
						21. DATA STRUCTURES LABORATORY	PR	50	20	40	P	

GRAND TOTAL = 1104/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

S8054227	DAMLE VISHWAS MILIND				MUGDHA		, 71100773L			PICT		S8054227
01. DISCRETE STRUCTURES	PP	100	40	62	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	63	P	
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	59	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	53	P	
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	81	P C	13. DATA STRUCTURES	PP	100	40	69	P	
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	83	P C	14. COMPUTER GRAPHICS	PP	100	40	58	P	
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	56	P C	15. COMPUTER ORGANIZATION	PP	100	40	69	P	
06. PROGRAMMING LABORATORY	TW	25	10	20	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	45	P	
07. PROGRAMMING LABORATORY	PR	50	20	44	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	45	P	
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	22	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	43	P	
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	46	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	44	P	
10. SOFT SKILLS	TW	50	20	40	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	44	P	
						21. DATA STRUCTURES LABORATORY	PR	50	20	43	P	

GRAND TOTAL = 1089/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

S8054228	DAMSE SAYALI LAXMAN				LATA		, 71100774J			PICT		S8054228
01. DISCRETE STRUCTURES	PP	100	40	40	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	16	F	
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	40	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	40	P	
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	40	P	13. DATA STRUCTURES	PP	100	40	58	P	
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	40	P C	14. COMPUTER GRAPHICS	PP	100	40	41	P	
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	40	P C	15. COMPUTER ORGANIZATION	PP	100	40	40	P	
06. PROGRAMMING LABORATORY	TW	25	10	18	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	34	P	
07. PROGRAMMING LABORATORY	PR	50	20	05	F	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	10	F	
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	17	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	36	P	
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	21	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	27	P	
10. SOFT SKILLS	TW	50	20	34	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	34	P	
						21. DATA STRUCTURES LABORATORY	PR	50	20	10	F	

GRAND TOTAL = 641/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8054229	DEBOJEET CHATTERJEE			SHIKHA		, 71100775G			PICT		S8054229
01. DISCRETE STRUCTURES	PP	100	40	86	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	71	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	56	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	40	P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	66	P C	13. DATA STRUCTURES	PP	100	40	65	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	71	P C	14. COMPUTER GRAPHICS	PP	100	40	52	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	58	P C	15. COMPUTER ORGANIZATION	PP	100	40	67	P
06. PROGRAMMING LABORATORY	TW	25	10	22	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	40	P
07. PROGRAMMING LABORATORY	PR	50	20	43	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	37	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	20	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	43	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	38	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	40	P
10. SOFT SKILLS	TW	50	20	38	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	40	P
						21. DATA STRUCTURES LABORATORY	PR	50	20	44	P

GRAND TOTAL = 1037/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

S8054230	DEEPANSHU JOSHI			LALITA		, 71100776E			PICT		S8054230
01. DISCRETE STRUCTURES	PP	100	40	40	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	40	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	41	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	20	F
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	40	P C	13. DATA STRUCTURES	PP	100	40	49	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	44	P	14. COMPUTER GRAPHICS	PP	100	40	40	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	50	P C	15. COMPUTER ORGANIZATION	PP	100	40	40	P
06. PROGRAMMING LABORATORY	TW	25	10	20	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	30	P
07. PROGRAMMING LABORATORY	PR	50	20	22	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	30	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	18	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	28	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	34	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	04	F
10. SOFT SKILLS	TW	50	20	30	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	23	P
						21. DATA STRUCTURES LABORATORY	PR	50	20	08	F

GRAND TOTAL = 651/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

S8054231	DESALE VRUSHALI SANJEEV			MEENAKSHI		, 71239294H			PICT		S8054231
01. DISCRETE STRUCTURES	PP	100	40	53	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	49	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	65	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	62	P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	67	P C	13. DATA STRUCTURES	PP	100	40	82	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	68	P C	14. COMPUTER GRAPHICS	PP	100	40	71	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	68	P C	15. COMPUTER ORGANIZATION	PP	100	40	72	P
06. PROGRAMMING LABORATORY	TW	25	10	22	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	39	P
07. PROGRAMMING LABORATORY	PR	50	20	40	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	39	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	19	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	41	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	37	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	24	P
10. SOFT SKILLS	TW	50	20	32	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	46	P
						21. DATA STRUCTURES LABORATORY	PR	50	20	35	P

GRAND TOTAL = 1031/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8054232	DESHMUKH AKSHAY ANIL			JYOTI		, 71100778M			PICT		S8054232
01. DISCRETE STRUCTURES	PP	100	40	46	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	70	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	49	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	45	P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	44	P C	13. DATA STRUCTURES	PP	100	40	68	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	61	P C	14. COMPUTER GRAPHICS	PP	100	40	56	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	41	P C	15. COMPUTER ORGANIZATION	PP	100	40	61	P
06. PROGRAMMING LABORATORY	TW	25	10	18	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	33	P
07. PROGRAMMING LABORATORY	PR	50	20	42	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	25	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	17	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	35	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	36	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	40	P
10. SOFT SKILLS	TW	50	20	32	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	32	P
						21. DATA STRUCTURES LABORATORY	PR	50	20	34	P

GRAND TOTAL = 885/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

S8054233	DESHPANDE ANUJ ABHAY			KALPANA		, 71100782K			PICT		S8054233
01. DISCRETE STRUCTURES	PP	100	40	53	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	70	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	53	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	40	P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	52	P C	13. DATA STRUCTURES	PP	100	40	55	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	44	P C	14. COMPUTER GRAPHICS	PP	100	40	49	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	53	P C	15. COMPUTER ORGANIZATION	PP	100	40	58	P
06. PROGRAMMING LABORATORY	TW	25	10	24	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	35	P
07. PROGRAMMING LABORATORY	PR	50	20	38	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	41	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	23	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	23	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	24	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	40	P
10. SOFT SKILLS	TW	50	20	35	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	30	P
						21. DATA STRUCTURES LABORATORY	PR	50	20	35	P

GRAND TOTAL = 875+05/1500, RESULT: HIGHER SECOND CLASS[0.163]

ORDN. 1 MARKS :

S8054234	DHAPTE OMKAR HANUMANT			MINAXI		, 71239295F			PICT		S8054234
01. DISCRETE STRUCTURES	PP	100	40	40	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	62	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	52	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	57	P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	46	P C	13. DATA STRUCTURES	PP	100	40	76	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	63	P C	14. COMPUTER GRAPHICS	PP	100	40	56	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	51	P C	15. COMPUTER ORGANIZATION	PP	100	40	64	P
06. PROGRAMMING LABORATORY	TW	25	10	19	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	34	P
07. PROGRAMMING LABORATORY	PR	50	20	34	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	34	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	14	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	25	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	26	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	36	P
10. SOFT SKILLS	TW	50	20	24	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	32	P
						21. DATA STRUCTURES LABORATORY	PR	50	20	39	P

GRAND TOTAL = 884/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8054235	DIPESH SINGH REEN			KOMAL KAUR		, 71100787L			PICT		S8054235
01. DISCRETE STRUCTURES	PP	100	40	64	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	40	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	57	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	40	P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	49	P C	13. DATA STRUCTURES	PP	100	40	65	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	58	P C	14. COMPUTER GRAPHICS	PP	100	40	53	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	54	P C	15. COMPUTER ORGANIZATION	PP	100	40	47	P
06. PROGRAMMING LABORATORY	TW	25	10	20	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	36	P
07. PROGRAMMING LABORATORY	PR	50	20	21	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	37	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	21	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	43	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	30	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	39	P
10. SOFT SKILLS	TW	50	20	39	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	39	P
						21. DATA STRUCTURES LABORATORY	PR	50	20	30	P

GRAND TOTAL = 882/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

S8054236	DIXIT PRATIKSHA SUBHASHCHANDRA			POONAM		, 71100788J			PICT		S8054236
01. DISCRETE STRUCTURES	PP	100	40	61	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	48	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	62	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	54	P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	60	P C	13. DATA STRUCTURES	PP	100	40	73	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	63	P C	14. COMPUTER GRAPHICS	PP	100	40	63	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	59	P C	15. COMPUTER ORGANIZATION	PP	100	40	62	P
06. PROGRAMMING LABORATORY	TW	25	10	20	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	41	P
07. PROGRAMMING LABORATORY	PR	50	20	25	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	38	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	18	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	43	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	39	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	39	P
10. SOFT SKILLS	TW	50	20	38	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	42	P
						21. DATA STRUCTURES LABORATORY	PR	50	20	24	P

GRAND TOTAL = 972/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

S8054237	DOIPHODE NISHIGANDHA SANJAY			MADHUBALA		, 71100789G			PICT		S8054237
01. DISCRETE STRUCTURES	PP	100	40	40	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	25	F
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	48	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	27	F
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	40	P C	13. DATA STRUCTURES	PP	100	40	49	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	40	P C	14. COMPUTER GRAPHICS	PP	100	40	41	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	51	P C	15. COMPUTER ORGANIZATION	PP	100	40	40	P
06. PROGRAMMING LABORATORY	TW	25	10	18	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	31	P
07. PROGRAMMING LABORATORY	PR	50	20	22	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	20	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	16	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	34	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	27	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	07	F
10. SOFT SKILLS	TW	50	20	37	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	32	P
						21. DATA STRUCTURES LABORATORY	PR	50	20	12	F

GRAND TOTAL = 657/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8054238	DOSHI ARCHIT RAVINDRA			ARCHANA		, 71100790L			PICT		S8054238
01. DISCRETE STRUCTURES	PP	100	40	40	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	52	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	48	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	40	P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	41	P C	13. DATA STRUCTURES	PP	100	40	49	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	53	P C	14. COMPUTER GRAPHICS	PP	100	40	40	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	54	P C	15. COMPUTER ORGANIZATION	PP	100	40	43	P
06. PROGRAMMING LABORATORY	TW	25	10	17	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	34	P
07. PROGRAMMING LABORATORY	PR	50	20	20	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	28	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	20	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	33	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	31	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	27	P
10. SOFT SKILLS	TW	50	20	40	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	30	P
						21. DATA STRUCTURES LABORATORY	PR	50	20	30	P

GRAND TOTAL = 770/1500, RESULT: SECOND CLASS

ORDN. 1 MARKS :

S8054239	EKHANDE ATUL ANIL			TRISHALA		, 71100791J			PICT		S8054239
01. DISCRETE STRUCTURES	PP	100	40	51	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	59	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	53	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	40	P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	42	P C	13. DATA STRUCTURES	PP	100	40	57	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	51	P C	14. COMPUTER GRAPHICS	PP	100	40	53	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	53	P C	15. COMPUTER ORGANIZATION	PP	100	40	44	P
06. PROGRAMMING LABORATORY	TW	25	10	17	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	29	P
07. PROGRAMMING LABORATORY	PR	50	20	30	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	26	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	18	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	32	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	35	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	36	P
10. SOFT SKILLS	TW	50	20	35	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	32	P
						21. DATA STRUCTURES LABORATORY	PR	50	20	31	P

GRAND TOTAL = 824+01/1500, RESULT: HIGHER SECOND CLASS [0.2]

ORDN. 1 MARKS :

S8054240	GABHALE VARSHA LIMBAJI			JANABAI		, 71045426M			PICT		S8054240
01. DISCRETE STRUCTURES	PP	100	40	52	P	11. ENGINEERING MATHEMATICS III	PP	100	40	40	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	44	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	40	P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	40	P C	13. DATA STRUCTURES	PP	100	40	53	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	48	P C	14. COMPUTER GRAPHICS	PP	100	40	27	F
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	40	P	15. COMPUTER ORGANIZATION	PP	100	40	23	F
06. PROGRAMMING LABORATORY	TW	25	10	21	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	39	P
07. PROGRAMMING LABORATORY	PR	50	20	30	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	34	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	20	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	39	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	27	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	07	F
10. SOFT SKILLS	TW	50	20	35	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	36	P
						21. DATA STRUCTURES LABORATORY	PR	50	20	10	F

GRAND TOTAL = 705/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8054241	GALGALI VAISHNAVI VITHAL			VAISHALI		, 71100795M			PICT		S8054241
01. DISCRETE STRUCTURES	PP	100	40	53	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	62	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	56	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	57	P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	68	P C	13. DATA STRUCTURES	PP	100	40	69	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	66	P C	14. COMPUTER GRAPHICS	PP	100	40	53	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	47	P C	15. COMPUTER ORGANIZATION	PP	100	40	47	P
06. PROGRAMMING LABORATORY	TW	25	10	20	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	30	P
07. PROGRAMMING LABORATORY	PR	50	20	21	P	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	38	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	17	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	36	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	38	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	05	F
10. SOFT SKILLS	TW	50	20	33	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	39	P
						21. DATA STRUCTURES LABORATORY	PR	50	20	31	P

GRAND TOTAL = 886/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

S8054242	GANVIR AJAY SUDHAKAR			SUREKHA		, 71100797H			PICT		S8054242
01. DISCRETE STRUCTURES	PP	100	40	58	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	41	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	50	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	40	P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	66	P C	13. DATA STRUCTURES	PP	100	40	71	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	63	P C	14. COMPUTER GRAPHICS	PP	100	40	40	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	50	P C	15. COMPUTER ORGANIZATION	PP	100	40	41	P
06. PROGRAMMING LABORATORY	TW	25	10	18	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	26	P
07. PROGRAMMING LABORATORY	PR	50	20	07	F	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	28	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	14	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	28	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	23	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	06	F
10. SOFT SKILLS	TW	50	20	32	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	20	P
						21. DATA STRUCTURES LABORATORY	PR	50	20	20	P

GRAND TOTAL = 742/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

S8054243	GAVALI TANUJA KASHINATH			PARVATA		, 71100799D			PICT		S8054243
01. DISCRETE STRUCTURES	PP	100	40	46	P	11. ENGINEERING MATHEMATICS III	PP	100	40	40	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	41	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	40	P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	44	P C	13. DATA STRUCTURES	PP	100	40	61	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	40	P C	14. COMPUTER GRAPHICS	PP	100	40	40	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	46	P C	15. COMPUTER ORGANIZATION	PP	100	40	32	F
06. PROGRAMMING LABORATORY	TW	25	10	21	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	35	P
07. PROGRAMMING LABORATORY	PR	50	20	07	F	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	06	F
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	17	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	31	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	21	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	21	P
10. SOFT SKILLS	TW	50	20	31	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	37	P
						21. DATA STRUCTURES LABORATORY	PR	50	20	05	F

GRAND TOTAL = 662/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8054244	GEET KAUR GIRVAR SINGH SUKHMANI		PAVANJEET		, 71100801K			PICT		S8054244
01. DISCRETE STRUCTURES	PP	100	40	54	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	40 P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	56	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	40 P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	53	P C	13. DATA STRUCTURES	PP	100	40	68 P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	57	P C	14. COMPUTER GRAPHICS	PP	100	40	50 P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	59	P C	15. COMPUTER ORGANIZATION	PP	100	40	41 P
06. PROGRAMMING LABORATORY	TW	25	10	21	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	34 P
07. PROGRAMMING LABORATORY	PR	50	20	23	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	22 P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	19	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	40 P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	33	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	32 P
10. SOFT SKILLS	TW	50	20	37	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	30 P
						21. DATA STRUCTURES LABORATORY	PR	50	20	30 P

GRAND TOTAL = 839/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

S8054245	GHULE RUTUJA POPAT		SANGEETA		, 71100803F			PICT		S8054245
01. DISCRETE STRUCTURES	PP	100	40	68	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	59 P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	47	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	40 P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	67	P C	13. DATA STRUCTURES	PP	100	40	60 P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	56	P C	14. COMPUTER GRAPHICS	PP	100	40	41 P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	41	P C	15. COMPUTER ORGANIZATION	PP	100	40	47 P
06. PROGRAMMING LABORATORY	TW	25	10	22	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	41 P
07. PROGRAMMING LABORATORY	PR	50	20	38	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	42 P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	23	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	37 P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	43	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	29 P
10. SOFT SKILLS	TW	50	20	30	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	32 P
						21. DATA STRUCTURES LABORATORY	PR	50	20	35 P

GRAND TOTAL = 898+02/1500, RESULT: FIRST CLASS [0.2]

ORDN. 1 MARKS :

S8054246	GINA RIGZIN CHETEN		ARCHANA		, 71100804D			PICT		S8054246
01. DISCRETE STRUCTURES	PP	100	40	58	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	93 P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	56	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	51 P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	76	P C	13. DATA STRUCTURES	PP	100	40	64 P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	60	P C	14. COMPUTER GRAPHICS	PP	100	40	48 P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	56	P C	15. COMPUTER ORGANIZATION	PP	100	40	61 P
06. PROGRAMMING LABORATORY	TW	25	10	24	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	40 P
07. PROGRAMMING LABORATORY	PR	50	20	43	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	46 P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	22	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	44 P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	42	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	42 P
10. SOFT SKILLS	TW	50	20	43	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	43 P
						21. DATA STRUCTURES LABORATORY	PR	50	20	41 P

GRAND TOTAL = 1053/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8054247	GOLE MADHURI JAYANT				SANJIVANI		, 71100809E			PICT		S8054247
01. DISCRETE STRUCTURES	PP	100	40	61	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	72	P	
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	55	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	40	P	
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	46	P C	13. DATA STRUCTURES	PP	100	40	66	P	
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	59	P C	14. COMPUTER GRAPHICS	PP	100	40	61	P	
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	51	P C	15. COMPUTER ORGANIZATION	PP	100	40	52	P	
06. PROGRAMMING LABORATORY	TW	25	10	23	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	35	P	
07. PROGRAMMING LABORATORY	PR	50	20	44	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	43	P	
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	19	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	34	P	
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	40	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	43	P	
10. SOFT SKILLS	TW	50	20	22	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	36	P	
						21. DATA STRUCTURES LABORATORY	PR	50	20	42	P	

GRAND TOTAL = 944/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

S8054248	GUJAR MANDAR SHRINIVAS				LEELA		, 71239296D			PICT		S8054248
01. DISCRETE STRUCTURES	PP	100	40	40	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	40	P	
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	58	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	41	P	
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	45	P C	13. DATA STRUCTURES	PP	100	40	73	P	
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	63	P C	14. COMPUTER GRAPHICS	PP	100	40	46	P	
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	62	P C	15. COMPUTER ORGANIZATION	PP	100	40	59	P	
06. PROGRAMMING LABORATORY	TW	25	10	22	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	39	P	
07. PROGRAMMING LABORATORY	PR	50	20	44	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	39	P	
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	20	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	43	P	
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	36	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	41	P	
10. SOFT SKILLS	TW	50	20	36	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	43	P	
						21. DATA STRUCTURES LABORATORY	PR	50	20	36	P	

GRAND TOTAL = 926/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

S8054250	GUPTA TARUN SHITALPRASAD				USHA		, 71100813C			PICT		S8054250
01. DISCRETE STRUCTURES	PP	100	40	63	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	54	P	
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	60	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	53	P	
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	64	P C	13. DATA STRUCTURES	PP	100	40	70	P	
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	76	P C	14. COMPUTER GRAPHICS	PP	100	40	55	P	
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	53	P C	15. COMPUTER ORGANIZATION	PP	100	40	42	P	
06. PROGRAMMING LABORATORY	TW	25	10	19	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	39	P	
07. PROGRAMMING LABORATORY	PR	50	20	35	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	44	P	
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	18	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	36	P	
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	35	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	24	P	
10. SOFT SKILLS	TW	50	20	26	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	36	P	
						21. DATA STRUCTURES LABORATORY	PR	50	20	43	P	

GRAND TOTAL = 945/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8054251	GURJAR ADITYA SHRIKANT				BHARATI		, 71100814M			PICT		S8054251
01. DISCRETE STRUCTURES	PP	100	40	69	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	85	P	
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	59	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	47	P	
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	60	P C	13. DATA STRUCTURES	PP	100	40	72	P	
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	63	P C	14. COMPUTER GRAPHICS	PP	100	40	49	P	
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	55	P C	15. COMPUTER ORGANIZATION	PP	100	40	55	P	
06. PROGRAMMING LABORATORY	TW	25	10	21	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	30	P	
07. PROGRAMMING LABORATORY	PR	50	20	37	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	38	P	
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	17	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	39	P	
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	37	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	27	P	
10. SOFT SKILLS	TW	50	20	37	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	36	P	
						21. DATA STRUCTURES LABORATORY	PR	50	20	39	P	

GRAND TOTAL = 972/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

S8054252	HARSHITA GANGRADE				NEETA		, 71100818D			PICT		S8054252
01. DISCRETE STRUCTURES	PP	100	40	78	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	78	P	
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	71	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	58	P	
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	76	P C	13. DATA STRUCTURES	PP	100	40	74	P	
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	76	P C	14. COMPUTER GRAPHICS	PP	100	40	60	P	
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	59	P C	15. COMPUTER ORGANIZATION	PP	100	40	62	P	
06. PROGRAMMING LABORATORY	TW	25	10	23	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	38	P	
07. PROGRAMMING LABORATORY	PR	50	20	46	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	40	P	
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	19	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	27	P	
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	40	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	46	P	
10. SOFT SKILLS	TW	50	20	25	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	35	P	
						21. DATA STRUCTURES LABORATORY	PR	50	20	37	P	

GRAND TOTAL = 1068/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

S8054253	IBUSE AMANIYA MUZIR				YASMEEN		, 71239297B			PICT		S8054253
01. DISCRETE STRUCTURES	PP	100	40	43	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	40	P	
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	61	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	47	P	
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	58	P C	13. DATA STRUCTURES	PP	100	40	72	P	
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	57	P C	14. COMPUTER GRAPHICS	PP	100	40	61	P	
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	58	P C	15. COMPUTER ORGANIZATION	PP	100	40	51	P	
06. PROGRAMMING LABORATORY	TW	25	10	22	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	38	P	
07. PROGRAMMING LABORATORY	PR	50	20	21	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	39	P	
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	18	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	41	P	
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	25	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	30	P	
10. SOFT SKILLS	TW	50	20	34	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	42	P	
						21. DATA STRUCTURES LABORATORY	PR	50	20	34	P	

GRAND TOTAL = 892+08/1500, RESULT: FIRST CLASS [0.2]

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8054254	INGOLE SHAILESH GANESH			ARTI		, 71100822B			PICT		S8054254
01. DISCRETE STRUCTURES	PP	100	40	58	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	75	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	48	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	46	P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	66	P C	13. DATA STRUCTURES	PP	100	40	72	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	74	P C	14. COMPUTER GRAPHICS	PP	100	40	61	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	40	P C	15. COMPUTER ORGANIZATION	PP	100	40	52	P
06. PROGRAMMING LABORATORY	TW	25	10	23	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	39	P
07. PROGRAMMING LABORATORY	PR	50	20	42	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	36	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	19	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	35	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	37	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	22	P
10. SOFT SKILLS	TW	50	20	36	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	42	P
						21. DATA STRUCTURES LABORATORY	PR	50	20	40	P

GRAND TOTAL = 963/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

S8054255	JADHAV AMOL JETA			LATA		, 71045453J			PICT		S8054255
01. DISCRETE STRUCTURES	PP	100	40	40	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	43	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	40	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	40	P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	43	P C	13. DATA STRUCTURES	PP	100	40	57	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	55	P C	14. COMPUTER GRAPHICS	PP	100	40	49	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	40	P C	15. COMPUTER ORGANIZATION	PP	100	40	27	F
06. PROGRAMMING LABORATORY	TW	25	10	20	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	24	P
07. PROGRAMMING LABORATORY	PR	50	20	40	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	20	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	16	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	28	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	32	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	07	F
10. SOFT SKILLS	TW	50	20	26	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	29	P
						21. DATA STRUCTURES LABORATORY	PR	50	20	AA	F

GRAND TOTAL = 676/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

S8054256	JADHAV APURWA MANIK			SANDHYA		, 71100823L			PICT		S8054256
01. DISCRETE STRUCTURES	PP	100	40	61	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	72	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	53	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	46	P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	65	P C	13. DATA STRUCTURES	PP	100	40	66	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	64	P C	14. COMPUTER GRAPHICS	PP	100	40	60	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	59	P C	15. COMPUTER ORGANIZATION	PP	100	40	46	P
06. PROGRAMMING LABORATORY	TW	25	10	23	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	40	P
07. PROGRAMMING LABORATORY	PR	50	20	45	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	37	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	18	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	34	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	39	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	37	P
10. SOFT SKILLS	TW	50	20	25	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	35	P
						21. DATA STRUCTURES LABORATORY	PR	50	20	35	P

GRAND TOTAL = 960/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8054257 JADHAV ROHINI DAGADU RANJANA , 71239298L , PICT , S8054257

01. DISCRETE STRUCTURES	PP	100	40	41	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	62	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	62	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	48	P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	58	P C	13. DATA STRUCTURES	PP	100	40	67	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	58	P C	14. COMPUTER GRAPHICS	PP	100	40	56	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	59	P C	15. COMPUTER ORGANIZATION	PP	100	40	48	P
06. PROGRAMMING LABORATORY	TW	25	10	20	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	36	P
07. PROGRAMMING LABORATORY	PR	50	20	23	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	30	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	18	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	40	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	24	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	31	P
10. SOFT SKILLS	TW	50	20	32	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	40	P
						21. DATA STRUCTURES LABORATORY	PR	50	20	25	P

GRAND TOTAL = 878/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

S8054258 JANTRE NEHA ASHOK ARUNA , 71239299J , PICT , S8054258

01. DISCRETE STRUCTURES	PP	100	40	46	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	46	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	53	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	49	P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	54	P	13. DATA STRUCTURES	PP	100	40	66	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	40	P C	14. COMPUTER GRAPHICS	PP	100	40	60	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	57	P C	15. COMPUTER ORGANIZATION	PP	100	40	52	P
06. PROGRAMMING LABORATORY	TW	25	10	22	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	38	P
07. PROGRAMMING LABORATORY	PR	50	20	26	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	40	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	19	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	41	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	38	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	25	P
10. SOFT SKILLS	TW	50	20	36	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	42	P
						21. DATA STRUCTURES LABORATORY	PR	50	20	30	P

GRAND TOTAL = 880/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

S8054259 JASIM MAIMUNA , 71100830C , PICT , S8054259

01. DISCRETE STRUCTURES	PP	100	40	55	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	61	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	52	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	46	P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	56	P C	13. DATA STRUCTURES	PP	100	40	63	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	59	P C	14. COMPUTER GRAPHICS	PP	100	40	47	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	49	P C	15. COMPUTER ORGANIZATION	PP	100	40	40	P
06. PROGRAMMING LABORATORY	TW	25	10	24	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	38	P
07. PROGRAMMING LABORATORY	PR	50	20	45	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	38	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	20	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	41	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	40	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	34	P
10. SOFT SKILLS	TW	50	20	38	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	42	P
						21. DATA STRUCTURES LABORATORY	PR	50	20	42	P

GRAND TOTAL = 930/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8054260	JOGLEKAR SPHOORTI CHANDRASHEKHAR				POOJA			, 71100832K			PICT		S8054260
01. DISCRETE STRUCTURES	PP	100	40	70	P C			11. ENGINEERING MATHEMATICS III	PP	100	40	85	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	71	P C			12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	64	P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	79	P C			13. DATA STRUCTURES	PP	100	40	70	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	73	P C			14. COMPUTER GRAPHICS	PP	100	40	71	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	61	P C			15. COMPUTER ORGANIZATION	PP	100	40	55	P
06. PROGRAMMING LABORATORY	TW	25	10	24	P C			16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	42	P
07. PROGRAMMING LABORATORY	PR	50	20	47	P C			17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	45	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	19	P C			18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	29	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	36	P C			19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	43	P
10. SOFT SKILLS	TW	50	20	32	P C			20. DATA STRUCTURES LABORATORY	TW	50	20	33	P
								21. DATA STRUCTURES LABORATORY	PR	50	20	40	P

GRAND TOTAL = 1089/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

S8054261	JOSHI ANUJA RAMKRUSHNA				VEENA			, 71100834F			PICT		S8054261
01. DISCRETE STRUCTURES	PP	100	40	80	P C			11. ENGINEERING MATHEMATICS III	PP	100	40	81	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	61	P C			12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	55	P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	58	P C			13. DATA STRUCTURES	PP	100	40	71	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	64	P C			14. COMPUTER GRAPHICS	PP	100	40	72	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	58	P C			15. COMPUTER ORGANIZATION	PP	100	40	53	P
06. PROGRAMMING LABORATORY	TW	25	10	22	P C			16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	38	P
07. PROGRAMMING LABORATORY	PR	50	20	37	P C			17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	35	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	20	P C			18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	39	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	35	P C			19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	28	P
10. SOFT SKILLS	TW	50	20	40	P C			20. DATA STRUCTURES LABORATORY	TW	50	20	34	P
								21. DATA STRUCTURES LABORATORY	PR	50	20	35	P

GRAND TOTAL = 1016/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

S8054262	JOSHI ESHAN CHANDRASHEKHAR				MEDHAVINI			, 71100837L			PICT		S8054262
01. DISCRETE STRUCTURES	PP	100	40	72	P C			11. ENGINEERING MATHEMATICS III	PP	100	40	64	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	63	P C			12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	44	P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	62	P C			13. DATA STRUCTURES	PP	100	40	63	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	63	P C			14. COMPUTER GRAPHICS	PP	100	40	51	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	57	P C			15. COMPUTER ORGANIZATION	PP	100	40	43	P
06. PROGRAMMING LABORATORY	TW	25	10	24	P C			16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	35	P
07. PROGRAMMING LABORATORY	PR	50	20	37	P C			17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	44	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	22	P C			18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	35	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	38	P C			19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	29	P
10. SOFT SKILLS	TW	50	20	29	P C			20. DATA STRUCTURES LABORATORY	TW	50	20	32	P
								21. DATA STRUCTURES LABORATORY	PR	50	20	34	P

GRAND TOTAL = 941/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8054263	KALE APARNA SUNIL				MADHURI			, 71100843E			PICT		S8054263
01. DISCRETE STRUCTURES	PP	100	40	74	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	85	P		
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	74	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	56	P		
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	79	P C	13. DATA STRUCTURES	PP	100	40	75	P		
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	84	P C	14. COMPUTER GRAPHICS	PP	100	40	82	P		
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	68	P C	15. COMPUTER ORGANIZATION	PP	100	40	55	P		
06. PROGRAMMING LABORATORY	TW	25	10	24	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	47	P		
07. PROGRAMMING LABORATORY	PR	50	20	45	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	46	P		
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	23	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	46	P		
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	46	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	44	P		
10. SOFT SKILLS	TW	50	20	46	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	46	P		
						21. DATA STRUCTURES LABORATORY	PR	50	20	46	P		

GRAND TOTAL = 1191/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

S8054264	KAMBLE VIDYA ARUN				SINDHU			, 71100846K			PICT		S8054264
01. DISCRETE STRUCTURES	PP	100	40	43	P	11. ENGINEERING MATHEMATICS III	PP	100	40	40	P		
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	40	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	40	P		
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	55	P	13. DATA STRUCTURES	PP	100	40	59	P		
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	40	P C	14. COMPUTER GRAPHICS	PP	100	40	41	P		
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	40	P C	15. COMPUTER ORGANIZATION	PP	100	40	42	P		
06. PROGRAMMING LABORATORY	TW	25	10	22	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	39	P		
07. PROGRAMMING LABORATORY	PR	50	20	26	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	39	P		
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	19	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	39	P		
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	36	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	34	P		
10. SOFT SKILLS	TW	50	20	34	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	36	P		
						21. DATA STRUCTURES LABORATORY	PR	50	20	15#	P		

GRAND TOTAL = 779/1500, RESULT: SECOND CLASS # [0.4]

ORDN. 1 MARKS :

S8054265	KANTALE SACHIN WAMANRAO				PRAMILA			, 71100848F			PICT		S8054265
01. DISCRETE STRUCTURES	PP	100	40	40	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	27	F		
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	40	P	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	40	P		
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	32	F	13. DATA STRUCTURES	PP	100	40	40	P		
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	46	P	14. COMPUTER GRAPHICS	PP	100	40	40	P		
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	44	P C	15. COMPUTER ORGANIZATION	PP	100	40	26	F		
06. PROGRAMMING LABORATORY	TW	25	10	18	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	36	P		
07. PROGRAMMING LABORATORY	PR	50	20	28	P	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	08	F		
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	16	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	24	P		
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	21	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	08	F		
10. SOFT SKILLS	TW	50	20	31	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	29	P		
						21. DATA STRUCTURES LABORATORY	PR	50	20	30	P		

GRAND TOTAL = 624/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8054266	KARANDE PRADNYA VASANT				CHHAYA			, 71100850H			PICT		S8054266
01. DISCRETE STRUCTURES	PP	100	40	52	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	58	P		
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	55	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	48	P		
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	62	P C	13. DATA STRUCTURES	PP	100	40	68	P		
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	69	P C	14. COMPUTER GRAPHICS	PP	100	40	54	P		
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	50	P C	15. COMPUTER ORGANIZATION	PP	100	40	50	P		
06. PROGRAMMING LABORATORY	TW	25	10	23	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	39	P		
07. PROGRAMMING LABORATORY	PR	50	20	24	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	35	P		
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	22	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	39	P		
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	43	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	36	P		
10. SOFT SKILLS	TW	50	20	32	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	39	P		
						21. DATA STRUCTURES LABORATORY	PR	50	20	30	P		

GRAND TOTAL = 928/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

S8054267	KARANDE SHWETA RAMESH				SANGEETA			, 71100851F			PICT		S8054267
01. DISCRETE STRUCTURES	PP	100	40	52	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	64	P		
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	45	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	40	P		
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	41	P C	13. DATA STRUCTURES	PP	100	40	53	P		
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	46	P C	14. COMPUTER GRAPHICS	PP	100	40	53	P		
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	40	P C	15. COMPUTER ORGANIZATION	PP	100	40	31	F		
06. PROGRAMMING LABORATORY	TW	25	10	19	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	38	P		
07. PROGRAMMING LABORATORY	PR	50	20	30	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	32	P		
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	18	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	28	P		
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	27	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	21	P		
10. SOFT SKILLS	TW	50	20	33	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	34	P		
						21. DATA STRUCTURES LABORATORY	PR	50	20	10	F		

GRAND TOTAL = 755/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

S8054268	KATRE RAHUL SURESH				USHA			, 71100852D			PICT		S8054268
01. DISCRETE STRUCTURES	PP	100	40	40	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	48	P		
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	43	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	40	P		
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	50	P C	13. DATA STRUCTURES	PP	100	40	63	P		
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	67	P C	14. COMPUTER GRAPHICS	PP	100	40	52	P		
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	43	P C	15. COMPUTER ORGANIZATION	PP	100	40	40	P		
06. PROGRAMMING LABORATORY	TW	25	10	20	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	29	P		
07. PROGRAMMING LABORATORY	PR	50	20	35	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	36	P		
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	19	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	32	P		
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	35	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	21	P		
10. SOFT SKILLS	TW	50	20	29	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	31	P		
						21. DATA STRUCTURES LABORATORY	PR	50	20	32	P		

GRAND TOTAL = 805/1500, RESULT: SECOND CLASS

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8054269	KEDARE SWAPNIL PANDIT				NIRMALA		, 71045479B			PICT		S8054269
01. DISCRETE STRUCTURES	PP	100	40	40	P		11. ENGINEERING MATHEMATICS III	PP	100	40	05	F
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	18	F		12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	23	F
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	40	P		13. DATA STRUCTURES	PP	100	40	40	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	26	F		14. COMPUTER GRAPHICS	PP	100	40	43	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	40	P C		15. COMPUTER ORGANIZATION	PP	100	40	31	F
06. PROGRAMMING LABORATORY	TW	25	10	10	P C		16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	20	P
07. PROGRAMMING LABORATORY	PR	50	20	10	F		17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	AA	F
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	10	P C		18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	20	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	08	F		19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	03	F
10. SOFT SKILLS	TW	50	20	20	P C		20. DATA STRUCTURES LABORATORY	TW	50	20	20	P
							21. DATA STRUCTURES LABORATORY	PR	50	20	09	F

GRAND TOTAL = 436/1500, RESULT: FAILS

ORDN. 1 MARKS :

S8054270	KEDIYA RIDDHI PRAVIN				MANJUSHA		, 71100854L			PICT		S8054270
01. DISCRETE STRUCTURES	PP	100	40	60	P C		11. ENGINEERING MATHEMATICS III	PP	100	40	44	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	54	P C		12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	40	P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	49	P C		13. DATA STRUCTURES	PP	100	40	56	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	64	P C		14. COMPUTER GRAPHICS	PP	100	40	40	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	49	P C		15. COMPUTER ORGANIZATION	PP	100	40	40	P
06. PROGRAMMING LABORATORY	TW	25	10	19	P C		16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	37	P
07. PROGRAMMING LABORATORY	PR	50	20	31	P C		17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	16#	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	18	P C		18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	37	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	32	P C		19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	34	P
10. SOFT SKILLS	TW	50	20	36	P C		20. DATA STRUCTURES LABORATORY	TW	50	20	35	P
							21. DATA STRUCTURES LABORATORY	PR	50	20	20	P

GRAND TOTAL = 811/1500, RESULT: SECOND CLASS # [0.4]

ORDN. 1 MARKS :

S8054271	KHAIRNAR APOORV DATTATRAYA				PRAGATI		, 71100858C			PICT		S8054271
01. DISCRETE STRUCTURES	PP	100	40	67	P C		11. ENGINEERING MATHEMATICS III	PP	100	40	69	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	66	P C		12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	52	P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	62	P C		13. DATA STRUCTURES	PP	100	40	65	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	70	P C		14. COMPUTER GRAPHICS	PP	100	40	62	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	47	P C		15. COMPUTER ORGANIZATION	PP	100	40	49	P
06. PROGRAMMING LABORATORY	TW	25	10	23	P C		16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	40	P
07. PROGRAMMING LABORATORY	PR	50	20	45	P C		17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	37	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	22	P C		18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	33	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	25	P C		19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	37	P
10. SOFT SKILLS	TW	50	20	28	P C		20. DATA STRUCTURES LABORATORY	TW	50	20	32	P
							21. DATA STRUCTURES LABORATORY	PR	50	20	34	P

GRAND TOTAL = 965/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8054272	KHANDAGALE RAVEE SANJEEV				ASHWINI			, 71100861C			PICT		S8054272
01. DISCRETE STRUCTURES	PP	100	40	55	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	40	P		
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	55	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	40	P		
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	40	P C	13. DATA STRUCTURES	PP	100	40	49	P		
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	48	P C	14. COMPUTER GRAPHICS	PP	100	40	46	P		
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	52	P C	15. COMPUTER ORGANIZATION	PP	100	40	32#	P		
06. PROGRAMMING LABORATORY	TW	25	10	17	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	35	P		
07. PROGRAMMING LABORATORY	PR	50	20	35	P	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	39	P		
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	14	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	38	P		
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	29	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	23	P		
10. SOFT SKILLS	TW	50	20	32	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	23	P		
						21. DATA STRUCTURES LABORATORY	PR	50	20	30	P		

GRAND TOTAL = 772/1500, RESULT: SECOND CLASS # [0.4]

ORDN. 1 MARKS :

S8054273	KHANDEKAR GAUTAMI MILIND				VAISHALI			, 71100862M			PICT		S8054273
01. DISCRETE STRUCTURES	PP	100	40	62	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	77	P		
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	66	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	50	P		
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	60	P C	13. DATA STRUCTURES	PP	100	40	62	P		
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	68	P C	14. COMPUTER GRAPHICS	PP	100	40	67	P		
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	53	P C	15. COMPUTER ORGANIZATION	PP	100	40	59	P		
06. PROGRAMMING LABORATORY	TW	25	10	22	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	43	P		
07. PROGRAMMING LABORATORY	PR	50	20	42	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	40	P		
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	23	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	42	P		
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	45	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	38	P		
10. SOFT SKILLS	TW	50	20	45	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	43	P		
						21. DATA STRUCTURES LABORATORY	PR	50	20	40	P		

GRAND TOTAL = 1047/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

S8054274	KHARADE TUSHAR DILIP				PURNIMA			, 71100864H			PICT		S8054274
01. DISCRETE STRUCTURES	PP	100	40	60	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	70	P		
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	64	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	46	P		
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	58	P C	13. DATA STRUCTURES	PP	100	40	58	P		
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	53	P C	14. COMPUTER GRAPHICS	PP	100	40	57	P		
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	44	P C	15. COMPUTER ORGANIZATION	PP	100	40	55	P		
06. PROGRAMMING LABORATORY	TW	25	10	20	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	44	P		
07. PROGRAMMING LABORATORY	PR	50	20	36	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	41	P		
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	20	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	39	P		
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	36	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	21	P		
10. SOFT SKILLS	TW	50	20	39	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	40	P		
						21. DATA STRUCTURES LABORATORY	PR	50	20	39	P		

GRAND TOTAL = 940/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8054275	KSHIRSAGAR NIRANJAN VALMIK			JAYASHRI		, 71100868L			PICT		S8054275
01. DISCRETE STRUCTURES	PP	100	40	55	P	11. ENGINEERING MATHEMATICS III	PP	100	40	57	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	53	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	40	P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	50	P C	13. DATA STRUCTURES	PP	100	40	60	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	40	P C	14. COMPUTER GRAPHICS	PP	100	40	51	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	48	P C	15. COMPUTER ORGANIZATION	PP	100	40	47	P
06. PROGRAMMING LABORATORY	TW	25	10	21	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	41	P
07. PROGRAMMING LABORATORY	PR	50	20	20	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	06	F
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	21	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	36	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	35	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	21	P
10. SOFT SKILLS	TW	50	20	40	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	31	P
						21. DATA STRUCTURES LABORATORY	PR	50	20	10	F

GRAND TOTAL = 783/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

S8054276	KUBER SHRUTI KRISHNADATTA			SUNITA		, 71100869J			PICT		S8054276
01. DISCRETE STRUCTURES	PP	100	40	71	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	85	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	68	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	59	P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	80	P C	13. DATA STRUCTURES	PP	100	40	77	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	76	P C	14. COMPUTER GRAPHICS	PP	100	40	62	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	60	P C	15. COMPUTER ORGANIZATION	PP	100	40	62	P
06. PROGRAMMING LABORATORY	TW	25	10	23	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	33	P
07. PROGRAMMING LABORATORY	PR	50	20	32	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	38	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	20	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	36	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	40	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	41	P
10. SOFT SKILLS	TW	50	20	39	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	34	P
						21. DATA STRUCTURES LABORATORY	PR	50	20	34	P

GRAND TOTAL = 1070/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

S8054277	KULKARNI APARNA HARISHCHANDRA			PRADNYA		, 71239300F			PICT		S8054277
01. DISCRETE STRUCTURES	PP	100	40	40	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	61	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	66	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	47	P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	61	P C	13. DATA STRUCTURES	PP	100	40	65	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	54	P C	14. COMPUTER GRAPHICS	PP	100	40	61	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	49	P C	15. COMPUTER ORGANIZATION	PP	100	40	55	P
06. PROGRAMMING LABORATORY	TW	25	10	18	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	38	P
07. PROGRAMMING LABORATORY	PR	50	20	43	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	35	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	18	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	40	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	31	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	27	P
10. SOFT SKILLS	TW	50	20	32	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	32	P
						21. DATA STRUCTURES LABORATORY	PR	50	20	32	P

GRAND TOTAL = 905/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8054278	KULKARNI CHAITANYA AVINASH			AMRUTA			, 71100870B			PICT		S8054278
01. DISCRETE STRUCTURES	PP	100	40	61	P	C	11. ENGINEERING MATHEMATICS III	PP	100	40	95	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	67	P	C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	57	P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	78	P	C	13. DATA STRUCTURES	PP	100	40	62	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	62	P	C	14. COMPUTER GRAPHICS	PP	100	40	57	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	55	P	C	15. COMPUTER ORGANIZATION	PP	100	40	72	P
06. PROGRAMMING LABORATORY	TW	25	10	21	P	C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	42	P
07. PROGRAMMING LABORATORY	PR	50	20	45	P	C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	36	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	22	P	C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	37	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	38	P	C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	22	P
10. SOFT SKILLS	TW	50	20	40	P	C	20. DATA STRUCTURES LABORATORY	TW	50	20	42	P
							21. DATA STRUCTURES LABORATORY	PR	50	20	35	P

GRAND TOTAL = 1046/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

S8054279	KULKARNI SARANG SANTOSH			SNEHA			, 71100873G			PICT		S8054279
01. DISCRETE STRUCTURES	PP	100	40	40	P	C	11. ENGINEERING MATHEMATICS III	PP	100	40	47	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	57	P	C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	40	P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	52	P		13. DATA STRUCTURES	PP	100	40	48	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	43	P	C	14. COMPUTER GRAPHICS	PP	100	40	53	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	41	P	C	15. COMPUTER ORGANIZATION	PP	100	40	59	P
06. PROGRAMMING LABORATORY	TW	25	10	20	P	C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	33	P
07. PROGRAMMING LABORATORY	PR	50	20	32	P	C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	30	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	16	P	C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	32	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	25	P	C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	21	P
10. SOFT SKILLS	TW	50	20	31	P	C	20. DATA STRUCTURES LABORATORY	TW	50	20	29	P
							21. DATA STRUCTURES LABORATORY	PR	50	20	05	F

GRAND TOTAL = 754/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

S8054280	KULKARNI SAURABH NITIN			REVATI			, 71100874E			PICT		S8054280
01. DISCRETE STRUCTURES	PP	100	40	70	P	C	11. ENGINEERING MATHEMATICS III	PP	100	40	62	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	65	P	C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	40	P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	48	P	C	13. DATA STRUCTURES	PP	100	40	60	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	55	P	C	14. COMPUTER GRAPHICS	PP	100	40	49	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	45	P	C	15. COMPUTER ORGANIZATION	PP	100	40	48	P
06. PROGRAMMING LABORATORY	TW	25	10	23	P	C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	40	P
07. PROGRAMMING LABORATORY	PR	50	20	45	P	C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	38	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	23	P	C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	43	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	41	P	C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	45	P
10. SOFT SKILLS	TW	50	20	39	P	C	20. DATA STRUCTURES LABORATORY	TW	50	20	42	P
							21. DATA STRUCTURES LABORATORY	PR	50	20	43	P

GRAND TOTAL = 964/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8054281	LAJPAT BISHNOI				SAROJ		, 71100879F			PICT		S8054281
01. DISCRETE STRUCTURES	PP	100	40	49	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	83	P	
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	57	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	40	P	
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	73	P C	13. DATA STRUCTURES	PP	100	40	60	P	
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	63	P C	14. COMPUTER GRAPHICS	PP	100	40	48	P	
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	42	P C	15. COMPUTER ORGANIZATION	PP	100	40	56	P	
06. PROGRAMMING LABORATORY	TW	25	10	24	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	43	P	
07. PROGRAMMING LABORATORY	PR	50	20	40	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	45	P	
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	22	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	42	P	
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	42	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	41	P	
10. SOFT SKILLS	TW	50	20	40	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	40	P	
						21. DATA STRUCTURES LABORATORY	PR	50	20	39	P	

GRAND TOTAL = 989/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

S8054282	LALANI SANIA SALIM				NASHINA		, 71100880K			PICT		S8054282
01. DISCRETE STRUCTURES	PP	100	40	65	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	90	P	
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	65	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	41	P	
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	68	P C	13. DATA STRUCTURES	PP	100	40	55	P	
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	47	P C	14. COMPUTER GRAPHICS	PP	100	40	49	P	
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	51	P C	15. COMPUTER ORGANIZATION	PP	100	40	60	P	
06. PROGRAMMING LABORATORY	TW	25	10	20	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	32	P	
07. PROGRAMMING LABORATORY	PR	50	20	38	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	42	P	
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	20	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	35	P	
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	38	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	36	P	
10. SOFT SKILLS	TW	50	20	33	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	34	P	
						21. DATA STRUCTURES LABORATORY	PR	50	20	38	P	

GRAND TOTAL = 957/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

S8054283	LIMAYE ARNAV ABHAY				VEENA		, 71100882F			PICT		S8054283
01. DISCRETE STRUCTURES	PP	100	40	66	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	81	P	
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	62	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	40	P	
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	74	P C	13. DATA STRUCTURES	PP	100	40	68	P	
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	53	P C	14. COMPUTER GRAPHICS	PP	100	40	51	P	
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	50	P C	15. COMPUTER ORGANIZATION	PP	100	40	45	P	
06. PROGRAMMING LABORATORY	TW	25	10	22	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	36	P	
07. PROGRAMMING LABORATORY	PR	50	20	30	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	40	P	
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	17	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	35	P	
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	35	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	40	P	
10. SOFT SKILLS	TW	50	20	32	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	40	P	
						21. DATA STRUCTURES LABORATORY	PR	50	20	36	P	

GRAND TOTAL = 953/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8054284	LOYA SHRUTI SHAMSUNDER				PREMLATA		, 71100883D			PICT		S8054284
01. DISCRETE STRUCTURES	PP	100	40	65	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	87	P	
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	72	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	56	P	
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	55	P C	13. DATA STRUCTURES	PP	100	40	63	P	
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	68	P C	14. COMPUTER GRAPHICS	PP	100	40	66	P	
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	54	P C	15. COMPUTER ORGANIZATION	PP	100	40	65	P	
06. PROGRAMMING LABORATORY	TW	25	10	18	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	31	P	
07. PROGRAMMING LABORATORY	PR	50	20	28	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	42	P	
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	19	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	35	P	
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	24	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	21	P	
10. SOFT SKILLS	TW	50	20	33	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	36	P	
						21. DATA STRUCTURES LABORATORY	PR	50	20	34	P	

GRAND TOTAL = 972/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

S8054285	MAHAJAN ABHIJEET SHIVDAS				PRABHAVATI		, 71100887G			PICT		S8054285
01. DISCRETE STRUCTURES	PP	100	40	60	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	40	P	
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	40	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	19	F	
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	44	P C	13. DATA STRUCTURES	PP	100	40	59	P	
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	40	P C	14. COMPUTER GRAPHICS	PP	100	40	44	P	
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	40	P C	15. COMPUTER ORGANIZATION	PP	100	40	44	P	
06. PROGRAMMING LABORATORY	TW	25	10	20	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	29	P	
07. PROGRAMMING LABORATORY	PR	50	20	27	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	21	P	
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	16	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	30	P	
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	30	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	28	P	
10. SOFT SKILLS	TW	50	20	30	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	35	P	
						21. DATA STRUCTURES LABORATORY	PR	50	20	25	P	

GRAND TOTAL = 721/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

S8054286	MARATHE SONALI ASHOK				ARUNA		, 71239301D			PICT		S8054286
01. DISCRETE STRUCTURES	PP	100	40	41	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	46	P	
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	64	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	40	P	
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	52	P C	13. DATA STRUCTURES	PP	100	40	64	P	
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	49	P C	14. COMPUTER GRAPHICS	PP	100	40	54	P	
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	41	P C	15. COMPUTER ORGANIZATION	PP	100	40	44	P	
06. PROGRAMMING LABORATORY	TW	25	10	22	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	42	P	
07. PROGRAMMING LABORATORY	PR	50	20	22	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	35	P	
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	23	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	44	P	
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	46	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	32	P	
10. SOFT SKILLS	TW	50	20	45	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	40	P	
						21. DATA STRUCTURES LABORATORY	PR	50	20	28	P	

GRAND TOTAL = 874/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8054287	MARUDA PRAVIN POPATLAL				NANDA		, 71239302B			PICT		S8054287
01. DISCRETE STRUCTURES	PP	100	40	48	P		11. ENGINEERING MATHEMATICS III	PP	100	40	40	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	65	P C		12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	40	P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	40	P C		13. DATA STRUCTURES	PP	100	40	66	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	42	P C		14. COMPUTER GRAPHICS	PP	100	40	55	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	56	P C		15. COMPUTER ORGANIZATION	PP	100	40	53	P
06. PROGRAMMING LABORATORY	TW	25	10	21	P C		16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	42	P
07. PROGRAMMING LABORATORY	PR	50	20	21	P		17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	25	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	17	P C		18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	42	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	37	P		19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	08	F
10. SOFT SKILLS	TW	50	20	30	P C		20. DATA STRUCTURES LABORATORY	TW	50	20	43	P
							21. DATA STRUCTURES LABORATORY	PR	50	20	37	P

GRAND TOTAL = 828/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

S8054288	MEHTA MIHIR ARPIT				FALGUNI		, 71100894K			PICT		S8054288
01. DISCRETE STRUCTURES	PP	100	40	48	P C		11. ENGINEERING MATHEMATICS III	PP	100	40	82	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	50	P C		12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	51	P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	64	P C		13. DATA STRUCTURES	PP	100	40	62	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	70	P C		14. COMPUTER GRAPHICS	PP	100	40	57	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	48	P C		15. COMPUTER ORGANIZATION	PP	100	40	58	P
06. PROGRAMMING LABORATORY	TW	25	10	19	P C		16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	41	P
07. PROGRAMMING LABORATORY	PR	50	20	41	P C		17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	39	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	21	P C		18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	38	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	41	P C		19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	40	P
10. SOFT SKILLS	TW	50	20	38	P C		20. DATA STRUCTURES LABORATORY	TW	50	20	38	P
							21. DATA STRUCTURES LABORATORY	PR	50	20	39	P

GRAND TOTAL = 985/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

S8054289	MISTRY HASIT NIPUN				SONAL		, 71100896F			PICT		S8054289
01. DISCRETE STRUCTURES	PP	100	40	40	P C		11. ENGINEERING MATHEMATICS III	PP	100	40	40	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	52	P C		12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	40	P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	40	P C		13. DATA STRUCTURES	PP	100	40	52	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	45	P C		14. COMPUTER GRAPHICS	PP	100	40	43	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	45	P C		15. COMPUTER ORGANIZATION	PP	100	40	41	P
06. PROGRAMMING LABORATORY	TW	25	10	23	P C		16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	32	P
07. PROGRAMMING LABORATORY	PR	50	20	36	P C		17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	32	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	18	P C		18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	40	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	36	P C		19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	31	P
10. SOFT SKILLS	TW	50	20	34	P C		20. DATA STRUCTURES LABORATORY	TW	50	20	33	P
							21. DATA STRUCTURES LABORATORY	PR	50	20	30	P

GRAND TOTAL = 783/1500, RESULT: SECOND CLASS

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8054290	MOHIT RAJVARDHAN				MEENA KUMARI		, 71045671K			PICT		S8054290
01. DISCRETE STRUCTURES	PP	100	40	43	P		11. ENGINEERING MATHEMATICS III	PP	100	40	40	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	40	P C		12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	23	F
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	47	P C		13. DATA STRUCTURES	PP	100	40	51	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	51	P C		14. COMPUTER GRAPHICS	PP	100	40	31	F
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	40	P C		15. COMPUTER ORGANIZATION	PP	100	40	43	P
06. PROGRAMMING LABORATORY	TW	25	10	23	P C		16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	34	P
07. PROGRAMMING LABORATORY	PR	50	20	38	P C		17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	36	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	18	P C		18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	34	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	32	P C		19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	30	P
10. SOFT SKILLS	TW	50	20	31	P C		20. DATA STRUCTURES LABORATORY	TW	50	20	34	P
							21. DATA STRUCTURES LABORATORY	PR	50	20	34	P

GRAND TOTAL = 753/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

S8054291	MORANKAR SNEHAL RAMESH				JAYASHREE		, 71100899L			PICT		S8054291
01. DISCRETE STRUCTURES	PP	100	40	40	P C		11. ENGINEERING MATHEMATICS III	PP	100	40	59	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	58	P C		12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	42	P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	51	P C		13. DATA STRUCTURES	PP	100	40	57	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	56	P C		14. COMPUTER GRAPHICS	PP	100	40	51	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	44	P C		15. COMPUTER ORGANIZATION	PP	100	40	52	P
06. PROGRAMMING LABORATORY	TW	25	10	18	P C		16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	36	P
07. PROGRAMMING LABORATORY	PR	50	20	28	P C		17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	35	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	19	P C		18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	37	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	33	P C		19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	30	P
10. SOFT SKILLS	TW	50	20	34	P C		20. DATA STRUCTURES LABORATORY	TW	50	20	37	P
							21. DATA STRUCTURES LABORATORY	PR	50	20	37	P

GRAND TOTAL = 854/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

S8054292	MUNMOON GHOSH				REKHA		, 71100902D			PICT		S8054292
01. DISCRETE STRUCTURES	PP	100	40	40	P C		11. ENGINEERING MATHEMATICS III	PP	100	40	48	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	54	P C		12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	40	P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	40	P C		13. DATA STRUCTURES	PP	100	40	55	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	44	P C		14. COMPUTER GRAPHICS	PP	100	40	51	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	41	P C		15. COMPUTER ORGANIZATION	PP	100	40	50	P
06. PROGRAMMING LABORATORY	TW	25	10	18	P C		16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	28	P
07. PROGRAMMING LABORATORY	PR	50	20	22	P C		17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	23	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	12	P C		18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	30	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	09	F		19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	22	P
10. SOFT SKILLS	TW	50	20	32	P C		20. DATA STRUCTURES LABORATORY	TW	50	20	34	P
							21. DATA STRUCTURES LABORATORY	PR	50	20	30	P

GRAND TOTAL = 723/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8054293	NAGRE KARTIK ATUL			MANISHA		, 71100905J			PICT		S8054293
01. DISCRETE STRUCTURES	PP	100	40	41	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	57	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	62	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	42	P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	65	P C	13. DATA STRUCTURES	PP	100	40	57	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	60	P C	14. COMPUTER GRAPHICS	PP	100	40	57	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	43	P C	15. COMPUTER ORGANIZATION	PP	100	40	69	P
06. PROGRAMMING LABORATORY	TW	25	10	19	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	29	P
07. PROGRAMMING LABORATORY	PR	50	20	28	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	40	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	17	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	35	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	33	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	33	P
10. SOFT SKILLS	TW	50	20	35	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	35	P
						21. DATA STRUCTURES LABORATORY	PR	50	20	35	P

GRAND TOTAL = 892+08/1500, RESULT: FIRST CLASS [0.2]

ORDN. 1 MARKS :

S8054294	NAIK SANDESH TARSING			KAMAL		, 71100908C			PICT		S8054294
01. DISCRETE STRUCTURES	PP	100	40	40	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	45	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	60	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	40	P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	49	P	13. DATA STRUCTURES	PP	100	40	59	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	63	P C	14. COMPUTER GRAPHICS	PP	100	40	43	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	43	P C	15. COMPUTER ORGANIZATION	PP	100	40	47	P
06. PROGRAMMING LABORATORY	TW	25	10	19	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	27	P
07. PROGRAMMING LABORATORY	PR	50	20	25	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	38	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	15	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	27	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	24	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	23	P
10. SOFT SKILLS	TW	50	20	32	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	36	P
						21. DATA STRUCTURES LABORATORY	PR	50	20	25	P

GRAND TOTAL = 780/1500, RESULT: SECOND CLASS

ORDN. 1 MARKS :

S8054295	NARGUNDE TUSHAR VYANKATESH			ANJALI		, 71100913K			PICT		S8054295
01. DISCRETE STRUCTURES	PP	100	40	71	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	81	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	57	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	44	P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	50	P C	13. DATA STRUCTURES	PP	100	40	62	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	71	P C	14. COMPUTER GRAPHICS	PP	100	40	58	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	54	P C	15. COMPUTER ORGANIZATION	PP	100	40	61	P
06. PROGRAMMING LABORATORY	TW	25	10	22	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	38	P
07. PROGRAMMING LABORATORY	PR	50	20	38	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	38	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	22	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	41	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	36	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	42	P
10. SOFT SKILLS	TW	50	20	38	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	39	P
						21. DATA STRUCTURES LABORATORY	PR	50	20	43	P

GRAND TOTAL = 1006/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8054296	NAVEEN KUMAR GUPTA			ANITA		, 71100915F			PICT		S8054296
01. DISCRETE STRUCTURES	PP	100	40	40	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	53	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	55	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	45	P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	58	P C	13. DATA STRUCTURES	PP	100	40	62	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	42	P C	14. COMPUTER GRAPHICS	PP	100	40	55	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	52	P C	15. COMPUTER ORGANIZATION	PP	100	40	60	P
06. PROGRAMMING LABORATORY	TW	25	10	17	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	21	P
07. PROGRAMMING LABORATORY	PR	50	20	AA	F	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	39	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	15	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	25	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	35	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	10	F
10. SOFT SKILLS	TW	50	20	29	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	23	P
						21. DATA STRUCTURES LABORATORY	PR	50	20	15	F

GRAND TOTAL = 751/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

S8054297	PALNITKAR AMOGH NARENDRA			JAYASHREE		, 71100924E			PICT		S8054297
01. DISCRETE STRUCTURES	PP	100	40	63	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	79	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	57	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	52	P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	57	P C	13. DATA STRUCTURES	PP	100	40	55	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	64	P C	14. COMPUTER GRAPHICS	PP	100	40	47	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	50	P C	15. COMPUTER ORGANIZATION	PP	100	40	55	P
06. PROGRAMMING LABORATORY	TW	25	10	22	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	39	P
07. PROGRAMMING LABORATORY	PR	50	20	44	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	44	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	18	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	40	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	32	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	38	P
10. SOFT SKILLS	TW	50	20	36	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	41	P
						21. DATA STRUCTURES LABORATORY	PR	50	20	41	P

GRAND TOTAL = 974/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

S8054298	PANDE SWAPNIL SURESH			SADHANA		, 71100926M			PICT		S8054298
01. DISCRETE STRUCTURES	PP	100	40	45	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	43	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	60	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	45	P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	53	P C	13. DATA STRUCTURES	PP	100	40	62	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	69	P C	14. COMPUTER GRAPHICS	PP	100	40	55	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	52	P C	15. COMPUTER ORGANIZATION	PP	100	40	48	P
06. PROGRAMMING LABORATORY	TW	25	10	20	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	34	P
07. PROGRAMMING LABORATORY	PR	50	20	43	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	42	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	17	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	31	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	21	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	39	P
10. SOFT SKILLS	TW	50	20	32	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	38	P
						21. DATA STRUCTURES LABORATORY	PR	50	20	38	P

GRAND TOTAL = 887/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8054299	PANHALKAR SHREYAS SHRIKANT				NAYANA			, 71100929F			PICT		S8054299
01. DISCRETE STRUCTURES	PP	100	40	57	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	71	P		
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	62	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	40	P		
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	74	P C	13. DATA STRUCTURES	PP	100	40	59	P		
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	64	P C	14. COMPUTER GRAPHICS	PP	100	40	55	P		
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	50	P C	15. COMPUTER ORGANIZATION	PP	100	40	54	P		
06. PROGRAMMING LABORATORY	TW	25	10	19	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	36	P		
07. PROGRAMMING LABORATORY	PR	50	20	38	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	37	P		
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	20	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	36	P		
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	34	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	24	P		
10. SOFT SKILLS	TW	50	20	36	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	41	P		
						21. DATA STRUCTURES LABORATORY	PR	50	20	40	P		

GRAND TOTAL = 947/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

S8054300	PAPAT VAIBHAV VINAYAK				VANDANA			, 71100930K			PICT		S8054300
01. DISCRETE STRUCTURES	PP	100	40	58	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	70	P		
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	60	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	41	P		
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	67	P C	13. DATA STRUCTURES	PP	100	40	69	P		
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	57	P C	14. COMPUTER GRAPHICS	PP	100	40	51	P		
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	50	P C	15. COMPUTER ORGANIZATION	PP	100	40	60	P		
06. PROGRAMMING LABORATORY	TW	25	10	20	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	38	P		
07. PROGRAMMING LABORATORY	PR	50	20	41	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	37	P		
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	18	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	40	P		
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	42	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	35	P		
10. SOFT SKILLS	TW	50	20	35	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	44	P		
						21. DATA STRUCTURES LABORATORY	PR	50	20	35	P		

GRAND TOTAL = 968/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

S8054301	PATHAK ROHAN ABHAY				ARCHANA			, 71100935L			PICT		S8054301
01. DISCRETE STRUCTURES	PP	100	40	45	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	56	P		
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	63	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	10	F		
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	43	P C	13. DATA STRUCTURES	PP	100	40	51	P		
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	58	P C	14. COMPUTER GRAPHICS	PP	100	40	55	P		
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	40	P C	15. COMPUTER ORGANIZATION	PP	100	40	50	P		
06. PROGRAMMING LABORATORY	TW	25	10	20	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	38	P		
07. PROGRAMMING LABORATORY	PR	50	20	40	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	30	P		
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	21	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	42	P		
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	35	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	08	F		
10. SOFT SKILLS	TW	50	20	39	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	36	P		
						21. DATA STRUCTURES LABORATORY	PR	50	20	32	P		

GRAND TOTAL = 812/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8054302	PATIL MADHUSHRI VINOD				VRUSHALI			, 71239303L			PICT		S8054302
01. DISCRETE STRUCTURES	PP	100	40	51	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	61	P		
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	64	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	43	P		
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	45	P C	13. DATA STRUCTURES	PP	100	40	56	P		
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	63	P C	14. COMPUTER GRAPHICS	PP	100	40	51	P		
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	48	P C	15. COMPUTER ORGANIZATION	PP	100	40	58	P		
06. PROGRAMMING LABORATORY	TW	25	10	21	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	38	P		
07. PROGRAMMING LABORATORY	PR	50	20	42	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	38	P		
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	19	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	38	P		
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	32	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	27	P		
10. SOFT SKILLS	TW	50	20	42	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	41	P		
						21. DATA STRUCTURES LABORATORY	PR	50	20	39	P		

GRAND TOTAL = 917/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

S8054303	PATIL MANDAR RAMRAO				MUKTA			, 71100940G			PICT		S8054303
01. DISCRETE STRUCTURES	PP	100	40	42	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	43	P		
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	41	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	40	P		
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	46	P C	13. DATA STRUCTURES	PP	100	40	49	P		
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	41	P	14. COMPUTER GRAPHICS	PP	100	40	45	P		
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	47	P C	15. COMPUTER ORGANIZATION	PP	100	40	53	P		
06. PROGRAMMING LABORATORY	TW	25	10	21	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	21	P		
07. PROGRAMMING LABORATORY	PR	50	20	17#	P	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	32	P		
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	17	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	35	P		
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	30	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	23	P		
10. SOFT SKILLS	TW	50	20	33	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	36	P		
						21. DATA STRUCTURES LABORATORY	PR	50	20	20	P		

GRAND TOTAL = 732/1500, RESULT: PASS CLASS # [0.4]

ORDN. 1 MARKS :

S8054304	PATIL YOJANA VILAS				SARALA			, 71100945H			PICT		S8054304
01. DISCRETE STRUCTURES	PP	100	40	53	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	80	P		
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	60	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	40	P		
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	63	P C	13. DATA STRUCTURES	PP	100	40	61	P		
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	67	P C	14. COMPUTER GRAPHICS	PP	100	40	69	P		
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	69	P C	15. COMPUTER ORGANIZATION	PP	100	40	69	P		
06. PROGRAMMING LABORATORY	TW	25	10	22	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	39	P		
07. PROGRAMMING LABORATORY	PR	50	20	22	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	43	P		
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	17	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	41	P		
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	36	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	27	P		
10. SOFT SKILLS	TW	50	20	36	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	42	P		
						21. DATA STRUCTURES LABORATORY	PR	50	20	35	P		

GRAND TOTAL = 991/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8054305	PAWAR ABHIJEET SADANAND				RENUKA			, 71100947D			PICT		S8054305
01. DISCRETE STRUCTURES	PP	100	40	57	P C			11. ENGINEERING MATHEMATICS III	PP	100	40	72	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	57	P C			12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	40	P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	68	P C			13. DATA STRUCTURES	PP	100	40	60	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	67	P C			14. COMPUTER GRAPHICS	PP	100	40	62	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	40	P C			15. COMPUTER ORGANIZATION	PP	100	40	56	P
06. PROGRAMMING LABORATORY	TW	25	10	23	P C			16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	41	P
07. PROGRAMMING LABORATORY	PR	50	20	44	P C			17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	43	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	22	P C			18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	42	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	35	P C			19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	36	P
10. SOFT SKILLS	TW	50	20	40	P C			20. DATA STRUCTURES LABORATORY	TW	50	20	42	P
								21. DATA STRUCTURES LABORATORY	PR	50	20	38	P

GRAND TOTAL = 985/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

S8054306	PAWAR NAYAN SUNIL				NALINI			, 71239304J			PICT		S8054306
01. DISCRETE STRUCTURES	PP	100	40	40	P C			11. ENGINEERING MATHEMATICS III	PP	100	40	21	F
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	65	P C			12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	50	P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	46	P C			13. DATA STRUCTURES	PP	100	40	58	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	55	P C			14. COMPUTER GRAPHICS	PP	100	40	63	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	59	P C			15. COMPUTER ORGANIZATION	PP	100	40	61	P
06. PROGRAMMING LABORATORY	TW	25	10	18	P C			16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	37	P
07. PROGRAMMING LABORATORY	PR	50	20	30	P			17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	37	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	19	P C			18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	43	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	33	P C			19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	23	P
10. SOFT SKILLS	TW	50	20	30	P C			20. DATA STRUCTURES LABORATORY	TW	50	20	36	P
								21. DATA STRUCTURES LABORATORY	PR	50	20	05	F

GRAND TOTAL = 829/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

S8054307	PAWAR OM DILIP				MADHURI			, 71100950D			PICT		S8054307
01. DISCRETE STRUCTURES	PP	100	40	50	P C			11. ENGINEERING MATHEMATICS III	PP	100	40	50	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	69	P C			12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	40	P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	59	P C			13. DATA STRUCTURES	PP	100	40	54	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	82	P C			14. COMPUTER GRAPHICS	PP	100	40	56	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	54	P C			15. COMPUTER ORGANIZATION	PP	100	40	47	P
06. PROGRAMMING LABORATORY	TW	25	10	20	P C			16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	37	P
07. PROGRAMMING LABORATORY	PR	50	20	38	P C			17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	40	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	18	P C			18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	41	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	40	P C			19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	40	P
10. SOFT SKILLS	TW	50	20	36	P C			20. DATA STRUCTURES LABORATORY	TW	50	20	40	P
								21. DATA STRUCTURES LABORATORY	PR	50	20	35	P

GRAND TOTAL = 946/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8054308	PAYAL KALRA				SIMMI		, 71100953J			PICT		S8054308
01. DISCRETE STRUCTURES	PP	100	40	49	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	42	P	
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	63	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	40	P	
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	70	P C	13. DATA STRUCTURES	PP	100	40	66	P	
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	67	P C	14. COMPUTER GRAPHICS	PP	100	40	48	P	
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	44	P C	15. COMPUTER ORGANIZATION	PP	100	40	49	P	
06. PROGRAMMING LABORATORY	TW	25	10	20	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	34	P	
07. PROGRAMMING LABORATORY	PR	50	20	22	P	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	35	P	
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	22	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	34	P	
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	38	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	05	F	
10. SOFT SKILLS	TW	50	20	40	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	35	P	
						21. DATA STRUCTURES LABORATORY	PR	50	20	34	P	

GRAND TOTAL = 857/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

S8054309	POL ANIKET BHIMRAO				ARUNA		, 71100956C			PICT		S8054309
01. DISCRETE STRUCTURES	PP	100	40	62	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	83	P	
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	63	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	52	P	
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	70	P C	13. DATA STRUCTURES	PP	100	40	78	P	
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	73	P C	14. COMPUTER GRAPHICS	PP	100	40	69	P	
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	53	P C	15. COMPUTER ORGANIZATION	PP	100	40	50	P	
06. PROGRAMMING LABORATORY	TW	25	10	20	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	42	P	
07. PROGRAMMING LABORATORY	PR	50	20	42	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	40	P	
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	17	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	38	P	
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	35	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	40	P	
10. SOFT SKILLS	TW	50	20	34	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	42	P	
						21. DATA STRUCTURES LABORATORY	PR	50	20	33	P	

GRAND TOTAL = 1036/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

S8054310	RAMTIRTH IRA NITIN				ANJALI		, 71100965B			PICT		S8054310
01. DISCRETE STRUCTURES	PP	100	40	69	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	22	F	
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	64	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	47	P	
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	60	P C	13. DATA STRUCTURES	PP	100	40	70	P	
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	58	P C	14. COMPUTER GRAPHICS	PP	100	40	68	P	
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	55	P C	15. COMPUTER ORGANIZATION	PP	100	40	53	P	
06. PROGRAMMING LABORATORY	TW	25	10	20	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	39	P	
07. PROGRAMMING LABORATORY	PR	50	20	41	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	35	P	
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	18	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	40	P	
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	39	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	38	P	
10. SOFT SKILLS	TW	50	20	36	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	40	P	
						21. DATA STRUCTURES LABORATORY	PR	50	20	37	P	

GRAND TOTAL = 949/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8054311	RANGNEKAR SARVESH DATTATRAYA				MANJIRI				, 71100967J				PICT		S8054311
01. DISCRETE STRUCTURES	PP	100	40	62	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	74	P				
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	64	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	59	P				
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	62	P C	13. DATA STRUCTURES	PP	100	40	71	P				
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	75	P C	14. COMPUTER GRAPHICS	PP	100	40	69	P				
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	56	P C	15. COMPUTER ORGANIZATION	PP	100	40	50	P				
06. PROGRAMMING LABORATORY	TW	25	10	23	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	43	P				
07. PROGRAMMING LABORATORY	PR	50	20	43	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	36	P				
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	20	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	43	P				
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	41	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	35	P				
10. SOFT SKILLS	TW	50	20	36	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	44	P				
						21. DATA STRUCTURES LABORATORY	PR	50	20	41	P				

GRAND TOTAL = 1047/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

S8054312	RATHI ROSHANI DIPAK				JYOTI				, 71100969E				PICT		S8054312
01. DISCRETE STRUCTURES	PP	100	40	48	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	40	P				
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	62	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	40	P				
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	51	P C	13. DATA STRUCTURES	PP	100	40	66	P				
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	53	P C	14. COMPUTER GRAPHICS	PP	100	40	62	P				
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	52	P C	15. COMPUTER ORGANIZATION	PP	100	40	40	P				
06. PROGRAMMING LABORATORY	TW	25	10	20	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	33	P				
07. PROGRAMMING LABORATORY	PR	50	20	30	P	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	33	P				
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	18	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	32	P				
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	23	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	36	P				
10. SOFT SKILLS	TW	50	20	35	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	32	P				
						21. DATA STRUCTURES LABORATORY	PR	50	20	32	P				

GRAND TOTAL = 838/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

S8054313	RAUT VAIBHAV SHARAD				RUKMINI				, 71100973C				PICT		S8054313
01. DISCRETE STRUCTURES	PP	100	40	58	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	41	P				
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	59	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	40	P				
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	52	P C	13. DATA STRUCTURES	PP	100	40	64	P				
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	75	P C	14. COMPUTER GRAPHICS	PP	100	40	64	P				
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	49	P C	15. COMPUTER ORGANIZATION	PP	100	40	55	P				
06. PROGRAMMING LABORATORY	TW	25	10	19	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	28	P				
07. PROGRAMMING LABORATORY	PR	50	20	45	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	36	P				
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	16	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	29	P				
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	22	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	27	P				
10. SOFT SKILLS	TW	50	20	30	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	33	P				
						21. DATA STRUCTURES LABORATORY	PR	50	20	35	P				

GRAND TOTAL = 877/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8054314	RENUKE JAGDISH SHIVAJIRAO				VANAMALA			, 71100974M				PICT		S8054314
01. DISCRETE STRUCTURES	PP	100	40	43	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	40	P			
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	54	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	40	P			
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	51	P C	13. DATA STRUCTURES	PP	100	40	53	P			
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	58	P C	14. COMPUTER GRAPHICS	PP	100	40	43	P			
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	48	P C	15. COMPUTER ORGANIZATION	PP	100	40	40	P			
06. PROGRAMMING LABORATORY	TW	25	10	18	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	25	P			
07. PROGRAMMING LABORATORY	PR	50	20	30	P	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	07	F			
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	19	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	28	P			
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	30	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	27	P			
10. SOFT SKILLS	TW	50	20	32	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	26	P			
						21. DATA STRUCTURES LABORATORY	PR	50	20	15	F			

GRAND TOTAL = 727/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

S8054315	RUPNER GEETA MADHAV				GOKULA			, 71239305G				PICT		S8054315
01. DISCRETE STRUCTURES	PP	100	40	56	P	11. ENGINEERING MATHEMATICS III	PP	100	40	40	P			
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	63	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	40	P			
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	61	P C	13. DATA STRUCTURES	PP	100	40	60	P			
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	62	P C	14. COMPUTER GRAPHICS	PP	100	40	63	P			
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	47	P C	15. COMPUTER ORGANIZATION	PP	100	40	60	P			
06. PROGRAMMING LABORATORY	TW	25	10	17	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	36	P			
07. PROGRAMMING LABORATORY	PR	50	20	28	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	24	P			
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	21	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	44	P			
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	35	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	31	P			
10. SOFT SKILLS	TW	50	20	35	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	39	P			
						21. DATA STRUCTURES LABORATORY	PR	50	20	20	P			

GRAND TOTAL = 882/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

S8054316	SALECHA DIVYA SANJAY				NIRMALA			, 71100983L				PICT		S8054316
01. DISCRETE STRUCTURES	PP	100	40	45	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	40	P			
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	55	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	26	F			
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	50	P C	13. DATA STRUCTURES	PP	100	40	45	P			
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	72	P C	14. COMPUTER GRAPHICS	PP	100	40	45	P			
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	42	P	15. COMPUTER ORGANIZATION	PP	100	40	28	F			
06. PROGRAMMING LABORATORY	TW	25	10	18	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	34	P			
07. PROGRAMMING LABORATORY	PR	50	20	30	P	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	32	P			
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	19	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	35	P			
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	35	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	08	F			
10. SOFT SKILLS	TW	50	20	32	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	39	P			
						21. DATA STRUCTURES LABORATORY	PR	50	20	32	P			

GRAND TOTAL = 762/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8054317	SAMRIDDH HADA MAHESH			RUBY		, 71100985G			PICT		S8054317
01. DISCRETE STRUCTURES	PP	100	40	40	P	11. ENGINEERING MATHEMATICS III	PP	100	40	40	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	44	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	09	F
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	43	P C	13. DATA STRUCTURES	PP	100	40	42	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	44	P	14. COMPUTER GRAPHICS	PP	100	40	50	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	47	P C	15. COMPUTER ORGANIZATION	PP	100	40	30	F
06. PROGRAMMING LABORATORY	TW	25	10	18	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	21	P
07. PROGRAMMING LABORATORY	PR	50	20	20	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	08	F
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	15	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	28	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	30	P	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	02	F
10. SOFT SKILLS	TW	50	20	28	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	25	P
						21. DATA STRUCTURES LABORATORY	PR	50	20	00	F

GRAND TOTAL = 584/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

S8054318	SARAF HRISHIKESH SHRIRAM			VRUSHALI		, 71100987C			PICT		S8054318
01. DISCRETE STRUCTURES	PP	100	40	70	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	58	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	65	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	44	P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	67	P C	13. DATA STRUCTURES	PP	100	40	57	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	71	P C	14. COMPUTER GRAPHICS	PP	100	40	62	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	59	P C	15. COMPUTER ORGANIZATION	PP	100	40	56	P
06. PROGRAMMING LABORATORY	TW	25	10	23	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	36	P
07. PROGRAMMING LABORATORY	PR	50	20	40	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	40	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	18	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	36	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	36	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	35	P
10. SOFT SKILLS	TW	50	20	35	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	41	P
						21. DATA STRUCTURES LABORATORY	PR	50	20	41	P

GRAND TOTAL = 990/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

S8054319	SATHE DEEPASHREE CHANDRAKANT			CHITRA		, 71239306E			PICT		S8054319
01. DISCRETE STRUCTURES	PP	100	40	40	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	67	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	60	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	49	P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	70	P C	13. DATA STRUCTURES	PP	100	40	68	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	89	P C	14. COMPUTER GRAPHICS	PP	100	40	66	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	51	P C	15. COMPUTER ORGANIZATION	PP	100	40	58	P
06. PROGRAMMING LABORATORY	TW	25	10	24	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	44	P
07. PROGRAMMING LABORATORY	PR	50	20	44	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	44	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	20	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	46	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	38	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	40	P
10. SOFT SKILLS	TW	50	20	39	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	44	P
						21. DATA STRUCTURES LABORATORY	PR	50	20	39	P

GRAND TOTAL = 1040/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8054321	SATYANARAYAN RAJAGOPALAN				VIJAYASHANTHI			, 71100990C			PICT		S8054321
01. DISCRETE STRUCTURES	PP	100	40	45	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	58	P		
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	44	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	40	P		
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	40	P C	13. DATA STRUCTURES	PP	100	40	45	P		
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	45	P C	14. COMPUTER GRAPHICS	PP	100	40	41	P		
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	46	P C	15. COMPUTER ORGANIZATION	PP	100	40	40	P		
06. PROGRAMMING LABORATORY	TW	25	10	17	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	39	P		
07. PROGRAMMING LABORATORY	PR	50	20	25	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	38	P		
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	18	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	39	P		
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	31	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	07	F		
10. SOFT SKILLS	TW	50	20	33	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	35	P		
						21. DATA STRUCTURES LABORATORY	PR	50	20	33	P		

GRAND TOTAL = 759/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

S8054322	SAVAJI ADITYA JAYANT				SHRUTI			, 71100991M			PICT		S8054322
01. DISCRETE STRUCTURES	PP	100	40	54	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	53	P		
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	63	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	40	P		
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	51	P C	13. DATA STRUCTURES	PP	100	40	60	P		
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	66	P C	14. COMPUTER GRAPHICS	PP	100	40	56	P		
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	50	P C	15. COMPUTER ORGANIZATION	PP	100	40	40	P		
06. PROGRAMMING LABORATORY	TW	25	10	21	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	36	P		
07. PROGRAMMING LABORATORY	PR	50	20	22	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	37	P		
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	21	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	38	P		
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	39	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	27	P		
10. SOFT SKILLS	TW	50	20	40	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	38	P		
						21. DATA STRUCTURES LABORATORY	PR	50	20	20	P		

GRAND TOTAL = 872/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

S8054323	SAWANT TEJASWINI NANASAHEB				SUNITA			, 71100992K			PICT		S8054323
01. DISCRETE STRUCTURES	PP	100	40	48	P	11. ENGINEERING MATHEMATICS III	PP	100	40	40	P		
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	47	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	21	F		
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	40	P C	13. DATA STRUCTURES	PP	100	40	57	P		
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	40	P C	14. COMPUTER GRAPHICS	PP	100	40	42	P		
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	58	P C	15. COMPUTER ORGANIZATION	PP	100	40	30	F		
06. PROGRAMMING LABORATORY	TW	25	10	19	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	38	P		
07. PROGRAMMING LABORATORY	PR	50	20	21	P	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	34	P		
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	16	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	38	P		
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	29	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	05	F		
10. SOFT SKILLS	TW	50	20	29	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	37	P		
						21. DATA STRUCTURES LABORATORY	PR	50	20	04	F		

GRAND TOTAL = 693/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8054324	SAYALI ANUP LUNKAD				SMITA		, 71100993H			PICT		S8054324
01. DISCRETE STRUCTURES	PP	100	40	58	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	78	P	
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	60	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	41	P	
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	68	P C	13. DATA STRUCTURES	PP	100	40	65	P	
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	71	P C	14. COMPUTER GRAPHICS	PP	100	40	70	P	
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	46	P C	15. COMPUTER ORGANIZATION	PP	100	40	53	P	
06. PROGRAMMING LABORATORY	TW	25	10	18	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	35	P	
07. PROGRAMMING LABORATORY	PR	50	20	32	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	34	P	
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	18	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	37	P	
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	35	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	44	P	
10. SOFT SKILLS	TW	50	20	34	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	35	P	
						21. DATA STRUCTURES LABORATORY	PR	50	20	28	P	

GRAND TOTAL = 960/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

S8054325	SAYKAR SAURABH VIJAY				ARCHANA		, 71100994F			PICT		S8054325
01. DISCRETE STRUCTURES	PP	100	40	44	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	81	P	
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	67	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	48	P	
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	68	P C	13. DATA STRUCTURES	PP	100	40	69	P	
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	61	P C	14. COMPUTER GRAPHICS	PP	100	40	62	P	
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	60	P C	15. COMPUTER ORGANIZATION	PP	100	40	61	P	
06. PROGRAMMING LABORATORY	TW	25	10	20	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	37	P	
07. PROGRAMMING LABORATORY	PR	50	20	21	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	25	P	
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	19	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	38	P	
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	36	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	31	P	
10. SOFT SKILLS	TW	50	20	36	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	42	P	
						21. DATA STRUCTURES LABORATORY	PR	50	20	30	P	

GRAND TOTAL = 956/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

S8054326	SHAH ROUNAK BHARAT				SHILPA		, 71100995D			PICT		S8054326
01. DISCRETE STRUCTURES	PP	100	40	84	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	88	P	
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	68	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	43	P	
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	77	P C	13. DATA STRUCTURES	PP	100	40	80	P	
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	75	P C	14. COMPUTER GRAPHICS	PP	100	40	86	P	
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	73	P C	15. COMPUTER ORGANIZATION	PP	100	40	68	P	
06. PROGRAMMING LABORATORY	TW	25	10	24	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	45	P	
07. PROGRAMMING LABORATORY	PR	50	20	43	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	43	P	
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	24	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	46	P	
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	44	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	42	P	
10. SOFT SKILLS	TW	50	20	44	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	46	P	
						21. DATA STRUCTURES LABORATORY	PR	50	20	44	P	

GRAND TOTAL = 1187/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8054327	SHAH SAMKIT UPESHKUMAR			JAGRUTI		, 71100996B			PICT		S8054327
01. DISCRETE STRUCTURES	PP	100	40	60	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	54	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	66	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	48	P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	63	P C	13. DATA STRUCTURES	PP	100	40	67	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	83	P C	14. COMPUTER GRAPHICS	PP	100	40	66	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	57	P C	15. COMPUTER ORGANIZATION	PP	100	40	51	P
06. PROGRAMMING LABORATORY	TW	25	10	24	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	36	P
07. PROGRAMMING LABORATORY	PR	50	20	45	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	44	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	23	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	44	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	45	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	44	P
10. SOFT SKILLS	TW	50	20	43	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	44	P
						21. DATA STRUCTURES LABORATORY	PR	50	20	40	P

GRAND TOTAL = 1047/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

S8054328	SHELKANDE AKASH DAGADU			SEETA		, 71101000F			PICT		S8054328
01. DISCRETE STRUCTURES	PP	100	40	26	F	11. ENGINEERING MATHEMATICS III	PP	100	40	15	F
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	40	P	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	17	F
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	21	F	13. DATA STRUCTURES	PP	100	40	23	F
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	43	P C	14. COMPUTER GRAPHICS	PP	100	40	31	F
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	40	P C	15. COMPUTER ORGANIZATION	PP	100	40	26	F
06. PROGRAMMING LABORATORY	TW	25	10	17	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	26	P
07. PROGRAMMING LABORATORY	PR	50	20	21	P	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	08	F
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	17	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	33	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	20	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	04	F
10. SOFT SKILLS	TW	50	20	33	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	32	P
						21. DATA STRUCTURES LABORATORY	PR	50	20	05	F

GRAND TOTAL = 498/1500, RESULT: FAILS

ORDN. 1 MARKS :

S8054329	SHENROY ROHIT VEDVYAS			VEENA		, 71101002B			PICT		S8054329
01. DISCRETE STRUCTURES	PP	100	40	54	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	52	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	49	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	40	P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	47	P C	13. DATA STRUCTURES	PP	100	40	60	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	68	P C	14. COMPUTER GRAPHICS	PP	100	40	40	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	51	P C	15. COMPUTER ORGANIZATION	PP	100	40	46	P
06. PROGRAMMING LABORATORY	TW	25	10	19	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	33	P
07. PROGRAMMING LABORATORY	PR	50	20	45	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	38	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	18	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	38	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	43	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	41	P
10. SOFT SKILLS	TW	50	20	37	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	30	P
						21. DATA STRUCTURES LABORATORY	PR	50	20	32	P

GRAND TOTAL = 881/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8054330	SHIKHA AGARWAL				RITA		, 71101003L			PICT		S8054330
01. DISCRETE STRUCTURES	PP	100	40	58	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	52	P	
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	59	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	40	P	
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	46	P C	13. DATA STRUCTURES	PP	100	40	66	P	
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	69	P C	14. COMPUTER GRAPHICS	PP	100	40	47	P	
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	43	P C	15. COMPUTER ORGANIZATION	PP	100	40	40	P	
06. PROGRAMMING LABORATORY	TW	25	10	18	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	30	P	
07. PROGRAMMING LABORATORY	PR	50	20	15	F	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	12	F	
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	17	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	32	P	
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	27	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	05	F	
10. SOFT SKILLS	TW	50	20	30	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	28	P	
						21. DATA STRUCTURES LABORATORY	PR	50	20	03	F	

GRAND TOTAL = 737/1500, RESULT: FAILS

ORDN. 1 MARKS :

S8054331	SHINDE SHWETA SANJAY				SNEHA		, 71101007C			PICT		S8054331
01. DISCRETE STRUCTURES	PP	100	40	50	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	60	P	
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	71	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	42	P	
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	63	P C	13. DATA STRUCTURES	PP	100	40	71	P	
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	82	P C	14. COMPUTER GRAPHICS	PP	100	40	63	P	
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	55	P C	15. COMPUTER ORGANIZATION	PP	100	40	54	P	
06. PROGRAMMING LABORATORY	TW	25	10	19	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	39	P	
07. PROGRAMMING LABORATORY	PR	50	20	32	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	36	P	
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	18	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	38	P	
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	44	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	28	P	
10. SOFT SKILLS	TW	50	20	36	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	40	P	
						21. DATA STRUCTURES LABORATORY	PR	50	20	33	P	

GRAND TOTAL = 974/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

S8054332	SHINDE VIRENDRA BIJWANT				SEEMA		, 71101008M			PICT		S8054332
01. DISCRETE STRUCTURES	PP	100	40	42	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	53	P	
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	62	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	41	P	
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	65	P C	13. DATA STRUCTURES	PP	100	40	68	P	
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	54	P C	14. COMPUTER GRAPHICS	PP	100	40	57	P	
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	50	P C	15. COMPUTER ORGANIZATION	PP	100	40	47	P	
06. PROGRAMMING LABORATORY	TW	25	10	23	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	40	P	
07. PROGRAMMING LABORATORY	PR	50	20	35	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	34	P	
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	23	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	45	P	
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	39	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	33	P	
10. SOFT SKILLS	TW	50	20	40	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	44	P	
						21. DATA STRUCTURES LABORATORY	PR	50	20	43	P	

GRAND TOTAL = 938/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8054333	SHIVANGI BOHRA				MONA			, 71101012K			PICT		S8054333
01. DISCRETE STRUCTURES	PP	100	40	52	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	40	P		
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	53	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	24	F		
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	58	P C	13. DATA STRUCTURES	PP	100	40	66	P		
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	53	P C	14. COMPUTER GRAPHICS	PP	100	40	49	P		
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	46	P C	15. COMPUTER ORGANIZATION	PP	100	40	41	P		
06. PROGRAMMING LABORATORY	TW	25	10	18	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	40	P		
07. PROGRAMMING LABORATORY	PR	50	20	21	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	06	F		
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	18	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	38	P		
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	36	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	27	P		
10. SOFT SKILLS	TW	50	20	31	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	33	P		
						21. DATA STRUCTURES LABORATORY	PR	50	20	21	P		

GRAND TOTAL = 771/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

S8054334	SOLANKI GAURAV NANAJI				SHOBHA			, 71101018J			PICT		S8054334
01. DISCRETE STRUCTURES	PP	100	40	44	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	47	P		
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	41	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	24	F		
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	59	P C	13. DATA STRUCTURES	PP	100	40	54	P		
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	46	P C	14. COMPUTER GRAPHICS	PP	100	40	48	P		
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	40	P C	15. COMPUTER ORGANIZATION	PP	100	40	43	P		
06. PROGRAMMING LABORATORY	TW	25	10	18	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	30	P		
07. PROGRAMMING LABORATORY	PR	50	20	25	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	12	F		
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	16	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	32	P		
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	37	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	26	P		
10. SOFT SKILLS	TW	50	20	33	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	32	P		
						21. DATA STRUCTURES LABORATORY	PR	50	20	25	P		

GRAND TOTAL = 732/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

S8054335	TAKALKAR SIYA HEMANT				VAISHALI			, 71239308M			PICT		S8054335
01. DISCRETE STRUCTURES	PP	100	40	40	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	40	P		
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	69	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	45	P		
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	57	P C	13. DATA STRUCTURES	PP	100	40	68	P		
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	73	P C	14. COMPUTER GRAPHICS	PP	100	40	59	P		
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	49	P C	15. COMPUTER ORGANIZATION	PP	100	40	58	P		
06. PROGRAMMING LABORATORY	TW	25	10	19	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	38	P		
07. PROGRAMMING LABORATORY	PR	50	20	33	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	40	P		
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	20	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	40	P		
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	37	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	34	P		
10. SOFT SKILLS	TW	50	20	34	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	39	P		
						21. DATA STRUCTURES LABORATORY	PR	50	20	30	P		

GRAND TOTAL = 922/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8054336 TARANNUM BI BI RASHEED AHMED SIDDIQUI NAZEMA BEGUM , 71101027H , PICT , S8054336

01. DISCRETE STRUCTURES	PP	100	40	60	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	54	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	57	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	40	P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	61	P C	13. DATA STRUCTURES	PP	100	40	60	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	69	P C	14. COMPUTER GRAPHICS	PP	100	40	63	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	56	P C	15. COMPUTER ORGANIZATION	PP	100	40	48	P
06. PROGRAMMING LABORATORY	TW	25	10	19	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	40	P
07. PROGRAMMING LABORATORY	PR	50	20	38	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	46	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	20	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	40	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	47	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	38	P
10. SOFT SKILLS	TW	50	20	38	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	32	P
						21. DATA STRUCTURES LABORATORY	PR	50	20	35	P

GRAND TOTAL = 961/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

S8054337 TELHARKAR ARVIND DATTATREYA MEENAKSHI , 71101029D , PICT , S8054337

01. DISCRETE STRUCTURES	PP	100	40	65	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	86	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	53	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	40	P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	53	P C	13. DATA STRUCTURES	PP	100	40	61	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	69	P C	14. COMPUTER GRAPHICS	PP	100	40	61	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	50	P C	15. COMPUTER ORGANIZATION	PP	100	40	44	P
06. PROGRAMMING LABORATORY	TW	25	10	22	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	40	P
07. PROGRAMMING LABORATORY	PR	50	20	41	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	36	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	18	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	40	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	29	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	34	P
10. SOFT SKILLS	TW	50	20	34	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	39	P
						21. DATA STRUCTURES LABORATORY	PR	50	20	37	P

GRAND TOTAL = 952/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

S8054338 THIGALE ANIKET SUNIL MADHURI , 71101033B , PICT , S8054338

01. DISCRETE STRUCTURES	PP	100	40	69	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	72	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	62	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	25	F
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	45	P C	13. DATA STRUCTURES	PP	100	40	62	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	68	P C	14. COMPUTER GRAPHICS	PP	100	40	63	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	44	P C	15. COMPUTER ORGANIZATION	PP	100	40	59	P
06. PROGRAMMING LABORATORY	TW	25	10	21	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	43	P
07. PROGRAMMING LABORATORY	PR	50	20	36	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	40	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	20	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	43	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	29	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	40	P
10. SOFT SKILLS	TW	50	20	39	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	42	P
						21. DATA STRUCTURES LABORATORY	PR	50	20	40	P

GRAND TOTAL = 962/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8054339	UPPAL SAHIL RAJIV			JYOTI		, 71101041C			PICT		S8054339
01. DISCRETE STRUCTURES	PP	100	40	79	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	71	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	65	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	40	P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	69	P C	13. DATA STRUCTURES	PP	100	40	66	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	77	P C	14. COMPUTER GRAPHICS	PP	100	40	67	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	53	P C	15. COMPUTER ORGANIZATION	PP	100	40	56	P
06. PROGRAMMING LABORATORY	TW	25	10	23	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	39	P
07. PROGRAMMING LABORATORY	PR	50	20	37	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	43	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	19	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	38	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	30	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	02	F
10. SOFT SKILLS	TW	50	20	39	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	30	P
						21. DATA STRUCTURES LABORATORY	PR	50	20	38	P

GRAND TOTAL = 981/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

S8054340	VARMA NITESH PREMSING			REKHA		, 71239309K			PICT		S8054340
01. DISCRETE STRUCTURES	PP	100	40	50	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	40	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	54	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	40	P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	60	P C	13. DATA STRUCTURES	PP	100	40	65	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	62	P C	14. COMPUTER GRAPHICS	PP	100	40	60	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	55	P C	15. COMPUTER ORGANIZATION	PP	100	40	52	P
06. PROGRAMMING LABORATORY	TW	25	10	22	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	38	P
07. PROGRAMMING LABORATORY	PR	50	20	20	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	25	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	21	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	41	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	30	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	29	P
10. SOFT SKILLS	TW	50	20	34	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	45	P
						21. DATA STRUCTURES LABORATORY	PR	50	20	33	P

GRAND TOTAL = 876/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

S8054341	VIKRAM DATTU			SHOBHA		, 71101049J			PICT		S8054341
01. DISCRETE STRUCTURES	PP	100	40	72	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	84	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	46	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	40	P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	61	P C	13. DATA STRUCTURES	PP	100	40	63	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	72	P C	14. COMPUTER GRAPHICS	PP	100	40	68	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	56	P C	15. COMPUTER ORGANIZATION	PP	100	40	51	P
06. PROGRAMMING LABORATORY	TW	25	10	21	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	38	P
07. PROGRAMMING LABORATORY	PR	50	20	42	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	36	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	22	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	40	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	40	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	33	P
10. SOFT SKILLS	TW	50	20	39	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	40	P
						21. DATA STRUCTURES LABORATORY	PR	50	20	32	P

GRAND TOTAL = 996/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8054342	YADAV PRAKHAR SUNILKUMAR			CHHAYA		, 71101059F			PICT		S8054342
01. DISCRETE STRUCTURES	PP	100	40	40	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	AA	F
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	41	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	AA	F
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	40	P C	13. DATA STRUCTURES	PP	100	40	AA	F
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	AA	F	14. COMPUTER GRAPHICS	PP	100	40	AA	F
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	55	P C	15. COMPUTER ORGANIZATION	PP	100	40	AA	F
06. PROGRAMMING LABORATORY	TW	25	10	15	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	20	P
07. PROGRAMMING LABORATORY	PR	50	20	10	F	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	20	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	13	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	21	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	35	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	01	F
10. SOFT SKILLS	TW	50	20	24	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	20	P
						21. DATA STRUCTURES LABORATORY	PR	50	20	00	F

GRAND TOTAL = 355/1500, RESULT: FAILS

ORDN. 1 MARKS :

S8054343	YARDI JUHI PRAKASH			PRIYA		, 71101060K			PICT		S8054343
01. DISCRETE STRUCTURES	PP	100	40	63	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	89	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	63	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	50	P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	76	P C	13. DATA STRUCTURES	PP	100	40	60	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	75	P C	14. COMPUTER GRAPHICS	PP	100	40	67	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	60	P C	15. COMPUTER ORGANIZATION	PP	100	40	60	P
06. PROGRAMMING LABORATORY	TW	25	10	24	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	43	P
07. PROGRAMMING LABORATORY	PR	50	20	46	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	45	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	24	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	45	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	46	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	38	P
10. SOFT SKILLS	TW	50	20	43	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	46	P
						21. DATA STRUCTURES LABORATORY	PR	50	20	45	P

GRAND TOTAL = 1108/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

S8054344	YEMUL SANDEEP PRAKASH			BHULAXMI		, 71101062F			PICT		S8054344
01. DISCRETE STRUCTURES	PP	100	40	50	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	57	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	55	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	40	P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	55	P C	13. DATA STRUCTURES	PP	100	40	53	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	58	P C	14. COMPUTER GRAPHICS	PP	100	40	53	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	43	P C	15. COMPUTER ORGANIZATION	PP	100	40	53	P
06. PROGRAMMING LABORATORY	TW	25	10	22	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	36	P
07. PROGRAMMING LABORATORY	PR	50	20	35	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	36	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	20	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	37	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	35	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	35	P
10. SOFT SKILLS	TW	50	20	34	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	34	P
						21. DATA STRUCTURES LABORATORY	PR	50	20	35	P

GRAND TOTAL = 876/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8054345	YEREKAR KARISHMA TUKARAM				RANJANA			, 71101064B			PICT		S8054345
01. DISCRETE STRUCTURES	PP	100	40	60	P			11. ENGINEERING MATHEMATICS III	PP	100	40	63	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	47	P C			12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	54	P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	59	P C			13. DATA STRUCTURES	PP	100	40	55	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	61	P C			14. COMPUTER GRAPHICS	PP	100	40	60	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	55	P C			15. COMPUTER ORGANIZATION	PP	100	40	63	P
06. PROGRAMMING LABORATORY	TW	25	10	17	P C			16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	38	P
07. PROGRAMMING LABORATORY	PR	50	20	39	P C			17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	30	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	17	P C			18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	33	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	25	P C			19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	06	F
10. SOFT SKILLS	TW	50	20	46	P C			20. DATA STRUCTURES LABORATORY	TW	50	20	33	P
								21. DATA STRUCTURES LABORATORY	PR	50	20	31	P

GRAND TOTAL = 892/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

S8054346	YEWALE SHUBHANGI KAILAS				MANISHA			, 71101065L			PICT		S8054346
01. DISCRETE STRUCTURES	PP	100	40	40	P			11. ENGINEERING MATHEMATICS III	PP	100	40	17	F
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	41	P C			12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	15	F
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	40	P C			13. DATA STRUCTURES	PP	100	40	40	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	40	P C			14. COMPUTER GRAPHICS	PP	100	40	22	F
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	49	P C			15. COMPUTER ORGANIZATION	PP	100	40	24	F
06. PROGRAMMING LABORATORY	TW	25	10	22	P C			16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	33	P
07. PROGRAMMING LABORATORY	PR	50	20	20	P C			17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	12	F
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	21	P C			18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	39	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	30	P C			19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	04	F
10. SOFT SKILLS	TW	50	20	37	P C			20. DATA STRUCTURES LABORATORY	TW	50	20	36	P
								21. DATA STRUCTURES LABORATORY	PR	50	20	10	F

GRAND TOTAL = 592/1500, RESULT: FAILS

ORDN. 1 MARKS :

S8054352	BHOSALE SHITAL MANOHAR				JAYA			, 71132426D			PICT		S8054352
01. DISCRETE STRUCTURES	PP	100	40	40	P C			11. ENGINEERING MATHEMATICS III	PP	100	40	25	F
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	44	P C			12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	40	P C
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	41	P C			13. DATA STRUCTURES	PP	100	40	40	P C
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	47	P C			14. COMPUTER GRAPHICS	PP	100	40	46	P C
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	40	P C			15. COMPUTER ORGANIZATION	PP	100	40	40	P C
06. PROGRAMMING LABORATORY	TW	25	10	17	P C			16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	39	P C
07. PROGRAMMING LABORATORY	PR	50	20	25	P C			17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	20	P C
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	19	P C			18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	34	P C
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	20	P C			19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	28	P C
10. SOFT SKILLS	TW	50	20	34	P C			20. DATA STRUCTURES LABORATORY	TW	50	20	32	P C
								21. DATA STRUCTURES LABORATORY	PR	50	20	20	P C

GRAND TOTAL = 691/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8054356	DURUGKAR SURAJ ANIL				NIRMALA		, 71045424E		, PICT		, S8054356			
01. DISCRETE STRUCTURES	PP	100	40	61	P	C	11. ENGINEERING MATHEMATICS III	PP	100	40	40	P	C	
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	45	P	C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	08	F		
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	44	P	C	13. DATA STRUCTURES	PP	100	40	40	P	C	
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	70	P	C	14. COMPUTER GRAPHICS	PP	100	40	58	P	C	
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	40	P	C	15. COMPUTER ORGANIZATION	PP	100	40	46	P	C	
06. PROGRAMMING LABORATORY	TW	25	10	11	P	C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	22	P	C	
07. PROGRAMMING LABORATORY	PR	50	20	34	P	C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	26	P	C	
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	14	P	C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	21	P	C	
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	20	P	C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	24	P		
10. SOFT SKILLS	TW	50	20	26	P	C	20. DATA STRUCTURES LABORATORY	TW	50	20	21	P	C	
							21. DATA STRUCTURES LABORATORY	PR	50	20	20	P		

GRAND TOTAL = 691/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

S8054357	GADODIA ANUSHREE RAJENDRA					GEETA					, 71072126K					, PICT					, S8054357				
01. DISCRETE STRUCTURES	PP	100	40	40	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	40	P C														
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	40	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	42	P C														
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	49	P C	13. DATA STRUCTURES	PP	100	40	49	P C														
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	43	P C	14. COMPUTER GRAPHICS	PP	100	40	49	P C														
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	43	P C	15. COMPUTER ORGANIZATION	PP	100	40	41	P C														
06. PROGRAMMING LABORATORY	TW	25	10	19	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	36	P C														
07. PROGRAMMING LABORATORY	PR	50	20	30	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	28	P C														
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	19	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	33	P C														
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	29	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	20	P C														
10. SOFT SKILLS	TW	50	20	42	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	32	P C														
						21. DATA STRUCTURES LABORATORY	PR	50	20	23	P														

GRAND TOTAL = 747+03/1500, RESULT: SECOND CLASS [0.2]

ORDN. 1 MARKS :

S8054358	GAIKWAD MAHESH EKNATH					LATA	, 71045431H	,	,	PICT	, S8054358
01. DISCRETE STRUCTURES	PP	100	40	40	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	40	P C
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	40	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	40	P C
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	47	P C	13. DATA STRUCTURES	PP	100	40	45	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	42	P C	14. COMPUTER GRAPHICS	PP	100	40	46	P C
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	40	P C	15. COMPUTER ORGANIZATION	PP	100	40	40	P C
06. PROGRAMMING LABORATORY	TW	25	10	14	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	27	P C
07. PROGRAMMING LABORATORY	PR	50	20	26	P	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	35	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	16	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	26	P C
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	20	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	22	P C
10. SOFT SKILLS	TW	50	20	31	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	26	P C
						21. DATA STRUCTURES LABORATORY	PR	50	20	25	P C

GRAND TOTAL = 688/1500, RESULT: PASS CLASS

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8054359	GOKHALE GAURAV SUHAS				SWATI			, 71132428L			PICT		S8054359
01. DISCRETE STRUCTURES	PP	100	40	40	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	40	P C		
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	48	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	42	P C		
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	40	P C	13. DATA STRUCTURES	PP	100	40	51	P		
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	66	P C	14. COMPUTER GRAPHICS	PP	100	40	57	P C		
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	58	P C	15. COMPUTER ORGANIZATION	PP	100	40	50	P C		
06. PROGRAMMING LABORATORY	TW	25	10	16	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	24	P C		
07. PROGRAMMING LABORATORY	PR	50	20	22	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	22	P C		
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	18	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	20	P C		
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	27	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	20	P C		
10. SOFT SKILLS	TW	50	20	29	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	20	P C		
						21. DATA STRUCTURES LABORATORY	PR	50	20	27	P C		

GRAND TOTAL = 737/1500, RESULT: PASS CLASS

ORDN. 1 MARKS :

S8054361	JADHAV PRASHANT UTTAM				ANITA			, 71045454G			PICT		S8054361
01. DISCRETE STRUCTURES	PP	100	40	40	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	40	P C		
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	40	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	40	P		
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	40	P C	13. DATA STRUCTURES	PP	100	40	40	P C		
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	53	P C	14. COMPUTER GRAPHICS	PP	100	40	49	P C		
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	40	P C	15. COMPUTER ORGANIZATION	PP	100	40	40	P C		
06. PROGRAMMING LABORATORY	TW	25	10	13	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	28	P C		
07. PROGRAMMING LABORATORY	PR	50	20	35	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	20	P C		
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	11	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	22	P C		
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	21	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	26	P		
10. SOFT SKILLS	TW	50	20	30	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	21	P C		
						21. DATA STRUCTURES LABORATORY	PR	50	20	33	P C		

GRAND TOTAL = 682/1500, RESULT: PASS CLASS

ORDN. 1 MARKS :

S8054364	JAMES DADO				ANIA			, 71045460M			PICT		S8054364
01. DISCRETE STRUCTURES	PP	100	40	40	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	40	P C		
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	40	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	40	P C		
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	57	P C	13. DATA STRUCTURES	PP	100	40	47	P C		
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	65	P C	14. COMPUTER GRAPHICS	PP	100	40	53	P C		
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	64	P C	15. COMPUTER ORGANIZATION	PP	100	40	47	P C		
06. PROGRAMMING LABORATORY	TW	25	10	16	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	28	P C		
07. PROGRAMMING LABORATORY	PR	50	20	25	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	26	P C		
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	16	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	26	P C		
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	24	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	28	P C		
10. SOFT SKILLS	TW	50	20	37	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	22	P C		
						21. DATA STRUCTURES LABORATORY	PR	50	20	24	P		

GRAND TOTAL = 765/1500, RESULT: SECOND CLASS

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8054367	KARAD NEHA DASHRATH				USHA				, 71045471G				PICT		S8054367
01. DISCRETE STRUCTURES	PP	100	40	66	P C				11. ENGINEERING MATHEMATICS III	PP	100	40	50	P	
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	46	P C				12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	40	P C	
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	60	P C				13. DATA STRUCTURES	PP	100	40	42	P C	
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	57	P C				14. COMPUTER GRAPHICS	PP	100	40	49	P C	
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	40	P C				15. COMPUTER ORGANIZATION	PP	100	40	50	P C	
06. PROGRAMMING LABORATORY	TW	25	10	19	P C				16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	33	P C	
07. PROGRAMMING LABORATORY	PR	50	20	38	P C				17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	33	P C	
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	20	P C				18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	29	P C	
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	21	P C				19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	21	P C	
10. SOFT SKILLS	TW	50	20	38	P C				20. DATA STRUCTURES LABORATORY	TW	50	20	26	P C	
									21. DATA STRUCTURES LABORATORY	PR	50	20	34	P	

GRAND TOTAL = 812/1500, RESULT: SECOND CLASS

RESULT RESERVED FOR BKL

ORDN. 1 MARKS :

S8054368	KARANDE RAHUL MADAN					SUNITA					, 71045472E , , PICT , S8054368				
01. DISCRETE STRUCTURES	PP	100	40	49	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	40	P C				
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	40	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	09	F				
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	54	P C	13. DATA STRUCTURES	PP	100	40	42	P				
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	44	P C	14. COMPUTER GRAPHICS	PP	100	40	60	P C				
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	40	P C	15. COMPUTER ORGANIZATION	PP	100	40	41	P C				
06. PROGRAMMING LABORATORY	TW	25	10	15	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	24	P C				
07. PROGRAMMING LABORATORY	PR	50	20	26	P	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	35	P				
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	16	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	30	P C				
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	24	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	21	P				
10. SOFT SKILLS	TW	50	20	35	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	30	P C				
						21. DATA STRUCTURES LABORATORY	PR	50	20	27	P				

GRAND TOTAL = 702/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

S8054372	KHALID RAZA KHAN					AYESHA					, 70925473D					, PICT					, S8054372				
01. DISCRETE STRUCTURES	PP	100	40	40	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	09	F														
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	40	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	15	F														
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	40	P	13. DATA STRUCTURES	PP	100	40	29	F														
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	40	P C	14. COMPUTER GRAPHICS	PP	100	40	43	P														
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	42	P	15. COMPUTER ORGANIZATION	PP	100	40	40	P														
06. PROGRAMMING LABORATORY	TW	25	10	10	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	20	P														
07. PROGRAMMING LABORATORY	PR	50	20	25	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	07	F														
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	10	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	20	P														
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	23	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	02	F														
10. SOFT SKILLS	TW	50	20	20	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	20	P														
						21. DATA STRUCTURES LABORATORY	PR	50	20	02	F														

GRAND TOTAL = 497/1500, RESULT: FAILS

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8054375	KUNTALWAD GNYANESH GAJJARAM				SARASWATHI			, 71045501B			PICT		S8054375
01. DISCRETE STRUCTURES	PP	100	40	40	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	40	P C		
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	51	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	40	P C		
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	48	P C	13. DATA STRUCTURES	PP	100	40	41	P C		
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	40	P C	14. COMPUTER GRAPHICS	PP	100	40	57	P C		
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	41	P C	15. COMPUTER ORGANIZATION	PP	100	40	40	P C		
06. PROGRAMMING LABORATORY	TW	25	10	16	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	22	P C		
07. PROGRAMMING LABORATORY	PR	50	20	20	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	23	P C		
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	14	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	25	P C		
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	30	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	21	P		
10. SOFT SKILLS	TW	50	20	38	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	34	P C		
						21. DATA STRUCTURES LABORATORY	PR	50	20	12	F		

GRAND TOTAL = 693/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

S8054376	LALHMANGAIHA				NGURTHANKIMI			, 71045503J			PICT		S8054376
01. DISCRETE STRUCTURES	PP	100	40	40	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	AA	F		
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	AA	F	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	AA	F		
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	40	P C	13. DATA STRUCTURES	PP	100	40	AA	F		
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	AA	F	14. COMPUTER GRAPHICS	PP	100	40	AA	F		
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	45	P C	15. COMPUTER ORGANIZATION	PP	100	40	AA	F		
06. PROGRAMMING LABORATORY	TW	25	10	14	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	20	P C		
07. PROGRAMMING LABORATORY	PR	50	20	26	P	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	AA	F		
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	10	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	20	P C		
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	20	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	02	F		
10. SOFT SKILLS	TW	50	20	24	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	20	P C		
						21. DATA STRUCTURES LABORATORY	PR	50	20	AA	F		

GRAND TOTAL = 281/1500, RESULT: FAILS

ORDN. 1 MARKS :

S8054380	NAJAN KAUSTUBH GORAKSHANATH				MEERA			, 71045540C			PICT		S8054380
01. DISCRETE STRUCTURES	PP	100	40	42	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	54	P C		
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	40	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	40	P		
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	47	P C	13. DATA STRUCTURES	PP	100	40	40	P C		
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	48	P C	14. COMPUTER GRAPHICS	PP	100	40	41	P C		
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	42	P C	15. COMPUTER ORGANIZATION	PP	100	40	40	P C		
06. PROGRAMMING LABORATORY	TW	25	10	17	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	41	P C		
07. PROGRAMMING LABORATORY	PR	50	20	30	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	30	P C		
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	18	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	40	P C		
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	20	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	36	P C		
10. SOFT SKILLS	TW	50	20	34	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	35	P C		
						21. DATA STRUCTURES LABORATORY	PR	50	20	32	P C		

GRAND TOTAL = 767/1500, RESULT: SECOND CLASS

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8054382	PANDYA PARTH VIPUL				ASHA		, 71045553E		, PICT		, S8054382			
01. DISCRETE STRUCTURES	PP	100	40	51	P	C	11. ENGINEERING MATHEMATICS III	PP	100	40	40	P	C	
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	40	P	C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	40	P	C	
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	40	P	C	13. DATA STRUCTURES	PP	100	40	40	P	C	
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	46	P	C	14. COMPUTER GRAPHICS	PP	100	40	58	P	C	
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	49	P	C	15. COMPUTER ORGANIZATION	PP	100	40	50	P	C	
06. PROGRAMMING LABORATORY	TW	25	10	19	P	C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	33	P	C	
07. PROGRAMMING LABORATORY	PR	50	20	22	P	C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	20	P	C	
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	16	P	C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	27	P	C	
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	21	P	C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	21	P	C	
10. SOFT SKILLS	TW	50	20	34	P	C	20. DATA STRUCTURES LABORATORY	TW	50	20	33	P	C	
							21. DATA STRUCTURES LABORATORY	PR	50	20	20	P		

GRAND TOTAL = 720/1500, RESULT: PASS CLASS

ORDN. 1 MARKS :

S8054386	POORVI ARVIND DHARWAD				ROHINI		, 70925566H			PICT		S8054386
01. DISCRETE STRUCTURES	PP	100	40	40	P C		11. ENGINEERING MATHEMATICS III	PP	100	40	44	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	40	P C		12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	40	P C
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	40	P C		13. DATA STRUCTURES	PP	100	40	40	P C
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	41	P C		14. COMPUTER GRAPHICS	PP	100	40	40	P C
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	40	P C		15. COMPUTER ORGANIZATION	PP	100	40	41	P C
06. PROGRAMMING LABORATORY	TW	25	10	14	P C		16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	23	P C
07. PROGRAMMING LABORATORY	PR	50	20	28	P C		17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	24	P C
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	19	P C		18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	25	P C
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	26	P C		19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	30	P C
10. SOFT SKILLS	TW	50	20	32	P C		20. DATA STRUCTURES LABORATORY	TW	50	20	22	P C
							21. DATA STRUCTURES LABORATORY	PR	50	20	12	F

GRAND TOTAL = 661/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

S8054387	RAWALE SUHAS SUDAMRAO	RATNMALA					, 71045588H	,	,	PICT	, S8054387				
01. DISCRETE STRUCTURES	PP	100	40	49	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	63	P				
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	52	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	40	P C				
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	54	P C	13. DATA STRUCTURES	PP	100	40	49	P C				
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	53	P C	14. COMPUTER GRAPHICS	PP	100	40	40	P C				
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	40	P C	15. COMPUTER ORGANIZATION	PP	100	40	44	P C				
06. PROGRAMMING LABORATORY	TW	25	10	20	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	32	P C				
07. PROGRAMMING LABORATORY	PR	50	20	24	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	29	P C				
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	19	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	32	P C				
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	22	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	28	P C				
10. SOFT SKILLS	TW	50	20	34	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	27	P C				
						21. DATA STRUCTURES LABORATORY	PR	50	20	32	P C				

GRAND TOTAL = 783/1500, RESULT: SECOND CLASS

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8054389	SANDEEP AGARWAL				RITA				, 70925587L				PICT		, S8054389
01. DISCRETE STRUCTURES	PP	100	40	59	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	53	P C				
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	40	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	27	F				
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	47	P C	13. DATA STRUCTURES	PP	100	40	43	P C				
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	40	P C	14. COMPUTER GRAPHICS	PP	100	40	40	P				
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	52	P C	15. COMPUTER ORGANIZATION	PP	100	40	43	P				
06. PROGRAMMING LABORATORY	TW	25	10	20	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	30	P C				
07. PROGRAMMING LABORATORY	PR	50	20	30	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	24	P C				
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	17	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	32	P C				
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	25	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	20	P C				
10. SOFT SKILLS	TW	50	20	40	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	22	P C				
						21. DATA STRUCTURES LABORATORY	PR	50	20	15	F				

GRAND TOTAL = 719/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

S8054391	SAURAV JAUHARI				POONAM				, 70925593E				PICT		, S8054391
01. DISCRETE STRUCTURES	PP	100	40	AA	F	11. ENGINEERING MATHEMATICS III	PP	100	40	00	F				
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	40	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	11	F				
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	31	F	13. DATA STRUCTURES	PP	100	40	03	F				
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	14	F	14. COMPUTER GRAPHICS	PP	100	40	11	F				
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	47	P	15. COMPUTER ORGANIZATION	PP	100	40	28	F				
06. PROGRAMMING LABORATORY	TW	25	10	13	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	20	P C				
07. PROGRAMMING LABORATORY	PR	50	20	08	F	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	20	P C				
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	10	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	20	P C				
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	22	P	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	20	P				
10. SOFT SKILLS	TW	50	20	26	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	20	P C				
						21. DATA STRUCTURES LABORATORY	PR	50	20	AA	F				

GRAND TOTAL = 364/1500, RESULT: FAILS

ORDN. 1 MARKS :

S8054394	SHINDE AMIT KEDARNATH				SANGEETA				, 71045614L				PICT		, S8054394
01. DISCRETE STRUCTURES	PP	100	40	53	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	40	P C				
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	40	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	27	F				
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	49	P C	13. DATA STRUCTURES	PP	100	40	54	P C				
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	40	P C	14. COMPUTER GRAPHICS	PP	100	40	46	P C				
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	40	P C	15. COMPUTER ORGANIZATION	PP	100	40	54	P C				
06. PROGRAMMING LABORATORY	TW	25	10	17	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	32	P C				
07. PROGRAMMING LABORATORY	PR	50	20	35	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	32	P C				
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	16	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	36	P C				
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	21	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	38	P C				
10. SOFT SKILLS	TW	50	20	34	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	30	P C				
						21. DATA STRUCTURES LABORATORY	PR	50	20	21	P C				

GRAND TOTAL = 755/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8054395	SIDDHARTH BATRA				SANGEETA			, 71045624H			PICT		S8054395
01. DISCRETE STRUCTURES	PP	100	40	71	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	27	F		
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	40	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	15	F		
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	40	P C	13. DATA STRUCTURES	PP	100	40	40	P		
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	48	P C	14. COMPUTER GRAPHICS	PP	100	40	43	P		
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	43	P C	15. COMPUTER ORGANIZATION	PP	100	40	61	P		
06. PROGRAMMING LABORATORY	TW	25	10	14	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	21	P		
07. PROGRAMMING LABORATORY	PR	50	20	24	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	30	P		
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	10	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	24	P		
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	20	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	20	P		
10. SOFT SKILLS	TW	50	20	23	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	26	P		
						21. DATA STRUCTURES LABORATORY	PR	50	20	08	F		

GRAND TOTAL = 648/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

S8054399	THAKARE SAGAR SATISH				JAYASHRI			, 70925632K			PICT		S8054399
01. DISCRETE STRUCTURES	PP	100	40	40	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	40	P		
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	40	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	40	P C		
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	40	P	13. DATA STRUCTURES	PP	100	40	40	P C		
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	40	P C	14. COMPUTER GRAPHICS	PP	100	40	40	P C		
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	48	P C	15. COMPUTER ORGANIZATION	PP	100	40	40	P C		
06. PROGRAMMING LABORATORY	TW	25	10	17	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	32	P C		
07. PROGRAMMING LABORATORY	PR	50	20	20	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	28	P C		
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	19	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	33	P C		
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	21	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	25	P C		
10. SOFT SKILLS	TW	50	20	25	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	28	P C		
						21. DATA STRUCTURES LABORATORY	PR	50	20	06	F		

GRAND TOTAL = 662/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

S8054400	THAKUR DHIRAJ SING UDAYSING				SAROJADEVI			, 71045648E			PICT		S8054400
01. DISCRETE STRUCTURES	PP	100	40	48	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	AA	F		
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	40	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	13	F		
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	40	P C	13. DATA STRUCTURES	PP	100	40	40	P C		
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	45	P C	14. COMPUTER GRAPHICS	PP	100	40	23	F		
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	40	P C	15. COMPUTER ORGANIZATION	PP	100	40	40	P C		
06. PROGRAMMING LABORATORY	TW	25	10	13	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	21	P C		
07. PROGRAMMING LABORATORY	PR	50	20	37	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	35	P		
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	12	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	25	P C		
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	20	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	21	P		
10. SOFT SKILLS	TW	50	20	29	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	28	P C		
						21. DATA STRUCTURES LABORATORY	PR	50	20	02	F		

GRAND TOTAL = 572/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8054401	UIKEY ANURAG VINAYAK			JYOTI		, 70925640L			PICT		S8054401
01. DISCRETE STRUCTURES	PP	100	40	40	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	AA	F
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	40	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	17	F
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	47	P C	13. DATA STRUCTURES	PP	100	40	AA	F
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	40	P C	14. COMPUTER GRAPHICS	PP	100	40	52	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	62	P C	15. COMPUTER ORGANIZATION	PP	100	40	40	P
06. PROGRAMMING LABORATORY	TW	25	10	15	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	24	P C
07. PROGRAMMING LABORATORY	PR	50	20	AA	F	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	20	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	11	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	30	P C
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	24	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	30	P C
10. SOFT SKILLS	TW	50	20	38	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	20	P C
						21. DATA STRUCTURES LABORATORY	PR	50	20	10	F

GRAND TOTAL = 560/1500, RESULT: FAILS A.T.K.T.

RESULT RESERVED FOR BKL

ORDN. 1 MARKS :

S8054403	VAIRAGE ROHIT VISHNU			RAJASHRI		, 70925643E			PICT		S8054403
01. DISCRETE STRUCTURES	PP	100	40	51	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	AA	F
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	40	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	AA	F
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	48	P C	13. DATA STRUCTURES	PP	100	40	41	P C
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	40	P C	14. COMPUTER GRAPHICS	PP	100	40	46	P C
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	55	P C	15. COMPUTER ORGANIZATION	PP	100	40	AA	F
06. PROGRAMMING LABORATORY	TW	25	10	16	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	34	P C
07. PROGRAMMING LABORATORY	PR	50	20	24	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	20	P C
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	16	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	20	P C
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	31	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	AA	F
10. SOFT SKILLS	TW	50	20	40	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	20	P C
						21. DATA STRUCTURES LABORATORY	PR	50	20	AA	F

GRAND TOTAL = 542/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

S8054405	ABNANE OMKAR RAJENDRA			SHOBHA		, 71045358C			PICT		S8054405
01. DISCRETE STRUCTURES	PP	100	40	40	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	42	P C
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	40	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	43	P C
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	40	P C	13. DATA STRUCTURES	PP	100	40	40	P C
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	40	P C	14. COMPUTER GRAPHICS	PP	100	40	57	P C
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	41	P C	15. COMPUTER ORGANIZATION	PP	100	40	45	P C
06. PROGRAMMING LABORATORY	TW	25	10	13	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	23	P C
07. PROGRAMMING LABORATORY	PR	50	20	AA	F	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	AA	F
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	13	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	21	P C
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	23	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	20	P C
10. SOFT SKILLS	TW	50	20	22	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	22	P C
						21. DATA STRUCTURES LABORATORY	PR	50	20	AA	F

GRAND TOTAL = 585/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8054406	SHUBHRANK GUPTA			INDIRA		, 71045621C			PICT		S8054406
01. DISCRETE STRUCTURES	PP	100	40	40	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	AA	F
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	40	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	AA	F
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	40	P	13. DATA STRUCTURES	PP	100	40	40	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	40	P	14. COMPUTER GRAPHICS	PP	100	40	AA	F
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	40	P C	15. COMPUTER ORGANIZATION	PP	100	40	AA	F
06. PROGRAMMING LABORATORY	TW	25	10	13	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	22	P
07. PROGRAMMING LABORATORY	PR	50	20	AA	F	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	28	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	10	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	20	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	AA	F	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	AA	F
10. SOFT SKILLS	TW	50	20	24	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	24	P
						21. DATA STRUCTURES LABORATORY	PR	50	20	27	P

GRAND TOTAL = 408/1500, RESULT: FAILS

RESULT RESERVED FOR BKLG

ORDN. 1 MARKS :

S8054407	KULKARNI VARAD SURESH			TILOTTAMA		, 70818756M			PICT		S8054407
01. DISCRETE STRUCTURES	PP	100	40	AA	F	11. ENGINEERING MATHEMATICS III	PP	100	40	03	F
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	AA	F	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	40	P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	AA	F	13. DATA STRUCTURES	PP	100	40	42	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	AA	F	14. COMPUTER GRAPHICS	PP	100	40	46	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	AA	F	15. COMPUTER ORGANIZATION	PP	100	40	40	P
06. PROGRAMMING LABORATORY	TW	25	10	AA	F	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	42	P
07. PROGRAMMING LABORATORY	PR	50	20	AA	F	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	46	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	AA	F	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	39	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	AA	F	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	40	P
10. SOFT SKILLS	TW	50	20	AA	F	20. DATA STRUCTURES LABORATORY	TW	50	20	40	P
						21. DATA STRUCTURES LABORATORY	PR	50	20	41	P

GRAND TOTAL = 419/1500, RESULT: FAILS

RESULT RESERVED FOR OTHR

ORDN. 1 MARKS :

S8054408	SALUNKE DEVENDRAKUMAR ASHOK			VIJAYA		, 70701614C		S8054352		PICT		S8054408
01. DISCRETE STRUCTURES	PP	100	40	AA	F	11. ENGINEERING MATHEMATICS III	PP	100	40	10	F	
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	AA	F	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	03	F	
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	AA	F	13. DATA STRUCTURES	PP	100	40	46	P	
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	AA	F	14. COMPUTER GRAPHICS	PP	100	40	24	F	
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	AA	F	15. COMPUTER ORGANIZATION	PP	100	40	41	P	
06. PROGRAMMING LABORATORY	TW	25	10	AA	F	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	23	P	
07. PROGRAMMING LABORATORY	PR	50	20	AA	F	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	08	F	
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	AA	F	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	24	P	
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	AA	F	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	02	F	
10. SOFT SKILLS	TW	50	20	AA	F	20. DATA STRUCTURES LABORATORY	TW	50	20	22	P	
						21. DATA STRUCTURES LABORATORY	PR	50	20	AA	F	

GRAND TOTAL = 203/1500, RESULT: FAILS

RESULT RESERVED FOR BKLG + OTHR

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8058501	AAYUSH FUSKELAY				SHASHI		, 71100711L			PICT		S8058501
01. DISCRETE STRUCTURES	PP	100	40	74	P C	11. ENG MATHS III	PP	100	40	54	P	
02. COMPUTER ORGANIZATION	PP	100	40	54	P C	12. COMPUTER GRAPHICS	PP	100	40	62	P	
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	68	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	64	P	
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	58	P C	14. DATA STRUCTURES AND FILES	PP	100	40	49	P	
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	54	P C	15. DATA COMMUNICATION	PP	100	40	46	P	
06. DIGITAL LABORATORY	TW	50	20	33	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	17	P	
07. DIGITAL LABORATORY	PR	50	20	35	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	29	P	
08. PROGRAMMING LABORATORY	TW	50	20	38	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	16	P	
09. PROGRAMMING LABORATORY	PR	50	20	23	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	35	P	
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	39	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	33	P	
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	35	P	

GRAND TOTAL = 916/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

S8058502	ABHISHEK BHATTACHARJEE				MOUSUMI		, 71100713G			PICT		S8058502
01. DISCRETE STRUCTURES	PP	100	40	43	P C	11. ENG MATHS III	PP	100	40	79	P	
02. COMPUTER ORGANIZATION	PP	100	40	67	P C	12. COMPUTER GRAPHICS	PP	100	40	57	P	
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	51	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	60	P	
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	65	P C	14. DATA STRUCTURES AND FILES	PP	100	40	65	P	
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	49	P C	15. DATA COMMUNICATION	PP	100	40	65	P	
06. DIGITAL LABORATORY	TW	50	20	37	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	17	P	
07. DIGITAL LABORATORY	PR	50	20	40	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	33	P	
08. PROGRAMMING LABORATORY	TW	50	20	34	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	19	P	
09. PROGRAMMING LABORATORY	PR	50	20	38	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	40	P	
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	37	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	43	P	
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	30	P	

GRAND TOTAL = 969/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

S8058503	ADEP AKASH RAJENDRA				KALPANA		, 71100717K			PICT		S8058503
01. DISCRETE STRUCTURES	PP	100	40	40	P	11. ENG MATHS III	PP	100	40	26	F	
02. COMPUTER ORGANIZATION	PP	100	40	40	P C	12. COMPUTER GRAPHICS	PP	100	40	57	P	
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	40	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	31	F	
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	40	P C	14. DATA STRUCTURES AND FILES	PP	100	40	40	P	
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	40	P C	15. DATA COMMUNICATION	PP	100	40	28	F	
06. DIGITAL LABORATORY	TW	50	20	31	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	10	P	
07. DIGITAL LABORATORY	PR	50	20	23	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	05	F	
08. PROGRAMMING LABORATORY	TW	50	20	27	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	11	P	
09. PROGRAMMING LABORATORY	PR	50	20	20	P	19. DATA STRUCTURES AND FILES LAB	PR	50	20	38	P	
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	35	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	33	P	
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	32	P	

GRAND TOTAL = 647/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8058504	ADITYA SINGH SOLANKI			DIMPLE		, 71100719F			PICT		S8058504
01. DISCRETE STRUCTURES	PP	100	40	53	P	11. ENG MATHS III	PP	100	40	40	P
02. COMPUTER ORGANIZATION	PP	100	40	51	P C	12. COMPUTER GRAPHICS	PP	100	40	43	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	40	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	40	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	42	P C	14. DATA STRUCTURES AND FILES	PP	100	40	40	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	46	P C	15. DATA COMMUNICATION	PP	100	40	41	P
06. DIGITAL LABORATORY	TW	50	20	30	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	18	P
07. DIGITAL LABORATORY	PR	50	20	30	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	20	P
08. PROGRAMMING LABORATORY	TW	50	20	34	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	17	P
09. PROGRAMMING LABORATORY	PR	50	20	23	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	09	F
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	36	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	36	P
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	10	F

GRAND TOTAL = 699/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

S8058505	AGARWAL RUHI DINESH			RADHA		, 71100720K			PICT		S8058505
01. DISCRETE STRUCTURES	PP	100	40	72	P C	11. ENG MATHS III	PP	100	40	90	P
02. COMPUTER ORGANIZATION	PP	100	40	58	P C	12. COMPUTER GRAPHICS	PP	100	40	64	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	70	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	64	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	62	P C	14. DATA STRUCTURES AND FILES	PP	100	40	79	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	44	P C	15. DATA COMMUNICATION	PP	100	40	68	P
06. DIGITAL LABORATORY	TW	50	20	43	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	24	P
07. DIGITAL LABORATORY	PR	50	20	42	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	42	P
08. PROGRAMMING LABORATORY	TW	50	20	42	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	20	P
09. PROGRAMMING LABORATORY	PR	50	20	40	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	45	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	43	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	46	P
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	40	P

GRAND TOTAL = 1098/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

S8058506	AHERKAR SHRIJEET CHANDRASHEKHAR			MANGALA		, 71100722F			PICT		S8058506
01. DISCRETE STRUCTURES	PP	100	40	43	P C	11. ENG MATHS III	PP	100	40	85	P
02. COMPUTER ORGANIZATION	PP	100	40	42	P C	12. COMPUTER GRAPHICS	PP	100	40	48	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	48	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	49	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	63	P C	14. DATA STRUCTURES AND FILES	PP	100	40	55	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	41	P C	15. DATA COMMUNICATION	PP	100	40	51	P
06. DIGITAL LABORATORY	TW	50	20	35	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	20	P
07. DIGITAL LABORATORY	PR	50	20	37	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	39	P
08. PROGRAMMING LABORATORY	TW	50	20	36	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	19	P
09. PROGRAMMING LABORATORY	PR	50	20	26	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	28	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	41	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	42	P
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	39	P

GRAND TOTAL = 887/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8058507	ALAMWALE SAMEER GAMA				BUDDHO		, 71241751G			PICT		S8058507
01. DISCRETE STRUCTURES	PP	100	40	45	P		11. ENG MATHS III	PP	100	40	27	F
02. COMPUTER ORGANIZATION	PP	100	40	57	P C		12. COMPUTER GRAPHICS	PP	100	40	44	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	42	P C		13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	58	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	61	P C		14. DATA STRUCTURES AND FILES	PP	100	40	26	F
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	45	P C		15. DATA COMMUNICATION	PP	100	40	41	P
06. DIGITAL LABORATORY	TW	50	20	36	P C		16. PROCESSOR INTERFACING LABORATORY	TW	25	10	16	P
07. DIGITAL LABORATORY	PR	50	20	25	P C		17. PROCESSOR INTERFACING LABORATORY	OR	50	20	28	P
08. PROGRAMMING LABORATORY	TW	50	20	36	P C		18. DATA STRUCTURES AND FILES LAB	TW	25	10	17	P
09. PROGRAMMING LABORATORY	PR	50	20	24	P C		19. DATA STRUCTURES AND FILES LAB	PR	50	20	20	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	41	P C		20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	39	P
							21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	40	P

GRAND TOTAL = 768/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

S8058508	ALHAT ANIKETH KANILAL				MANGAL		, 71241752E			PICT		S8058508
01. DISCRETE STRUCTURES	PP	100	40	63	P C		11. ENG MATHS III	PP	100	40	59	P
02. COMPUTER ORGANIZATION	PP	100	40	65	P C		12. COMPUTER GRAPHICS	PP	100	40	70	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	67	P C		13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	63	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	71	P C		14. DATA STRUCTURES AND FILES	PP	100	40	48	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	52	P C		15. DATA COMMUNICATION	PP	100	40	66	P
06. DIGITAL LABORATORY	TW	50	20	43	P C		16. PROCESSOR INTERFACING LABORATORY	TW	25	10	23	P
07. DIGITAL LABORATORY	PR	50	20	40	P C		17. PROCESSOR INTERFACING LABORATORY	OR	50	20	40	P
08. PROGRAMMING LABORATORY	TW	50	20	44	P C		18. DATA STRUCTURES AND FILES LAB	TW	25	10	20	P
09. PROGRAMMING LABORATORY	PR	50	20	28	P C		19. DATA STRUCTURES AND FILES LAB	PR	50	20	43	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	43	P C		20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	46	P
							21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	43	P

GRAND TOTAL = 1037/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

S8058509	ANIL BAJAJ				KAMAL		, 71100729C			PICT		S8058509
01. DISCRETE STRUCTURES	PP	100	40	60	P C		11. ENG MATHS III	PP	100	40	40	P
02. COMPUTER ORGANIZATION	PP	100	40	43	P C		12. COMPUTER GRAPHICS	PP	100	40	57	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	57	P C		13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	48	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	40	P C		14. DATA STRUCTURES AND FILES	PP	100	40	59	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	51	P C		15. DATA COMMUNICATION	PP	100	40	43	P
06. DIGITAL LABORATORY	TW	50	20	36	P C		16. PROCESSOR INTERFACING LABORATORY	TW	25	10	13	P
07. DIGITAL LABORATORY	PR	50	20	33	P C		17. PROCESSOR INTERFACING LABORATORY	OR	50	20	29	P
08. PROGRAMMING LABORATORY	TW	50	20	33	P C		18. DATA STRUCTURES AND FILES LAB	TW	25	10	15	P
09. PROGRAMMING LABORATORY	PR	50	20	37	P		19. DATA STRUCTURES AND FILES LAB	PR	50	20	43	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	36	P C		20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	42	P
							21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	34	P

GRAND TOTAL = 849/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8058510	ANIRUDH SHISHODIA				VIMLESH					, 71100730G				PICT		S8058510
01. DISCRETE STRUCTURES	PP	100	40	68	P C	11. ENG MATHS III	PP	100	40	61	P					
02. COMPUTER ORGANIZATION	PP	100	40	49	P C	12. COMPUTER GRAPHICS	PP	100	40	51	P					
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	54	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	40	P					
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	56	P C	14. DATA STRUCTURES AND FILES	PP	100	40	63	P					
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	43	P C	15. DATA COMMUNICATION	PP	100	40	44	P					
06. DIGITAL LABORATORY	TW	50	20	36	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	21	P					
07. DIGITAL LABORATORY	PR	50	20	34	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	34	P					
08. PROGRAMMING LABORATORY	TW	50	20	41	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	20	P					
09. PROGRAMMING LABORATORY	PR	50	20	22	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	10	F					
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	43	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	42	P					
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	38	P					

GRAND TOTAL = 870/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

S8058511	ANKIT V BANSAL				URMILA					, 71045371L				PICT		S8058511
01. DISCRETE STRUCTURES	PP	100	40	40	P	11. ENG MATHS III	PP	100	40	AA	F					
02. COMPUTER ORGANIZATION	PP	100	40	40	P C	12. COMPUTER GRAPHICS	PP	100	40	40	P					
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	40	P	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	21	F					
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	27	F	14. DATA STRUCTURES AND FILES	PP	100	40	29	F					
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	40	P C	15. DATA COMMUNICATION	PP	100	40	40	P					
06. DIGITAL LABORATORY	TW	50	20	25	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	10	P					
07. DIGITAL LABORATORY	PR	50	20	21	P	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	06	F					
08. PROGRAMMING LABORATORY	TW	50	20	39	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	10	P					
09. PROGRAMMING LABORATORY	PR	50	20	12	F	19. DATA STRUCTURES AND FILES LAB	PR	50	20	AA	F					
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	28	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	30	P					
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	08	F					

GRAND TOTAL = 506/1500, RESULT: FAILS

RESULT RESERVED FOR BKLG

ORDN. 1 MARKS :

S8058512	ANUPAMA TRIVEDI				SANGITA					, 71100732C				PICT		S8058512
01. DISCRETE STRUCTURES	PP	100	40	57	P C	11. ENG MATHS III	PP	100	40	42	P					
02. COMPUTER ORGANIZATION	PP	100	40	65	P C	12. COMPUTER GRAPHICS	PP	100	40	59	P					
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	51	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	64	P					
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	56	P C	14. DATA STRUCTURES AND FILES	PP	100	40	64	P					
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	52	P C	15. DATA COMMUNICATION	PP	100	40	67	P					
06. DIGITAL LABORATORY	TW	50	20	39	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	20	P					
07. DIGITAL LABORATORY	PR	50	20	43	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	28	P					
08. PROGRAMMING LABORATORY	TW	50	20	38	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	18	P					
09. PROGRAMMING LABORATORY	PR	50	20	36	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	38	P					
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	39	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	41	P					
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	42	P					

GRAND TOTAL = 959/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8058513	ANUSHKA GHOGALE				NIVEDITA				, 70925337M				PICT		S8058513
01. DISCRETE STRUCTURES	PP	100	40	13	F	11. ENG MATHS III	PP	100	40	00	F				
02. COMPUTER ORGANIZATION	PP	100	40	40	P C	12. COMPUTER GRAPHICS	PP	100	40	31	F				
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	15	F	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	16	F				
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	40	P	14. DATA STRUCTURES AND FILES	PP	100	40	40	P				
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	40	P C	15. DATA COMMUNICATION	PP	100	40	23	F				
06. DIGITAL LABORATORY	TW	50	20	23	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	16	P				
07. DIGITAL LABORATORY	PR	50	20	32	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	25	P				
08. PROGRAMMING LABORATORY	TW	50	20	27	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	12	P				
09. PROGRAMMING LABORATORY	PR	50	20	20	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	07	F				
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	31	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	34	P				
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	12	F				

GRAND TOTAL = 497/1500, RESULT: FAILS

ORDN. 1 MARKS :

S8058514	ASHUTOSH PANDEY				MAMTA				, 71100733M				PICT		S8058514
01. DISCRETE STRUCTURES	PP	100	40	57	P	11. ENG MATHS III	PP	100	40	40	P				
02. COMPUTER ORGANIZATION	PP	100	40	61	P C	12. COMPUTER GRAPHICS	PP	100	40	59	P				
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	45	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	22	F				
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	59	P C	14. DATA STRUCTURES AND FILES	PP	100	40	40	P				
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	44	P C	15. DATA COMMUNICATION	PP	100	40	40	P				
06. DIGITAL LABORATORY	TW	50	20	34	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	13	P				
07. DIGITAL LABORATORY	PR	50	20	38	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	20	P				
08. PROGRAMMING LABORATORY	TW	50	20	29	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	16	P				
09. PROGRAMMING LABORATORY	PR	50	20	26	P	19. DATA STRUCTURES AND FILES LAB	PR	50	20	35	P				
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	38	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	40	P				
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	30	P				

GRAND TOTAL = 786/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

S8058515	ASHWIN AJAY HABBU				AARTI				, 71100734K				PICT		S8058515
01. DISCRETE STRUCTURES	PP	100	40	56	P C	11. ENG MATHS III	PP	100	40	72	P				
02. COMPUTER ORGANIZATION	PP	100	40	61	P C	12. COMPUTER GRAPHICS	PP	100	40	49	P				
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	58	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	44	P				
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	58	P C	14. DATA STRUCTURES AND FILES	PP	100	40	50	P				
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	50	P C	15. DATA COMMUNICATION	PP	100	40	50	P				
06. DIGITAL LABORATORY	TW	50	20	28	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	15	P				
07. DIGITAL LABORATORY	PR	50	20	32	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	20	P				
08. PROGRAMMING LABORATORY	TW	50	20	35	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	14	P				
09. PROGRAMMING LABORATORY	PR	50	20	38	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	40	P				
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	35	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	30	P				
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	28	P				

GRAND TOTAL = 863/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8058516	AYMAN MUSHTAQ AHMAD				VIKAR UN NISA		, 71100736F			PICT		S8058516
01. DISCRETE STRUCTURES	PP	100	40	40	P C	11. ENG MATHS III	PP	100	40	40	P	
02. COMPUTER ORGANIZATION	PP	100	40	53	P C	12. COMPUTER GRAPHICS	PP	100	40	62	P	
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	57	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	45	P	
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	50	P C	14. DATA STRUCTURES AND FILES	PP	100	40	55	P	
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	50	P C	15. DATA COMMUNICATION	PP	100	40	40	P	
06. DIGITAL LABORATORY	TW	50	20	35	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	14	P	
07. DIGITAL LABORATORY	PR	50	20	36	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	30	P	
08. PROGRAMMING LABORATORY	TW	50	20	29	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	18	P	
09. PROGRAMMING LABORATORY	PR	50	20	40	P	19. DATA STRUCTURES AND FILES LAB	PR	50	20	35	P	
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	39	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	38	P	
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	35	P	

GRAND TOTAL = 841/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

S8058517	BAHETI MAHESH RAJENDRA				CHANDA		, 71100739L			PICT		S8058517
01. DISCRETE STRUCTURES	PP	100	40	54	P C	11. ENG MATHS III	PP	100	40	40	P	
02. COMPUTER ORGANIZATION	PP	100	40	43	P C	12. COMPUTER GRAPHICS	PP	100	40	44	P	
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	54	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	29	F	
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	70	P C	14. DATA STRUCTURES AND FILES	PP	100	40	50	P	
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	40	P C	15. DATA COMMUNICATION	PP	100	40	45	P	
06. DIGITAL LABORATORY	TW	50	20	30	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	21	P	
07. DIGITAL LABORATORY	PR	50	20	43	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	31	P	
08. PROGRAMMING LABORATORY	TW	50	20	46	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	18	P	
09. PROGRAMMING LABORATORY	PR	50	20	45	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	42	P	
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	36	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	41	P	
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	33	P	

GRAND TOTAL = 855/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

S8058518	BHAKKAD MOHIT KISHANLALJI				POOJA		, 71100747M			PICT		S8058518
01. DISCRETE STRUCTURES	PP	100	40	61	P C	11. ENG MATHS III	PP	100	40	50	P	
02. COMPUTER ORGANIZATION	PP	100	40	59	P C	12. COMPUTER GRAPHICS	PP	100	40	59	P	
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	64	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	58	P	
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	79	P C	14. DATA STRUCTURES AND FILES	PP	100	40	70	P	
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	52	P C	15. DATA COMMUNICATION	PP	100	40	51	P	
06. DIGITAL LABORATORY	TW	50	20	41	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	19	P	
07. DIGITAL LABORATORY	PR	50	20	40	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	30	P	
08. PROGRAMMING LABORATORY	TW	50	20	39	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	21	P	
09. PROGRAMMING LABORATORY	PR	50	20	45	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	39	P	
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	43	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	46	P	
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	44	P	

GRAND TOTAL = 1010/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8058519 BHAND PRITI PARAJI RUKHMINI , 71100748K , PICT , S8058519

01. DISCRETE STRUCTURES	PP	100	40	57	P	11. ENG MATHS III	PP	100	40	46	P
02. COMPUTER ORGANIZATION	PP	100	40	47	P C	12. COMPUTER GRAPHICS	PP	100	40	44	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	40	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	25	F
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	45	P C	14. DATA STRUCTURES AND FILES	PP	100	40	61	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	42	P C	15. DATA COMMUNICATION	PP	100	40	42	P
06. DIGITAL LABORATORY	TW	50	20	30	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	17	P
07. DIGITAL LABORATORY	PR	50	20	32	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	20	P
08. PROGRAMMING LABORATORY	TW	50	20	37	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	16	P
09. PROGRAMMING LABORATORY	PR	50	20	20	\$ P	19. DATA STRUCTURES AND FILES LAB	PR	50	20	10	F
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	38	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	33	P
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	30	P

GRAND TOTAL = 732/1500, RESULT: FAILS A.T.K.T. [\$ 0.1]

ORDN. 1 MARKS : (09)2,

S8058520 BHANDWALKAR ONKAR MADHUKAR PRABHAVATI , 71100749H , PICT , S8058520

01. DISCRETE STRUCTURES	PP	100	40	67	P C	11. ENG MATHS III	PP	100	40	58	P
02. COMPUTER ORGANIZATION	PP	100	40	60	P C	12. COMPUTER GRAPHICS	PP	100	40	55	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	65	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	60	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	68	P C	14. DATA STRUCTURES AND FILES	PP	100	40	72	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	42	P C	15. DATA COMMUNICATION	PP	100	40	57	P
06. DIGITAL LABORATORY	TW	50	20	41	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	20	P
07. DIGITAL LABORATORY	PR	50	20	40	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	42	P
08. PROGRAMMING LABORATORY	TW	50	20	34	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	18	P
09. PROGRAMMING LABORATORY	PR	50	20	33	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	35	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	44	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	43	P
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	43	P

GRAND TOTAL = 997/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

S8058521 BHOIR KIRTI RAMCHANDRA ROHINI , 71241753C , PICT , S8058521

01. DISCRETE STRUCTURES	PP	100	40	40	P C	11. ENG MATHS III	PP	100	40	40	P
02. COMPUTER ORGANIZATION	PP	100	40	56	P C	12. COMPUTER GRAPHICS	PP	100	40	56	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	66	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	56	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	50	P C	14. DATA STRUCTURES AND FILES	PP	100	40	50	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	46	P C	15. DATA COMMUNICATION	PP	100	40	68	P
06. DIGITAL LABORATORY	TW	50	20	40	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	19	P
07. DIGITAL LABORATORY	PR	50	20	40	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	25	P
08. PROGRAMMING LABORATORY	TW	50	20	42	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	22	P
09. PROGRAMMING LABORATORY	PR	50	20	22	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	20	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	45	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	45	P
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	15	# P

GRAND TOTAL = 863/1500, RESULT: HIGHER SECOND CLASS # [0.4]

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8058522	BORHADE BHUSHAN MALHARI			SUREKHA		, 71241754M			PICT		S8058522
01. DISCRETE STRUCTURES	PP	100	40	40	P C	11. ENG MATHS III	PP	100	40	40	P
02. COMPUTER ORGANIZATION	PP	100	40	66	P C	12. COMPUTER GRAPHICS	PP	100	40	65	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	40	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	62	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	71	P C	14. DATA STRUCTURES AND FILES	PP	100	40	69	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	50	P C	15. DATA COMMUNICATION	PP	100	40	56	P
06. DIGITAL LABORATORY	TW	50	20	37	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	21	P
07. DIGITAL LABORATORY	PR	50	20	40	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	33	P
08. PROGRAMMING LABORATORY	TW	50	20	39	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	21	P
09. PROGRAMMING LABORATORY	PR	50	20	35	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	32	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	42	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	45	P
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	43	P

GRAND TOTAL = 947/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

S8058523	BOTHARA PRIYANKA RAMESH			VIJAYA		, 71100755B			PICT		S8058523
01. DISCRETE STRUCTURES	PP	100	40	60	P C	11. ENG MATHS III	PP	100	40	80	P
02. COMPUTER ORGANIZATION	PP	100	40	67	P C	12. COMPUTER GRAPHICS	PP	100	40	74	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	74	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	61	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	75	P C	14. DATA STRUCTURES AND FILES	PP	100	40	65	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	44	P C	15. DATA COMMUNICATION	PP	100	40	66	P
06. DIGITAL LABORATORY	TW	50	20	40	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	21	P
07. DIGITAL LABORATORY	PR	50	20	42	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	41	P
08. PROGRAMMING LABORATORY	TW	50	20	45	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	18	P
09. PROGRAMMING LABORATORY	PR	50	20	44	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	42	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	45	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	39	P
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	46	P

GRAND TOTAL = 1089/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

S8058524	BOTHRA PAYAL SUMATILAL			SANDHYA		, 71100756L			PICT		S8058524
01. DISCRETE STRUCTURES	PP	100	40	63	P C	11. ENG MATHS III	PP	100	40	81	P
02. COMPUTER ORGANIZATION	PP	100	40	61	P C	12. COMPUTER GRAPHICS	PP	100	40	55	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	86	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	46	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	72	P C	14. DATA STRUCTURES AND FILES	PP	100	40	62	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	40	P C	15. DATA COMMUNICATION	PP	100	40	58	P
06. DIGITAL LABORATORY	TW	50	20	36	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	21	P
07. DIGITAL LABORATORY	PR	50	20	42	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	43	P
08. PROGRAMMING LABORATORY	TW	50	20	29	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	20	P
09. PROGRAMMING LABORATORY	PR	50	20	40	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	38	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	43	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	42	P
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	37	P

GRAND TOTAL = 1015/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8058525	BURSE RASHMI KIRAN			MADHURI		, 71100758G			PICT		S8058525
01. DISCRETE STRUCTURES	PP	100	40	84	P C	11. ENG MATHS III	PP	100	40	79	P
02. COMPUTER ORGANIZATION	PP	100	40	59	P C	12. COMPUTER GRAPHICS	PP	100	40	71	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	67	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	55	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	74	P C	14. DATA STRUCTURES AND FILES	PP	100	40	65	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	50	P C	15. DATA COMMUNICATION	PP	100	40	56	P
06. DIGITAL LABORATORY	TW	50	20	41	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	20	P
07. DIGITAL LABORATORY	PR	50	20	41	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	34	P
08. PROGRAMMING LABORATORY	TW	50	20	39	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	17	P
09. PROGRAMMING LABORATORY	PR	50	20	33	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	32	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	43	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	42	P
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	39	P

GRAND TOTAL = 1041/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

S8058526	CHANDAN PRITI DILIP			MALATI		, 71241755K			PICT		S8058526
01. DISCRETE STRUCTURES	PP	100	40	47	P C	11. ENG MATHS III	PP	100	40	46	P
02. COMPUTER ORGANIZATION	PP	100	40	58	P C	12. COMPUTER GRAPHICS	PP	100	40	54	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	43	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	58	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	63	P C	14. DATA STRUCTURES AND FILES	PP	100	40	51	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	50	P C	15. DATA COMMUNICATION	PP	100	40	57	P
06. DIGITAL LABORATORY	TW	50	20	42	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	23	P
07. DIGITAL LABORATORY	PR	50	20	39	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	39	P
08. PROGRAMMING LABORATORY	TW	50	20	45	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	19	P
09. PROGRAMMING LABORATORY	PR	50	20	38	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	37	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	43	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	40	P
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	45	P

GRAND TOTAL = 937/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

S8058527	CHAUDHARI PRANITA RAVISHANKAR			MEENA		, 71100764M			PICT		S8058527
01. DISCRETE STRUCTURES	PP	100	40	45	P	11. ENG MATHS III	PP	100	40	40	P
02. COMPUTER ORGANIZATION	PP	100	40	40	P C	12. COMPUTER GRAPHICS	PP	100	40	45	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	49	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	32	F
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	44	P C	14. DATA STRUCTURES AND FILES	PP	100	40	32	F
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	40	P	15. DATA COMMUNICATION	PP	100	40	20	F
06. DIGITAL LABORATORY	TW	50	20	36	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	14	P
07. DIGITAL LABORATORY	PR	50	20	35	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	AA	F
08. PROGRAMMING LABORATORY	TW	50	20	37	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	10	P
09. PROGRAMMING LABORATORY	PR	50	20	26	P	19. DATA STRUCTURES AND FILES LAB	PR	50	20	20	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	37	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	36	P
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	35	P

GRAND TOTAL = 673/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8058528	CHAVAN SHRIKANT SHIVAJI				MALAN			, 71241756H				PICT		S8058528
01. DISCRETE STRUCTURES	PP	100	40	53	P C	11. ENG MATHS III	PP	100	40	54	P			
02. COMPUTER ORGANIZATION	PP	100	40	50	P C	12. COMPUTER GRAPHICS	PP	100	40	54	P			
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	59	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	66	P			
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	71	P C	14. DATA STRUCTURES AND FILES	PP	100	40	62	P			
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	44	P C	15. DATA COMMUNICATION	PP	100	40	60	P			
06. DIGITAL LABORATORY	TW	50	20	36	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	21	P			
07. DIGITAL LABORATORY	PR	50	20	32	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	37	P			
08. PROGRAMMING LABORATORY	TW	50	20	34	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	19	P			
09. PROGRAMMING LABORATORY	PR	50	20	20	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	42	P			
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	40	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	40	P			
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	40	P			

GRAND TOTAL = 934/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

S8058529	CHOKHAR PRITHVIRAJ BALASAHEB				VIMAL			, 71100770F				PICT		S8058529
01. DISCRETE STRUCTURES	PP	100	40	23	F	11. ENG MATHS III	PP	100	40	05	F			
02. COMPUTER ORGANIZATION	PP	100	40	22	F	12. COMPUTER GRAPHICS	PP	100	40	19	F			
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	25	F	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	16	F			
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	40	P	14. DATA STRUCTURES AND FILES	PP	100	40	40	P			
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	40	P C	15. DATA COMMUNICATION	PP	100	40	17	F			
06. DIGITAL LABORATORY	TW	50	20	23	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	15	P			
07. DIGITAL LABORATORY	PR	50	20	22	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	05	F			
08. PROGRAMMING LABORATORY	TW	50	20	29	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	15	P			
09. PROGRAMMING LABORATORY	PR	50	20	30	P	19. DATA STRUCTURES AND FILES LAB	PR	50	20	09	F			
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	31	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	29	P			
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	10	F			

GRAND TOTAL = 465/1500, RESULT: FAILS

ORDN. 1 MARKS :

RESULT RESERVED FOR BKLG

S8058530	DAHAWAD SUNIL NAMDEO				VANITA			, 71241757F				PICT		S8058530
01. DISCRETE STRUCTURES	PP	100	40	23	F	11. ENG MATHS III	PP	100	40	10	F			
02. COMPUTER ORGANIZATION	PP	100	40	57	P C	12. COMPUTER GRAPHICS	PP	100	40	40	P			
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	40	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	54	P			
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	54	P	14. DATA STRUCTURES AND FILES	PP	100	40	40	P			
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	42	P C	15. DATA COMMUNICATION	PP	100	40	43	P			
06. DIGITAL LABORATORY	TW	50	20	35	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	13	P			
07. DIGITAL LABORATORY	PR	50	20	22	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	06	F			
08. PROGRAMMING LABORATORY	TW	50	20	38	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	15	P			
09. PROGRAMMING LABORATORY	PR	50	20	32	P	19. DATA STRUCTURES AND FILES LAB	PR	50	20	05	F			
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	39	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	35	P			
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	16	F			

GRAND TOTAL = 659/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8058531 DARDA MAYOOR MONISH KALPANA , 71045410E , PICT , S8058531

01. DISCRETE STRUCTURES	PP	100	40	40	P	11. ENG MATHS III	PP	100	40	40	P
02. COMPUTER ORGANIZATION	PP	100	40	41	P C	12. COMPUTER GRAPHICS	PP	100	40	49	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	44	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	40	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	46	P C	14. DATA STRUCTURES AND FILES	PP	100	40	43	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	40	P C	15. DATA COMMUNICATION	PP	100	40	42	P
06. DIGITAL LABORATORY	TW	50	20	23	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	10	P
07. DIGITAL LABORATORY	PR	50	20	40	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	07	F
08. PROGRAMMING LABORATORY	TW	50	20	24	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	10	P
09. PROGRAMMING LABORATORY	PR	50	20	23	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	AA	F
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	27	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	23	P
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	07	F

GRAND TOTAL = 619/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

S8058532 DHANAVE PRATIK BALASAHEB UJJWALLA , 71100783H , PICT , S8058532

01. DISCRETE STRUCTURES	PP	100	40	54	P	11. ENG MATHS III	PP	100	40	43	P
02. COMPUTER ORGANIZATION	PP	100	40	47	P C	12. COMPUTER GRAPHICS	PP	100	40	50	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	54	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	52	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	57	P C	14. DATA STRUCTURES AND FILES	PP	100	40	46	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	42	P C	15. DATA COMMUNICATION	PP	100	40	49	P
06. DIGITAL LABORATORY	TW	50	20	30	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	10	P
07. DIGITAL LABORATORY	PR	50	20	40	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	32	P
08. PROGRAMMING LABORATORY	TW	50	20	30	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	12	P
09. PROGRAMMING LABORATORY	PR	50	20	28	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	20	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	35	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	38	P
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	33	P

GRAND TOTAL = 802/1500, RESULT: SECOND CLASS

ORDN. 1 MARKS :

S8058533 DHAWALE PRACHI PRAMOD MEENA , 71100784F , PICT , S8058533

01. DISCRETE STRUCTURES	PP	100	40	40	P C	11. ENG MATHS III	PP	100	40	55	P
02. COMPUTER ORGANIZATION	PP	100	40	58	P C	12. COMPUTER GRAPHICS	PP	100	40	53	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	74	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	49	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	65	P C	14. DATA STRUCTURES AND FILES	PP	100	40	69	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	46	P C	15. DATA COMMUNICATION	PP	100	40	55	P
06. DIGITAL LABORATORY	TW	50	20	33	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	20	P
07. DIGITAL LABORATORY	PR	50	20	40	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	40	P
08. PROGRAMMING LABORATORY	TW	50	20	38	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	18	P
09. PROGRAMMING LABORATORY	PR	50	20	28	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	32	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	38	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	36	P
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	42	P

GRAND TOTAL = 929/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8058534	DHEPLE ARCHANA HARIBHAU			MEENA			, 71100785D			PICT		S8058534
01. DISCRETE STRUCTURES	PP	100	40	40	P	C	11. ENG MATHS III	PP	100	40	40	P
02. COMPUTER ORGANIZATION	PP	100	40	52	P	C	12. COMPUTER GRAPHICS	PP	100	40	49	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	42	P	C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	51	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	54	P	C	14. DATA STRUCTURES AND FILES	PP	100	40	51	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	53	P	C	15. DATA COMMUNICATION	PP	100	40	47	P
06. DIGITAL LABORATORY	TW	50	20	36	P	C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	15	P
07. DIGITAL LABORATORY	PR	50	20	35	P	C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	22	P
08. PROGRAMMING LABORATORY	TW	50	20	33	P	C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	15	P
09. PROGRAMMING LABORATORY	PR	50	20	22	P	C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	25	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	39	P	C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	36	P
							21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	30	P

GRAND TOTAL = 787/1500, RESULT: SECOND CLASS

ORDN. 1 MARKS :

S8058535	GAIKWAD NETRANJALI SHAMRAO			MOHINI			, 71241758D			PICT		S8058535
01. DISCRETE STRUCTURES	PP	100	40	56	P	C	11. ENG MATHS III	PP	100	40	55	P
02. COMPUTER ORGANIZATION	PP	100	40	63	P	C	12. COMPUTER GRAPHICS	PP	100	40	64	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	74	P	C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	57	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	79	P	C	14. DATA STRUCTURES AND FILES	PP	100	40	67	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	40	P	C	15. DATA COMMUNICATION	PP	100	40	62	P
06. DIGITAL LABORATORY	TW	50	20	40	P	C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	22	P
07. DIGITAL LABORATORY	PR	50	20	43	P	C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	38	P
08. PROGRAMMING LABORATORY	TW	50	20	42	P	C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	19	P
09. PROGRAMMING LABORATORY	PR	50	20	34	P	C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	35	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	42	P	C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	43	P
							21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	41	P

GRAND TOTAL = 1016/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

S8058536	GAIKWAD PIYUSH GHANASHYAM			VASUDHA			, 71100793E			PICT		S8058536
01. DISCRETE STRUCTURES	PP	100	40	46	P	C	11. ENG MATHS III	PP	100	40	40	P
02. COMPUTER ORGANIZATION	PP	100	40	48	P	C	12. COMPUTER GRAPHICS	PP	100	40	49	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	56	P	C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	42	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	51	P	C	14. DATA STRUCTURES AND FILES	PP	100	40	51	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	59	P	C	15. DATA COMMUNICATION	PP	100	40	53	P
06. DIGITAL LABORATORY	TW	50	20	34	P	C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	14	P
07. DIGITAL LABORATORY	PR	50	20	22	P		17. PROCESSOR INTERFACING LABORATORY	OR	50	20	25	P
08. PROGRAMMING LABORATORY	TW	50	20	39	P	C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	15	P
09. PROGRAMMING LABORATORY	PR	50	20	20	P	C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	38	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	40	P	C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	31	P
							21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	38	P

GRAND TOTAL = 811/1500, RESULT: SECOND CLASS

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8058537	GANGARDE MAYURI MUKUND				PRAMILA		, 71100796K			PICT		S8058537
01. DISCRETE STRUCTURES	PP	100	40	53	P C	11. ENG MATHS III	PP	100	40	40	P	
02. COMPUTER ORGANIZATION	PP	100	40	40	P C	12. COMPUTER GRAPHICS	PP	100	40	54	P	
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	55	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	49	P	
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	48	P C	14. DATA STRUCTURES AND FILES	PP	100	40	43	P	
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	59	P C	15. DATA COMMUNICATION	PP	100	40	45	P	
06. DIGITAL LABORATORY	TW	50	20	42	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	16	P	
07. DIGITAL LABORATORY	PR	50	20	40	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	30	P	
08. PROGRAMMING LABORATORY	TW	50	20	39	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	17	P	
09. PROGRAMMING LABORATORY	PR	50	20	30	P	19. DATA STRUCTURES AND FILES LAB	PR	50	20	12	F	
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	44	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	39	P	
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	15	F	

GRAND TOTAL = 810/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

S8058538	GAWANDE ANAGHA ARVIND				MAYA		, 71100800M			PICT		S8058538
01. DISCRETE STRUCTURES	PP	100	40	47	P	11. ENG MATHS III	PP	100	40	23	F	
02. COMPUTER ORGANIZATION	PP	100	40	42	P C	12. COMPUTER GRAPHICS	PP	100	40	53	P	
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	58	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	52	P	
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	43	P C	14. DATA STRUCTURES AND FILES	PP	100	40	53	P	
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	47	P C	15. DATA COMMUNICATION	PP	100	40	51	P	
06. DIGITAL LABORATORY	TW	50	20	33	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	18	P	
07. DIGITAL LABORATORY	PR	50	20	32	P	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	28	P	
08. PROGRAMMING LABORATORY	TW	50	20	38	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	16	P	
09. PROGRAMMING LABORATORY	PR	50	20	23	P	19. DATA STRUCTURES AND FILES LAB	PR	50	20	38	P	
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	39	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	36	P	
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	37	P	

GRAND TOTAL = 807/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

S8058539	GHODAKE PRAJAKTA DHANANJAY				SWATI		, 71100802H			PICT		S8058539
01. DISCRETE STRUCTURES	PP	100	40	49	P	11. ENG MATHS III	PP	100	40	24	F	
02. COMPUTER ORGANIZATION	PP	100	40	47	P C	12. COMPUTER GRAPHICS	PP	100	40	45	P	
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	43	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	40	P	
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	40	P C	14. DATA STRUCTURES AND FILES	PP	100	40	43	P	
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	45	P C	15. DATA COMMUNICATION	PP	100	40	26	F	
06. DIGITAL LABORATORY	TW	50	20	30	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	10	P	
07. DIGITAL LABORATORY	PR	50	20	25	P	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	06	F	
08. PROGRAMMING LABORATORY	TW	50	20	24	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	13	P	
09. PROGRAMMING LABORATORY	PR	50	20	30	P	19. DATA STRUCTURES AND FILES LAB	PR	50	20	20	P	
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	31	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	30	P	
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	10	F	

GRAND TOTAL = 631/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

GRAND TOTAL = 825/1500, RESULT: FAILS A.T.K.T.
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8058543	GORADE TUSHAR BHAUSAHEB			ALKA		, 71100810J			PICT		S8058543
01. DISCRETE STRUCTURES	PP	100	40	40	P	11. ENG MATHS III	PP	100	40	10	F
02. COMPUTER ORGANIZATION	PP	100	40	23	F	12. COMPUTER GRAPHICS	PP	100	40	40	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	42	P	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	31	F
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	46	P	14. DATA STRUCTURES AND FILES	PP	100	40	40	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	40	P	15. DATA COMMUNICATION	PP	100	40	23	F
06. DIGITAL LABORATORY	TW	50	20	32	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	10	P
07. DIGITAL LABORATORY	PR	50	20	24	P	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	05	F
08. PROGRAMMING LABORATORY	TW	50	20	22	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	13	P
09. PROGRAMMING LABORATORY	PR	50	20	28	P	19. DATA STRUCTURES AND FILES LAB	PR	50	20	34	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	29	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	35	P
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	35	P

GRAND TOTAL = 602/1500, RESULT: FAILS

RESULT RESERVED FOR BKLG

ORDN. 1 MARKS :

S8058544	GUJRATHI DHANESH NARESH			JYOTI		, 71100811G			PICT		S8058544
01. DISCRETE STRUCTURES	PP	100	40	52	P C	11. ENG MATHS III	PP	100	40	69	P
02. COMPUTER ORGANIZATION	PP	100	40	51	P C	12. COMPUTER GRAPHICS	PP	100	40	65	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	68	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	58	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	70	P C	14. DATA STRUCTURES AND FILES	PP	100	40	46	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	53	P C	15. DATA COMMUNICATION	PP	100	40	61	P
06. DIGITAL LABORATORY	TW	50	20	39	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	23	P
07. DIGITAL LABORATORY	PR	50	20	38	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	33	P
08. PROGRAMMING LABORATORY	TW	50	20	42	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	20	P
09. PROGRAMMING LABORATORY	PR	50	20	40	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	37	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	44	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	44	P
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	38	P

GRAND TOTAL = 991/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

S8058545	GUTTE ASHWINI ASHOKRAO			SUNANDA		, 71100815K			PICT		S8058545
01. DISCRETE STRUCTURES	PP	100	40	40	P C	11. ENG MATHS III	PP	100	40	60	P
02. COMPUTER ORGANIZATION	PP	100	40	51	P C	12. COMPUTER GRAPHICS	PP	100	40	44	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	66	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	51	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	60	P C	14. DATA STRUCTURES AND FILES	PP	100	40	56	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	52	P C	15. DATA COMMUNICATION	PP	100	40	48	P
06. DIGITAL LABORATORY	TW	50	20	39	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	14	P
07. DIGITAL LABORATORY	PR	50	20	35	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	07	F
08. PROGRAMMING LABORATORY	TW	50	20	33	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	17	P
09. PROGRAMMING LABORATORY	PR	50	20	30	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	30	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	40	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	41	P
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	40	P

GRAND TOTAL = 854/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8058546	HARAL SMITA DATTATRAYA				SARITA				, 71100816H				PICT		S8058546
01. DISCRETE STRUCTURES	PP	100	40	56	P C	11. ENG MATHS III	PP	100	40	77	P				
02. COMPUTER ORGANIZATION	PP	100	40	63	P C	12. COMPUTER GRAPHICS	PP	100	40	67	P				
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	84	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	61	P				
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	73	P C	14. DATA STRUCTURES AND FILES	PP	100	40	57	P				
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	59	P C	15. DATA COMMUNICATION	PP	100	40	65	P				
06. DIGITAL LABORATORY	TW	50	20	41	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	22	P				
07. DIGITAL LABORATORY	PR	50	20	32	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	28	P				
08. PROGRAMMING LABORATORY	TW	50	20	46	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	22	P				
09. PROGRAMMING LABORATORY	PR	50	20	32	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	38	P				
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	46	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	45	P				
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	39	P				

GRAND TOTAL = 1053/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

S8058547	HARKIRPAL SINGH				PARAMJEET KAUR				, 71045444K				PICT		S8058547
01. DISCRETE STRUCTURES	PP	100	40	AA	F	11. ENG MATHS III	PP	100	40	00	F				
02. COMPUTER ORGANIZATION	PP	100	40	01	F	12. COMPUTER GRAPHICS	PP	100	40	AA	F				
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	03	F	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	02	F				
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	AA	F	14. DATA STRUCTURES AND FILES	PP	100	40	03	F				
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	AA	F	15. DATA COMMUNICATION	PP	100	40	AA	F				
06. DIGITAL LABORATORY	TW	50	20	21	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	10	P				
07. DIGITAL LABORATORY	PR	50	20	10	F	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	21	P				
08. PROGRAMMING LABORATORY	TW	50	20	20	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	10	P				
09. PROGRAMMING LABORATORY	PR	50	20	22	P	19. DATA STRUCTURES AND FILES LAB	PR	50	20	02	F				
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	32	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	22	P				
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	00	F				

GRAND TOTAL = 179/1500, RESULT: FAILS

RESULT RESERVED FOR BKLG

ORDN. 1 MARKS :

S8058548	JADHWANI KANCHAN OMPRAKASH				NIRMALAL				, 71100824J				PICT		S8058548
01. DISCRETE STRUCTURES	PP	100	40	60	P C	11. ENG MATHS III	PP	100	40	79	P				
02. COMPUTER ORGANIZATION	PP	100	40	67	P C	12. COMPUTER GRAPHICS	PP	100	40	56	P				
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	66	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	53	P				
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	63	P C	14. DATA STRUCTURES AND FILES	PP	100	40	59	P				
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	63	P C	15. DATA COMMUNICATION	PP	100	40	64	P				
06. DIGITAL LABORATORY	TW	50	20	45	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	17	P				
07. DIGITAL LABORATORY	PR	50	20	39	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	32	P				
08. PROGRAMMING LABORATORY	TW	50	20	40	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	17	P				
09. PROGRAMMING LABORATORY	PR	50	20	42	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	30	P				
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	47	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	43	P				
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	33	P				

GRAND TOTAL = 1015/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8058549	JAIN BHUSHAN SUMATILAL				MANISHA			, 71100826E			PICT		S8058549
01. DISCRETE STRUCTURES	PP	100	40	65	P C	11. ENG MATHS III	PP	100	40	56	P		
02. COMPUTER ORGANIZATION	PP	100	40	60	P C	12. COMPUTER GRAPHICS	PP	100	40	68	P		
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	68	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	58	P		
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	66	P C	14. DATA STRUCTURES AND FILES	PP	100	40	60	P		
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	60	P C	15. DATA COMMUNICATION	PP	100	40	55	P		
06. DIGITAL LABORATORY	TW	50	20	33	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	19	P		
07. DIGITAL LABORATORY	PR	50	20	38	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	39	P		
08. PROGRAMMING LABORATORY	TW	50	20	37	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	17	P		
09. PROGRAMMING LABORATORY	PR	50	20	25	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	38	P		
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	39	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	39	P		
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	37	P		

GRAND TOTAL = 977+05/1500, RESULT: FIRST CLASS[0.163]

ORDN. 1 MARKS :

S8058550	JAIPURIA ROHIT SHARADKUMAR				AMITA			, 71100828M			PICT		S8058550
01. DISCRETE STRUCTURES	PP	100	40	47	P	11. ENG MATHS III	PP	100	40	40	P		
02. COMPUTER ORGANIZATION	PP	100	40	40	P	12. COMPUTER GRAPHICS	PP	100	40	40	P		
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	40	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	40	P		
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	42	P	14. DATA STRUCTURES AND FILES	PP	100	40	43	P		
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	43	P C	15. DATA COMMUNICATION	PP	100	40	40	P		
06. DIGITAL LABORATORY	TW	50	20	33	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	19	P		
07. DIGITAL LABORATORY	PR	50	20	35	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	29	P		
08. PROGRAMMING LABORATORY	TW	50	20	25	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	15	P		
09. PROGRAMMING LABORATORY	PR	50	20	22	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	07	F		
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	33	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	39	P		
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	28	P		

GRAND TOTAL = 700/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

S8058551	JAMBHULKAR SAYALI SUNIL				SHUBHAANGI			, 71100829K			PICT		S8058551
01. DISCRETE STRUCTURES	PP	100	40	71	P C	11. ENG MATHS III	PP	100	40	76	P		
02. COMPUTER ORGANIZATION	PP	100	40	68	P C	12. COMPUTER GRAPHICS	PP	100	40	63	P		
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	78	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	45	P		
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	77	P C	14. DATA STRUCTURES AND FILES	PP	100	40	59	P		
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	66	P C	15. DATA COMMUNICATION	PP	100	40	62	P		
06. DIGITAL LABORATORY	TW	50	20	37	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	23	P		
07. DIGITAL LABORATORY	PR	50	20	40	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	42	P		
08. PROGRAMMING LABORATORY	TW	50	20	41	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	19	P		
09. PROGRAMMING LABORATORY	PR	50	20	20	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	40	P		
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	42	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	39	P		
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	41	P		

GRAND TOTAL = 1049/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8058552	JOSHI AKHIL NIRANJAN			NEHA		, 71100833H			PICT		S8058552
01. DISCRETE STRUCTURES	PP	100	40	40	P C	11. ENG MATHS III	PP	100	40	49	P
02. COMPUTER ORGANIZATION	PP	100	40	48	P C	12. COMPUTER GRAPHICS	PP	100	40	60	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	66	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	49	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	53	P C	14. DATA STRUCTURES AND FILES	PP	100	40	56	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	49	P C	15. DATA COMMUNICATION	PP	100	40	66	P
06. DIGITAL LABORATORY	TW	50	20	33	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	14	P
07. DIGITAL LABORATORY	PR	50	20	40	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	32	P
08. PROGRAMMING LABORATORY	TW	50	20	25	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	17	P
09. PROGRAMMING LABORATORY	PR	50	20	37	P	19. DATA STRUCTURES AND FILES LAB	PR	50	20	35	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	36	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	41	P
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	27	P

GRAND TOTAL = 873/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

S8058553	KABRE AMEYA ATUL			MANGALA		, 71100838J			PICT		S8058553
01. DISCRETE STRUCTURES	PP	100	40	61	P C	11. ENG MATHS III	PP	100	40	56	P
02. COMPUTER ORGANIZATION	PP	100	40	61	P C	12. COMPUTER GRAPHICS	PP	100	40	61	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	65	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	55	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	70	P C	14. DATA STRUCTURES AND FILES	PP	100	40	58	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	58	P C	15. DATA COMMUNICATION	PP	100	40	52	P
06. DIGITAL LABORATORY	TW	50	20	40	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	23	P
07. DIGITAL LABORATORY	PR	50	20	35	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	41	P
08. PROGRAMMING LABORATORY	TW	50	20	43	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	21	P
09. PROGRAMMING LABORATORY	PR	50	20	44	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	45	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	43	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	44	P
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	45	P

GRAND TOTAL = 1021/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

S8058554	KADAM CHIRAG RAM			APSARA		, 71100839G			PICT		S8058554
01. DISCRETE STRUCTURES	PP	100	40	68	P C	11. ENG MATHS III	PP	100	40	80	P
02. COMPUTER ORGANIZATION	PP	100	40	75	P C	12. COMPUTER GRAPHICS	PP	100	40	62	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	77	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	61	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	71	P C	14. DATA STRUCTURES AND FILES	PP	100	40	60	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	55	P C	15. DATA COMMUNICATION	PP	100	40	62	P
06. DIGITAL LABORATORY	TW	50	20	41	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	22	P
07. DIGITAL LABORATORY	PR	50	20	42	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	44	P
08. PROGRAMMING LABORATORY	TW	50	20	42	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	20	P
09. PROGRAMMING LABORATORY	PR	50	20	42	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	43	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	43	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	45	P
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	44	P

GRAND TOTAL = 1099/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8058555	KAHATE SAUMITRA SHYAMKANT			JYOTI			, 71100841J			PICT		S8058555
01. DISCRETE STRUCTURES	PP	100	40	62	P	C	11. ENG MATHS III	PP	100	40	54	P
02. COMPUTER ORGANIZATION	PP	100	40	63	P	C	12. COMPUTER GRAPHICS	PP	100	40	59	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	64	P	C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	55	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	65	P	C	14. DATA STRUCTURES AND FILES	PP	100	40	60	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	62	P	C	15. DATA COMMUNICATION	PP	100	40	60	P
06. DIGITAL LABORATORY	TW	50	20	40	P	C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	21	P
07. DIGITAL LABORATORY	PR	50	20	38	P	C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	40	P
08. PROGRAMMING LABORATORY	TW	50	20	38	P	C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	20	P
09. PROGRAMMING LABORATORY	PR	50	20	22	P	C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	44	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	43	P	C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	41	P
							21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	38	P

GRAND TOTAL = 989/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

S8058557	KAUTHALE TRUPTI SAMPAT			JAYASHRI			, 71100853B			PICT		S8058557
01. DISCRETE STRUCTURES	PP	100	40	48	P	C	11. ENG MATHS III	PP	100	40	40	P
02. COMPUTER ORGANIZATION	PP	100	40	59	P	C	12. COMPUTER GRAPHICS	PP	100	40	47	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	52	P	C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	56	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	61	P	C	14. DATA STRUCTURES AND FILES	PP	100	40	47	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	55	P	C	15. DATA COMMUNICATION	PP	100	40	44	P
06. DIGITAL LABORATORY	TW	50	20	36	P	C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	18	P
07. DIGITAL LABORATORY	PR	50	20	40	P	C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	31	P
08. PROGRAMMING LABORATORY	TW	50	20	35	P	C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	17	P
09. PROGRAMMING LABORATORY	PR	50	20	32	P	C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	32	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	40	P	C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	38	P
							21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	40	P

GRAND TOTAL = 868/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

S8058558	KHADKIWALA HATIM JAINUDDIN			MASUMA			, 71100856G			PICT		S8058558
01. DISCRETE STRUCTURES	PP	100	40	70	P	C	11. ENG MATHS III	PP	100	40	86	P
02. COMPUTER ORGANIZATION	PP	100	40	70	P	C	12. COMPUTER GRAPHICS	PP	100	40	60	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	77	P	C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	46	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	72	P	C	14. DATA STRUCTURES AND FILES	PP	100	40	59	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	55	P	C	15. DATA COMMUNICATION	PP	100	40	52	P
06. DIGITAL LABORATORY	TW	50	20	42	P	C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	23	P
07. DIGITAL LABORATORY	PR	50	20	42	P	C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	41	P
08. PROGRAMMING LABORATORY	TW	50	20	47	P	C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	20	P
09. PROGRAMMING LABORATORY	PR	50	20	42	P	C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	46	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	47	P	C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	43	P
							21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	42	P

GRAND TOTAL = 1082/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8058559	KHADSE MAYUR DILIP				PRAMILA		, 71100857E			PICT		S8058559
01. DISCRETE STRUCTURES	PP	100	40	40	P C	11. ENG MATHS III	PP	100	40	20	F	
02. COMPUTER ORGANIZATION	PP	100	40	40	P C	12. COMPUTER GRAPHICS	PP	100	40	41	P	
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	45	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	41	P	
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	51	P C	14. DATA STRUCTURES AND FILES	PP	100	40	42	P	
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	51	P C	15. DATA COMMUNICATION	PP	100	40	40	P	
06. DIGITAL LABORATORY	TW	50	20	23	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	12	P	
07. DIGITAL LABORATORY	PR	50	20	38	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	26	P	
08. PROGRAMMING LABORATORY	TW	50	20	28	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	11	P	
09. PROGRAMMING LABORATORY	PR	50	20	29	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	27	P	
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	32	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	29	P	
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	25	P	

GRAND TOTAL = 691/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

S8058560	KHAN RUQAIYA IQBAL				SHAHEEN		, 71100860E			PICT		S8058560
01. DISCRETE STRUCTURES	PP	100	40	73	P C	11. ENG MATHS III	PP	100	40	83	P	
02. COMPUTER ORGANIZATION	PP	100	40	58	P C	12. COMPUTER GRAPHICS	PP	100	40	55	P	
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	76	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	70	P	
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	77	P C	14. DATA STRUCTURES AND FILES	PP	100	40	51	P	
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	55	P C	15. DATA COMMUNICATION	PP	100	40	59	P	
06. DIGITAL LABORATORY	TW	50	20	45	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	23	P	
07. DIGITAL LABORATORY	PR	50	20	45	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	42	P	
08. PROGRAMMING LABORATORY	TW	50	20	47	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	22	P	
09. PROGRAMMING LABORATORY	PR	50	20	44	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	46	P	
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	46	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	46	P	
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	40	P	

GRAND TOTAL = 1103/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

S8058561	KODITKAR POOJA NAMDEO				JYOTI		, 71241761D			PICT		S8058561
01. DISCRETE STRUCTURES	PP	100	40	46	P	11. ENG MATHS III	PP	100	40	41	P	
02. COMPUTER ORGANIZATION	PP	100	40	55	P C	12. COMPUTER GRAPHICS	PP	100	40	59	P	
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	62	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	60	P	
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	63	P C	14. DATA STRUCTURES AND FILES	PP	100	40	52	P	
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	59	P C	15. DATA COMMUNICATION	PP	100	40	57	P	
06. DIGITAL LABORATORY	TW	50	20	39	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	21	P	
07. DIGITAL LABORATORY	PR	50	20	40	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	25	P	
08. PROGRAMMING LABORATORY	TW	50	20	41	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	21	P	
09. PROGRAMMING LABORATORY	PR	50	20	20	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	20	P	
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	40	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	43	P	
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	21	P	

GRAND TOTAL = 885/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8058562	KOSHTI ANUJA RAMESH				MANGAL					, 71045485G				PICT			S8058562
01. DISCRETE STRUCTURES	PP	100	40	58	P	11. ENG MATHS III	PP	100	40	16	F						
02. COMPUTER ORGANIZATION	PP	100	40	41	P C	12. COMPUTER GRAPHICS	PP	100	40	27	F						
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	53	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	44	P						
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	50	P C	14. DATA STRUCTURES AND FILES	PP	100	40	49	P						
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	54	P C	15. DATA COMMUNICATION	PP	100	40	40	P						
06. DIGITAL LABORATORY	TW	50	20	34	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	13	P						
07. DIGITAL LABORATORY	PR	50	20	22	P	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	23	P						
08. PROGRAMMING LABORATORY	TW	50	20	37	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	17	P						
09. PROGRAMMING LABORATORY	PR	50	20	35	P	19. DATA STRUCTURES AND FILES LAB	PR	50	20	10	F						
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	37	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	36	P						
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	20	P						

GRAND TOTAL = 716/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

S8058563	KULKARNI MITALI SUHAS				RUPA					, 71100872J				PICT			S8058563
01. DISCRETE STRUCTURES	PP	100	40	47	P C	11. ENG MATHS III	PP	100	40	62	P						
02. COMPUTER ORGANIZATION	PP	100	40	51	P C	12. COMPUTER GRAPHICS	PP	100	40	50	P						
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	54	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	60	P						
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	56	P C	14. DATA STRUCTURES AND FILES	PP	100	40	56	P						
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	47	P C	15. DATA COMMUNICATION	PP	100	40	55	P						
06. DIGITAL LABORATORY	TW	50	20	40	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	19	P						
07. DIGITAL LABORATORY	PR	50	20	40	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	37	P						
08. PROGRAMMING LABORATORY	TW	50	20	37	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	18	P						
09. PROGRAMMING LABORATORY	PR	50	20	32	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	23	P						
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	43	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	38	P						
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	32	P						

GRAND TOTAL = 897+03/1500, RESULT: FIRST CLASS [0.2]

ORDN. 1 MARKS :

S8058564	LAGWANKAR NIKHIL KISHOR				RASHMI					, 71100878H				PICT			S8058564
01. DISCRETE STRUCTURES	PP	100	40	62	P C	11. ENG MATHS III	PP	100	40	68	P						
02. COMPUTER ORGANIZATION	PP	100	40	59	P C	12. COMPUTER GRAPHICS	PP	100	40	59	P						
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	80	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	49	P						
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	70	P C	14. DATA STRUCTURES AND FILES	PP	100	40	62	P						
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	60	P C	15. DATA COMMUNICATION	PP	100	40	54	P						
06. DIGITAL LABORATORY	TW	50	20	46	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	23	P						
07. DIGITAL LABORATORY	PR	50	20	43	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	35	P						
08. PROGRAMMING LABORATORY	TW	50	20	47	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	19	P						
09. PROGRAMMING LABORATORY	PR	50	20	43	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	40	P						
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	47	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	45	P						
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	43	P						

GRAND TOTAL = 1054/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8058565	LUV VARMA				PARVEEN				, 71100884B			PICT		S8058565
01. DISCRETE STRUCTURES	PP	100	40	58	P C	11. ENG MATHS III	PP	100	40	82	P			
02. COMPUTER ORGANIZATION	PP	100	40	75	P C	12. COMPUTER GRAPHICS	PP	100	40	69	P			
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	79	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	75	P			
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	56	P C	14. DATA STRUCTURES AND FILES	PP	100	40	58	P			
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	67	P C	15. DATA COMMUNICATION	PP	100	40	68	P			
06. DIGITAL LABORATORY	TW	50	20	32	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	19	P			
07. DIGITAL LABORATORY	PR	50	20	40	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	42	P			
08. PROGRAMMING LABORATORY	TW	50	20	33	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	20	P			
09. PROGRAMMING LABORATORY	PR	50	20	25	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	38	P			
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	38	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	42	P			
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	34	P			

GRAND TOTAL = 1050/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

S8058566	MADGULWAR AMIT ASHOK				VIJAYA				, 71100886J			PICT		S8058566
01. DISCRETE STRUCTURES	PP	100	40	55	P C	11. ENG MATHS III	PP	100	40	70	P			
02. COMPUTER ORGANIZATION	PP	100	40	60	P C	12. COMPUTER GRAPHICS	PP	100	40	74	P			
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	77	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	59	P			
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	78	P C	14. DATA STRUCTURES AND FILES	PP	100	40	72	P			
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	57	P C	15. DATA COMMUNICATION	PP	100	40	60	P			
06. DIGITAL LABORATORY	TW	50	20	40	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	21	P			
07. DIGITAL LABORATORY	PR	50	20	32	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	35	P			
08. PROGRAMMING LABORATORY	TW	50	20	42	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	21	P			
09. PROGRAMMING LABORATORY	PR	50	20	38	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	43	P			
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	41	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	44	P			
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	36	P			

GRAND TOTAL = 1055/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

S8058567	MAHAJAN DEEPIKA UDDHAV				MALATI				, 71241762B			PICT		S8058567
01. DISCRETE STRUCTURES	PP	100	40	40	P C	11. ENG MATHS III	PP	100	40	40	P			
02. COMPUTER ORGANIZATION	PP	100	40	50	P C	12. COMPUTER GRAPHICS	PP	100	40	61	P			
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	66	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	63	P			
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	77	P C	14. DATA STRUCTURES AND FILES	PP	100	40	56	P			
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	57	P C	15. DATA COMMUNICATION	PP	100	40	49	P			
06. DIGITAL LABORATORY	TW	50	20	40	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	23	P			
07. DIGITAL LABORATORY	PR	50	20	38	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	39	P			
08. PROGRAMMING LABORATORY	TW	50	20	42	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	21	P			
09. PROGRAMMING LABORATORY	PR	50	20	30	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	41	P			
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	41	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	45	P			
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	41	P			

GRAND TOTAL = 960/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8058568	MAHARSHI PAYAL VIJAY				NIRMALA		, 71241763L			PICT		S8058568
01. DISCRETE STRUCTURES	PP	100	40	48	P C	11. ENG MATHS III	PP	100	40	41	P	
02. COMPUTER ORGANIZATION	PP	100	40	61	P C	12. COMPUTER GRAPHICS	PP	100	40	56	P	
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	61	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	49	P	
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	71	P C	14. DATA STRUCTURES AND FILES	PP	100	40	59	P	
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	57	P C	15. DATA COMMUNICATION	PP	100	40	53	P	
06. DIGITAL LABORATORY	TW	50	20	33	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	20	P	
07. DIGITAL LABORATORY	PR	50	20	32	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	41	P	
08. PROGRAMMING LABORATORY	TW	50	20	34	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	21	P	
09. PROGRAMMING LABORATORY	PR	50	20	32	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	38	P	
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	37	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	43	P	
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	40	P	

GRAND TOTAL = 927/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

S8058569	MANSI MATHUR				PREETI		, 71100890G			PICT		S8058569
01. DISCRETE STRUCTURES	PP	100	40	40	P C	11. ENG MATHS III	PP	100	40	26	F	
02. COMPUTER ORGANIZATION	PP	100	40	48	P C	12. COMPUTER GRAPHICS	PP	100	40	46	P	
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	54	P	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	42	P	
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	46	P C	14. DATA STRUCTURES AND FILES	PP	100	40	51	P	
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	53	P C	15. DATA COMMUNICATION	PP	100	40	46	P	
06. DIGITAL LABORATORY	TW	50	20	31	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	14	P	
07. DIGITAL LABORATORY	PR	50	20	30	P	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	28	P	
08. PROGRAMMING LABORATORY	TW	50	20	30	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	13	P	
09. PROGRAMMING LABORATORY	PR	50	20	20	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	04	F	
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	31	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	35	P	
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	16	F	

GRAND TOTAL = 704/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

S8058570	MANTRI ANKIT JAGDISH				SANGITA		, 71100891E			PICT		S8058570
01. DISCRETE STRUCTURES	PP	100	40	50	P C	11. ENG MATHS III	PP	100	40	77	P	
02. COMPUTER ORGANIZATION	PP	100	40	51	P C	12. COMPUTER GRAPHICS	PP	100	40	53	P	
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	67	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	48	P	
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	65	P C	14. DATA STRUCTURES AND FILES	PP	100	40	57	P	
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	49	P C	15. DATA COMMUNICATION	PP	100	40	50	P	
06. DIGITAL LABORATORY	TW	50	20	43	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	21	P	
07. DIGITAL LABORATORY	PR	50	20	40	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	36	P	
08. PROGRAMMING LABORATORY	TW	50	20	43	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	23	P	
09. PROGRAMMING LABORATORY	PR	50	20	44	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	42	P	
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	45	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	46	P	
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	38	P	

GRAND TOTAL = 988/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8058571 MATHANGI KRISHNAMURTHI K JEYANTHY , 71100892C , PICT , S8058571

01. DISCRETE STRUCTURES	PP	100	40	74	P C	11. ENG MATHS III	PP	100	40	97	P
02. COMPUTER ORGANIZATION	PP	100	40	77	P C	12. COMPUTER GRAPHICS	PP	100	40	71	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	75	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	71	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	79	P C	14. DATA STRUCTURES AND FILES	PP	100	40	61	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	65	P C	15. DATA COMMUNICATION	PP	100	40	58	P
06. DIGITAL LABORATORY	TW	50	20	44	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	24	P
07. DIGITAL LABORATORY	PR	50	20	46	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	42	P
08. PROGRAMMING LABORATORY	TW	50	20	48	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	21	P
09. PROGRAMMING LABORATORY	PR	50	20	38	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	42	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	45	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	47	P
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	42	P

GRAND TOTAL = 1167/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

S8058572 MATNANI PRIYA SHIVADAS UPASANA , 71100893M , PICT , S8058572

01. DISCRETE STRUCTURES	PP	100	40	57	P C	11. ENG MATHS III	PP	100	40	87	P
02. COMPUTER ORGANIZATION	PP	100	40	66	P C	12. COMPUTER GRAPHICS	PP	100	40	70	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	67	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	70	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	80	P C	14. DATA STRUCTURES AND FILES	PP	100	40	61	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	64	P C	15. DATA COMMUNICATION	PP	100	40	68	P
06. DIGITAL LABORATORY	TW	50	20	36	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	23	P
07. DIGITAL LABORATORY	PR	50	20	43	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	40	P
08. PROGRAMMING LABORATORY	TW	50	20	36	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	21	P
09. PROGRAMMING LABORATORY	PR	50	20	28	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	44	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	38	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	43	P
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	44	P

GRAND TOTAL = 1086/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

S8058573 MONGA MANPREET KAUR DARSHAN SINGH GURPREET KAUR , 71100898B , PICT , S8058573

01. DISCRETE STRUCTURES	PP	100	40	40	P C	11. ENG MATHS III	PP	100	40	41	P
02. COMPUTER ORGANIZATION	PP	100	40	56	P C	12. COMPUTER GRAPHICS	PP	100	40	54	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	44	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	42	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	45	P C	14. DATA STRUCTURES AND FILES	PP	100	40	46	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	48	P C	15. DATA COMMUNICATION	PP	100	40	56	P
06. DIGITAL LABORATORY	TW	50	20	35	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	22	P
07. DIGITAL LABORATORY	PR	50	20	35	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	30	P
08. PROGRAMMING LABORATORY	TW	50	20	34	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	17	P
09. PROGRAMMING LABORATORY	PR	50	20	20	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	36	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	38	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	40	P
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	39	P

GRAND TOTAL = 818+07/1500, RESULT: HIGHER SECOND CLASS [0.2]

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8058574	MORE SWATI ANNARAO				ANITA		, 71241764J				PICT		S8058574
01. DISCRETE STRUCTURES	PP	100	40	64	P C		11. ENG MATHS III	PP	100	40	81	P	
02. COMPUTER ORGANIZATION	PP	100	40	63	P C		12. COMPUTER GRAPHICS	PP	100	40	68	P	
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	68	P C		13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	61	P	
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	71	P C		14. DATA STRUCTURES AND FILES	PP	100	40	69	P	
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	63	P C		15. DATA COMMUNICATION	PP	100	40	72	P	
06. DIGITAL LABORATORY	TW	50	20	40	P C		16. PROCESSOR INTERFACING LABORATORY	TW	25	10	24	P	
07. DIGITAL LABORATORY	PR	50	20	46	P C		17. PROCESSOR INTERFACING LABORATORY	OR	50	20	42	P	
08. PROGRAMMING LABORATORY	TW	50	20	41	P C		18. DATA STRUCTURES AND FILES LAB	TW	25	10	21	P	
09. PROGRAMMING LABORATORY	PR	50	20	32	P C		19. DATA STRUCTURES AND FILES LAB	PR	50	20	42	P	
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	43	P C		20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	47	P	
							21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	41	P	

GRAND TOTAL = 1099/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

S8058575	MULCHANDANI POOJA OM				PRITI		, 71100901F				PICT		S8058575
01. DISCRETE STRUCTURES	PP	100	40	56	P C		11. ENG MATHS III	PP	100	40	48	P	
02. COMPUTER ORGANIZATION	PP	100	40	40	P C		12. COMPUTER GRAPHICS	PP	100	40	53	P	
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	40	P C		13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	46	P	
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	57	P C		14. DATA STRUCTURES AND FILES	PP	100	40	57	P	
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	40	P C		15. DATA COMMUNICATION	PP	100	40	40	P	
06. DIGITAL LABORATORY	TW	50	20	38	P C		16. PROCESSOR INTERFACING LABORATORY	TW	25	10	19	P	
07. DIGITAL LABORATORY	PR	50	20	36	P C		17. PROCESSOR INTERFACING LABORATORY	OR	50	20	35	P	
08. PROGRAMMING LABORATORY	TW	50	20	40	P C		18. DATA STRUCTURES AND FILES LAB	TW	25	10	21	P	
09. PROGRAMMING LABORATORY	PR	50	20	42	P C		19. DATA STRUCTURES AND FILES LAB	PR	50	20	42	P	
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	44	P C		20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	47	P	
							21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	38	P	

GRAND TOTAL = 879/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

S8058576	MUNOT SHRENIK SUMTILAL				SADHANA		, 71100903B				PICT		S8058576
01. DISCRETE STRUCTURES	PP	100	40	40	P		11. ENG MATHS III	PP	100	40	40	P	
02. COMPUTER ORGANIZATION	PP	100	40	31	F		12. COMPUTER GRAPHICS	PP	100	40	40	P	
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	41	P C		13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	13	F	
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	41	P		14. DATA STRUCTURES AND FILES	PP	100	40	40	P	
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	32	F		15. DATA COMMUNICATION	PP	100	40	30	F	
06. DIGITAL LABORATORY	TW	50	20	32	P C		16. PROCESSOR INTERFACING LABORATORY	TW	25	10	14	P	
07. DIGITAL LABORATORY	PR	50	20	28	P		17. PROCESSOR INTERFACING LABORATORY	OR	50	20	06	F	
08. PROGRAMMING LABORATORY	TW	50	20	28	P C		18. DATA STRUCTURES AND FILES LAB	TW	25	10	12	P	
09. PROGRAMMING LABORATORY	PR	50	20	22	P C		19. DATA STRUCTURES AND FILES LAB	PR	50	20	28	P	
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	37	P C		20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	33	P	
							21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	21	P	

GRAND TOTAL = 609/1500, RESULT: FAILS

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8058577 MUPADE PRIYA SHIVAJI KANTABAI , 71241765G , PICT , S8058577

01. DISCRETE STRUCTURES	PP	100	40	40	P	11. ENG MATHS III	PP	100	40	40	P
02. COMPUTER ORGANIZATION	PP	100	40	56	P C	12. COMPUTER GRAPHICS	PP	100	40	44	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	40	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	47	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	47	P	14. DATA STRUCTURES AND FILES	PP	100	40	42	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	40	P C	15. DATA COMMUNICATION	PP	100	40	45	P
06. DIGITAL LABORATORY	TW	50	20	37	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	16	P
07. DIGITAL LABORATORY	PR	50	20	05	F	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	05	F
08. PROGRAMMING LABORATORY	TW	50	20	36	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	21	P
09. PROGRAMMING LABORATORY	PR	50	20	30	P	19. DATA STRUCTURES AND FILES LAB	PR	50	20	34	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	43	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	36	P
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	32	P

GRAND TOTAL = 736/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

S8058578 MUTHA VINITA VINOD SUREKHA , 71100904L , PICT , S8058578

01. DISCRETE STRUCTURES	PP	100	40	58	P	11. ENG MATHS III	PP	100	40	22	F
02. COMPUTER ORGANIZATION	PP	100	40	40	P C	12. COMPUTER GRAPHICS	PP	100	40	51	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	42	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	48	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	49	P C	14. DATA STRUCTURES AND FILES	PP	100	40	50	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	40	P C	15. DATA COMMUNICATION	PP	100	40	51	P
06. DIGITAL LABORATORY	TW	50	20	32	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	13	P
07. DIGITAL LABORATORY	PR	50	20	32	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	14	F
08. PROGRAMMING LABORATORY	TW	50	20	35	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	13	P
09. PROGRAMMING LABORATORY	PR	50	20	38	P	19. DATA STRUCTURES AND FILES LAB	PR	50	20	31	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	34	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	35	P
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	44	P

GRAND TOTAL = 772/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

S8058579 NAIK RASHMI BHARAT MEENA , 71100907E , PICT , S8058579

01. DISCRETE STRUCTURES	PP	100	40	46	P	11. ENG MATHS III	PP	100	40	17	F
02. COMPUTER ORGANIZATION	PP	100	40	30	F	12. COMPUTER GRAPHICS	PP	100	40	43	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	40	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	40	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	49	P C	14. DATA STRUCTURES AND FILES	PP	100	40	46	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	48	P C	15. DATA COMMUNICATION	PP	100	40	48	P
06. DIGITAL LABORATORY	TW	50	20	38	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	18	P
07. DIGITAL LABORATORY	PR	50	20	22	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	31	P
08. PROGRAMMING LABORATORY	TW	50	20	34	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	18	P
09. PROGRAMMING LABORATORY	PR	50	20	20	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	38	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	41	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	40	P
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	10	F

GRAND TOTAL = 717/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8058580	NARAD POONAM DEEPAK				VIDYA		, 71100911C			PICT		S8058580
01. DISCRETE STRUCTURES	PP	100	40	63	P		11. ENG MATHS III	PP	100	40	40	P
02. COMPUTER ORGANIZATION	PP	100	40	40	P C		12. COMPUTER GRAPHICS	PP	100	40	64	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	49	P C		13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	56	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	49	P C		14. DATA STRUCTURES AND FILES	PP	100	40	48	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	63	P C		15. DATA COMMUNICATION	PP	100	40	52	P
06. DIGITAL LABORATORY	TW	50	20	37	P C		16. PROCESSOR INTERFACING LABORATORY	TW	25	10	17	P
07. DIGITAL LABORATORY	PR	50	20	25	P C		17. PROCESSOR INTERFACING LABORATORY	OR	50	20	37	P
08. PROGRAMMING LABORATORY	TW	50	20	38	P C		18. DATA STRUCTURES AND FILES LAB	TW	25	10	20	P
09. PROGRAMMING LABORATORY	PR	50	20	23	P C		19. DATA STRUCTURES AND FILES LAB	PR	50	20	35	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	39	P C		20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	43	P
							21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	08	F

GRAND TOTAL = 846/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

S8058581	NARANG KARISHMA KISHOR				SUNITA		, 71100912M			PICT		S8058581
01. DISCRETE STRUCTURES	PP	100	40	49	P C		11. ENG MATHS III	PP	100	40	41	P
02. COMPUTER ORGANIZATION	PP	100	40	63	P C		12. COMPUTER GRAPHICS	PP	100	40	54	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	62	P C		13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	50	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	58	P C		14. DATA STRUCTURES AND FILES	PP	100	40	63	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	57	P C		15. DATA COMMUNICATION	PP	100	40	40	P
06. DIGITAL LABORATORY	TW	50	20	39	P C		16. PROCESSOR INTERFACING LABORATORY	TW	25	10	21	P
07. DIGITAL LABORATORY	PR	50	20	40	P C		17. PROCESSOR INTERFACING LABORATORY	OR	50	20	44	P
08. PROGRAMMING LABORATORY	TW	50	20	36	P C		18. DATA STRUCTURES AND FILES LAB	TW	25	10	17	P
09. PROGRAMMING LABORATORY	PR	50	20	41	P C		19. DATA STRUCTURES AND FILES LAB	PR	50	20	34	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	39	P C		20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	40	P
							21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	42	P

GRAND TOTAL = 930/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

S8058582	NEVATIA SAKSHI SUNIL				ANJU		, 71100916D			PICT		S8058582
01. DISCRETE STRUCTURES	PP	100	40	40	P C		11. ENG MATHS III	PP	100	40	66	P
02. COMPUTER ORGANIZATION	PP	100	40	65	P C		12. COMPUTER GRAPHICS	PP	100	40	69	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	51	P C		13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	56	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	62	P C		14. DATA STRUCTURES AND FILES	PP	100	40	44	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	60	P C		15. DATA COMMUNICATION	PP	100	40	72	P
06. DIGITAL LABORATORY	TW	50	20	40	P C		16. PROCESSOR INTERFACING LABORATORY	TW	25	10	18	P
07. DIGITAL LABORATORY	PR	50	20	30	P C		17. PROCESSOR INTERFACING LABORATORY	OR	50	20	43	P
08. PROGRAMMING LABORATORY	TW	50	20	40	P C		18. DATA STRUCTURES AND FILES LAB	TW	25	10	19	P
09. PROGRAMMING LABORATORY	PR	50	20	22	P C		19. DATA STRUCTURES AND FILES LAB	PR	50	20	38	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	41	P C		20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	41	P
							21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	33	P

GRAND TOTAL = 950/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8058583	OKA TANVI VISHWAS				MEDHA			, 71100918L				PICT		S8058583
01. DISCRETE STRUCTURES	PP	100	40	58	P C	11. ENG MATHS III	PP	100	40	86	P			
02. COMPUTER ORGANIZATION	PP	100	40	51	P C	12. COMPUTER GRAPHICS	PP	100	40	79	P			
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	73	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	67	P			
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	66	P C	14. DATA STRUCTURES AND FILES	PP	100	40	64	P			
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	58	P C	15. DATA COMMUNICATION	PP	100	40	70	P			
06. DIGITAL LABORATORY	TW	50	20	27	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	18	P			
07. DIGITAL LABORATORY	PR	50	20	44	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	42	P			
08. PROGRAMMING LABORATORY	TW	50	20	27	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	16	P			
09. PROGRAMMING LABORATORY	PR	50	20	36	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	38	P			
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	32	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	33	P			
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	45	P			

GRAND TOTAL = 1030/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

S8058584	OSWAL DARSHAN MANISH				NEETA			, 71100919J				PICT		S8058584
01. DISCRETE STRUCTURES	PP	100	40	66	P C	11. ENG MATHS III	PP	100	40	71	P			
02. COMPUTER ORGANIZATION	PP	100	40	57	P C	12. COMPUTER GRAPHICS	PP	100	40	63	P			
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	66	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	56	P			
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	52	P C	14. DATA STRUCTURES AND FILES	PP	100	40	69	P			
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	45	P C	15. DATA COMMUNICATION	PP	100	40	60	P			
06. DIGITAL LABORATORY	TW	50	20	43	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	24	P			
07. DIGITAL LABORATORY	PR	50	20	40	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	46	P			
08. PROGRAMMING LABORATORY	TW	50	20	44	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	24	P			
09. PROGRAMMING LABORATORY	PR	50	20	43	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	44	P			
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	44	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	47	P			
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	41	P			

GRAND TOTAL = 1045/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

S8058585	PAGAR NILESH DADAJI				YOGITA			, 71100922J				PICT		S8058585
01. DISCRETE STRUCTURES	PP	100	40	46	P C	11. ENG MATHS III	PP	100	40	69	P			
02. COMPUTER ORGANIZATION	PP	100	40	66	P C	12. COMPUTER GRAPHICS	PP	100	40	57	P			
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	60	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	53	P			
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	61	P C	14. DATA STRUCTURES AND FILES	PP	100	40	55	P			
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	55	P C	15. DATA COMMUNICATION	PP	100	40	58	P			
06. DIGITAL LABORATORY	TW	50	20	41	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	19	P			
07. DIGITAL LABORATORY	PR	50	20	32	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	42	P			
08. PROGRAMMING LABORATORY	TW	50	20	42	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	20	P			
09. PROGRAMMING LABORATORY	PR	50	20	20	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	40	P			
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	44	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	44	P			
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	29	P			

GRAND TOTAL = 953/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8058586	PANDAY PRASHANT RAMDHARASH			ASHA			, 71100925C			PICT		S8058586
01. DISCRETE STRUCTURES	PP	100	40	49	P	C	11. ENG MATHS III	PP	100	40	87	P
02. COMPUTER ORGANIZATION	PP	100	40	61	P	C	12. COMPUTER GRAPHICS	PP	100	40	58	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	56	P	C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	60	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	58	P	C	14. DATA STRUCTURES AND FILES	PP	100	40	54	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	61	P	C	15. DATA COMMUNICATION	PP	100	40	64	P
06. DIGITAL LABORATORY	TW	50	20	32	P	C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	19	P
07. DIGITAL LABORATORY	PR	50	20	42	P	C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	45	P
08. PROGRAMMING LABORATORY	TW	50	20	29	P	C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	19	P
09. PROGRAMMING LABORATORY	PR	50	20	26	P	C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	38	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	33	P	C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	33	P
							21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	28	P

GRAND TOTAL = 952/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

S8058587	PANDIT SNEHALATA GANAPATI			SHOBHA			, 71241766E			PICT		S8058587
01. DISCRETE STRUCTURES	PP	100	40	58	P	C	11. ENG MATHS III	PP	100	40	64	P
02. COMPUTER ORGANIZATION	PP	100	40	61	P	C	12. COMPUTER GRAPHICS	PP	100	40	62	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	63	P	C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	66	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	56	P	C	14. DATA STRUCTURES AND FILES	PP	100	40	72	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	52	P	C	15. DATA COMMUNICATION	PP	100	40	65	P
06. DIGITAL LABORATORY	TW	50	20	37	P	C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	22	P
07. DIGITAL LABORATORY	PR	50	20	42	P	C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	39	P
08. PROGRAMMING LABORATORY	TW	50	20	41	P	C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	22	P
09. PROGRAMMING LABORATORY	PR	50	20	36	P	C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	42	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	41	P	C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	44	P
							21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	41	P

GRAND TOTAL = 1026/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

S8058588	PARSE JYOTI SHRIKRISHNA			AMBIKA			, 71241767C			PICT		S8058588
01. DISCRETE STRUCTURES	PP	100	40	40	P	C	11. ENG MATHS III	PP	100	40	40	P
02. COMPUTER ORGANIZATION	PP	100	40	66	P	C	12. COMPUTER GRAPHICS	PP	100	40	68	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	63	P	C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	62	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	58	P	C	14. DATA STRUCTURES AND FILES	PP	100	40	60	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	47	P	C	15. DATA COMMUNICATION	PP	100	40	74	P
06. DIGITAL LABORATORY	TW	50	20	37	P	C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	22	P
07. DIGITAL LABORATORY	PR	50	20	22	P	C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	41	P
08. PROGRAMMING LABORATORY	TW	50	20	40	P	C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	18	P
09. PROGRAMMING LABORATORY	PR	50	20	27	P	C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	37	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	40	P	C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	42	P
							21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	39	P

GRAND TOTAL = 943/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8058589	PATEL MANISH UTTAM			KAILAS			, 71100933D			PICT		S8058589
01. DISCRETE STRUCTURES	PP	100	40	63	P	C	11. ENG MATHS III	PP	100	40	90	P
02. COMPUTER ORGANIZATION	PP	100	40	73	P	C	12. COMPUTER GRAPHICS	PP	100	40	74	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	68	P	C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	61	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	65	P	C	14. DATA STRUCTURES AND FILES	PP	100	40	59	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	64	P	C	15. DATA COMMUNICATION	PP	100	40	72	P
06. DIGITAL LABORATORY	TW	50	20	42	P	C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	19	P
07. DIGITAL LABORATORY	PR	50	20	33	P	C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	34	P
08. PROGRAMMING LABORATORY	TW	50	20	43	P	C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	19	P
09. PROGRAMMING LABORATORY	PR	50	20	36	P	C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	36	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	45	P	C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	41	P
							21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	41	P

GRAND TOTAL = 1078/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

S8058590	PATHAK ANUJA PRAVIN			UJJWALA			, 71100934B			PICT		S8058590
01. DISCRETE STRUCTURES	PP	100	40	40	P	C	11. ENG MATHS III	PP	100	40	23	F
02. COMPUTER ORGANIZATION	PP	100	40	40	P		12. COMPUTER GRAPHICS	PP	100	40	40	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	43	P	C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	40	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	48	P	C	14. DATA STRUCTURES AND FILES	PP	100	40	47	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	55	P	C	15. DATA COMMUNICATION	PP	100	40	48	P
06. DIGITAL LABORATORY	TW	50	20	34	P	C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	16	P
07. DIGITAL LABORATORY	PR	50	20	30	P	C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	38	P
08. PROGRAMMING LABORATORY	TW	50	20	29	P	C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	16	P
09. PROGRAMMING LABORATORY	PR	50	20	26	P	C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	20	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	36	P	C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	36	P
							21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	27	P

GRAND TOTAL = 732/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

S8058591	PATIL AMOL MADHUKAR			JYOTI			, 71100938E			PICT		S8058591
01. DISCRETE STRUCTURES	PP	100	40	50	P	C	11. ENG MATHS III	PP	100	40	40	P
02. COMPUTER ORGANIZATION	PP	100	40	40	P	C	12. COMPUTER GRAPHICS	PP	100	40	48	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	55	P	C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	26	F
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	44	P	C	14. DATA STRUCTURES AND FILES	PP	100	40	55	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	47	P	C	15. DATA COMMUNICATION	PP	100	40	50	P
06. DIGITAL LABORATORY	TW	50	20	33	P	C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	14	P
07. DIGITAL LABORATORY	PR	50	20	28	P	C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	20	P
08. PROGRAMMING LABORATORY	TW	50	20	33	P	C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	14	P
09. PROGRAMMING LABORATORY	PR	50	20	25	P	C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	42	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	37	P	C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	35	P
							21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	32	P

GRAND TOTAL = 768/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8058592 PATIL PRIYA KISHOR PUSHPA , 71100941E , PICT , S8058592

01. DISCRETE STRUCTURES	PP	100	40	41	P C	11. ENG MATHS III	PP	100	40	85	P
02. COMPUTER ORGANIZATION	PP	100	40	66	P C	12. COMPUTER GRAPHICS	PP	100	40	69	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	66	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	59	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	52	P C	14. DATA STRUCTURES AND FILES	PP	100	40	60	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	57	P C	15. DATA COMMUNICATION	PP	100	40	54	P
06. DIGITAL LABORATORY	TW	50	20	38	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	20	P
07. DIGITAL LABORATORY	PR	50	20	30	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	36	P
08. PROGRAMMING LABORATORY	TW	50	20	36	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	21	P
09. PROGRAMMING LABORATORY	PR	50	20	26	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	38	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	38	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	43	P
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	43	P

GRAND TOTAL = 978/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

S8058593 PATIL RUPESH SING RAMESH KANCHAN , 71100943M , PICT , S8058593

01. DISCRETE STRUCTURES	PP	100	40	41	P C	11. ENG MATHS III	PP	100	40	24	F
02. COMPUTER ORGANIZATION	PP	100	40	49	P C	12. COMPUTER GRAPHICS	PP	100	40	60	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	63	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	52	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	53	P C	14. DATA STRUCTURES AND FILES	PP	100	40	43	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	58	P C	15. DATA COMMUNICATION	PP	100	40	61	P
06. DIGITAL LABORATORY	TW	50	20	30	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	11	P
07. DIGITAL LABORATORY	PR	50	20	40	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	16	F
08. PROGRAMMING LABORATORY	TW	50	20	34	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	13	P
09. PROGRAMMING LABORATORY	PR	50	20	22	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	16	F
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	34	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	33	P
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	21	P

GRAND TOTAL = 774/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

S8058594 PATIL RUSHIKESH VASANTRAO SHOBHANA , 71100944K , PICT , S8058594

01. DISCRETE STRUCTURES	PP	100	40	47	P C	11. ENG MATHS III	PP	100	40	66	P
02. COMPUTER ORGANIZATION	PP	100	40	58	P C	12. COMPUTER GRAPHICS	PP	100	40	58	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	63	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	53	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	60	P C	14. DATA STRUCTURES AND FILES	PP	100	40	58	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	45	P C	15. DATA COMMUNICATION	PP	100	40	51	P
06. DIGITAL LABORATORY	TW	50	20	32	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	18	P
07. DIGITAL LABORATORY	PR	50	20	42	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	38	P
08. PROGRAMMING LABORATORY	TW	50	20	31	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	15	P
09. PROGRAMMING LABORATORY	PR	50	20	35	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	39	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	35	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	32	P
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	28	P

GRAND TOTAL = 904/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8058595	PAWAR NEHA ANIL				JAYA					, 71100949L				PICT		S8058595
01. DISCRETE STRUCTURES	PP	100	40	31	F					11. ENG MATHS III	PP	100	40	15	F	
02. COMPUTER ORGANIZATION	PP	100	40	45	P C					12. COMPUTER GRAPHICS	PP	100	40	55	P	
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	40	P C					13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	27	F	
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	50	P C					14. DATA STRUCTURES AND FILES	PP	100	40	44	P	
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	51	P C					15. DATA COMMUNICATION	PP	100	40	42	P	
06. DIGITAL LABORATORY	TW	50	20	35	P C					16. PROCESSOR INTERFACING LABORATORY	TW	25	10	12	P	
07. DIGITAL LABORATORY	PR	50	20	34	P C					17. PROCESSOR INTERFACING LABORATORY	OR	50	20	24	P	
08. PROGRAMMING LABORATORY	TW	50	20	34	P C					18. DATA STRUCTURES AND FILES LAB	TW	25	10	15	P	
09. PROGRAMMING LABORATORY	PR	50	20	10	F					19. DATA STRUCTURES AND FILES LAB	PR	50	20	02	F	
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	38	P C					20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	33	P	
										21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	12	F	

GRAND TOTAL = 649/1500, RESULT: FAILS

RESULT RESERVED FOR BKLG

ORDN. 1 MARKS :

S8058596	PAWAR PRASAD GORAKSH	MEENAKSHI					, 71100951B	,	,	PICT	, S8058596			
01. DISCRETE STRUCTURES	PP	100	40	43	P	11. ENG MATHS III			PP	100	40	19	F	
02. COMPUTER ORGANIZATION	PP	100	40	41	P	12. COMPUTER GRAPHICS			PP	100	40	41	P	
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	41	P C	13. PROCESSOR ARCHITECTURE & INTER.			PP	100	40	40	P	
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	43	P C	14. DATA STRUCTURES AND FILES			PP	100	40	45	P	
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	53	P C	15. DATA COMMUNICATION			PP	100	40	40	P	
06. DIGITAL LABORATORY	TW	50	20	24	P C	16. PROCESSOR INTERFACING LABORATORY	TW		25	10	10	P		
07. DIGITAL LABORATORY	PR	50	20	30	P	17. PROCESSOR INTERFACING LABORATORY	OR		50	20	28	P		
08. PROGRAMMING LABORATORY	TW	50	20	28	P C	18. DATA STRUCTURES AND FILES LAB	TW		25	10	10	P		
09. PROGRAMMING LABORATORY	PR	50	20	22	P C	19. DATA STRUCTURES AND FILES LAB	PR		50	20	AA	F		
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	34	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW		50	20	24	P		
						21. OBJECT ORIENTED PROGRAMMING LAB	PR		50	20	08	F		

GRAND TOTAL = 624/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

S8058597	PAWAR SNEHA SADASHIV					JYOTI					, 71241768M					, , PICT					, S8058597				
01. DISCRETE STRUCTURES	PP	100	40	43	P C	11. ENG MATHS III	PP	100	40	63	P														
02. COMPUTER ORGANIZATION	PP	100	40	75	P C	12. COMPUTER GRAPHICS	PP	100	40	74	P														
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	56	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	59	P														
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	79	P C	14. DATA STRUCTURES AND FILES	PP	100	40	58	P														
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	56	P C	15. DATA COMMUNICATION	PP	100	40	71	P														
06. DIGITAL LABORATORY	TW	50	20	39	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	24	P														
07. DIGITAL LABORATORY	PR	50	20	32	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	25	P														
08. PROGRAMMING LABORATORY	TW	50	20	44	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	17	P														
09. PROGRAMMING LABORATORY	PR	50	20	36	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	38	P														
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	43	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	40	P														
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	45	P														

GRAND TOTAL = 1017/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8058598 PAYGUDE GAUTAMI SANJAY SUMAN , 71241769K , PICT , S8058598

01. DISCRETE STRUCTURES	PP	100	40	49	P C	11. ENG MATHS III	PP	100	40	46	P
02. COMPUTER ORGANIZATION	PP	100	40	56	P C	12. COMPUTER GRAPHICS	PP	100	40	58	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	61	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	59	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	40	P C	14. DATA STRUCTURES AND FILES	PP	100	40	52	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	50	P C	15. DATA COMMUNICATION	PP	100	40	62	P
06. DIGITAL LABORATORY	TW	50	20	41	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	20	P
07. DIGITAL LABORATORY	PR	50	20	25	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	41	P
08. PROGRAMMING LABORATORY	TW	50	20	43	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	21	P
09. PROGRAMMING LABORATORY	PR	50	20	22	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	41	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	45	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	47	P
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	40	P

GRAND TOTAL = 919/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

S8058599 PHADKE MUGDHA RAVINDRA PRADNYA , 71100954G , PICT , S8058599

01. DISCRETE STRUCTURES	PP	100	40	66	P C	11. ENG MATHS III	PP	100	40	56	P
02. COMPUTER ORGANIZATION	PP	100	40	50	P C	12. COMPUTER GRAPHICS	PP	100	40	59	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	49	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	43	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	67	P C	14. DATA STRUCTURES AND FILES	PP	100	40	56	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	54	P C	15. DATA COMMUNICATION	PP	100	40	62	P
06. DIGITAL LABORATORY	TW	50	20	40	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	19	P
07. DIGITAL LABORATORY	PR	50	20	37	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	36	P
08. PROGRAMMING LABORATORY	TW	50	20	41	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	20	P
09. PROGRAMMING LABORATORY	PR	50	20	39	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	38	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	42	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	43	P
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	20	P

GRAND TOTAL = 937/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

S8058600 POTE ISHWAR VITTHAL SUNITA , 71100957M , PICT , S8058600

01. DISCRETE STRUCTURES	PP	100	40	58	P	11. ENG MATHS III	PP	100	40	53	P
02. COMPUTER ORGANIZATION	PP	100	40	53	P	12. COMPUTER GRAPHICS	PP	100	40	53	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	42	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	40	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	46	P C	14. DATA STRUCTURES AND FILES	PP	100	40	47	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	44	P C	15. DATA COMMUNICATION	PP	100	40	53	P
06. DIGITAL LABORATORY	TW	50	20	33	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	20	P
07. DIGITAL LABORATORY	PR	50	20	22	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	38	P
08. PROGRAMMING LABORATORY	TW	50	20	36	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	16	P
09. PROGRAMMING LABORATORY	PR	50	20	37	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	34	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	39	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	40	P
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	21	P

GRAND TOTAL = 825/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8058601	RAHUL BAIJAL				SADHNA				, 71100962H				PICT		S8058601
01. DISCRETE STRUCTURES	PP	100	40	48	P C	11. ENG MATHS III	PP	100	40	50	P				
02. COMPUTER ORGANIZATION	PP	100	40	69	P C	12. COMPUTER GRAPHICS	PP	100	40	73	P				
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	68	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	59	P				
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	54	P C	14. DATA STRUCTURES AND FILES	PP	100	40	61	P				
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	55	P C	15. DATA COMMUNICATION	PP	100	40	62	P				
06. DIGITAL LABORATORY	TW	50	20	40	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	17	P				
07. DIGITAL LABORATORY	PR	50	20	45	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	41	P				
08. PROGRAMMING LABORATORY	TW	50	20	42	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	19	P				
09. PROGRAMMING LABORATORY	PR	50	20	25	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	35	P				
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	42	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	45	P				
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	39	P				

GRAND TOTAL = 989/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

S8058602	RAHUL SHARMA				PROMILA				, 71100963F				PICT		S8058602
01. DISCRETE STRUCTURES	PP	100	40	50	P	11. ENG MATHS III	PP	100	40	44	P				
02. COMPUTER ORGANIZATION	PP	100	40	44	P C	12. COMPUTER GRAPHICS	PP	100	40	40	P				
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	40	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	40	P				
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	52	P C	14. DATA STRUCTURES AND FILES	PP	100	40	40	P				
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	49	P C	15. DATA COMMUNICATION	PP	100	40	66	P				
06. DIGITAL LABORATORY	TW	50	20	21	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	10	P				
07. DIGITAL LABORATORY	PR	50	20	23	P	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	20	P				
08. PROGRAMMING LABORATORY	TW	50	20	20	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	10	P				
09. PROGRAMMING LABORATORY	PR	50	20	23	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	AA	F				
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	31	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	22	P				
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	AA	F				

GRAND TOTAL = 645/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

S8058603	RAMPURAWALA MARIYA FIROZ				FARIDA				, 71100964D				PICT		S8058603
01. DISCRETE STRUCTURES	PP	100	40	62	P C	11. ENG MATHS III	PP	100	40	59	P				
02. COMPUTER ORGANIZATION	PP	100	40	45	P C	12. COMPUTER GRAPHICS	PP	100	40	48	P				
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	64	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	52	P				
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	59	P C	14. DATA STRUCTURES AND FILES	PP	100	40	52	P				
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	45	P C	15. DATA COMMUNICATION	PP	100	40	59	P				
06. DIGITAL LABORATORY	TW	50	20	38	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	18	P				
07. DIGITAL LABORATORY	PR	50	20	39	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	42	P				
08. PROGRAMMING LABORATORY	TW	50	20	41	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	19	P				
09. PROGRAMMING LABORATORY	PR	50	20	34	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	40	P				
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	41	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	41	P				
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	46	P				

GRAND TOTAL = 944/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8058604	RANADIVE ABHIJEET JANARDAN				KANCHAN					, 71100966L					PICT			S8058604
01. DISCRETE STRUCTURES	PP	100	40	41	P C	11. ENG MATHS III	PP	100	40	62	P							
02. COMPUTER ORGANIZATION	PP	100	40	59	P C	12. COMPUTER GRAPHICS	PP	100	40	60	P							
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	53	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	55	P							
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	64	P C	14. DATA STRUCTURES AND FILES	PP	100	40	61	P							
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	51	P C	15. DATA COMMUNICATION	PP	100	40	50	P							
06. DIGITAL LABORATORY	TW	50	20	32	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	19	P							
07. DIGITAL LABORATORY	PR	50	20	42	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	43	P							
08. PROGRAMMING LABORATORY	TW	50	20	32	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	18	P							
09. PROGRAMMING LABORATORY	PR	50	20	29	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	41	P							
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	40	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	33	P							
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	35	P							

GRAND TOTAL = 920/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

S8058606	RATHOD ASHWINKUMAR AMARSING				RANJANA					, 71241770C					PICT			S8058606
01. DISCRETE STRUCTURES	PP	100	40	40	P C	11. ENG MATHS III	PP	100	40	61	P							
02. COMPUTER ORGANIZATION	PP	100	40	70	P C	12. COMPUTER GRAPHICS	PP	100	40	64	P							
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	70	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	59	P							
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	58	P C	14. DATA STRUCTURES AND FILES	PP	100	40	57	P							
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	41	P C	15. DATA COMMUNICATION	PP	100	40	76	P							
06. DIGITAL LABORATORY	TW	50	20	38	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	17	P							
07. DIGITAL LABORATORY	PR	50	20	35	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	41	P							
08. PROGRAMMING LABORATORY	TW	50	20	38	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	19	P							
09. PROGRAMMING LABORATORY	PR	50	20	32	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	33	P							
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	43	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	38	P							
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	46	P							

GRAND TOTAL = 976/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

S8058607	RATTAN NAKUL VISHWAS				BIMLA					, 71100971G					PICT			S8058607
01. DISCRETE STRUCTURES	PP	100	40	47	P C	11. ENG MATHS III	PP	100	40	20	F							
02. COMPUTER ORGANIZATION	PP	100	40	41	P C	12. COMPUTER GRAPHICS	PP	100	40	44	P							
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	53	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	48	P							
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	52	P C	14. DATA STRUCTURES AND FILES	PP	100	40	40	P							
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	40	P C	15. DATA COMMUNICATION	PP	100	40	49	P							
06. DIGITAL LABORATORY	TW	50	20	33	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	16	P							
07. DIGITAL LABORATORY	PR	50	20	36	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	38	P							
08. PROGRAMMING LABORATORY	TW	50	20	37	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	17	P							
09. PROGRAMMING LABORATORY	PR	50	20	38	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	39	P							
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	38	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	40	P							
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	39	P							

GRAND TOTAL = 805/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8058608	RIA NARAYAN				MANJU				, 71100975K				PICT		, S8058608
01. DISCRETE STRUCTURES	PP	100	40	56	P C	11. ENG MATHS III	PP	100	40	40	P				
02. COMPUTER ORGANIZATION	PP	100	40	63	P C	12. COMPUTER GRAPHICS	PP	100	40	59	P				
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	67	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	62	P				
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	66	P C	14. DATA STRUCTURES AND FILES	PP	100	40	41	P				
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	50	P C	15. DATA COMMUNICATION	PP	100	40	61	P				
06. DIGITAL LABORATORY	TW	50	20	31	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	15	P				
07. DIGITAL LABORATORY	PR	50	20	37	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	31	P				
08. PROGRAMMING LABORATORY	TW	50	20	28	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	15	P				
09. PROGRAMMING LABORATORY	PR	50	20	40	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	38	P				
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	38	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	31	P				
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	25	P				

GRAND TOTAL = 894+06/1500, RESULT: FIRST CLASS [0.2]

ORDN. 1 MARKS :

S8058609	ROMIL MEHTA				SHEELA				, 71100978D				PICT		, S8058609
01. DISCRETE STRUCTURES	PP	100	40	66	P C	11. ENG MATHS III	PP	100	40	67	P				
02. COMPUTER ORGANIZATION	PP	100	40	60	P C	12. COMPUTER GRAPHICS	PP	100	40	57	P				
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	67	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	59	P				
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	55	P C	14. DATA STRUCTURES AND FILES	PP	100	40	69	P				
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	40	P C	15. DATA COMMUNICATION	PP	100	40	68	P				
06. DIGITAL LABORATORY	TW	50	20	39	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	18	P				
07. DIGITAL LABORATORY	PR	50	20	35	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	36	P				
08. PROGRAMMING LABORATORY	TW	50	20	43	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	19	P				
09. PROGRAMMING LABORATORY	PR	50	20	40	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	38	P				
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	42	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	41	P				
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	42	P				

GRAND TOTAL = 1001/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

S8058610	SABALE BHUSHAN BALASAHEB				SUMAN				, 71100980F				PICT		, S8058610
01. DISCRETE STRUCTURES	PP	100	40	49	P C	11. ENG MATHS III	PP	100	40	73	P				
02. COMPUTER ORGANIZATION	PP	100	40	58	P C	12. COMPUTER GRAPHICS	PP	100	40	63	P				
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	64	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	54	P				
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	60	P C	14. DATA STRUCTURES AND FILES	PP	100	40	44	P				
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	45	P C	15. DATA COMMUNICATION	PP	100	40	59	P				
06. DIGITAL LABORATORY	TW	50	20	36	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	19	P				
07. DIGITAL LABORATORY	PR	50	20	40	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	32	P				
08. PROGRAMMING LABORATORY	TW	50	20	37	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	17	P				
09. PROGRAMMING LABORATORY	PR	50	20	30	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	30	P				
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	41	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	36	P				
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	32	P				

GRAND TOTAL = 919/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8058611	SAGAR SABIR DAMANI			SHAHIN		, 71100981D			PICT		S8058611
01. DISCRETE STRUCTURES	PP	100	40	79	P C	11. ENG MATHS III	PP	100	40	90	P
02. COMPUTER ORGANIZATION	PP	100	40	80	P C	12. COMPUTER GRAPHICS	PP	100	40	65	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	66	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	66	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	87	P C	14. DATA STRUCTURES AND FILES	PP	100	40	69	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	40	P C	15. DATA COMMUNICATION	PP	100	40	68	P
06. DIGITAL LABORATORY	TW	50	20	44	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	24	P
07. DIGITAL LABORATORY	PR	50	20	45	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	44	P
08. PROGRAMMING LABORATORY	TW	50	20	44	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	22	P
09. PROGRAMMING LABORATORY	PR	50	20	44	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	40	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	44	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	47	P
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	44	P

GRAND TOTAL = 1152/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

S8058612	SAHARE NUPUR ARVIND			MAMTA		, 71100982B			PICT		S8058612
01. DISCRETE STRUCTURES	PP	100	40	40	P C	11. ENG MATHS III	PP	100	40	58	P
02. COMPUTER ORGANIZATION	PP	100	40	54	P C	12. COMPUTER GRAPHICS	PP	100	40	53	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	46	P	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	58	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	52	P C	14. DATA STRUCTURES AND FILES	PP	100	40	46	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	43	P C	15. DATA COMMUNICATION	PP	100	40	56	P
06. DIGITAL LABORATORY	TW	50	20	34	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	17	P
07. DIGITAL LABORATORY	PR	50	20	22	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	30	P
08. PROGRAMMING LABORATORY	TW	50	20	38	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	15	P
09. PROGRAMMING LABORATORY	PR	50	20	30	P	19. DATA STRUCTURES AND FILES LAB	PR	50	20	16	F
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	39	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	38	P
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	07	F

GRAND TOTAL = 792/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

S8058613	SARAF DARPAN RAJENDRA			AMITA		, 71100986E			PICT		S8058613
01. DISCRETE STRUCTURES	PP	100	40	40	P C	11. ENG MATHS III	PP	100	40	65	P
02. COMPUTER ORGANIZATION	PP	100	40	57	P C	12. COMPUTER GRAPHICS	PP	100	40	46	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	57	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	48	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	63	P C	14. DATA STRUCTURES AND FILES	PP	100	40	47	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	40	P C	15. DATA COMMUNICATION	PP	100	40	63	P
06. DIGITAL LABORATORY	TW	50	20	34	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	19	P
07. DIGITAL LABORATORY	PR	50	20	42	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	35	P
08. PROGRAMMING LABORATORY	TW	50	20	32	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	20	P
09. PROGRAMMING LABORATORY	PR	50	20	40	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	16#	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	41	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	39	P
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	34	P

GRAND TOTAL = 878/1500, RESULT: HIGHER SECOND CLASS # [0.4]

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8058614	SASANE PRANITA RAMESH			ALKA		, 71100989K			PICT		S8058614
01. DISCRETE STRUCTURES	PP	100	40	49	P C	11. ENG MATHS III	PP	100	40	40	P
02. COMPUTER ORGANIZATION	PP	100	40	30	F	12. COMPUTER GRAPHICS	PP	100	40	49	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	51	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	31	F
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	40	P C	14. DATA STRUCTURES AND FILES	PP	100	40	65	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	40	P C	15. DATA COMMUNICATION	PP	100	40	40	P
06. DIGITAL LABORATORY	TW	50	20	34	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	14	P
07. DIGITAL LABORATORY	PR	50	20	22	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	38	P
08. PROGRAMMING LABORATORY	TW	50	20	35	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	17	P
09. PROGRAMMING LABORATORY	PR	50	20	20	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	28	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	39	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	38	P
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	25	P

GRAND TOTAL = 745/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

S8058615	SAYYED FAKHRUDDIN KAMALUDDIN			RAZIYA KHANAM		, 71241771M			PICT		S8058615
01. DISCRETE STRUCTURES	PP	100	40	62	P C	11. ENG MATHS III	PP	100	40	55	P
02. COMPUTER ORGANIZATION	PP	100	40	63	P C	12. COMPUTER GRAPHICS	PP	100	40	68	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	66	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	68	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	70	P C	14. DATA STRUCTURES AND FILES	PP	100	40	61	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	40	P C	15. DATA COMMUNICATION	PP	100	40	66	P
06. DIGITAL LABORATORY	TW	50	20	38	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	24	P
07. DIGITAL LABORATORY	PR	50	20	36	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	46	P
08. PROGRAMMING LABORATORY	TW	50	20	38	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	20	P
09. PROGRAMMING LABORATORY	PR	50	20	30	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	41	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	44	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	47	P
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	47	P

GRAND TOTAL = 1030/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

S8058616	SHARMA LAKSHMI			KANAK		, 71100999G			PICT		S8058616
01. DISCRETE STRUCTURES	PP	100	40	58	P C	11. ENG MATHS III	PP	100	40	52	P
02. COMPUTER ORGANIZATION	PP	100	40	58	P C	12. COMPUTER GRAPHICS	PP	100	40	57	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	56	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	46	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	69	P C	14. DATA STRUCTURES AND FILES	PP	100	40	59	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	52	P C	15. DATA COMMUNICATION	PP	100	40	67	P
06. DIGITAL LABORATORY	TW	50	20	39	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	21	P
07. DIGITAL LABORATORY	PR	50	20	40	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	34	P
08. PROGRAMMING LABORATORY	TW	50	20	44	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	21	P
09. PROGRAMMING LABORATORY	PR	50	20	35	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	40	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	44	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	46	P
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	47	P

GRAND TOTAL = 985/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8058618	SHINDE NUTAN BALASAHEB				MANISHA					, 71101006E				PICT		S8058618
01. DISCRETE STRUCTURES	PP	100	40	69	P C	11. ENG MATHS III	PP	100	40	50	P					
02. COMPUTER ORGANIZATION	PP	100	40	72	P C	12. COMPUTER GRAPHICS	PP	100	40	74	P					
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	60	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	57	P					
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	77	P C	14. DATA STRUCTURES AND FILES	PP	100	40	69	P					
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	52	P C	15. DATA COMMUNICATION	PP	100	40	66	P					
06. DIGITAL LABORATORY	TW	50	20	42	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	22	P					
07. DIGITAL LABORATORY	PR	50	20	43	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	40	P					
08. PROGRAMMING LABORATORY	TW	50	20	44	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	20	P					
09. PROGRAMMING LABORATORY	PR	50	20	28	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	28	P					
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	44	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	43	P					
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	46	P					

GRAND TOTAL = 1046/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

S8058619	SHINDE SUSHANT GAUTAM				SANGITA					, 71241772K				PICT		S8058619
01. DISCRETE STRUCTURES	PP	100	40	44	P C	11. ENG MATHS III	PP	100	40	40	P					
02. COMPUTER ORGANIZATION	PP	100	40	51	P C	12. COMPUTER GRAPHICS	PP	100	40	58	P					
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	43	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	40	P					
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	80	P C	14. DATA STRUCTURES AND FILES	PP	100	40	51	P					
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	42	P C	15. DATA COMMUNICATION	PP	100	40	69	P					
06. DIGITAL LABORATORY	TW	50	20	41	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	22	P					
07. DIGITAL LABORATORY	PR	50	20	25	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	28	P					
08. PROGRAMMING LABORATORY	TW	50	20	42	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	18	P					
09. PROGRAMMING LABORATORY	PR	50	20	28	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	34	P					
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	42	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	43	P					
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	42	P					

GRAND TOTAL = 883/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

S8058620	SHIRA VISHAL BHAGWANSING				MALTA					, 71101009K				PICT		S8058620
01. DISCRETE STRUCTURES	PP	100	40	42	P C	11. ENG MATHS III	PP	100	40	49	P					
02. COMPUTER ORGANIZATION	PP	100	40	54	P C	12. COMPUTER GRAPHICS	PP	100	40	48	P					
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	62	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	41	P					
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	49	P C	14. DATA STRUCTURES AND FILES	PP	100	40	58	P					
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	40	P C	15. DATA COMMUNICATION	PP	100	40	59	P					
06. DIGITAL LABORATORY	TW	50	20	35	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	17	P					
07. DIGITAL LABORATORY	PR	50	20	28	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	27	P					
08. PROGRAMMING LABORATORY	TW	50	20	34	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	17	P					
09. PROGRAMMING LABORATORY	PR	50	20	28	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	37	P					
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	41	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	39	P					
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	29	P					

GRAND TOTAL = 834/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8058621	SHIRBHATE ABHILASH DNYANESHWAR				RAJSHRI				, 71101010C				PICT		S8058621
01. DISCRETE STRUCTURES	PP	100	40	46	P C	11. ENG MATHS III	PP	100	40	18	F				
02. COMPUTER ORGANIZATION	PP	100	40	40	P C	12. COMPUTER GRAPHICS	PP	100	40	43	P				
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	63	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	26	F				
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	48	P C	14. DATA STRUCTURES AND FILES	PP	100	40	40	P				
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	40	P C	15. DATA COMMUNICATION	PP	100	40	43	P				
06. DIGITAL LABORATORY	TW	50	20	33	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	10	P				
07. DIGITAL LABORATORY	PR	50	20	22	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	36	P				
08. PROGRAMMING LABORATORY	TW	50	20	36	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	13	P				
09. PROGRAMMING LABORATORY	PR	50	20	27	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	32	P				
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	35	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	29	P				
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	08	F				

GRAND TOTAL = 688/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

S8058622	SHUKLA MANISH NITYANAND				MADHURI				, 71045622M				PICT		S8058622
01. DISCRETE STRUCTURES	PP	100	40	57	P C	11. ENG MATHS III	PP	100	40	23	F				
02. COMPUTER ORGANIZATION	PP	100	40	44	P C	12. COMPUTER GRAPHICS	PP	100	40	40	P				
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	57	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	10	F				
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	49	P C	14. DATA STRUCTURES AND FILES	PP	100	40	40	P				
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	45	P C	15. DATA COMMUNICATION	PP	100	40	45	P				
06. DIGITAL LABORATORY	TW	50	20	28	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	12	P				
07. DIGITAL LABORATORY	PR	50	20	30	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	21	P				
08. PROGRAMMING LABORATORY	TW	50	20	28	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	11	P				
09. PROGRAMMING LABORATORY	PR	50	20	32	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	28	P				
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	31	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	27	P				
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	06	F				

GRAND TOTAL = 664/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

S8058623	SIDDHANT BANSAL				SUJATA				, 71101015D				PICT		S8058623
01. DISCRETE STRUCTURES	PP	100	40	64	P C	11. ENG MATHS III	PP	100	40	57	P				
02. COMPUTER ORGANIZATION	PP	100	40	52	P C	12. COMPUTER GRAPHICS	PP	100	40	50	P				
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	63	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	44	P				
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	62	P C	14. DATA STRUCTURES AND FILES	PP	100	40	62	P				
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	45	P C	15. DATA COMMUNICATION	PP	100	40	56	P				
06. DIGITAL LABORATORY	TW	50	20	27	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	15	P				
07. DIGITAL LABORATORY	PR	50	20	32	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	39	P				
08. PROGRAMMING LABORATORY	TW	50	20	27	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	16	P				
09. PROGRAMMING LABORATORY	PR	50	20	34	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	37	P				
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	36	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	37	P				
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	21	P				

GRAND TOTAL = 876/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8058624	SOLANKE BHUSHAN NARENDRA				REKHA				, 71101017L				PICT		S8058624
01. DISCRETE STRUCTURES	PP	100	40	44	P C	11. ENG MATHS III	PP	100	40	06	F				
02. COMPUTER ORGANIZATION	PP	100	40	43	P C	12. COMPUTER GRAPHICS	PP	100	40	43	P				
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	40	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	19	F				
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	48	P C	14. DATA STRUCTURES AND FILES	PP	100	40	57	P				
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	40	P C	15. DATA COMMUNICATION	PP	100	40	46	P				
06. DIGITAL LABORATORY	TW	50	20	33	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	10	P				
07. DIGITAL LABORATORY	PR	50	20	30	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	14	F				
08. PROGRAMMING LABORATORY	TW	50	20	31	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	12	P				
09. PROGRAMMING LABORATORY	PR	50	20	24	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	15	F				
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	35	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	33	P				
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	10	F				

GRAND TOTAL = 633/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

S8058625	AVINASH SONAWANE				SOJARBAI				, 71045378H				PICT		S8058625
01. DISCRETE STRUCTURES	PP	100	40	65	P C	11. ENG MATHS III	PP	100	40	07	F				
02. COMPUTER ORGANIZATION	PP	100	40	48	P C	12. COMPUTER GRAPHICS	PP	100	40	40	P				
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	40	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	27	F				
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	57	P C	14. DATA STRUCTURES AND FILES	PP	100	40	42	P				
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	47	P C	15. DATA COMMUNICATION	PP	100	40	57	P				
06. DIGITAL LABORATORY	TW	50	20	20	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	10	P				
07. DIGITAL LABORATORY	PR	50	20	22	P	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	12	F				
08. PROGRAMMING LABORATORY	TW	50	20	34	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	12	P				
09. PROGRAMMING LABORATORY	PR	50	20	27	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	36	P				
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	31	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	35	P				
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	34	P				

GRAND TOTAL = 703/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

S8058626	SONAWANE JAGDISH RAJENDRA				HIRABAI				, 71241773H				PICT		S8058626
01. DISCRETE STRUCTURES	PP	100	40	43	P C	11. ENG MATHS III	PP	100	40	53	P				
02. COMPUTER ORGANIZATION	PP	100	40	68	P C	12. COMPUTER GRAPHICS	PP	100	40	58	P				
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	55	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	59	P				
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	71	P C	14. DATA STRUCTURES AND FILES	PP	100	40	50	P				
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	46	P C	15. DATA COMMUNICATION	PP	100	40	64	P				
06. DIGITAL LABORATORY	TW	50	20	36	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	19	P				
07. DIGITAL LABORATORY	PR	50	20	42	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	39	P				
08. PROGRAMMING LABORATORY	TW	50	20	36	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	20	P				
09. PROGRAMMING LABORATORY	PR	50	20	27	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	36	P				
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	41	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	41	P				
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	29	P				

GRAND TOTAL = 933/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8058627	STUTI MITTAL				BHAVNA		, 71101022G			PICT		S8058627
01. DISCRETE STRUCTURES	PP	100	40	48	P		11. ENG MATHS III	PP	100	40	44	P
02. COMPUTER ORGANIZATION	PP	100	40	51	P C		12. COMPUTER GRAPHICS	PP	100	40	46	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	44	P C		13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	40	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	43	P C		14. DATA STRUCTURES AND FILES	PP	100	40	53	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	40	P C		15. DATA COMMUNICATION	PP	100	40	47	P
06. DIGITAL LABORATORY	TW	50	20	33	P C		16. PROCESSOR INTERFACING LABORATORY	TW	25	10	18	P
07. DIGITAL LABORATORY	PR	50	20	32	P C		17. PROCESSOR INTERFACING LABORATORY	OR	50	20	41	P
08. PROGRAMMING LABORATORY	TW	50	20	30	P C		18. DATA STRUCTURES AND FILES LAB	TW	25	10	14	P
09. PROGRAMMING LABORATORY	PR	50	20	22	P		19. DATA STRUCTURES AND FILES LAB	PR	50	20	36	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	37	P C		20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	38	P
							21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	06	F

GRAND TOTAL = 763/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

S8058628	SWETAMBARI				ANJU		, 71101024C			PICT		S8058628
01. DISCRETE STRUCTURES	PP	100	40	67	P C		11. ENG MATHS III	PP	100	40	89	P
02. COMPUTER ORGANIZATION	PP	100	40	70	P C		12. COMPUTER GRAPHICS	PP	100	40	78	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	57	P C		13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	53	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	71	P C		14. DATA STRUCTURES AND FILES	PP	100	40	70	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	49	P C		15. DATA COMMUNICATION	PP	100	40	73	P
06. DIGITAL LABORATORY	TW	50	20	43	P C		16. PROCESSOR INTERFACING LABORATORY	TW	25	10	22	P
07. DIGITAL LABORATORY	PR	50	20	38	P C		17. PROCESSOR INTERFACING LABORATORY	OR	50	20	44	P
08. PROGRAMMING LABORATORY	TW	50	20	45	P C		18. DATA STRUCTURES AND FILES LAB	TW	25	10	18	P
09. PROGRAMMING LABORATORY	PR	50	20	38	P C		19. DATA STRUCTURES AND FILES LAB	PR	50	20	35	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	45	P C		20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	40	P
							21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	47	P

GRAND TOTAL = 1092/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

S8058629	TEJASH KUMAR				RITA		, 71045642F			PICT		S8058629
01. DISCRETE STRUCTURES	PP	100	40	27	F		11. ENG MATHS III	PP	100	40	18	F
02. COMPUTER ORGANIZATION	PP	100	40	44	P C		12. COMPUTER GRAPHICS	PP	100	40	22	F
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	40	P C		13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	24	F
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	40	P C		14. DATA STRUCTURES AND FILES	PP	100	40	28	F
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	40	P C		15. DATA COMMUNICATION	PP	100	40	23	F
06. DIGITAL LABORATORY	TW	50	20	24	P C		16. PROCESSOR INTERFACING LABORATORY	TW	25	10	10	P
07. DIGITAL LABORATORY	PR	50	20	06	F		17. PROCESSOR INTERFACING LABORATORY	OR	50	20	11	F
08. PROGRAMMING LABORATORY	TW	50	20	20	P C		18. DATA STRUCTURES AND FILES LAB	TW	25	10	10	P
09. PROGRAMMING LABORATORY	PR	50	20	20	P C		19. DATA STRUCTURES AND FILES LAB	PR	50	20	05	F
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	33	P C		20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	27	P
							21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	04	F

GRAND TOTAL = 476/1500, RESULT: FAILS

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8058630	TEKE AMRUTA PRAKASH				SHALINEE		, 71101028F			PICT		S8058630
01. DISCRETE STRUCTURES	PP	100	40	47	P C	11. ENG MATHS III	PP	100	40	25	F	
02. COMPUTER ORGANIZATION	PP	100	40	47	P C	12. COMPUTER GRAPHICS	PP	100	40	54	P	
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	42	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	44	P	
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	41	P C	14. DATA STRUCTURES AND FILES	PP	100	40	53	P	
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	40	P C	15. DATA COMMUNICATION	PP	100	40	54	P	
06. DIGITAL LABORATORY	TW	50	20	34	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	17	P	
07. DIGITAL LABORATORY	PR	50	20	22	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	28	P	
08. PROGRAMMING LABORATORY	TW	50	20	31	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	14	P	
09. PROGRAMMING LABORATORY	PR	50	20	20	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	26	P	
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	40	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	39	P	
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	30	P	

GRAND TOTAL = 748/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

S8058631	THORAT AKSHAY RAMAKANT				SUNANDA		, 71101034L			PICT		S8058631
01. DISCRETE STRUCTURES	PP	100	40	40	P C	11. ENG MATHS III	PP	100	40	51	P	
02. COMPUTER ORGANIZATION	PP	100	40	50	P C	12. COMPUTER GRAPHICS	PP	100	40	45	P	
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	67	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	40	P	
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	65	P C	14. DATA STRUCTURES AND FILES	PP	100	40	60	P	
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	41	P C	15. DATA COMMUNICATION	PP	100	40	58	P	
06. DIGITAL LABORATORY	TW	50	20	35	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	19	P	
07. DIGITAL LABORATORY	PR	50	20	40	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	38	P	
08. PROGRAMMING LABORATORY	TW	50	20	34	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	21	P	
09. PROGRAMMING LABORATORY	PR	50	20	27	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	30	P	
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	39	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	42	P	
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	40	P	

GRAND TOTAL = 882/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

S8058632	THOSARE YOGESH YASHWANT				USHADEVI		, 71101035J			PICT		S8058632
01. DISCRETE STRUCTURES	PP	100	40	54	P C	11. ENG MATHS III	PP	100	40	41	P	
02. COMPUTER ORGANIZATION	PP	100	40	54	P C	12. COMPUTER GRAPHICS	PP	100	40	63	P	
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	46	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	45	P	
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	54	P C	14. DATA STRUCTURES AND FILES	PP	100	40	68	P	
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	44	P C	15. DATA COMMUNICATION	PP	100	40	60	P	
06. DIGITAL LABORATORY	TW	50	20	39	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	21	P	
07. DIGITAL LABORATORY	PR	50	20	38	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	36	P	
08. PROGRAMMING LABORATORY	TW	50	20	41	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	16	P	
09. PROGRAMMING LABORATORY	PR	50	20	22	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	38	P	
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	41	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	39	P	
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	42	P	

GRAND TOTAL = 902/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8058633 TOSHNIWAL VIVEK ANILKUMAR SANJAYA , 71101039M , PICT , S8058633

01. DISCRETE STRUCTURES	PP	100	40	64	P C	11. ENG MATHS III	PP	100	40	61	P
02. COMPUTER ORGANIZATION	PP	100	40	56	P C	12. COMPUTER GRAPHICS	PP	100	40	59	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	63	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	31#	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	72	P C	14. DATA STRUCTURES AND FILES	PP	100	40	66	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	40	P C	15. DATA COMMUNICATION	PP	100	40	55	P
06. DIGITAL LABORATORY	TW	50	20	38	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	21	P
07. DIGITAL LABORATORY	PR	50	20	38	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	35	P
08. PROGRAMMING LABORATORY	TW	50	20	38	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	18	P
09. PROGRAMMING LABORATORY	PR	50	20	28	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	38	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	38	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	36	P
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	43	P

GRAND TOTAL = 938/1500, RESULT: FIRST CLASS # [0.4]

ORDN. 1 MARKS :

S8058634 TRIPATHI SHREYA BHARAT REENA , 71101040E , PICT , S8058634

01. DISCRETE STRUCTURES	PP	100	40	47	P C	11. ENG MATHS III	PP	100	40	46	P
02. COMPUTER ORGANIZATION	PP	100	40	44	P C	12. COMPUTER GRAPHICS	PP	100	40	43	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	53	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	51	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	54	P C	14. DATA STRUCTURES AND FILES	PP	100	40	41	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	42	P C	15. DATA COMMUNICATION	PP	100	40	63	P
06. DIGITAL LABORATORY	TW	50	20	27	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	19	P
07. DIGITAL LABORATORY	PR	50	20	39	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	42	P
08. PROGRAMMING LABORATORY	TW	50	20	27	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	12	P
09. PROGRAMMING LABORATORY	PR	50	20	34	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	38	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	37	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	33	P
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	30	P

GRAND TOTAL = 822+03/1500, RESULT: HIGHER SECOND CLASS [0.2]

ORDN. 1 MARKS :

S8058635 UTSAV DUSAD LAXMI , 71101042M , PICT , S8058635

01. DISCRETE STRUCTURES	PP	100	40	77	P C	11. ENG MATHS III	PP	100	40	91	P
02. COMPUTER ORGANIZATION	PP	100	40	62	P C	12. COMPUTER GRAPHICS	PP	100	40	66	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	68	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	56	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	73	P C	14. DATA STRUCTURES AND FILES	PP	100	40	66	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	44	P C	15. DATA COMMUNICATION	PP	100	40	60	P
06. DIGITAL LABORATORY	TW	50	20	41	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	22	P
07. DIGITAL LABORATORY	PR	50	20	36	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	40	P
08. PROGRAMMING LABORATORY	TW	50	20	45	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	22	P
09. PROGRAMMING LABORATORY	PR	50	20	34	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	43	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	46	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	45	P
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	46	P

GRAND TOTAL = 1083/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8058636 VARAT KAUSTUBH SOMKANT TILOTTAMA , 71101046D , PICT , S8058636

01. DISCRETE STRUCTURES	PP	100	40	58	P C	11. ENG MATHS III	PP	100	40	55	P
02. COMPUTER ORGANIZATION	PP	100	40	45	P C	12. COMPUTER GRAPHICS	PP	100	40	45	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	47	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	25	F
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	51	P C	14. DATA STRUCTURES AND FILES	PP	100	40	45	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	41	P C	15. DATA COMMUNICATION	PP	100	40	40	P
06. DIGITAL LABORATORY	TW	50	20	32	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	10	P
07. DIGITAL LABORATORY	PR	50	20	33	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	20	P
08. PROGRAMMING LABORATORY	TW	50	20	29	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	11	P
09. PROGRAMMING LABORATORY	PR	50	20	23	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	37	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	33	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	30	P
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	21	P

GRAND TOTAL = 731/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

S8058637 VICHARE GAURAV GIRISH VINA , 71101047B , PICT , S8058637

01. DISCRETE STRUCTURES	PP	100	40	64	P C	11. ENG MATHS III	PP	100	40	59	P
02. COMPUTER ORGANIZATION	PP	100	40	42	P C	12. COMPUTER GRAPHICS	PP	100	40	AA	F
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	46	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	AA	F
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	51	P C	14. DATA STRUCTURES AND FILES	PP	100	40	46	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	40	P C	15. DATA COMMUNICATION	PP	100	40	47	P
06. DIGITAL LABORATORY	TW	50	20	30	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	20	P
07. DIGITAL LABORATORY	PR	50	20	30	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	36	P
08. PROGRAMMING LABORATORY	TW	50	20	28	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	15	P
09. PROGRAMMING LABORATORY	PR	50	20	25	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	30	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	38	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	41	P
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	24	P

GRAND TOTAL = 712/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

S8058638 VIMAL BHAT KIRAN , 71101050B , PICT , S8058638

01. DISCRETE STRUCTURES	PP	100	40	58	P C	11. ENG MATHS III	PP	100	40	69	P
02. COMPUTER ORGANIZATION	PP	100	40	56	P C	12. COMPUTER GRAPHICS	PP	100	40	65	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	67	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	58	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	69	P C	14. DATA STRUCTURES AND FILES	PP	100	40	60	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	47	P C	15. DATA COMMUNICATION	PP	100	40	69	P
06. DIGITAL LABORATORY	TW	50	20	33	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	18	P
07. DIGITAL LABORATORY	PR	50	20	28	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	43	P
08. PROGRAMMING LABORATORY	TW	50	20	37	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	18	P
09. PROGRAMMING LABORATORY	PR	50	20	22	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	37	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	36	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	37	P
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	44	P

GRAND TOTAL = 971/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8058639	VIRWANI SUNNY JAMANLAL				KIRTI		, 71101051L			PICT		S8058639
01. DISCRETE STRUCTURES	PP	100	40	40	P C	11. ENG MATHS III	PP	100	40	23	F	
02. COMPUTER ORGANIZATION	PP	100	40	23	F	12. COMPUTER GRAPHICS	PP	100	40	40	P	
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	31	F	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	32	F	
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	45	P C	14. DATA STRUCTURES AND FILES	PP	100	40	41	P	
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	42	P C	15. DATA COMMUNICATION	PP	100	40	31	F	
06. DIGITAL LABORATORY	TW	50	20	28	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	10	P	
07. DIGITAL LABORATORY	PR	50	20	34	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	30	P	
08. PROGRAMMING LABORATORY	TW	50	20	33	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	11	P	
09. PROGRAMMING LABORATORY	PR	50	20	27	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	02	F	
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	34	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	30	P	
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	06	F	

GRAND TOTAL = 593/1500, RESULT: FAILS

ORDN. 1 MARKS :

S8058640	WADILE NITIN ADHAR				SUMAN		, 71101052J			PICT		S8058640
01. DISCRETE STRUCTURES	PP	100	40	51	P C	11. ENG MATHS III	PP	100	40	40	P	
02. COMPUTER ORGANIZATION	PP	100	40	46	P C	12. COMPUTER GRAPHICS	PP	100	40	29	F	
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	40	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	17	F	
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	48	P C	14. DATA STRUCTURES AND FILES	PP	100	40	51	P	
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	40	P C	15. DATA COMMUNICATION	PP	100	40	40	P	
06. DIGITAL LABORATORY	TW	50	20	35	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	10	P	
07. DIGITAL LABORATORY	PR	50	20	20	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	34	P	
08. PROGRAMMING LABORATORY	TW	50	20	37	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	12	P	
09. PROGRAMMING LABORATORY	PR	50	20	20	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	05	F	
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	37	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	32	P	
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	10	F	

GRAND TOTAL = 654/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

S8058642	WALKE AMRUTA GHANSHYAM				ASHA		, 71101054E			PICT		S8058642
01. DISCRETE STRUCTURES	PP	100	40	41	P C	11. ENG MATHS III	PP	100	40	63	P	
02. COMPUTER ORGANIZATION	PP	100	40	46	P C	12. COMPUTER GRAPHICS	PP	100	40	59	P	
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	50	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	59	P	
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	56	P C	14. DATA STRUCTURES AND FILES	PP	100	40	53	P	
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	53	P C	15. DATA COMMUNICATION	PP	100	40	53	P	
06. DIGITAL LABORATORY	TW	50	20	38	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	19	P	
07. DIGITAL LABORATORY	PR	50	20	21	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	34	P	
08. PROGRAMMING LABORATORY	TW	50	20	38	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	15	P	
09. PROGRAMMING LABORATORY	PR	50	20	32	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	28	P	
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	40	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	35	P	
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	28	P	

GRAND TOTAL = 861/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8058643	WANKHADE POOJA SUNIL				VIDYA			, 71101056M			PICT		S8058643
01. DISCRETE STRUCTURES	PP	100	40	57	P C	11. ENG MATHS III	PP	100	40	72	P		
02. COMPUTER ORGANIZATION	PP	100	40	69	P C	12. COMPUTER GRAPHICS	PP	100	40	65	P		
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	76	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	64	P		
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	58	P C	14. DATA STRUCTURES AND FILES	PP	100	40	58	P		
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	66	P C	15. DATA COMMUNICATION	PP	100	40	66	P		
06. DIGITAL LABORATORY	TW	50	20	35	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	21	P		
07. DIGITAL LABORATORY	PR	50	20	45	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	41	P		
08. PROGRAMMING LABORATORY	TW	50	20	34	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	20	P		
09. PROGRAMMING LABORATORY	PR	50	20	27	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	41	P		
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	38	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	44	P		
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	46	P		

GRAND TOTAL = 1043/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

S8058644	WAVAL BHAGYASHREE RAMESH				MEGHA			, 71241774F			PICT		S8058644
01. DISCRETE STRUCTURES	PP	100	40	55	P C	11. ENG MATHS III	PP	100	40	42	P		
02. COMPUTER ORGANIZATION	PP	100	40	67	P C	12. COMPUTER GRAPHICS	PP	100	40	64	P		
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	65	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	54	P		
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	52	P C	14. DATA STRUCTURES AND FILES	PP	100	40	57	P		
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	58	P C	15. DATA COMMUNICATION	PP	100	40	64	P		
06. DIGITAL LABORATORY	TW	50	20	43	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	22	P		
07. DIGITAL LABORATORY	PR	50	20	45	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	40	P		
08. PROGRAMMING LABORATORY	TW	50	20	45	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	21	P		
09. PROGRAMMING LABORATORY	PR	50	20	38	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	34	P		
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	44	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	39	P		
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	45	P		

GRAND TOTAL = 994/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

S8058645	WHABI JAIDEEP MAHESH				CHANDRIKA			, 71101058H			PICT		S8058645
01. DISCRETE STRUCTURES	PP	100	40	48	P C	11. ENG MATHS III	PP	100	40	40	P		
02. COMPUTER ORGANIZATION	PP	100	40	62	P C	12. COMPUTER GRAPHICS	PP	100	40	54	P		
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	43	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	47	P		
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	44	P C	14. DATA STRUCTURES AND FILES	PP	100	40	73	P		
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	41	P C	15. DATA COMMUNICATION	PP	100	40	47	P		
06. DIGITAL LABORATORY	TW	50	20	35	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	17	P		
07. DIGITAL LABORATORY	PR	50	20	37	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	40	P		
08. PROGRAMMING LABORATORY	TW	50	20	35	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	16	P		
09. PROGRAMMING LABORATORY	PR	50	20	20	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	40	P		
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	36	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	37	P		
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	41	P		

GRAND TOTAL = 853/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8058646	YEOLE SHRADDHA CHANDRASHEKHAR		HEMLATA		, 71101063D			PICT		S8058646
01. DISCRETE STRUCTURES	PP	100	40	43	P C	11. ENG MATHS III	PP	100	40	57 P
02. COMPUTER ORGANIZATION	PP	100	40	53	P C	12. COMPUTER GRAPHICS	PP	100	40	51 P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	71	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	60 P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	62	P C	14. DATA STRUCTURES AND FILES	PP	100	40	62 P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	56	P C	15. DATA COMMUNICATION	PP	100	40	49 P
06. DIGITAL LABORATORY	TW	50	20	37	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	17 P
07. DIGITAL LABORATORY	PR	50	20	45	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	28 P
08. PROGRAMMING LABORATORY	TW	50	20	34	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	14 P
09. PROGRAMMING LABORATORY	PR	50	20	30	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	35 P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	40	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	40 P
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	42 P

GRAND TOTAL = 926/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

S8058647	YUSRA AIJAZ SAHAF		SAMINA FARHAT		, 71101066J			PICT		S8058647
01. DISCRETE STRUCTURES	PP	100	40	52	P C	11. ENG MATHS III	PP	100	40	26 F
02. COMPUTER ORGANIZATION	PP	100	40	51	P C	12. COMPUTER GRAPHICS	PP	100	40	47 P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	49	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	40 P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	40	P C	14. DATA STRUCTURES AND FILES	PP	100	40	45 P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	51	P C	15. DATA COMMUNICATION	PP	100	40	51 P
06. DIGITAL LABORATORY	TW	50	20	34	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	17 P
07. DIGITAL LABORATORY	PR	50	20	34	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	30 P
08. PROGRAMMING LABORATORY	TW	50	20	32	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	16 P
09. PROGRAMMING LABORATORY	PR	50	20	23	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	27 P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	39	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	35 P
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	25 P

GRAND TOTAL = 764/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

S8058648	ANUJA WANGNOO		AMBICA		, 71045373G			PICT		S8058648
01. DISCRETE STRUCTURES	PP	100	40	60	P C	11. ENG MATHS III	PP	100	40	44 P C
02. COMPUTER ORGANIZATION	PP	100	40	48	P C	12. COMPUTER GRAPHICS	PP	100	40	49 P C
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	55	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	48 P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	59	P C	14. DATA STRUCTURES AND FILES	PP	100	40	45 P C
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	41	P C	15. DATA COMMUNICATION	PP	100	40	47 P C
06. DIGITAL LABORATORY	TW	50	20	32	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	12 P C
07. DIGITAL LABORATORY	PR	50	20	42	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	32 P C
08. PROGRAMMING LABORATORY	TW	50	20	33	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	15 P C
09. PROGRAMMING LABORATORY	PR	50	20	44	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	25 P C
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	34	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	36 P C
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	30 P C

GRAND TOTAL = 831/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8058653	BERDE SIDDESH VISHWANATH				VAISHALI				, 71073822G				PICT		S8058653
01. DISCRETE STRUCTURES	PP	100	40	40	P C	11. ENG MATHS III	PP	100	40	AA	F				
02. COMPUTER ORGANIZATION	PP	100	40	51	P C	12. COMPUTER GRAPHICS	PP	100	40	56	P C				
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	59	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	40	P C				
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	50	P C	14. DATA STRUCTURES AND FILES	PP	100	40	55	P C				
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	51	P C	15. DATA COMMUNICATION	PP	100	40	52	P C				
06. DIGITAL LABORATORY	TW	50	20	34	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	18	P C				
07. DIGITAL LABORATORY	PR	50	20	30	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	31	P C				
08. PROGRAMMING LABORATORY	TW	50	20	39	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	15	P C				
09. PROGRAMMING LABORATORY	PR	50	20	30	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	36	P C				
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	38	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	36	P C				
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	32	P C				

GRAND TOTAL = 793/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

S8058654	BHALERAO MANGESH GANESH				NANDA				, 70925361D				PICT		S8058654
01. DISCRETE STRUCTURES	PP	100	40	52	P C	11. ENG MATHS III	PP	100	40	44	P C				
02. COMPUTER ORGANIZATION	PP	100	40	40	P C	12. COMPUTER GRAPHICS	PP	100	40	51	P C				
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	45	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	40	P				
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	40	P C	14. DATA STRUCTURES AND FILES	PP	100	40	63	P C				
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	44	P C	15. DATA COMMUNICATION	PP	100	40	40	P C				
06. DIGITAL LABORATORY	TW	50	20	33	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	14	P C				
07. DIGITAL LABORATORY	PR	50	20	22	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	22	P C				
08. PROGRAMMING LABORATORY	TW	50	20	32	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	13	P C				
09. PROGRAMMING LABORATORY	PR	50	20	30	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	26	P C				
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	36	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	25	P C				
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	32	P C				

GRAND TOTAL = 744+06/1500, RESULT: SECOND CLASS [0.2]

ORDN. 1 MARKS :

S8058658	DEORE NIKHIL BAPUSAHEB				MANDAKINI				, 70801397L				PICT		S8058658
01. DISCRETE STRUCTURES	PP	100	40	53	P C	11. ENG MATHS III	PP	100	40	40	P C				
02. COMPUTER ORGANIZATION	PP	100	40	41	P C	12. COMPUTER GRAPHICS	PP	100	40	49	P C				
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	40	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	40	P C				
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	40	P C	14. DATA STRUCTURES AND FILES	PP	100	40	58	P C				
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	40	P C	15. DATA COMMUNICATION	PP	100	40	48	P C				
06. DIGITAL LABORATORY	TW	50	20	25	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	10	P C				
07. DIGITAL LABORATORY	PR	50	20	21	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	25	P				
08. PROGRAMMING LABORATORY	TW	50	20	33	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	10	P C				
09. PROGRAMMING LABORATORY	PR	50	20	28	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	25	P C				
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	32	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	20	P C				
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	25	P C				

GRAND TOTAL = 703/1500, RESULT: PASS CLASS

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8058660	DESHPANDE KIRTI VINOD				SHALINI					, 71134950K				PICT			S8058660
01. DISCRETE STRUCTURES	PP	100	40	44	P C	11. ENG MATHS III	PP	100	40	40	P						
02. COMPUTER ORGANIZATION	PP	100	40	49	P C	12. COMPUTER GRAPHICS	PP	100	40	58	P C						
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	60	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	53	P C						
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	55	P C	14. DATA STRUCTURES AND FILES	PP	100	40	54	P C						
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	51	P C	15. DATA COMMUNICATION	PP	100	40	69	P C						
06. DIGITAL LABORATORY	TW	50	20	29	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	19	P C						
07. DIGITAL LABORATORY	PR	50	20	22	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	27	P C						
08. PROGRAMMING LABORATORY	TW	50	20	37	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	16	P C						
09. PROGRAMMING LABORATORY	PR	50	20	27	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	29	P						
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	30	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	41	P C						
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	31	P C						

GRAND TOTAL = 841/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

S8058663	DIKE ASHISH VIJAY				RAJANI					, 71045418L				PICT			S8058663
01. DISCRETE STRUCTURES	PP	100	40	51	P C	11. ENG MATHS III	PP	100	40	40	P						
02. COMPUTER ORGANIZATION	PP	100	40	40	P C	12. COMPUTER GRAPHICS	PP	100	40	41	P C						
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	43	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	53	P						
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	40	P C	14. DATA STRUCTURES AND FILES	PP	100	40	45	P C						
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	50	P C	15. DATA COMMUNICATION	PP	100	40	50	P						
06. DIGITAL LABORATORY	TW	50	20	38	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	16	P C						
07. DIGITAL LABORATORY	PR	50	20	36	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	24	P C						
08. PROGRAMMING LABORATORY	TW	50	20	35	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	12	P C						
09. PROGRAMMING LABORATORY	PR	50	20	27	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	38	P						
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	38	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	34	P C						
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	30	P C						

GRAND TOTAL = 781/1500, RESULT: SECOND CLASS

ORDN. 1 MARKS :

S8058665	GHODE AMAR PRADEEPKUMAR				SINDHU					, 71045436J				PICT			S8058665
01. DISCRETE STRUCTURES	PP	100	40	52	P C	11. ENG MATHS III	PP	100	40	16	F						
02. COMPUTER ORGANIZATION	PP	100	40	42	P C	12. COMPUTER GRAPHICS	PP	100	40	42	P C						
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	44	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	40	P C						
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	40	P C	14. DATA STRUCTURES AND FILES	PP	100	40	49	P C						
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	41	P C	15. DATA COMMUNICATION	PP	100	40	45	P C						
06. DIGITAL LABORATORY	TW	50	20	20	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	11	P C						
07. DIGITAL LABORATORY	PR	50	20	33	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	22	P C						
08. PROGRAMMING LABORATORY	TW	50	20	30	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	12	P C						
09. PROGRAMMING LABORATORY	PR	50	20	33	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	30	P						
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	25	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	26	P C						
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	20	P C						

GRAND TOTAL = 673/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8058666	GUNDECHA ANIKET PRAKASH				UJWALA				, 71134959C				PICT		S8058666
01. DISCRETE STRUCTURES	PP	100	40	45	P C	11. ENG MATHS III	PP	100	40	AA	F				
02. COMPUTER ORGANIZATION	PP	100	40	52	P C	12. COMPUTER GRAPHICS	PP	100	40	49	P C				
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	40	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	40	P C				
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	53	P C	14. DATA STRUCTURES AND FILES	PP	100	40	41	P C				
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	57	P C	15. DATA COMMUNICATION	PP	100	40	69	P C				
06. DIGITAL LABORATORY	TW	50	20	35	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	10	P C				
07. DIGITAL LABORATORY	PR	50	20	21	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	24	P C				
08. PROGRAMMING LABORATORY	TW	50	20	33	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	11	P C				
09. PROGRAMMING LABORATORY	PR	50	20	37	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	35	P C				
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	36	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	27	P C				
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	20	P C				

GRAND TOTAL = 735/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

S8058668	JADHAV DEEPANJAN MADHAV				ARUNA				, 70925445J				PICT		S8058668
01. DISCRETE STRUCTURES	PP	100	40	40	P C	11. ENG MATHS III	PP	100	40	51	P C				
02. COMPUTER ORGANIZATION	PP	100	40	55	P C	12. COMPUTER GRAPHICS	PP	100	40	53	P C				
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	51	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	43	P C				
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	46	P C	14. DATA STRUCTURES AND FILES	PP	100	40	61	P C				
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	48	P C	15. DATA COMMUNICATION	PP	100	40	47	P C				
06. DIGITAL LABORATORY	TW	50	20	34	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	11	P C				
07. DIGITAL LABORATORY	PR	50	20	28	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	21	P C				
08. PROGRAMMING LABORATORY	TW	50	20	41	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	12	P C				
09. PROGRAMMING LABORATORY	PR	50	20	20	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	27	P				
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	41	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	34	P C				
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	21	P				

GRAND TOTAL = 785/1500, RESULT: SECOND CLASS

ORDN. 1 MARKS :

S8058669	JADHAV DHANRAJ KHANDERAU				CHHAYA				, 71134960G				PICT		S8058669
01. DISCRETE STRUCTURES	PP	100	40	49	P C	11. ENG MATHS III	PP	100	40	49	P				
02. COMPUTER ORGANIZATION	PP	100	40	65	P C	12. COMPUTER GRAPHICS	PP	100	40	53	P C				
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	59	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	53	P C				
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	75	P C	14. DATA STRUCTURES AND FILES	PP	100	40	71	P C				
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	53	P C	15. DATA COMMUNICATION	PP	100	40	68	P C				
06. DIGITAL LABORATORY	TW	50	20	39	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	20	P C				
07. DIGITAL LABORATORY	PR	50	20	30	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	43	P C				
08. PROGRAMMING LABORATORY	TW	50	20	42	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	23	P C				
09. PROGRAMMING LABORATORY	PR	50	20	46	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	35	P C				
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	38	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	46	P C				
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	32	P C				

GRAND TOTAL = 989/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8058673	KULKARNI ALOK DIPAK			GEETA		, 71045491M			PICT		S8058673
01. DISCRETE STRUCTURES	PP	100	40	AA	F	11. ENG MATHS III	PP	100	40	AA	F
02. COMPUTER ORGANIZATION	PP	100	40	AA	F	12. COMPUTER GRAPHICS	PP	100	40	AA	F
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	AA	F	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	AA	F
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	45	P C	14. DATA STRUCTURES AND FILES	PP	100	40	25	F
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	40	P C	15. DATA COMMUNICATION	PP	100	40	AA	F
06. DIGITAL LABORATORY	TW	50	20	22	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	15	P C
07. DIGITAL LABORATORY	PR	50	20	33	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	AA	F
08. PROGRAMMING LABORATORY	TW	50	20	35	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	15	P C
09. PROGRAMMING LABORATORY	PR	50	20	35	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	29	P C
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	37	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	35	P C
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	40	P C

GRAND TOTAL = 406/1500, RESULT: FAILS

RESULT RESERVED FOR BKLG

ORDN. 1 MARKS :

S8058674	KULKARNI PRAMOD SHIVAJIRAO			SUMATI		, 71045672H			PICT		S8058674
01. DISCRETE STRUCTURES	PP	100	40	40	P C	11. ENG MATHS III	PP	100	40	40	P C
02. COMPUTER ORGANIZATION	PP	100	40	52	P C	12. COMPUTER GRAPHICS	PP	100	40	43	P C
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	40	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	50	P C
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	42	P C	14. DATA STRUCTURES AND FILES	PP	100	40	50	P C
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	43	P C	15. DATA COMMUNICATION	PP	100	40	40	P C
06. DIGITAL LABORATORY	TW	50	20	20	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	19	P C
07. DIGITAL LABORATORY	PR	50	20	28	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	20	P
08. PROGRAMMING LABORATORY	TW	50	20	27	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	12	P C
09. PROGRAMMING LABORATORY	PR	50	20	27	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	38	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	23	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	32	P C
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	20	P C

GRAND TOTAL = 706/1500, RESULT: PASS CLASS

ORDN. 1 MARKS :

S8058676	MOHAMMED AAQUIB ANSARI MOHAMMED YUSUF			SUFIYA		, 71045526H			PICT		S8058676
01. DISCRETE STRUCTURES	PP	100	40	43	P C	11. ENG MATHS III	PP	100	40	40	P C
02. COMPUTER ORGANIZATION	PP	100	40	41	P C	12. COMPUTER GRAPHICS	PP	100	40	50	P C
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	45	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	40	P C
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	40	P C	14. DATA STRUCTURES AND FILES	PP	100	40	73	P C
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	40	P C	15. DATA COMMUNICATION	PP	100	40	45	P C
06. DIGITAL LABORATORY	TW	50	20	20	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	10	P C
07. DIGITAL LABORATORY	PR	50	20	AA	F	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	AA	F
08. PROGRAMMING LABORATORY	TW	50	20	20	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	13	P C
09. PROGRAMMING LABORATORY	PR	50	20	35	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	39	P C
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	20	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	20	P C
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	20	P C

GRAND TOTAL = 654/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8058677	MONISH PATEL	NEETA					, 70925508L	,	,	PICT	, S8058677				
01. DISCRETE STRUCTURES	PP	100	40	40	P C	11. ENG MATHS III			PP	100	40	40	P C		
02. COMPUTER ORGANIZATION	PP	100	40	40	P C	12. COMPUTER GRAPHICS			PP	100	40	40	P C		
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	62	P	13. PROCESSOR ARCHITECTURE & INTER.			PP	100	40	40	P C		
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	49	P C	14. DATA STRUCTURES AND FILES			PP	100	40	40	P C		
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	42	P C	15. DATA COMMUNICATION			PP	100	40	40	P C		
06. DIGITAL LABORATORY	TW	50	20	32	P C	16. PROCESSOR INTERFACING LABORATORY	TW		25	10	10	P C			
07. DIGITAL LABORATORY	PR	50	20	20	P C	17. PROCESSOR INTERFACING LABORATORY	OR		50	20	25	P C			
08. PROGRAMMING LABORATORY	TW	50	20	38	P C	18. DATA STRUCTURES AND FILES LAB	TW		25	10	10	P C			
09. PROGRAMMING LABORATORY	PR	50	20	30	P C	19. DATA STRUCTURES AND FILES LAB	PR		50	20	20	P C			
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	35	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW		50	20	20	P C			
						21. OBJECT ORIENTED PROGRAMMING LAB	PR		50	20	32	P C			

GRAND TOTAL = 705/1500, RESULT: PASS CLASS

ORDN. 1 MARKS :

S8058678	MULAY AMIT YESHWANT				SNEHAL		, 71045530F				PICT		S8058678
01. DISCRETE STRUCTURES	PP	100	40	61	P C		11. ENG MATHS III			PP	100	40	12 F
02. COMPUTER ORGANIZATION	PP	100	40	44	P C		12. COMPUTER GRAPHICS			PP	100	40	56 P C
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	44	P C		13. PROCESSOR ARCHITECTURE & INTER.			PP	100	40	40 P C
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	41	P C		14. DATA STRUCTURES AND FILES			PP	100	40	51 P C
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	53	P C		15. DATA COMMUNICATION			PP	100	40	40 P C
06. DIGITAL LABORATORY	TW	50	20	20	P C		16. PROCESSOR INTERFACING LABORATORY			TW	25	10	10 P C
07. DIGITAL LABORATORY	PR	50	20	38	P C		17. PROCESSOR INTERFACING LABORATORY			OR	50	20	20 P
08. PROGRAMMING LABORATORY	TW	50	20	25	P C		18. DATA STRUCTURES AND FILES LAB			TW	25	10	12 P C
09. PROGRAMMING LABORATORY	PR	50	20	29	P C		19. DATA STRUCTURES AND FILES LAB			PR	50	20	21 P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	25	P C		20. OBJECT ORIENTED PROGRAMMING LAB			TW	50	20	23 P C
							21. OBJECT ORIENTED PROGRAMMING LAB			PR	50	20	25 P C

GRAND TOTAL = 690/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

S8058680	MULLA ASMA MUSA				RABIYA		, 71134970D				PICT		S8058680
01. DISCRETE STRUCTURES	PP	100	40	49	P C		11. ENG MATHS III			PP	100	40	18 F
02. COMPUTER ORGANIZATION	PP	100	40	40	P C		12. COMPUTER GRAPHICS			PP	100	40	40 P C
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	40	P C		13. PROCESSOR ARCHITECTURE & INTER.			PP	100	40	40 P C
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	48	P C		14. DATA STRUCTURES AND FILES			PP	100	40	54 P C
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	44	P C		15. DATA COMMUNICATION			PP	100	40	43 P C
06. DIGITAL LABORATORY	TW	50	20	38	P C		16. PROCESSOR INTERFACING LABORATORY			TW	25	10	21 P C
07. DIGITAL LABORATORY	PR	50	20	22	P C		17. PROCESSOR INTERFACING LABORATORY			OR	50	20	29 P C
08. PROGRAMMING LABORATORY	TW	50	20	42	P C		18. DATA STRUCTURES AND FILES LAB			TW	25	10	22 P C
09. PROGRAMMING LABORATORY	PR	50	20	33	P C		19. DATA STRUCTURES AND FILES LAB			PR	50	20	22 P C
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	37	P C		20. OBJECT ORIENTED PROGRAMMING LAB			TW	50	20	43 P C
							21. OBJECT ORIENTED PROGRAMMING LAB			PR	50	20	20 P C

GRAND TOTAL = 745/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8058681	MUNOT PAYAL KAMLESHKUMAR				MEENAKSHI				, 70925517K				PICT		S8058681
01. DISCRETE STRUCTURES	PP	100	40	51	P C	11. ENG MATHS III	PP	100	40	26	F				
02. COMPUTER ORGANIZATION	PP	100	40	40	P C	12. COMPUTER GRAPHICS	PP	100	40	31	F				
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	50	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	29	F				
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	45	P C	14. DATA STRUCTURES AND FILES	PP	100	40	40	P C				
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	40	P C	15. DATA COMMUNICATION	PP	100	40	AA	F				
06. DIGITAL LABORATORY	TW	50	20	35	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	11	P C				
07. DIGITAL LABORATORY	PR	50	20	20	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	AA	F				
08. PROGRAMMING LABORATORY	TW	50	20	33	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	15	P C				
09. PROGRAMMING LABORATORY	PR	50	20	20	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	27	P C				
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	40	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	41	P C				
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	AA	F				

GRAND TOTAL = 594/1500, RESULT: FAILS

ORDN. 1 MARKS :

S8058682	N Y GARIN				YI MALIVATTEY				, 70925519F				PICT		S8058682
01. DISCRETE STRUCTURES	PP	100	40	45	P C	11. ENG MATHS III	PP	100	40	40	P C				
02. COMPUTER ORGANIZATION	PP	100	40	40	P C	12. COMPUTER GRAPHICS	PP	100	40	42	P C				
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	41	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	48	P				
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	40	P C	14. DATA STRUCTURES AND FILES	PP	100	40	40	P C				
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	40	P C	15. DATA COMMUNICATION	PP	100	40	40	P C				
06. DIGITAL LABORATORY	TW	50	20	33	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	16	P C				
07. DIGITAL LABORATORY	PR	50	20	30	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	35	P C				
08. PROGRAMMING LABORATORY	TW	50	20	35	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	18	P C				
09. PROGRAMMING LABORATORY	PR	50	20	30	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	40	P C				
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	38	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	44	P C				
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	29	P C				

GRAND TOTAL = 764/1500, RESULT: SECOND CLASS

ORDN. 1 MARKS :

S8058684	NILESH D PHADTARE				KUNDA				, 70925526J				PICT		S8058684
01. DISCRETE STRUCTURES	PP	100	40	43	P C	11. ENG MATHS III	PP	100	40	40	P C				
02. COMPUTER ORGANIZATION	PP	100	40	50	P C	12. COMPUTER GRAPHICS	PP	100	40	43	P C				
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	50	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	25	F				
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	40	P C	14. DATA STRUCTURES AND FILES	PP	100	40	72	P C				
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	40	P C	15. DATA COMMUNICATION	PP	100	40	43	P C				
06. DIGITAL LABORATORY	TW	50	20	35	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	20	P C				
07. DIGITAL LABORATORY	PR	50	20	31	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	20	P C				
08. PROGRAMMING LABORATORY	TW	50	20	43	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	23	P C				
09. PROGRAMMING LABORATORY	PR	50	20	32	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	25	P C				
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	36	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	44	P C				
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	41	P C				

GRAND TOTAL = 796/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8058685	PARTH SARTHI PIPLANI				SUREKHA					, 71045560H					PICT				S8058685
01. DISCRETE STRUCTURES	PP	100	40	56	P C	11. ENG MATHS III	PP	100	40	40	P C								
02. COMPUTER ORGANIZATION	PP	100	40	43	P C	12. COMPUTER GRAPHICS	PP	100	40	54	P C								
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	40	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	44	P								
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	45	P C	14. DATA STRUCTURES AND FILES	PP	100	40	72	P C								
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	44	P C	15. DATA COMMUNICATION	PP	100	40	40	P C								
06. DIGITAL LABORATORY	TW	50	20	22	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	10	P C								
07. DIGITAL LABORATORY	PR	50	20	23	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	28	P								
08. PROGRAMMING LABORATORY	TW	50	20	23	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	19	P C								
09. PROGRAMMING LABORATORY	PR	50	20	25	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	41	P C								
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	35	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	37	P C								
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	22	P C								

GRAND TOTAL = 763/1500, RESULT: SECOND CLASS

ORDN. 1 MARKS :

S8058686	PATEL NAZNIN JAVED				MAHERUNISSA					, 71134973J					PICT				S8058686
01. DISCRETE STRUCTURES	PP	100	40	40	P C	11. ENG MATHS III	PP	100	40	22	F								
02. COMPUTER ORGANIZATION	PP	100	40	40	P C	12. COMPUTER GRAPHICS	PP	100	40	48	P C								
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	45	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	40	P C								
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	44	P C	14. DATA STRUCTURES AND FILES	PP	100	40	64	P C								
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	46	P C	15. DATA COMMUNICATION	PP	100	40	53	P C								
06. DIGITAL LABORATORY	TW	50	20	35	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	14	P C								
07. DIGITAL LABORATORY	PR	50	20	40	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	22	P C								
08. PROGRAMMING LABORATORY	TW	50	20	42	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	19	P C								
09. PROGRAMMING LABORATORY	PR	50	20	22	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	35	P C								
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	43	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	41	P C								
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	35	P C								

GRAND TOTAL = 790/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

S8058687	POLE AKSHAYKUMAR RAHUL				SHEELA					, 71045576D					PICT				S8058687
01. DISCRETE STRUCTURES	PP	100	40	43	P C	11. ENG MATHS III	PP	100	40	40	P C								
02. COMPUTER ORGANIZATION	PP	100	40	41	P C	12. COMPUTER GRAPHICS	PP	100	40	44	P C								
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	40	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	40	P C								
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	49	P C	14. DATA STRUCTURES AND FILES	PP	100	40	46	P C								
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	47	P C	15. DATA COMMUNICATION	PP	100	40	49	P C								
06. DIGITAL LABORATORY	TW	50	20	30	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	12	P C								
07. DIGITAL LABORATORY	PR	50	20	20	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	20	P								
08. PROGRAMMING LABORATORY	TW	50	20	35	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	14	P C								
09. PROGRAMMING LABORATORY	PR	50	20	29	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	20	P C								
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	38	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	27	P C								
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	32	P C								

GRAND TOTAL = 716/1500, RESULT: PASS CLASS

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8058690	PUNTAMBEKAR SHREYA SHAILESH			SEEMA			, 71045584E			PICT		S8058690
01. DISCRETE STRUCTURES	PP	100	40	40	P C	11. ENG MATHS III	PP	100	40	40	P	
02. COMPUTER ORGANIZATION	PP	100	40	44	P C	12. COMPUTER GRAPHICS	PP	100	40	40	P C	
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	40	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	40	P C	
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	40	P C	14. DATA STRUCTURES AND FILES	PP	100	40	40	P C	
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	48	P C	15. DATA COMMUNICATION	PP	100	40	40	P C	
06. DIGITAL LABORATORY	TW	50	20	27	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	19	P C	
07. DIGITAL LABORATORY	PR	50	20	22	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	20	P C	
08. PROGRAMMING LABORATORY	TW	50	20	27	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	13	P C	
09. PROGRAMMING LABORATORY	PR	50	20	33	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	25	P	
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	23	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	35	P C	
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	20	P C	

GRAND TOTAL = 676/1500, RESULT: PASS CLASS

ORDN. 1 MARKS :

S8058693	SARTHAK MAJITHIA			POOJA			, 71045598E			PICT		S8058693
01. DISCRETE STRUCTURES	PP	100	40	63	P C	11. ENG MATHS III	PP	100	40	19	F	
02. COMPUTER ORGANIZATION	PP	100	40	52	P C	12. COMPUTER GRAPHICS	PP	100	40	57	P C	
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	62	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	41	P	
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	63	P C	14. DATA STRUCTURES AND FILES	PP	100	40	42	P C	
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	48	P C	15. DATA COMMUNICATION	PP	100	40	48	P C	
06. DIGITAL LABORATORY	TW	50	20	20	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	10	P C	
07. DIGITAL LABORATORY	PR	50	20	30	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	20	P C	
08. PROGRAMMING LABORATORY	TW	50	20	20	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	15	P C	
09. PROGRAMMING LABORATORY	PR	50	20	48	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	43	P C	
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	30	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	20	P C	
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	39	P C	

GRAND TOTAL = 790/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

S8058694	SARWAR PRIYANKA ASHOK			MEERA			, 71045599C			PICT		S8058694
01. DISCRETE STRUCTURES	PP	100	40	59	P C	11. ENG MATHS III	PP	100	40	40	P C	
02. COMPUTER ORGANIZATION	PP	100	40	44	P C	12. COMPUTER GRAPHICS	PP	100	40	50	P C	
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	52	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	40	P C	
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	49	P C	14. DATA STRUCTURES AND FILES	PP	100	40	55	P C	
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	55	P C	15. DATA COMMUNICATION	PP	100	40	46	P C	
06. DIGITAL LABORATORY	TW	50	20	31	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	19	P C	
07. DIGITAL LABORATORY	PR	50	20	22	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	20	P C	
08. PROGRAMMING LABORATORY	TW	50	20	38	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	14	P C	
09. PROGRAMMING LABORATORY	PR	50	20	30	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	25	P	
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	31	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	38	P C	
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	20	P C	

GRAND TOTAL = 778/1500, RESULT: SECOND CLASS

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8058695	SETHI SUPREET SATISHKUMAR					PREETI					, 71045605M	,		PICT					S8058695
01. DISCRETE STRUCTURES	PP	100	40	50	P C	11. ENG MATHS III	PP	100	40	40	P C								
02. COMPUTER ORGANIZATION	PP	100	40	49	P C	12. COMPUTER GRAPHICS	PP	100	40	40	P C								
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	53	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	43	P								
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	47	P C	14. DATA STRUCTURES AND FILES	PP	100	40	48	P C								
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	40	P C	15. DATA COMMUNICATION	PP	100	40	61	P C								
06. DIGITAL LABORATORY	TW	50	20	27	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	13	P C								
07. DIGITAL LABORATORY	PR	50	20	25	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	32	P C								
08. PROGRAMMING LABORATORY	TW	50	20	30	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	13	P C								
09. PROGRAMMING LABORATORY	PR	50	20	40	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	30	P C								
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	26	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	36	P C								
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	42	P C								

GRAND TOTAL = 785/1500, RESULT: SECOND CLASS

ORDN. 1 MARKS :

S8058696	SHEWALE ANULA TUSHAR					NEETA					, 71045613B	,		PICT					S8058696
01. DISCRETE STRUCTURES	PP	100	40	49	P C	11. ENG MATHS III	PP	100	40	57	P C								
02. COMPUTER ORGANIZATION	PP	100	40	50	P C	12. COMPUTER GRAPHICS	PP	100	40	40	P C								
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	40	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	40	P C								
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	42	P C	14. DATA STRUCTURES AND FILES	PP	100	40	55	P C								
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	40	P C	15. DATA COMMUNICATION	PP	100	40	40	P C								
06. DIGITAL LABORATORY	TW	50	20	27	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	16	P C								
07. DIGITAL LABORATORY	PR	50	20	30	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	24	P C								
08. PROGRAMMING LABORATORY	TW	50	20	29	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	14	P C								
09. PROGRAMMING LABORATORY	PR	50	20	38	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	26	P								
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	31	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	37	P C								
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	25	P C								

GRAND TOTAL = 750/1500, RESULT: SECOND CLASS

ORDN. 1 MARKS :

S8058700	SOMANI NEHA NANDKISHOR					ARATI					, 71045628L	,		PICT					S8058700
01. DISCRETE STRUCTURES	PP	100	40	52	P C	11. ENG MATHS III	PP	100	40	56	P C								
02. COMPUTER ORGANIZATION	PP	100	40	51	P C	12. COMPUTER GRAPHICS	PP	100	40	49	P C								
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	52	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	44	P								
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	40	P C	14. DATA STRUCTURES AND FILES	PP	100	40	67	P C								
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	51	P C	15. DATA COMMUNICATION	PP	100	40	69	P C								
06. DIGITAL LABORATORY	TW	50	20	26	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	17	P C								
07. DIGITAL LABORATORY	PR	50	20	37	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	23	P C								
08. PROGRAMMING LABORATORY	TW	50	20	28	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	15	P C								
09. PROGRAMMING LABORATORY	PR	50	20	20	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	37	P C								
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	36	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	41	P C								
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	35	P C								

GRAND TOTAL = 846/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8058703	SUYASH PANDEY					SHAILA					, 70925625G	,		PICT					S8058703
01. DISCRETE STRUCTURES	PP	100	40	58	P C	11. ENG MATHS III	PP	100	40	40	P C								
02. COMPUTER ORGANIZATION	PP	100	40	48	P C	12. COMPUTER GRAPHICS	PP	100	40	40	P C								
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	46	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	26	F								
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	50	P C	14. DATA STRUCTURES AND FILES	PP	100	40	40	P C								
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	40	P C	15. DATA COMMUNICATION	PP	100	40	40	P C								
06. DIGITAL LABORATORY	TW	50	20	21	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	10	P C								
07. DIGITAL LABORATORY	PR	50	20	27	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	AA	F								
08. PROGRAMMING LABORATORY	TW	50	20	23	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	10	P C								
09. PROGRAMMING LABORATORY	PR	50	20	30	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	20	P C								
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	25	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	20	P C								
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	32	P C								

GRAND TOTAL = 646/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

S8058705	THOMBRE PRIYANKA MILIND				JAYMALA			, 71045649C			PICT		S8058705		
01. DISCRETE STRUCTURES	PP	100	40	58	P C			11. ENG MATHS III			PP	100	40	53	P C
02. COMPUTER ORGANIZATION	PP	100	40	51	P			12. COMPUTER GRAPHICS			PP	100	40	53	P C
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	50	P C			13. PROCESSOR ARCHITECTURE & INTER.			PP	100	40	40	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	55	P C			14. DATA STRUCTURES AND FILES			PP	100	40	60	P C
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	47	P C			15. DATA COMMUNICATION			PP	100	40	40	P C
06. DIGITAL LABORATORY	TW	50	20	36	P C			16. PROCESSOR INTERFACING LABORATORY			TW	25	10	20	P C
07. DIGITAL LABORATORY	PR	50	20	20	P C			17. PROCESSOR INTERFACING LABORATORY			OR	50	20	37	P C
08. PROGRAMMING LABORATORY	TW	50	20	37	P C			18. DATA STRUCTURES AND FILES LAB			TW	25	10	17	P C
09. PROGRAMMING LABORATORY	PR	50	20	42	P C			19. DATA STRUCTURES AND FILES LAB			PR	50	20	28	P C
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	36	P C			20. OBJECT ORIENTED PROGRAMMING LAB			TW	50	20	42	P C
								21. OBJECT ORIENTED PROGRAMMING LAB			PR	50	20	30	P C

GRAND TOTAL = 852/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

S8058706	TIWARI ROHIT VINOD					SHAKUNTALA					, 71045652C , , PICT , S8058706				
01. DISCRETE STRUCTURES	PP	100	40	47	P C	11. ENG MATHS III	PP	100	40	40	P C				
02. COMPUTER ORGANIZATION	PP	100	40	40	P C	12. COMPUTER GRAPHICS	PP	100	40	40	P C				
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	43	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	40	P C				
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	46	P C	14. DATA STRUCTURES AND FILES	PP	100	40	47	P C				
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	46	P C	15. DATA COMMUNICATION	PP	100	40	53	P C				
06. DIGITAL LABORATORY	TW	50	20	20	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	13	P C				
07. DIGITAL LABORATORY	PR	50	20	35	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	30	P C				
08. PROGRAMMING LABORATORY	TW	50	20	28	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	11	P C				
09. PROGRAMMING LABORATORY	PR	50	20	32	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	29	P				
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	27	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	30	P C				
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	35	P C				

GRAND TOTAL = 732/1500, RESULT: PASS CLASS

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8058707	UPPOD BALAJI SAMBHAJI				SHOBHA					, 71045653M				PICT			S8058707
01. DISCRETE STRUCTURES	PP	100	40	40	P C	11. ENG MATHS III	PP	100	40	46	P						
02. COMPUTER ORGANIZATION	PP	100	40	51	P C	12. COMPUTER GRAPHICS	PP	100	40	40	P C						
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	40	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	40	P C						
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	40	P C	14. DATA STRUCTURES AND FILES	PP	100	40	43	P C						
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	45	P C	15. DATA COMMUNICATION	PP	100	40	47	P C						
06. DIGITAL LABORATORY	TW	50	20	35	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	13	P C						
07. DIGITAL LABORATORY	PR	50	20	23	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	28	P C						
08. PROGRAMMING LABORATORY	TW	50	20	34	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	13	P C						
09. PROGRAMMING LABORATORY	PR	50	20	20	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	33	P						
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	38	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	37	P C						
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	20	P C						

GRAND TOTAL = 726/1500, RESULT: PASS CLASS

ORDN. 1 MARKS :

S8058710	WAGH ABHISHEK KARBHARI				KRANTI					, 70925654L				PICT			S8058710
01. DISCRETE STRUCTURES	PP	100	40	46	P C	11. ENG MATHS III	PP	100	40	AA	F						
02. COMPUTER ORGANIZATION	PP	100	40	40	P C	12. COMPUTER GRAPHICS	PP	100	40	40	P C						
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	40	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	AA	F						
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	40	P C	14. DATA STRUCTURES AND FILES	PP	100	40	43	P C						
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	40	P C	15. DATA COMMUNICATION	PP	100	40	AA	F						
06. DIGITAL LABORATORY	TW	50	20	25	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	10	P C						
07. DIGITAL LABORATORY	PR	50	20	30	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	20	P C						
08. PROGRAMMING LABORATORY	TW	50	20	25	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	10	P C						
09. PROGRAMMING LABORATORY	PR	50	20	28	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	20	P C						
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	21	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	20	P C						
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	22	P C						

GRAND TOTAL = 520/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

S8058711	PAWAR CHANDAN DILIP				REKHA					, 70925555B				PICT			S8058711
01. DISCRETE STRUCTURES	PP	100	40	40	P C	11. ENG MATHS III	PP	100	40	40	P C						
02. COMPUTER ORGANIZATION	PP	100	40	40	P C	12. COMPUTER GRAPHICS	PP	100	40	54	P C						
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	40	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	40	P C						
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	50	P C	14. DATA STRUCTURES AND FILES	PP	100	40	49	P C						
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	53	P C	15. DATA COMMUNICATION	PP	100	40	42	P C						
06. DIGITAL LABORATORY	TW	50	20	29	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	13	P C						
07. DIGITAL LABORATORY	PR	50	20	25	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	36	P C						
08. PROGRAMMING LABORATORY	TW	50	20	41	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	13	P C						
09. PROGRAMMING LABORATORY	PR	50	20	30	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	33	P						
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	40	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	32	P C						
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	32	P						

GRAND TOTAL = 772/1500, RESULT: SECOND CLASS

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8058712 MAHAJAN SACHIN RAVINDRA						SARALA , 70701521K , PICT , S8058712					
01. DISCRETE STRUCTURES	PP	100	40	AA	F	11. ENG MATHS III	PP	100	40	40	P
02. COMPUTER ORGANIZATION	PP	100	40	AA	F	12. COMPUTER GRAPHICS	PP	100	40	48	P C
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	AA	F	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	40	P C
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	AA	F	14. DATA STRUCTURES AND FILES	PP	100	40	45	P C
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	AA	F	15. DATA COMMUNICATION	PP	100	40	57	P C
06. DIGITAL LABORATORY	TW	50	20	AA	F	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	11	P C
07. DIGITAL LABORATORY	PR	50	20	AA	F	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	20	P C
08. PROGRAMMING LABORATORY	TW	50	20	AA	F	18. DATA STRUCTURES AND FILES LAB	TW	25	10	12	P C
09. PROGRAMMING LABORATORY	PR	50	20	AA	F	19. DATA STRUCTURES AND FILES LAB	PR	50	20	25	P C
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	AA	F	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	24	P C
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	26	P C

GRAND TOTAL = 348/1500, RESULT: FAILS

ORDN. 1 MARKS :