DATE: 14 AUG. 2012 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 01 (372)

NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71100709J , , PICT , S8053001 MAMTA S8053001 AAKASH SHINDE PP 100 40 72 P 01. SIGNAL AND SYSTEMS PP 100 40 40 P C 11. ENGINEERING MATHEMATICS III O2. SIGNAL AND SYSTEMS OR 20 20 P C 25 10 23 P 50 12. ENGINEERING MATHEMATICS III TW 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 40 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 40 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 37 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 20 P 05. NETWORK ANALYSIS PP 100 PP 100 40 41 P C 15. ELECTROMAGNETIC 40 40 P PP 100 40 42 P C 25 10 20 P 06. DIGITAL LOGIC DESIGN 16. ELECTROMAGNETIC TW 100 07. DIGITAL LOGIC DESIGN 50 20 21 P C 40 54 P PR 17. DATA STRUCTURES PP 08. POWER DEVICES AND MACHINES PP 100 40 40 P C 50 20 18. DATA STRUCTURES PR 35 P 09. NETWORK AND POWER LAB. TW 50 20 36 P C 100 40 40 P 19. COMMUNICATION THEORY PP 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 33 P C 50 20 22 P 20. COMMUNICATION THEORY OR 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 42 P GRAND TOTAL = 758/1500, RESULT: SECOND CLASS ORDN. 1 MARKS: S8053002 AASHIQ HANEEF AHMED RASHEEDA , s8053002 PP 100 40 85 P C 11. ENGINEERING MATHEMATICS III PP 100 40 88 P 01. SIGNAL AND SYSTEMS 02. SIGNAL AND SYSTEMS OR 50 20 43 P C 12. ENGINEERING MATHEMATICS III TW 25 10 24 P 40 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 63 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 63 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 50 20 40 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 42 P PP 100 PP 100 79 P 05. NETWORK ANALYSIS 40 67 P.C 15. ELECTROMAGNETIC 40 PP 100 40 84 P C 16. ELECTROMAGNETIC 25 24 P 06. DIGITAL LOGIC DESIGN TW 10 50 20 28 P C 100 40 07. DIGITAL LOGIC DESIGN PR 17. DATA STRUCTURES PP 65 P 08. POWER DEVICES AND MACHINES PP 100 40 50 P C 18. DATA STRUCTURES PR 50 20 48 P 09. NETWORK AND POWER LAB. TW 50 20 43 P C 19. COMMUNICATION THEORY PP 100 40 73 P 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 43 P C 50 20 37 P 20. COMMUNICATION THEORY OR 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 42 P GRAND TOTAL = 1131/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS: , 71100714E , , PICT , s8053003 S8053003 ABHISHEK KUMAR KIRAN DEVI 01. SIGNAL AND SYSTEMS PP 100 40 61 P C 11. ENGINEERING MATHEMATICS III PP 100 40 83 P 12. ENGINEERING MATHEMATICS III TW 25 10 02. SIGNAL AND SYSTEMS or 50 20 32 P 17 P 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 55 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 48 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 39 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 30 P 05. NETWORK ANALYSIS PP 100 40 43 P C PP 100 40 60 P 15. ELECTROMAGNETIC 25 06. DIGITAL LOGIC DESIGN PP 100 40 64 P C 16. ELECTROMAGNETIC TW 10 15 P 07. DIGITAL LOGIC DESIGN 20 29 P C 100 40 53 P PR 50 17. DATA STRUCTURES PP 08. POWER DEVICES AND MACHINES PP 100 40 55 P C 18. DATA STRUCTURES PR 50 20 38 P 09. NETWORK AND POWER LAB. TW 50 20 29 P C 100 40 52 P 19. COMMUNICATION THEORY 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 30 P C 20. COMMUNICATION THEORY 50 20 27 P OR 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 34 P GRAND TOTAL = 894+06/1500, RESULT: FIRST CLASS [0.2] ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71100715C , , PICT , S8053004 KANTA S8053004 ABHISHEK SHARMA 01. SIGNAL AND SYSTEMS PP 100 40 72 P C 11. ENGINEERING MATHEMATICS III PP 100 40 80 P 02. SIGNAL AND SYSTEMS OR 50 20 28 P C 12. ENGINEERING MATHEMATICS III TW 25 10 18 P 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 53 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 52 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 30 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 36 P 05. NETWORK ANALYSIS PP 100 40 59 P C 15. ELECTROMAGNETIC PP 100 40 76 P PP 100 40 70 P C 25 10 16 P 06. DIGITAL LOGIC DESIGN 16. ELECTROMAGNETIC TW 100 07. DIGITAL LOGIC DESIGN 50 20 23 P C PP 40 68 P PR 17. DATA STRUCTURES 08. POWER DEVICES AND MACHINES PP 100 40 47 P C 50 20 18. DATA STRUCTURES PR 09. NETWORK AND POWER LAB. TW 50 20 36 P C 100 40 44 P 19. COMMUNICATION THEORY PP 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 40 P C 50 20 32 P 20. COMMUNICATION THEORY OR 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 37 P GRAND TOTAL = 961/1500, RESULT: FIRST CLASS ORDN. 1 MARKS: , 71100716M , , PICT , S8053005 S8053005 ABIZAR HUSSAIN NASEEM PP 100 40 50 P C 11. ENGINEERING MATHEMATICS III PP 100 40 81 P 01. SIGNAL AND SYSTEMS 02. SIGNAL AND SYSTEMS OR 50 20 33 P C 12. ENGINEERING MATHEMATICS III TW 25 10 21 P 40 59 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 03. SOLID STATES DEVICES AND CIRCUITSPP 100 31# P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 39 P C 50 20 30 P 14. INTEGRATED CIRCUITS APPLICATIONS PR PP 100 40 59 P C PP 100 05. NETWORK ANALYSIS 15. ELECTROMAGNETIC 40 64 P PP 100 40 57 P C 16. ELECTROMAGNETIC 25 19 P 06. DIGITAL LOGIC DESIGN TW 10 50 20 37 P C 100 07. DIGITAL LOGIC DESIGN PR 17. DATA STRUCTURES PP 40 58 P 08. POWER DEVICES AND MACHINES PP 100 40 42 P C 18. DATA STRUCTURES PR 50 20 44 P 09. NETWORK AND POWER LAB. TW 50 20 41 P C 40 56 P 19. COMMUNICATION THEORY PP 100 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 37 P C 50 20 35 P 20. COMMUNICATION THEORY OR 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 36 P GRAND TOTAL = 929/1500, RESULT: FIRST CLASS # [0.4]ORDN. 1 MARKS: , s8053006 S8053006 ADITYA KALIA NEELAM 01. SIGNAL AND SYSTEMS PP 100 40 40 P C 11. ENGINEERING MATHEMATICS III PP 100 40 40 P 12. ENGINEERING MATHEMATICS III TW 25 10 02. SIGNAL AND SYSTEMS or 50 20 22 P 14 P 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 43 P 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 29 F 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 20 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 05 F 05. NETWORK ANALYSIS PP 100 40 40 P PP 100 40 47 P 15. ELECTROMAGNETIC 25 06. DIGITAL LOGIC DESIGN PP 100 40 42 P C 16. ELECTROMAGNETIC TW 10 15 P 07. DIGITAL LOGIC DESIGN 20 20 P C 100 40 40 P PR 50 17. DATA STRUCTURES PP 08. POWER DEVICES AND MACHINES PP 100 40 P C 18. DATA STRUCTURES PR 50 20 38 P 09. NETWORK AND POWER LAB. 100 TW 50 20 25 P C 40 40 P 19. COMMUNICATION THEORY 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 29 P C 50 20 15 F 20. COMMUNICATION THEORY OR 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 34 P GRAND TOTAL = 638/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71100721H , , PICT , S8053007 S8053007 AGRAWAL PRANAT ADITYA ASHA PP 100 40 47 P C 11. ENGINEERING MATHEMATICS III PP 100 40 25 F 01. SIGNAL AND SYSTEMS or 50 20 28 P 25 10 17 P 02. SIGNAL AND SYSTEMS 12. ENGINEERING MATHEMATICS III TW 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 40 P 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 23 F 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 20 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 23 P 05. NETWORK ANALYSIS PP 100 PP 100 40 40 P C 15. ELECTROMAGNETIC 40 40 P PP 100 40 40 P C 25 10 17 P 06. DIGITAL LOGIC DESIGN 16. ELECTROMAGNETIC TW 100 07. DIGITAL LOGIC DESIGN 50 20 30 P C PP 40 45 P PR 17. DATA STRUCTURES 08. POWER DEVICES AND MACHINES PP 100 40 P 50 20 37 P 18. DATA STRUCTURES PR 09. NETWORK AND POWER LAB. TW 50 20 33 P C 100 40 19. COMMUNICATION THEORY PP 31 F 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 30 P C 50 20 21 P 20. COMMUNICATION THEORY OR 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 36 P GRAND TOTAL = 663/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS: , 71100723D , , PICT S8053008 AIKAL SANKET KABIRNATH SANGEETHA , s8053008 PP 100 40 51 P C 11. ENGINEERING MATHEMATICS III PP 100 40 78 P 01. SIGNAL AND SYSTEMS OR 50 20 42 P.C. 02. SIGNAL AND SYSTEMS 12. ENGINEERING MATHEMATICS III TW 25 10 17 P 41 P 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 41 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 50 20 36 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 36 P PP 100 PP 100 05. NETWORK ANALYSIS 40 53 P C 15. ELECTROMAGNETIC 40 45 P PP 100 40 67 P C 16. ELECTROMAGNETIC 25 06. DIGITAL LOGIC DESIGN TW 10 17 P 50 20 30 P 100 40 07. DIGITAL LOGIC DESIGN PR 17. DATA STRUCTURES PP 56 P 08. POWER DEVICES AND MACHINES PP 100 40 46 P C 18. DATA STRUCTURES PR 50 20 36 P 09. NETWORK AND POWER LAB. TW 50 20 33 P C 19. COMMUNICATION THEORY PP 100 40 47 P 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 35 P C 50 20 25 P 20. COMMUNICATION THEORY OR 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 38 P GRAND TOTAL = 870/1500, RESULT: HIGHER SECOND CLASS ORDN. 1 MARKS: , 71100724B , , PICT , s8053009 S8053009 ALI ASGER MODI ZEENAT 01. SIGNAL AND SYSTEMS PP 100 40 41 P C 11. ENGINEERING MATHEMATICS III PP 100 40 40 P 12. ENGINEERING MATHEMATICS III TW 25 10 02. SIGNAL AND SYSTEMS OR 50 20 22 P C 14 P 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 40 P 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 20 F 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 30 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 25 P 05. NETWORK ANALYSIS PP 100 40 32 F PP 100 40 15 F 15. ELECTROMAGNETIC 25 15 P 06. DIGITAL LOGIC DESIGN PP 100 40 42 P C 16. ELECTROMAGNETIC TW 10 07. DIGITAL LOGIC DESIGN 20 25 P C 100 40 54 P PR 50 17. DATA STRUCTURES PP 08. POWER DEVICES AND MACHINES PP 100 40 43 P 18. DATA STRUCTURES PR 50 20 37 P 09. NETWORK AND POWER LAB. TW 50 20 34 P C 100 40 40 P 19. COMMUNICATION THEORY 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 30 P C 50 20 25 P 20. COMMUNICATION THEORY OR 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 30 P GRAND TOTAL = 654/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71100726J , , PICT , S8053010 S8053010 AMBURE GANESH VITHALRAO ANITA PP 100 40 57 P C 11. ENGINEERING MATHEMATICS III PP 100 40 74 P 01. SIGNAL AND SYSTEMS 02. SIGNAL AND SYSTEMS OR 50 20 38 P C 25 10 12 P 12. ENGINEERING MATHEMATICS III TW 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 75 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 53 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 26 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 34 P 05. NETWORK ANALYSIS PP 100 PP 100 40 55 P C 15. ELECTROMAGNETIC 40 53 P PP 100 40 53 P C 25 10 13 P 06. DIGITAL LOGIC DESIGN 16. ELECTROMAGNETIC TW 50 20 27 P C 100 07. DIGITAL LOGIC DESIGN 40 53 P PR 17. DATA STRUCTURES PP 08. POWER DEVICES AND MACHINES PP 100 40 40 P C 50 20 18. DATA STRUCTURES PR 36 P 09. NETWORK AND POWER LAB. TW 50 20 24 P C 100 40 68 P 19. COMMUNICATION THEORY PP 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 31 P C 50 20 25 P 20. COMMUNICATION THEORY OR 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 33 P GRAND TOTAL = 880/1500, RESULT: HIGHER SECOND CLASS ORDN. 1 MARKS: , 71100731E , , PICT , S8053011 S8053011 ANUPAM BHATTACHARJEE MOUSUMI PP 100 40 64 P C 11. ENGINEERING MATHEMATICS III PP 100 40 83 P 01. SIGNAL AND SYSTEMS 02. SIGNAL AND SYSTEMS OR 50 20 42 P C 12. ENGINEERING MATHEMATICS III TW 25 10 17 P 61 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 40 p 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 43 P C 50 20 14. INTEGRATED CIRCUITS APPLICATIONS PR 44 P 05. NETWORK ANALYSIS PP 100 PP 100 40 49 P C 15. ELECTROMAGNETIC 40 68 P PP 100 40 70 P C 16. ELECTROMAGNETIC 25 06. DIGITAL LOGIC DESIGN TW 10 17 P 50 20 39 P C 100 07. DIGITAL LOGIC DESIGN PR 17. DATA STRUCTURES PP 40 61 P 08. POWER DEVICES AND MACHINES PP 100 40 56 P C 18. DATA STRUCTURES PR 50 20 46 P 09. NETWORK AND POWER LAB. TW 50 20 40 P C 19. COMMUNICATION THEORY PP 100 40 49 P 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 39 P C 50 20 37 P 20. COMMUNICATION THEORY OR 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 31 P GRAND TOTAL = 996/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS: , 71236306J , , PICT , s8053012 S8053012 BAGALE PRASHANT MUTYAPPA MAHADEVI 40 P 01. SIGNAL AND SYSTEMS PP 100 40 11. ENGINEERING MATHEMATICS III PP 100 40 23 F 02. SIGNAL AND SYSTEMS 12. ENGINEERING MATHEMATICS III TW 25 10 or 50 20 34 P C 18 P 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 60 P 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 63 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 20 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 30 P 05. NETWORK ANALYSIS PP 100 40 40 P PP 100 40 24 F 15. ELECTROMAGNETIC 25 06. DIGITAL LOGIC DESIGN PP 100 40 40 P C 16. ELECTROMAGNETIC TW 10 18 P 07. DIGITAL LOGIC DESIGN 20 23 P C 100 40 60 P PR 50 17. DATA STRUCTURES PP 08. POWER DEVICES AND MACHINES PP 100 40 43 P C 18. DATA STRUCTURES PR 50 20 39 P 09. NETWORK AND POWER LAB. 100 TW 50 20 38 P C 40 47 P 19. COMMUNICATION THEORY 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 41 P C 50 20 28 P 20. COMMUNICATION THEORY OR 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 39 P GRAND TOTAL = 768/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71100737D , , PICT , S8053013 S8053013 BAGALE PRITESH NAMDEV JIJABAI PP 100 40 52 P C PP 100 40 77 P 01. SIGNAL AND SYSTEMS 11. ENGINEERING MATHEMATICS III or 50 20 38 P C 25 10 02. SIGNAL AND SYSTEMS 12. ENGINEERING MATHEMATICS III TW 18 P 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 52 P 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 31 P 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 38 P 05. NETWORK ANALYSIS PP 100 40 49 P C 15. ELECTROMAGNETIC PP 100 40 57 P PP 100 40 76 P C 25 10 19 P 06. DIGITAL LOGIC DESIGN 16. ELECTROMAGNETIC TW 50 20 29 P C 100 07. DIGITAL LOGIC DESIGN PP 40 45 P PR 17. DATA STRUCTURES 08. POWER DEVICES AND MACHINES PP 100 40 43 P 50 20 39 P 18. DATA STRUCTURES PR 09. NETWORK AND POWER LAB. TW 50 20 32 P C 100 40 49 P 19. COMMUNICATION THEORY PP 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 36 P C 50 20 28 P 20. COMMUNICATION THEORY OR 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 39 P GRAND TOTAL = 887/1500, RESULT: HIGHER SECOND CLASS ORDN. 1 MARKS: , 71100738в , , рІСТ S8053014 BAGUL KAUSTUBH HITENDRA JAYSHRI , s8053014 PP 100 40 58 P C 11. ENGINEERING MATHEMATICS III PP 100 40 96 P 01. SIGNAL AND SYSTEMS 02. SIGNAL AND SYSTEMS OR 50 20 36 P.C. 12. ENGINEERING MATHEMATICS III TW 25 10 22 P 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 77 P.C 49 p 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 50 44 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 20 38 P PP 100 PP 100 05. NETWORK ANALYSIS 40 56 P C 15. ELECTROMAGNETIC 40 65 P PP 100 40 69 P C 16. ELECTROMAGNETIC 25 22 P 06. DIGITAL LOGIC DESIGN TW 10 50 20 30 P C 100 40 07. DIGITAL LOGIC DESIGN PR 17. DATA STRUCTURES PP 59 P 08. POWER DEVICES AND MACHINES PP 100 40 63 P C 18. DATA STRUCTURES PR 50 20 48 P 09. NETWORK AND POWER LAB. TW 50 20 41 P C 19. COMMUNICATION THEORY PP 100 40 53 P 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 42 P C 50 20 33 P 20. COMMUNICATION THEORY OR 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 42 P GRAND TOTAL = 1043/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS: , 71100750M , , PICT , s8053015 S8053015 BHOSALE ABHISHEK BHAUSAHEB MIRA 01. SIGNAL AND SYSTEMS PP 100 40 40 P C 11. ENGINEERING MATHEMATICS III PP 100 40 43 P 02. SIGNAL AND SYSTEMS 12. ENGINEERING MATHEMATICS III TW 25 10 17 P OR 50 20 34 P C 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 45 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 45 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 42 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 35 P 05. NETWORK ANALYSIS PP 100 40 47 P C PP 100 40 51 P 15. ELECTROMAGNETIC 25 06. DIGITAL LOGIC DESIGN PP 100 40 40 P C 16. ELECTROMAGNETIC TW 10 18 P 07. DIGITAL LOGIC DESIGN 20 27 P C 100 40 53 P PR 50 17. DATA STRUCTURES PP 08. POWER DEVICES AND MACHINES PP 100 40 43 P C 18. DATA STRUCTURES PR 50 20 34 P 09. NETWORK AND POWER LAB. 50 20 33 P C 100 40 45 P TW 19. COMMUNICATION THEORY 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 36 P C 50 20 24 P 20. COMMUNICATION THEORY OR 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 39 P GRAND TOTAL = 791/1500, RESULT: SECOND CLASS ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71100752H , , , РІСТ , S8053016 BASANTI S8053016 BHUTADA GOPAL MADHUSUDAN PP 100 40 65 P C 11. ENGINEERING MATHEMATICS III PP 100 40 96 P 01. SIGNAL AND SYSTEMS 02. SIGNAL AND SYSTEMS OR 50 20 39 P C 25 10 22 P 12. ENGINEERING MATHEMATICS III TW 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 64 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 60 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 43 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 25 P 05. NETWORK ANALYSIS PP 100 PP 100 40 67 P C 15. ELECTROMAGNETIC 40 63 P PP 100 40 79 P C 25 10 22 P 06. DIGITAL LOGIC DESIGN 16. ELECTROMAGNETIC TW 100 07. DIGITAL LOGIC DESIGN 50 20 30 P C PP 40 65 P PR 17. DATA STRUCTURES 08. POWER DEVICES AND MACHINES PP 100 40 61 P C 50 20 18. DATA STRUCTURES PR 31 P 09. NETWORK AND POWER LAB. TW 50 20 40 P C 100 40 72 P 19. COMMUNICATION THEORY PP 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 40 P C 50 20 30 P 20. COMMUNICATION THEORY OR 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 42 P GRAND TOTAL = 1056/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS: , 71100754D , , PICT , S8053017 S8053017 BORSE SWATI NANA KALPANA PP 100 40 23 F 11. ENGINEERING MATHEMATICS III PP 100 40 40 P 01. SIGNAL AND SYSTEMS 02. SIGNAL AND SYSTEMS OR 50 20 38 P 12. ENGINEERING MATHEMATICS III TW 25 10 18 P 40 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 18 F 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 22 F 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 23 P 50 20 20 P 14. INTEGRATED CIRCUITS APPLICATIONS PR 05. NETWORK ANALYSIS PP 100 40 29 F PP 100 15. ELECTROMAGNETIC 40 09 F PP 100 40 28 F 16. ELECTROMAGNETIC 25 17 P 06. DIGITAL LOGIC DESIGN TW 10 50 20 20 P 100 40 07. DIGITAL LOGIC DESIGN PR 17. DATA STRUCTURES PP 45 P 08. POWER DEVICES AND MACHINES PP 100 40 P 18. DATA STRUCTURES PR 50 20 32 P 09. NETWORK AND POWER LAB. TW 50 20 27 P C 19. COMMUNICATION THEORY PP 100 40 27 F 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 32 P C 50 20 15 F 20. COMMUNICATION THEORY OR 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 33 P GRAND TOTAL = 556/1500, RESULT: FAILS ORDN. 1 MARKS: , 71100757J , , PICT , s8053018 S8053018 BUDHWANT UMA SHYAMRAO NANDINI 01. SIGNAL AND SYSTEMS PP 100 40 41 P C 11. ENGINEERING MATHEMATICS III PP 100 40 25 F OR 50 20 27 P C 02. SIGNAL AND SYSTEMS 12. ENGINEERING MATHEMATICS III TW 25 10 16 P 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 45 P 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 40 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 28 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 28 P 05. NETWORK ANALYSIS PP 100 40 40 P C PP 100 40 40 P 15. ELECTROMAGNETIC 25 06. DIGITAL LOGIC DESIGN PP 100 40 43 P C 16. ELECTROMAGNETIC TW 10 17 P 07. DIGITAL LOGIC DESIGN 20 28 P C 100 40 51 P PR 50 17. DATA STRUCTURES PP 08. POWER DEVICES AND MACHINES PP 100 40 41 P 18. DATA STRUCTURES PR 50 20 33 P 09. NETWORK AND POWER LAB. TW 50 20 38 P C 100 40 31 F 19. COMMUNICATION THEORY 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 34 P C 20. COMMUNICATION THEORY 50 20 25 P OR 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 32 P GRAND TOTAL = 703/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71100759E , , PICT , S8053019 NANDINI S8053019 BUTALA KARAN SANDESH 01. SIGNAL AND SYSTEMS PP 100 40 79 P C 11. ENGINEERING MATHEMATICS III PP 100 40 64 P 02. SIGNAL AND SYSTEMS OR 50 20 44 P C 25 10 21 P 12. ENGINEERING MATHEMATICS III TW 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 50 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 50 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 26 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 25 P 05. NETWORK ANALYSIS PP 100 PP 100 40 52 P C 15. ELECTROMAGNETIC 40 73 P PP 100 40 57 P C 25 10 21 P 06. DIGITAL LOGIC DESIGN 16. ELECTROMAGNETIC TW 100 07. DIGITAL LOGIC DESIGN 50 20 20 P C PP 40 59 P PR 17. DATA STRUCTURES 08. POWER DEVICES AND MACHINES PP 100 40 40 P C 50 20 18. DATA STRUCTURES PR 47 P 09. NETWORK AND POWER LAB. TW 50 20 32 P C 100 40 53 P 19. COMMUNICATION THEORY PP 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 35 P C 50 20 30 P 20. COMMUNICATION THEORY OR 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 40 P GRAND TOTAL = 918/1500, RESULT: FIRST CLASS ORDN. 1 MARKS: , 71100760J , , PICT S8053020 CHANDAK ANKIT PANDURANGJI MAYA , s8053020 PP 100 40 45 P C 11. ENGINEERING MATHEMATICS III PP 100 40 26 F 01. SIGNAL AND SYSTEMS 02. SIGNAL AND SYSTEMS OR 50 20 P C 12. ENGINEERING MATHEMATICS III TW 25 10 18 P 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 48 P C 18 F 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 40 P C 50 20 14. INTEGRATED CIRCUITS APPLICATIONS PR 36 P 05. NETWORK ANALYSIS PP 100 40 12 F PP 100 15. ELECTROMAGNETIC 40 13 F PP 100 40 60 P C 16. ELECTROMAGNETIC 25 18 P 06. DIGITAL LOGIC DESIGN TW 10 50 20 35 P C 100 07. DIGITAL LOGIC DESIGN PR 17. DATA STRUCTURES PP 40 43 P 08. POWER DEVICES AND MACHINES PP 100 40 28 F 18. DATA STRUCTURES PR 50 20 25 P 09. NETWORK AND POWER LAB. TW 50 20 37 P C 40 45 P 19. COMMUNICATION THEORY PP 100 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 35 P C 50 20 21 P 20. COMMUNICATION THEORY OR 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 37 P GRAND TOTAL = 660/1500, RESULT: FAILS ORDN. 1 MARKS: , s8053021 S8053021 CHANDAK RASIKA RAMGOPAL ALAKNANDA 01. SIGNAL AND SYSTEMS PP 100 40 42 P C 11. ENGINEERING MATHEMATICS III PP 100 40 51 P 02. SIGNAL AND SYSTEMS 12. ENGINEERING MATHEMATICS III TW 25 10 22 P OR 50 20 34 P C 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 40 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 42 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 30 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 25 P 05. NETWORK ANALYSIS PP 100 40 40 P C PP 100 40 57 P 15. ELECTROMAGNETIC 25 19 P 06. DIGITAL LOGIC DESIGN PP 100 40 50 P C 16. ELECTROMAGNETIC TW 10 07. DIGITAL LOGIC DESIGN 20 28 P C 100 40 57 P PR 50 17. DATA STRUCTURES PP 08. POWER DEVICES AND MACHINES PP 100 40 P C 18. DATA STRUCTURES PR 50 20 34 P 09. NETWORK AND POWER LAB. 50 20 42 P C 100 40 48 P TW 19. COMMUNICATION THEORY 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 42 P C 20. COMMUNICATION THEORY 50 20 30 P OR 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 41 P GRAND TOTAL = 814/1500, RESULT: SECOND CLASS ORDN. 1 MARKS:

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NOTE: FIRST LINE : SEAT NO., NAMI OTHER LINES: HEAD OF PASSING				•			「REG. NO., PREVIOUS SEAT NO., (RKS OBTAINED, P/F:PASS/FAIL, C:F		•			
S8053025 CHHATRE JAI VILAS				HE	EMA		, 71100767F , ,	, P	ICT	,	S8053	025
01. SIGNAL AND SYSTEMS	PP	100	40	60	РС	11	. ENGINEERING MATHEMATICS III	PP	100	40	74	Р
02. SIGNAL AND SYSTEMS	OR	50	20	39	РС	12	. ENGINEERING MATHEMATICS III	TW	25	10	23	Р
03. SOLID STATES DEVICES AND CIRC	JITSPP	100	40	60	РС	13	INTEGRATED CIRCUITS APPLICATIONS	S PP	100	40	40	Р
04. SOLID STATES DEVICES AND CIRC	JITSPR	50	20	40	РС	14	INTEGRATED CIRCUITS APPLICATIONS	S PR	50	20	36	Р
05. NETWORK ANALYSIS	PP	100	40	50	РС	15	ELECTROMAGNETIC	PP	100	40	65	Р
06. DIGITAL LOGIC DESIGN	PP	100	40	46	РС	16	ELECTROMAGNETIC	TW	25	10	21	Р
07. DIGITAL LOGIC DESIGN	PR	50	20	37	РС	17	DATA STRUCTURES	PP	100	40	53	Р
08. POWER DEVICES AND MACHINES	PP	100	40	40	РС	18	DATA STRUCTURES	PR	50	20	47	Р
09. NETWORK AND POWER LAB.	TW	50	20	42	РС	19	. COMMUNICATION THEORY	PP	100	40	43	Р
10. ELECTRONIC INSTRUMENTS AND TO	OLS TW	50	20	42	РС	20	. COMMUNICATION THEORY	OR	50	20	25	Р
						21	CIRCUIT SIMULATION AND TOOLS	TW	50	20	42	Р
GRAND TOTAL = 925/1500, RESULT: FINORDN. 1 MARKS :	RST CLAS	SS										
S8053026 CHIDRAWAR DNYANESHWAR					ETA		, 71100768D , , , , , , , , , , , , , , , , , , ,		 ICT		 S8053	
01. SIGNAL AND SYSTEMS	PP	100	40		P C	11	. ENGINEERING MATHEMATICS III	PP	100	40	94	
02. SIGNAL AND SYSTEMS	OR	50	20	33			ENGINEERING MATHEMATICS III	TW	25	10	23	
03. SOLID STATES DEVICES AND CIRC		100	40		PC		INTEGRATED CIRCUITS APPLICATIONS	S PP	100	40	46	
04. SOLID STATES DEVICES AND CIRC		50	20	35	P C		INTEGRATED CIRCUITS APPLICATIONS		50	20	41	
05. NETWORK ANALYSIS	PP	100	40	65	_		ELECTROMAGNETIC	PP	100	40	60	
06. DIGITAL LOGIC DESIGN	PP	100	40	75	PC		ELECTROMAGNETIC	TW	25	10	22	
07. DIGITAL LOGIC DESIGN	PR	50	20	30	РC		DATA STRUCTURES	PP	100	40	65	
08. POWER DEVICES AND MACHINES	PP	100	40	64	РC		DATA STRUCTURES	PR	50	20	48	Р
09. NETWORK AND POWER LAB.	TW	50	20	41	РС	19	. COMMUNICATION THEORY	PP	100	40	54	Р
10. ELECTRONIC INSTRUMENTS AND TO	OLS TW	50	20	40	РС	20	. COMMUNICATION THEORY	OR	50	20	29	Р
							CIRCUIT SIMULATION AND TOOLS	TW	50	20		
GRAND TOTAL = 1036/1500, RESULT: FI	RST CLAS	SS WIT	H DIS	TINCT	TION							
ORDN. 1 MARKS :												
S8053027 CHITALE JANHAVI GOVINI					 ARSHA		, 71100769в , , , ,					
01. SIGNAL AND SYSTEMS	PP	100	40	52	РС	11	. ENGINEERING MATHEMATICS III	PP	100	40	84	Р
02. SIGNAL AND SYSTEMS	OR	50	20	38	РС	12	. ENGINEERING MATHEMATICS III	TW	25	10	22	Р
03. SOLID STATES DEVICES AND CIRC	JITSPP	100	40	50	РС	13	. INTEGRATED CIRCUITS APPLICATIONS	S PP	100	40	43	Р
04. SOLID STATES DEVICES AND CIRC	JITSPR	50	20	38	РС	14	INTEGRATED CIRCUITS APPLICATIONS	S PR	50	20	37	Р
05. NETWORK ANALYSIS	PP	100	40	40	РС	15	ELECTROMAGNETIC	PP	100	40	40	Р
06. DIGITAL LOGIC DESIGN	PP	100	40	73	РС	16	ELECTROMAGNETIC	TW	25	10	20	Р
07. DIGITAL LOGIC DESIGN	PR	50	20	42	РС	17	DATA STRUCTURES	PP	100	40	56	Р
08. POWER DEVICES AND MACHINES	PP	100	40	41	РС	18	DATA STRUCTURES	PR	50	20	35	Р
09. NETWORK AND POWER LAB.	TW	50	20	42	РС	19	COMMUNICATION THEORY	PP	100	40	40	Р
10. ELECTRONIC INSTRUMENTS AND TO	OLS TW	50	20	41	РС	20	. COMMUNICATION THEORY	OR	50	20	34	Р
						21	CIRCUIT SIMULATION AND TOOLS	TW	50	20	40	Р
GRAND TOTAL = 908/1500, RESULT: FI	RST CLAS	SS										
ORDN. 1 MARKS :												

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71100779К , , , РІСТ , S8053028 ANJALI S8053028 DESHMUKH AMRUTA VILAS 01. SIGNAL AND SYSTEMS PP 100 40 60 P C 11. ENGINEERING MATHEMATICS III PP 100 40 64 P or 50 20 32 P C 25 10 21 P 02. SIGNAL AND SYSTEMS 12. ENGINEERING MATHEMATICS III TW 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 67 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 49 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 38 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 38 P 05. NETWORK ANALYSIS PP 100 40 54 P C 15. ELECTROMAGNETIC PP 100 40 77 P PP 100 40 74 P C 25 10 18 P 06. DIGITAL LOGIC DESIGN 16. ELECTROMAGNETIC TW 100 07. DIGITAL LOGIC DESIGN 50 20 28 P C 40 57 P PR 17. DATA STRUCTURES PP 08. POWER DEVICES AND MACHINES PP 100 40 65 P C 50 20 18. DATA STRUCTURES PR 47 P 09. NETWORK AND POWER LAB. TW 50 20 36 P C 100 40 58 P 19. COMMUNICATION THEORY PP 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 41 P C 50 20 40 P 20. COMMUNICATION THEORY OR 50 20 39 P 21. CIRCUIT SIMULATION AND TOOLS TW GRAND TOTAL = 1003/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS: , 71100780C , , PICT S8053029 DESHMUKH ASHUTOSH VILASRAO SUNITA , s8053029 PP 100 40 54 P 01. SIGNAL AND SYSTEMS PP 100 40 49 P C 11. ENGINEERING MATHEMATICS III 50 20 22 P C 02. SIGNAL AND SYSTEMS OR 12. ENGINEERING MATHEMATICS III TW 25 10 11 P 41 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 32 F 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 24 P C 50 20 05 F 14. INTEGRATED CIRCUITS APPLICATIONS PR PP 100 PP 100 05. NETWORK ANALYSIS 40 46 P.C 15. ELECTROMAGNETIC 40 31 F PP 100 40 40 P C 16. ELECTROMAGNETIC 25 06. DIGITAL LOGIC DESIGN TW 10 11 P 20 27 P 100 07. DIGITAL LOGIC DESIGN PR 50 17. DATA STRUCTURES PP 40 AA F 08. POWER DEVICES AND MACHINES PP 100 40 P C 18. DATA STRUCTURES PR 50 20 05 F 09. NETWORK AND POWER LAB. TW 50 20 29 P C 19. COMMUNICATION THEORY PP 100 40 40 P 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 25 P C 50 20 20 P 20. COMMUNICATION THEORY OR 50 20 24 P 21. CIRCUIT SIMULATION AND TOOLS TW GRAND TOTAL = 576/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS: , 71100781M , , PICT , S8053030 S8053030 DESHPANDE ADITI VIDHYADHAR ALKA 01. SIGNAL AND SYSTEMS PP 100 40 45 P C 11. ENGINEERING MATHEMATICS III PP 100 40 49 P 02. SIGNAL AND SYSTEMS 12. ENGINEERING MATHEMATICS III TW 25 10 or 50 20 20 P C 16 P 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 40 P 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 40 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 30 P 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 32 P 05. NETWORK ANALYSIS PP 100 40 40 P C PP 100 40 40 P 15. ELECTROMAGNETIC 25 06. DIGITAL LOGIC DESIGN PP 100 40 54 P C 16. ELECTROMAGNETIC TW 10 18 P 07. DIGITAL LOGIC DESIGN 20 21 P C 100 40 52 P PR 50 17. DATA STRUCTURES PP 08. POWER DEVICES AND MACHINES PP 100 40 P C 18. DATA STRUCTURES PR 50 20 41 P 09. NETWORK AND POWER LAB. 50 20 31 P C 100 40 51 P TW 19. COMMUNICATION THEORY 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 37 P C 50 20 21 P 20. COMMUNICATION THEORY OR 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 39 P GRAND TOTAL = 757/1500, RESULT: SECOND CLASS ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71236308E , , PICT , S8053031 S8053031 DHANDAR RUCHITA PANDURANG VIJAYA 01. SIGNAL AND SYSTEMS PP 100 40 40 P C 11. ENGINEERING MATHEMATICS III PP 100 40 64 P or 50 20 24 P C 25 10 22 P 02. SIGNAL AND SYSTEMS 12. ENGINEERING MATHEMATICS III TW 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 40 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 40 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 33 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 34 P 05. NETWORK ANALYSIS PP 100 40 40 P C 15. ELECTROMAGNETIC PP 100 40 52 P PP 100 40 65 P C 25 10 19 P 06. DIGITAL LOGIC DESIGN 16. ELECTROMAGNETIC TW 50 20 22 P 100 07. DIGITAL LOGIC DESIGN 40 55 P PR 17. DATA STRUCTURES PP 08. POWER DEVICES AND MACHINES PP 100 40 48 P C 50 20 18. DATA STRUCTURES PR 42 P 09. NETWORK AND POWER LAB. TW 50 20 42 P C 100 40 44 P 19. COMMUNICATION THEORY PP 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 41 P C 50 20 26 P 20. COMMUNICATION THEORY OR 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 41 P GRAND TOTAL = 834/1500, RESULT: HIGHER SECOND CLASS ORDN. 1 MARKS : , 71236309C , , PICT S8053032 DHANE AKSHAY MUKUNDRAO SHARADA , s8053032 PP 100 40 46 P C 11. ENGINEERING MATHEMATICS III PP 100 40 77 P 01. SIGNAL AND SYSTEMS 02. SIGNAL AND SYSTEMS OR 50 20 23 P C 12. ENGINEERING MATHEMATICS III TW 25 10 22 P 40 60 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 03. SOLID STATES DEVICES AND CIRCUITSPP 100 42 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 50 34 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 20 40 P PP 100 PP 100 05. NETWORK ANALYSIS 40 40 P C 15. ELECTROMAGNETIC 40 42 P PP 100 40 16. ELECTROMAGNETIC 25 06. DIGITAL LOGIC DESIGN 62 P C TW 10 18 P 50 20 30 P C 100 07. DIGITAL LOGIC DESIGN PR 17. DATA STRUCTURES PP 40 61 P 08. POWER DEVICES AND MACHINES PP 100 40 49 P C 18. DATA STRUCTURES PR 50 20 39 P 09. NETWORK AND POWER LAB. TW 50 20 40 P C 19. COMMUNICATION THEORY PP 100 40 54 P 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 39 P C 50 20 25 P 20. COMMUNICATION THEORY OR 50 20 42 P 21. CIRCUIT SIMULATION AND TOOLS TW GRAND TOTAL = 885/1500, RESULT: HIGHER SECOND CLASS ORDN. 1 MARKS: , s8053033 S8053033 DHODI SHRADDHA MADHU ARUNA 01. SIGNAL AND SYSTEMS PP 100 40 49 P C 11. ENGINEERING MATHEMATICS III PP 100 40 40 P 02. SIGNAL AND SYSTEMS 12. ENGINEERING MATHEMATICS III TW 25 10 OR 50 20 25 P C 18 P 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 23 F 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 23 F 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 24 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 27 P 05. NETWORK ANALYSIS PP 100 40 29 F PP 100 40 14 F 15. ELECTROMAGNETIC 25 06. DIGITAL LOGIC DESIGN PP 100 40 46 P C 16. ELECTROMAGNETIC TW 10 16 P 07. DIGITAL LOGIC DESIGN 20 22 P C 100 40 40 P PR 50 17. DATA STRUCTURES PP 08. POWER DEVICES AND MACHINES PP 100 40 30 F 18. DATA STRUCTURES PR 50 20 39 P 09. NETWORK AND POWER LAB. TW 100 50 20 20 P C 40 40 P 19. COMMUNICATION THEORY 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 25 P C 50 20 12 F 20. COMMUNICATION THEORY OR 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 32 P GRAND TOTAL = 594/1500, RESULT: FAILS ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71100786B , , PICT , S8053034 S8053034 DHOLE NILAKSHI AJAY CHHAYA 11. ENGINEERING MATHEMATICS III PP 100 40 71 P 01. SIGNAL AND SYSTEMS PP 100 40 58 P C or 50 20 28 P C 25 10 22 P 02. SIGNAL AND SYSTEMS 12. ENGINEERING MATHEMATICS III TW 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 64 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 43 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 32 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 28 P 05. NETWORK ANALYSIS PP 100 40 67 P C 15. ELECTROMAGNETIC PP 100 40 PP 100 40 64 P C 25 10 19 P 06. DIGITAL LOGIC DESIGN 16. ELECTROMAGNETIC TW 50 20 27 P C 100 07. DIGITAL LOGIC DESIGN 40 61 P PR 17. DATA STRUCTURES PP 08. POWER DEVICES AND MACHINES PP 100 40 57 P C 50 20 18. DATA STRUCTURES PR 46 P 09. NETWORK AND POWER LAB. TW 50 20 42 P C 100 40 50 P 19. COMMUNICATION THEORY PP 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 38 P C 50 20 30 P 20. COMMUNICATION THEORY OR 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 38 P GRAND TOTAL = 949/1500, RESULT: FIRST CLASS ORDN. 1 MARKS: S8053035 DOLAS LUBDHA RAJU SUNITA , s8053035 PP 100 40 40 P C 11. ENGINEERING MATHEMATICS III PP 100 40 25 F 01. SIGNAL AND SYSTEMS 02. SIGNAL AND SYSTEMS OR 50 20 22 P C 12. ENGINEERING MATHEMATICS III TW 25 10 14 P 41 P 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 40 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 21 P 50 20 20 14. INTEGRATED CIRCUITS APPLICATIONS PR 20 P PP 100 PP 100 05. NETWORK ANALYSIS 40 31 F 15. ELECTROMAGNETIC 40 22 F 40 P PP 100 16. ELECTROMAGNETIC 25 06. DIGITAL LOGIC DESIGN 40 TW 10 14 P 50 20 28 P 100 40 07. DIGITAL LOGIC DESIGN PR 17. DATA STRUCTURES PP 40 P 08. POWER DEVICES AND MACHINES PP 100 40 P 18. DATA STRUCTURES PR 50 20 36 P 09. NETWORK AND POWER LAB. TW 50 20 32 P C 19. COMMUNICATION THEORY PP 100 40 29 F 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 36 P C 50 20 29 P 20. COMMUNICATION THEORY OR 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 30 P GRAND TOTAL = 630/1500, RESULT: FAILS ORDN. 1 MARKS: , 71236311E , , PICT S8053036 DONGARE MANOJ JAYSING SHOBHA , S8053036 01. SIGNAL AND SYSTEMS PP 100 40 40 P C 11. ENGINEERING MATHEMATICS III PP 100 40 26 F OR 50 20 32 P C 02. SIGNAL AND SYSTEMS 12. ENGINEERING MATHEMATICS III TW 25 10 21 P 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 56 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 58 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 35 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 32 P 05. NETWORK ANALYSIS PP 100 40 40 P C PP 100 40 40 P 15. ELECTROMAGNETIC 25 06. DIGITAL LOGIC DESIGN PP 100 40 61 P C TW 10 18 P 16. ELECTROMAGNETIC 07. DIGITAL LOGIC DESIGN 20 21 P C 100 40 57 P PR 50 17. DATA STRUCTURES PP 08. POWER DEVICES AND MACHINES PP 100 40 62 P C 18. DATA STRUCTURES PR 50 20 38 P 09. NETWORK AND POWER LAB. TW 50 20 40 P C 100 40 57 P 19. COMMUNICATION THEORY 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 40 P C 50 20 35 P 20. COMMUNICATION THEORY OR 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 41 P GRAND TOTAL = 850/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71100792G , , PICT , S8053037 S8053037 GAIKWAD GAYATRI DILIP DIPALI PP 100 40 23 F 01. SIGNAL AND SYSTEMS 11. ENGINEERING MATHEMATICS III PP 100 40 40 P or 50 20 29 P C 25 10 18 P 02. SIGNAL AND SYSTEMS 12. ENGINEERING MATHEMATICS III TW 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 22 F 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 20 F 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 25 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 25 P 05. NETWORK ANALYSIS PP 100 40 19 F 15. ELECTROMAGNETIC PP 100 40 26 F 100 40 47 P C 25 10 18 P 06. DIGITAL LOGIC DESIGN PP 16. ELECTROMAGNETIC TW 100 07. DIGITAL LOGIC DESIGN 50 20 20 P C 40 32 F PR 17. DATA STRUCTURES PP 08. POWER DEVICES AND MACHINES PP 100 40 11 F 50 20 41 P 18. DATA STRUCTURES PR 09. NETWORK AND POWER LAB. TW 50 20 30 P C 100 40 40 P 19. COMMUNICATION THEORY PP 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 31 P C 50 20 22 P 20. COMMUNICATION THEORY OR 50 20 36 P 21. CIRCUIT SIMULATION AND TOOLS TW GRAND TOTAL = 575/1500, RESULT: FAILS ORDN. 1 MARKS : , 71100794C , , PICT S8053038 GAIKWAD PRATIKSHA JAGANNATH ANITA , s8053038 PP 100 40 42 P C 11. ENGINEERING MATHEMATICS III PP 100 40 64 P 01. SIGNAL AND SYSTEMS 50 20 30 P C 02. SIGNAL AND SYSTEMS OR 12. ENGINEERING MATHEMATICS III TW 25 10 18 P 49 P.C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 43 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 50 20 22 P 40 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR PP 100 PP 100 05. NETWORK ANALYSIS 40 41 PC 15. ELECTROMAGNETIC 40 52 P PP 100 40 56 P C 16. ELECTROMAGNETIC 25 18 P 06. DIGITAL LOGIC DESIGN TW 10 20 28 P 100 07. DIGITAL LOGIC DESIGN PR 50 17. DATA STRUCTURES PP 40 56 P 08. POWER DEVICES AND MACHINES PP 100 40 53 P 18. DATA STRUCTURES PR 50 20 46 P 09. NETWORK AND POWER LAB. TW 50 20 41 P C 19. COMMUNICATION THEORY PP 100 40 40 P 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 36 P C 50 20 25 P 20. COMMUNICATION THEORY OR 50 20 36 P 21. CIRCUIT SIMULATION AND TOOLS TW GRAND TOTAL = 836/1500, RESULT: HIGHER SECOND CLASS ORDN. 1 MARKS: , s8053039 S8053039 GAVADE SACHIN SHIVAJI SNEHAL 01. SIGNAL AND SYSTEMS PP 100 40 60 P C 11. ENGINEERING MATHEMATICS III PP 100 40 41 P 02. SIGNAL AND SYSTEMS 12. ENGINEERING MATHEMATICS III TW 25 10 OR 50 20 26 P C 19 P 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 49 P 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 53 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 26 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 25 P 05. NETWORK ANALYSIS PP 100 40 43 P C PP 100 40 40 P 15. ELECTROMAGNETIC 25 06. DIGITAL LOGIC DESIGN PP 100 40 41 P C TW 10 19 P 16. ELECTROMAGNETIC 07. DIGITAL LOGIC DESIGN 20 25 P 100 40 47 P PR 50 17. DATA STRUCTURES PP 08. POWER DEVICES AND MACHINES PP 100 40 P C 18. DATA STRUCTURES PR 50 20 37 P 09. NETWORK AND POWER LAB. 50 20 30 P C 100 40 40 P TW 19. COMMUNICATION THEORY 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 31 P C 50 20 30 P 20. COMMUNICATION THEORY OR 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 39 P GRAND TOTAL = 761/1500, RESULT: SECOND CLASS ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71236312C , , PICT , S8053040 SUREKHA S8053040 GENGAJE PRAJAKTA SHANTARAM PP 100 40 49 P PP 100 40 17 F 01. SIGNAL AND SYSTEMS 11. ENGINEERING MATHEMATICS III 50 20 25 P C 25 10 21 P 02. SIGNAL AND SYSTEMS 12. ENGINEERING MATHEMATICS III TW 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 40 P 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 31 F 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 25 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 22 P 05. NETWORK ANALYSIS PP 100 40 30 F 15. ELECTROMAGNETIC PP 100 40 27 F PP 100 40 40 P C 25 10 17 P 06. DIGITAL LOGIC DESIGN 16. ELECTROMAGNETIC TW 50 20 22 P 100 07. DIGITAL LOGIC DESIGN PP 40 31 F PR 17. DATA STRUCTURES 08. POWER DEVICES AND MACHINES PP 100 40 48 P 50 20 18. DATA STRUCTURES PR 38 P 09. NETWORK AND POWER LAB. TW 50 20 40 P C 100 40 45 P 19. COMMUNICATION THEORY PP 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 40 P C 50 20 23 P 20. COMMUNICATION THEORY OR 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 40 P GRAND TOTAL = 671/1500, RESULT: FAILS ORDN. 1 MARKS : S8053041 GHARDE SAGAR BHIMRAO SHARDA , s8053041 PP 100 40 48 P C 11. ENGINEERING MATHEMATICS III PP 100 40 41 P 01. SIGNAL AND SYSTEMS 02. SIGNAL AND SYSTEMS OR 50 20 34 P C 12. ENGINEERING MATHEMATICS III TW 25 10 19 P 40 40 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 25 P C 50 20 23 P 14. INTEGRATED CIRCUITS APPLICATIONS PR PP 100 40 40 P C PP 100 05. NETWORK ANALYSIS 15. ELECTROMAGNETIC 40 42 P PP 100 40 57 P C 16. ELECTROMAGNETIC 25 06. DIGITAL LOGIC DESIGN TW 10 17 P 50 20 20 P C 100 40 07. DIGITAL LOGIC DESIGN PR 17. DATA STRUCTURES PP 59 P 08. POWER DEVICES AND MACHINES PP 100 40 P C 18. DATA STRUCTURES PR 50 20 35 P 09. NETWORK AND POWER LAB. TW 50 20 33 P C 19. COMMUNICATION THEORY PP 100 40 40 P 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 37 P C 50 20 12 F 20. COMMUNICATION THEORY OR 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 40 P GRAND TOTAL = 742/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS: , 71100805B , , PICT , s8053042 S8053042 GIRI POOJA RAVINDRA LATA 01. SIGNAL AND SYSTEMS PP 100 40 64 P C 11. ENGINEERING MATHEMATICS III PP 100 40 63 P 02. SIGNAL AND SYSTEMS 12. ENGINEERING MATHEMATICS III TW 25 10 OR 50 20 41 P C 21 P 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 42 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 53 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 34 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 24 P 05. NETWORK ANALYSIS PP 100 40 51 P C PP 100 40 42 P 15. ELECTROMAGNETIC 25 06. DIGITAL LOGIC DESIGN PP 100 40 55 P C 16. ELECTROMAGNETIC TW 10 21 P 07. DIGITAL LOGIC DESIGN 20 37 P C 100 40 67 P PR 50 17. DATA STRUCTURES PP 08. POWER DEVICES AND MACHINES PP 100 40 P C 18. DATA STRUCTURES PR 50 20 43 P 09. NETWORK AND POWER LAB. 100 50 20 39 P C 40 40 P TW 19. COMMUNICATION THEORY 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 37 P C 50 20 27 P 20. COMMUNICATION THEORY OR 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 41 P GRAND TOTAL = 882/1500, RESULT: HIGHER SECOND CLASS ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71100808G , , PICT , S8053043 S8053043 GOKHALE SIDDHARTH ADESH SUJATA 11. ENGINEERING MATHEMATICS III PP 100 40 71 P 01. SIGNAL AND SYSTEMS PP 100 40 73 P C 02. SIGNAL AND SYSTEMS OR 50 20 46 P C 25 10 22 P 12. ENGINEERING MATHEMATICS III TW 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 70 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 60 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 46 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 44 P 05. NETWORK ANALYSIS PP 100 PP 100 40 64 P C 15. ELECTROMAGNETIC 40 65 P PP 100 40 84 P C 25 10 22 P 06. DIGITAL LOGIC DESIGN 16. ELECTROMAGNETIC TW 100 07. DIGITAL LOGIC DESIGN 50 20 48 P C 40 69 P PR 17. DATA STRUCTURES PP 08. POWER DEVICES AND MACHINES PP 100 40 56 P C 50 20 18. DATA STRUCTURES PR 48 P 09. NETWORK AND POWER LAB. TW 50 20 44 P C 100 40 55 P 19. COMMUNICATION THEORY PP 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 43 P C 50 20 47 P 20. COMMUNICATION THEORY OR 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 45 P GRAND TOTAL = 1122/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS: S8053044 HARSHIKA THUSU ANTTA , s8053044 53 P C 11. ENGINEERING MATHEMATICS III PP 100 40 56 P 01. SIGNAL AND SYSTEMS PP 100 40 02. SIGNAL AND SYSTEMS OR 50 20 42 P C 12. ENGINEERING MATHEMATICS III TW 25 10 22 P 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 64 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 62 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 37 P C 50 20 37 P 14. INTEGRATED CIRCUITS APPLICATIONS PR PP 100 PP 100 05. NETWORK ANALYSIS 40 54 P C 15. ELECTROMAGNETIC 40 77 P PP 100 40 58 P C 16. ELECTROMAGNETIC 25 06. DIGITAL LOGIC DESIGN TW 10 22 P 50 20 21 P C 100 75 P 07. DIGITAL LOGIC DESIGN PR 17. DATA STRUCTURES PP 40 08. POWER DEVICES AND MACHINES PP 100 40 47 P C 18. DATA STRUCTURES PR 50 20 48 P 09. NETWORK AND POWER LAB. TW 50 20 42 P C 19. COMMUNICATION THEORY PP 100 40 60 P 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 43 P C 50 20 30 P 20. COMMUNICATION THEORY OR 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 43 P GRAND TOTAL = 993/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS: , 71100821D , , PICT , S8053045 S8053045 HIMANSHU JAIN RACHANA 01. SIGNAL AND SYSTEMS PP 100 40 57 P C 11. ENGINEERING MATHEMATICS III PP 100 40 40 P 12. ENGINEERING MATHEMATICS III TW 25 10 12 P 02. SIGNAL AND SYSTEMS or 50 20 36 P C 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 28 F 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 40 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 20 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 35 P 05. NETWORK ANALYSIS PP 100 40 40 P C PP 100 40 67 P 15. ELECTROMAGNETIC 25 06. DIGITAL LOGIC DESIGN PP 100 40 47 P C 16. ELECTROMAGNETIC TW 10 15 P 07. DIGITAL LOGIC DESIGN 20 25 P 100 40 40 P PR 50 17. DATA STRUCTURES PP 08. POWER DEVICES AND MACHINES PP 100 40 P 18. DATA STRUCTURES PR 50 20 33 P 09. NETWORK AND POWER LAB. 100 TW 50 20 29 P C 40 40 P 19. COMMUNICATION THEORY 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 24 P C 20. COMMUNICATION THEORY 50 20 23 P OR 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 26 P GRAND TOTAL = 717/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71100825G , , PICT , S8053046 S8053046 JAGTAP MANJIRI ARVIND SHUBHANGI PP 100 40 40 P C PP 100 40 15 F 01. SIGNAL AND SYSTEMS 11. ENGINEERING MATHEMATICS III or 50 20 30 P 25 10 15 P 02. SIGNAL AND SYSTEMS 12. ENGINEERING MATHEMATICS III TW 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 40 P 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 40 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 25 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 25 P 05. NETWORK ANALYSIS PP 100 40 29 F 15. ELECTROMAGNETIC PP 100 40 16 F PP 100 40 40 P C 25 10 16 P 06. DIGITAL LOGIC DESIGN 16. ELECTROMAGNETIC TW 100 07. DIGITAL LOGIC DESIGN 50 20 21 P 40 40 P PR 17. DATA STRUCTURES PP 08. POWER DEVICES AND MACHINES PP 100 40 40 P C 50 20 32 P 18. DATA STRUCTURES PR 09. NETWORK AND POWER LAB. TW 50 20 33 P C 100 40 40 P 19. COMMUNICATION THEORY PP 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 33 P C 50 20 22 P 20. COMMUNICATION THEORY OR 50 20 38 P 21. CIRCUIT SIMULATION AND TOOLS TW GRAND TOTAL = 630/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS: , 71100827C , , PICT , S8053047 S8053047 JAIN SAJAL ABHAYKUMAR SUNITA PP 100 40 40 P C 11. ENGINEERING MATHEMATICS III PP 100 40 41 P 01. SIGNAL AND SYSTEMS 02. SIGNAL AND SYSTEMS OR 50 20 28 P 12. ENGINEERING MATHEMATICS III TW 25 10 16 P 40 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 23 F 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 30 P 50 20 22 P 14. INTEGRATED CIRCUITS APPLICATIONS PR PP 100 PP 100 05. NETWORK ANALYSIS 40 50 P C 15. ELECTROMAGNETIC 40 27 F 40 P C PP 100 40 16. ELECTROMAGNETIC 25 15 P 06. DIGITAL LOGIC DESIGN TW 10 50 20 25 P C 100 07. DIGITAL LOGIC DESIGN PR 17. DATA STRUCTURES PP 40 46 P 08. POWER DEVICES AND MACHINES PP 100 40 45 P 18. DATA STRUCTURES PR 50 20 32 P 09. NETWORK AND POWER LAB. TW 50 20 34 P C 19. COMMUNICATION THEORY PP 100 40 40 P 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 31 P C 50 20 23 P 20. COMMUNICATION THEORY OR 50 20 30 P 21. CIRCUIT SIMULATION AND TOOLS TW GRAND TOTAL = 678/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS: S8053048 JEDGULE UMASHANKAR RADHAKISAN ANITA , \$8053048 01. SIGNAL AND SYSTEMS PP 100 40 53 P C 11. ENGINEERING MATHEMATICS III PP 100 40 67 P 02. SIGNAL AND SYSTEMS 12. ENGINEERING MATHEMATICS III TW 25 10 OR 50 20 39 P C 15 P 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 41 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 64 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 29 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 40 P 05. NETWORK ANALYSIS PP 100 40 40 P C PP 100 40 45 P 15. ELECTROMAGNETIC 25 06. DIGITAL LOGIC DESIGN PP 100 40 57 P C TW 10 16 P 16. ELECTROMAGNETIC 07. DIGITAL LOGIC DESIGN 20 23 P C 100 40 PR 50 17. DATA STRUCTURES PP 64 P 08. POWER DEVICES AND MACHINES PP 100 40 48 P C 18. DATA STRUCTURES PR 50 20 47 P 09. NETWORK AND POWER LAB. 50 20 33 P C 100 40 51 P TW 19. COMMUNICATION THEORY 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 37 P C 50 20 25 P 20. COMMUNICATION THEORY OR 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 36 P GRAND TOTAL = 870/1500, RESULT: HIGHER SECOND CLASS ORDN. 1 MARKS:

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NOTE: FIRST LINE : SEAT NO., NAME							REG. NO., PREVIOUS SEAT NO., C					
OTHER LINES: HEAD OF PASSING	, MAX.	MARK:	S, M	IN. F	PASS MAR	RKS, MARI	KS OBTAINED, P/F:PASS/FAIL, C:P	REVIO	OUS CAI	RRY C	VER	
S8053049 JOSHI APOORV VIJAY		100	40		IAGHA		, 71100835D , , ,			-	S8053	
01. SIGNAL AND SYSTEMS	PP		40		P C		ENGINEERING MATHEMATICS III	PP —	100	40	76	
02. SIGNAL AND SYSTEMS	OR	50	20		P C		ENGINEERING MATHEMATICS III	TW	25	10	22	
03. SOLID STATES DEVICES AND CIRCU		100	40		P C		INTEGRATED CIRCUITS APPLICATIONS		100	40	50	-
04. SOLID STATES DEVICES AND CIRCU		50	20		P C		INTEGRATED CIRCUITS APPLICATIONS		50	20	43	
05. NETWORK ANALYSIS	PP	100	40	40	P C		ELECTROMAGNETIC	PP	100	40	51	
06. DIGITAL LOGIC DESIGN	PP	100	40	83	P C		ELECTROMAGNETIC	TW	25	10	20	
07. DIGITAL LOGIC DESIGN	PR	50	20	37			DATA STRUCTURES	PP	100	40	56	
701 7011 711 711 711 711 711 711 711 711	PP —.	100	40	49			DATA STRUCTURES	PR	50	20	47	
09. NETWORK AND POWER LAB.	TW	50	20	44	_		COMMUNICATION THEORY	PP	100	40	58	
10. ELECTRONIC INSTRUMENTS AND TOO	OLS IW	50	20	42	РС		COMMUNICATION THEORY	OR	50	20	30	
076/1500		_				21.	CIRCUIT SIMULATION AND TOOLS	TW	50	20	41	Р
GRAND TOTAL = 976/1500, RESULT: FIR ORDN. 1 MARKS :	ST CLAS	S										
					 ARSHA		, 71100836в , , ,		 ICT		 S8053	
01. SIGNAL AND SYSTEMS	PP	100	40	57	РС	11.	ENGINEERING MATHEMATICS III	PP	100	40	71	Р
02. SIGNAL AND SYSTEMS	OR	50	20	36	РС	12.	ENGINEERING MATHEMATICS III	TW	25	10	21	Р
03. SOLID STATES DEVICES AND CIRCU	ITSPP	100	40	40	РС	13.	INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	63	Р
04. SOLID STATES DEVICES AND CIRCU	ITSPR	50	20	40	РС	14.	INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	42	Р
05. NETWORK ANALYSIS	PP	100	40	60	РС	15.	ELECTROMAGNETIC	PP	100	40	59	Р
06. DIGITAL LOGIC DESIGN	PP	100	40	43	РС	16.	ELECTROMAGNETIC	TW	25	10	22	Р
07. DIGITAL LOGIC DESIGN	PR	50	20	30	P C	17.	DATA STRUCTURES	PP	100	40	63	Р
08. POWER DEVICES AND MACHINES	PP	100	40	55	P C	18.	DATA STRUCTURES	PR	50	20	46	Р
09. NETWORK AND POWER LAB.	TW	50	20	40	PС	19.	COMMUNICATION THEORY	PP	100	40	65	Р
10. ELECTRONIC INSTRUMENTS AND TOO	LS TW	50	20	41	P C	20.	COMMUNICATION THEORY	OR	50	20	42	Р
						21.	CIRCUIT SIMULATION AND TOOLS	TW	50	20	41	Р
GRAND TOTAL = $977/1500$, RESULT: FIR	ST CLAS	S										
ORDN. 1 MARKS :												
S8053051 KADAM SHRIKANT DEVIDAS		• •			NJIVAN		, 71100840L , , ,					
01. SIGNAL AND SYSTEMS	PP	100	40	49	PC	11.	ENGINEERING MATHEMATICS III	PP	100	40	40	Р
02. SIGNAL AND SYSTEMS	OR	50	20	35	PC	12.	ENGINEERING MATHEMATICS III	TW	25	10	19	Р
03. SOLID STATES DEVICES AND CIRCU	ITSPP	100	40	40	Р	13.	INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	40	Р
04. SOLID STATES DEVICES AND CIRCU	ITSPR	50	20	25	PC	14.	INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	32	Р
05. NETWORK ANALYSIS	PP	100	40	40	PC	15.	ELECTROMAGNETIC	PP	100	40	40	Р
06. DIGITAL LOGIC DESIGN		100	40	67	P C	16.	ELECTROMAGNETIC	TW	25	10	15	Р
07. DIGITAL LOGIC DESIGN	PR	50	20	28	P C	17.	DATA STRUCTURES	PP	100	40	44	Р
08. POWER DEVICES AND MACHINES	PP	100	40	45	Р	18.	DATA STRUCTURES	PR	50	20	46	Р
09. NETWORK AND POWER LAB.	TW	50	20	31	P C	19.	COMMUNICATION THEORY	PP	100	40	40	Р
10. ELECTRONIC INSTRUMENTS AND TOO	LS TW	50	20	27	P C	20.	COMMUNICATION THEORY	OR	50	20	20	Р
						21.	CIRCUIT SIMULATION AND TOOLS	TW	50	20	34	Р
GRAND TOTAL = $757/1500$, RESULT: SEC	OND CLA	SS										
ORDN. 1 MARKS :												

DATE: 14 AUG. 2012 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 18 (389)

DATE : 14 AUG. 2012	CEN	IKE :	PUNE	TN211	LIUIE	OF COMPUTE	R TECHNOLOGY, PUNE.	PA	GE NO.	18	(3	89)
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NOTE: FIRST LINE : SEAT NO., NAME				-	-				•			
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S8053052 KAMBLE AMRUTA GORAKH					ANDA			, P			S8053	
01. SIGNAL AND SYSTEMS	PP	100	40		РС		ENGINEERING MATHEMATICS III	PP	100	40	78	
02. SIGNAL AND SYSTEMS	OR	50	20		РС		ENGINEERING MATHEMATICS III	TW	25	10	19	
03. SOLID STATES DEVICES AND CIRCU		100	40	43	РС		INTEGRATED CIRCUITS APPLICATION		100	40	56	
04. SOLID STATES DEVICES AND CIRCU	ITSPR	50	20	35			INTEGRATED CIRCUITS APPLICATION	S PR	50	20	26	
05. NETWORK ANALYSIS	PP	100	40	49			ELECTROMAGNETIC	PP	100	40	50	
06. DIGITAL LOGIC DESIGN	PP	100	40	71	_	16.	ELECTROMAGNETIC	TW	25	10	18	
07. DIGITAL LOGIC DESIGN	PR	50	20	25	РС	17.	DATA STRUCTURES	PP	100	40	46	
08. POWER DEVICES AND MACHINES	PP	100	40	50	РС	18.	DATA STRUCTURES	PR	50	20	45	
09. NETWORK AND POWER LAB.	TW	50	20	40	РС	19.	COMMUNICATION THEORY	PP	100	40	53	Р
10. ELECTRONIC INSTRUMENTS AND TOO	LS TW	50	20	40	РС	20.	COMMUNICATION THEORY	OR	50	20	30	Р
						21.	CIRCUIT SIMULATION AND TOOLS	TW	50	20	34	Р
GRAND TOTAL = $913/1500$, RESULT: FIR	ST CLAS	SS										
ORDN. 1 MARKS :												
S8053053 KAMBLE DASHARATH SATAP	PA			KA	MAL		, 71236314к ,	, P	ICT	,	S8053	05
01. SIGNAL AND SYSTEMS	PP	100	40	40	РС	11.	ENGINEERING MATHEMATICS III	PP	100	40	40	Р
02. SIGNAL AND SYSTEMS	OR	50	20	33	РС	12.	ENGINEERING MATHEMATICS III	TW	25	10	19	P
03. SOLID STATES DEVICES AND CIRCU	ITSPP	100	40	49	РС	13.	INTEGRATED CIRCUITS APPLICATION	S PP	100	40	44	P
04. SOLID STATES DEVICES AND CIRCU	ITSPR	50	20	27	РС	14.	INTEGRATED CIRCUITS APPLICATION	S PR	50	20	27	P
05. NETWORK ANALYSIS	PP	100	40	40	РС	15.	ELECTROMAGNETIC	PP	100	40	58	P
06. DIGITAL LOGIC DESIGN	PP	100	40	44	РС	16.	ELECTROMAGNETIC	TW	25	10	15	P
07. DIGITAL LOGIC DESIGN	PR	50	20	27	РС	17.	DATA STRUCTURES	PP	100	40	45	P
08. POWER DEVICES AND MACHINES	PP	100	40	46	РС	18.	DATA STRUCTURES	PR	50	20	39	P
09. NETWORK AND POWER LAB.	TW	50	20	38	РС	19.	COMMUNICATION THEORY	PP	100	40	57	P
10. ELECTRONIC INSTRUMENTS AND TOO	LS TW	50	20	38	РС	20.	COMMUNICATION THEORY	OR	50	20	29	P
						21.	CIRCUIT SIMULATION AND TOOLS	TW	50	20	34	Р
GRAND TOTAL = 789/1500, RESULT: SEC	OND CLA	ASS										
ORDN. 1 MARKS :												
S8053054 KAMBLE PRATIK RAVINDRA				SU	JMAN		, 71100845m ,	, Р	ICT	,	S8053	05
01. SIGNAL AND SYSTEMS	PP	100	40	40	РС	11.	ENGINEERING MATHEMATICS III			40	57	P
02. SIGNAL AND SYSTEMS	OR	50	20	34	РС		ENGINEERING MATHEMATICS III	TW	25	10	14	Р
03. SOLID STATES DEVICES AND CIRCU	ITSPP	100	40		РC		INTEGRATED CIRCUITS APPLICATION	S PP	100	40	48	
04. SOLID STATES DEVICES AND CIRCU		50	20		РC		INTEGRATED CIRCUITS APPLICATION		50	20	32	
05. NETWORK ANALYSIS	PP	100	40		F		ELECTROMAGNETIC	PP	100	40	30	
06. DIGITAL LOGIC DESIGN	PP	100	40		РС		ELECTROMAGNETIC	TW	25	10	12	
07. DIGITAL LOGIC DESIGN		50	20		PC		DATA STRUCTURES	PP	100	40	52	
08. POWER DEVICES AND MACHINES		100	40		P		DATA STRUCTURES	PR	50	20	30	
09. NETWORK AND POWER LAB.	TW	50	20		P C		COMMUNICATION THEORY	PP	100	40	61	
10. ELECTRONIC INSTRUMENTS AND TOO		50	20		РС		COMMUNICATION THEORY	OR	50	20	25	
TO LELCTRONIC INSTRUMENTS AND TOO	I W	50	20	50	, ,		CIRCUIT SIMULATION AND TOOLS			20	27	
GRAND TOTAL = 746/1500, RESULT: FAI		κт				۷1.	CINCOIT SIMULATION AND TOOLS	1 VV	50	20	۷.	۲
ORDN. 1 MARKS :	LJ A.I											
JUDIA: T MAKKS :												

DATE: 14 AUG. 2012 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 19 (390)

DATE . 14 AUG. 2012	CENT	KE . P	ONE .	TN21T	.IUIE (OF COMPUTE	R TECHNOLOGY, PUNE.	PAC	JE NU.	19	()	90)
NOTE: FIRST LINE : SEAT NO., NAME				-	-				•			
OTHER LINES: HEAD OF PASSING,	MAX.	MARKS	5, M	IN. P	PASS MA	ARKS, MARI	KS OBTAINED, P/F:PASS/FAIL, C:P	REVIO	DUS CAI	RRY O	VER	
S8053055 KAMTHE SNEHAL SUNIL					.KA		, 71236315н , , ,		ICT	-	S8053	
01. SIGNAL AND SYSTEMS		100	40		РС		ENGINEERING MATHEMATICS III		100	40	51	
02. SIGNAL AND SYSTEMS	OR	50	20		РС		ENGINEERING MATHEMATICS III		25	10	22	
03. SOLID STATES DEVICES AND CIRCUI	_	100	40		РС		INTEGRATED CIRCUITS APPLICATIONS		100	40	51	
04. SOLID STATES DEVICES AND CIRCUI		50	20	38			INTEGRATED CIRCUITS APPLICATIONS		50	20	27	
05. NETWORK ANALYSIS	PP 	100	40	43			ELECTROMAGNETIC	PP	100	40	56	
06. DIGITAL LOGIC DESIGN	PP	100	40	56	P C		ELECTROMAGNETIC	TW	25	10	18	
07. DIGITAL LOGIC DESIGN		50	20	27			DATA STRUCTURES	PP 	100	40	61	
	PP —	100	40		P C	_	DATA STRUCTURES	PR	50	20	38	
09. NETWORK AND POWER LAB.		50	20		P C		COMMUNICATION THEORY	PP	100	40	55	
10. ELECTRONIC INSTRUMENTS AND TOOL	.S TW	50	20	41	РС		COMMUNICATION THEORY	OR	50	20	30	
003/4500		_				21.	CIRCUIT SIMULATION AND TOOLS	TW	50	20	42	Р
GRAND TOTAL = 902/1500, RESULT: FIRS	ST CLAS	S										
ORDN. 1 MARKS :												
S8053056 KANSARA KRUTIKA SANJAY												
S8053056 KANSARA KRUTIKA SANJAY 01. SIGNAL AND SYSTEMS	DD	100	40		GISHA P C	11	,		100	, 40	S8053 73	
	OR	50	20		PC		ENGINEERING MATHEMATICS III	TW	25	10	23	
02. SIGNAL AND SYSTEMS 03. SOLID STATES DEVICES AND CIRCUI		100	40		PC		INTEGRATED CIRCUITS APPLICATIONS		100	40	49	
	_	50	20		PC	_			50		43	
04. SOLID STATES DEVICES AND CIRCUI 05. NETWORK ANALYSIS	PP	100	40	65			INTEGRATED CIRCUITS APPLICATIONS ELECTROMAGNETIC	PR PP	100	20 40	62	
06. DIGITAL LOGIC DESIGN	PP	100	40	61			ELECTROMAGNETIC	TW	25	10	23	
07		50	20	_	PC		DATA STRUCTURES	PP	100	40	74	
	PP	100	40	49			DATA STRUCTURES	PR	50	20	44	
09. NETWORK AND POWER LAB.	TW	50	20		РС		COMMUNICATION THEORY	PP	100	40	53	
10. ELECTRONIC INSTRUMENTS AND TOOL				44			COMMUNICATION THEORY	OR	50	20	39	
10. ELECTRONIC INSTRUMENTS AND TOOL	.5 IW	30	20	77	r C		CIRCUIT SIMULATION AND TOOLS	_	50	20	44	
GRAND TOTAL = 1022/1500, RESULT: FIRS	T CLAS	S WTTL	י אדם	TTNCT	TON	21.	CIRCUIT SIMULATION AND TOOLS	1 44	30	20	77	•
ORDN. 1 MARKS :	JI CLAS	J WIII	. 013	TINCI	1014							
S8053057 KANUNGO KAPIL RAMESH					ISHNA		, 71100849D , , ,		ICT .		s8053	
01. SIGNAL AND SYSTEMS	PP	100	40		РС	11.	ENGINEERING MATHEMATICS III			40	67	
02. SIGNAL AND SYSTEMS	OR	50	20		P C		ENGINEERING MATHEMATICS III		25	10	19	
03. SOLID STATES DEVICES AND CIRCUI	_	100	40		PC		INTEGRATED CIRCUITS APPLICATIONS		100	40	46	
04. SOLID STATES DEVICES AND CIRCUI		50	20		РС		INTEGRATED CIRCUITS APPLICATIONS		50	20	30	
05. NETWORK ANALYSIS	PP	100	40		РС		ELECTROMAGNETIC	PP	100	40	60	
06. DIGITAL LOGIC DESIGN	PP	100	40		РС		ELECTROMAGNETIC	TW	25	10	17	
07. DIGITAL LOGIC DESIGN	PR	50	20		PC		DATA STRUCTURES	PP	100	40	62	
	PP	100	40		P C		DATA STRUCTURES	PR	50	20	47	
09. NETWORK AND POWER LAB.	TW	50	20	40	РС		COMMUNICATION THEORY	PP	100	40	46	
10. ELECTRONIC INSTRUMENTS AND TOOL		50	20		P C		COMMUNICATION THEORY	OR	50	20	32	
	-		•		-		CIRCUIT SIMULATION AND TOOLS	TW	50	20	40	
GRAND TOTAL = 881/1500, RESULT: HIGH	IER SEC	OND CL	ASS						-	-	-	
ORDN. 1 MARKS :												

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71236316F , , PICT , S8053058 KALPANA S8053058 KASHID SONALI BHARAT 01. SIGNAL AND SYSTEMS PP 100 40 40 P C 11. ENGINEERING MATHEMATICS III PP 100 40 61 P 02. SIGNAL AND SYSTEMS OR 50 20 36 P C 25 10 19 P 12. ENGINEERING MATHEMATICS III TW 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 48 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 49 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 42 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 40 P 05. NETWORK ANALYSIS PP 100 PP 100 40 48 P C 15. ELECTROMAGNETIC 40 49 P PP 100 40 66 P C 25 10 19 P 06. DIGITAL LOGIC DESIGN 16. ELECTROMAGNETIC TW 100 07. DIGITAL LOGIC DESIGN 50 20 37 P C 40 67 P PR 17. DATA STRUCTURES PP 08. POWER DEVICES AND MACHINES PP 100 40 41 P C 50 20 18. DATA STRUCTURES PR 46 P 09. NETWORK AND POWER LAB. TW 50 20 41 P C 100 40 47 P 19. COMMUNICATION THEORY PP 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 42 P C 50 20 32 P 20. COMMUNICATION THEORY OR 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 40 P GRAND TOTAL = 910/1500, RESULT: FIRST CLASS ORDN. 1 MARKS: S8053059 KESARKAR ONKAR PANDURANG ANITA , s8053059 PP 100 40 52 P C 11. ENGINEERING MATHEMATICS III PP 100 40 40 P 01. SIGNAL AND SYSTEMS 02. SIGNAL AND SYSTEMS OR 50 20 34 P C 12. ENGINEERING MATHEMATICS III TW 25 10 20 P 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 46 P.C 64 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 32 P C 50 20 14. INTEGRATED CIRCUITS APPLICATIONS PR 39 P PP 100 PP 100 05. NETWORK ANALYSIS 40 53 P C 15. ELECTROMAGNETIC 40 51 P PP 100 40 69 P C 16. ELECTROMAGNETIC 25 19 P 06. DIGITAL LOGIC DESIGN TW 10 50 20 25 P C 100 40 07. DIGITAL LOGIC DESIGN PR 17. DATA STRUCTURES PP 65 P 08. POWER DEVICES AND MACHINES PP 100 40 56 P C 18. DATA STRUCTURES PR 50 20 35 P 09. NETWORK AND POWER LAB. TW 50 20 42 P C 19. COMMUNICATION THEORY PP 100 40 52 P 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 42 P C 50 20 36 P 20. COMMUNICATION THEORY OR 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 40 P GRAND TOTAL = 912/1500, RESULT: FIRST CLASS ORDN. 1 MARKS: TARA , S8053060 S8053060 KHADATKAR NIRAJ MANOHAR 01. SIGNAL AND SYSTEMS PP 100 40 62 P C 11. ENGINEERING MATHEMATICS III PP 100 40 64 P 02. SIGNAL AND SYSTEMS 12. ENGINEERING MATHEMATICS III TW 25 10 23 P OR 50 20 42 P C 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 55 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 48 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 33 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 36 P 05. NETWORK ANALYSIS PP 100 40 71 P C PP 100 40 70 P 15. ELECTROMAGNETIC 25 20 P 06. DIGITAL LOGIC DESIGN PP 100 40 79 P C TW 10 16. ELECTROMAGNETIC 07. DIGITAL LOGIC DESIGN 20 28 P C 100 40 65 P PR 50 17. DATA STRUCTURES PP 08. POWER DEVICES AND MACHINES PP 100 40 63 P C 18. DATA STRUCTURES PR 50 20 43 P 09. NETWORK AND POWER LAB. 50 20 37 P C 100 40 56 P TW 19. COMMUNICATION THEORY 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 41 P C 50 20 37 P 20. COMMUNICATION THEORY OR 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 42 P GRAND TOTAL = 1015/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS:

UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(ELECTRONICS & TELECOM.) DATE : 14 AUG. 2012 CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 21 (392) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S80	53061 KHAMBE DIGVIJAY RAJ	JARAM			MAN	JUSHA		, 711008	Б59м ,	,	Р	ICT	,	S8053	061
01.	SIGNAL AND SYSTEMS	PP	100	40	41	P C	11	. ENGINEERING	MATHEMATICS	III	PP	100	40	66	Р
02.	SIGNAL AND SYSTEMS	OR	50	20	20	P C	12	. ENGINEERING	MATHEMATICS	III	TW	25	10	16	Р
03.	SOLID STATES DEVICES AND CI	RCUITSPP	100	40	40	PС	13	. INTEGRATED	CIRCUITS APP	LICATIONS	PP	100	40	40	Р
04.	SOLID STATES DEVICES AND CI	RCUITSPR	50	20	35	PС	14	. INTEGRATED	CIRCUITS APP	LICATIONS	PR	50	20	40	Р
05.	NETWORK ANALYSIS	PP	100	40	56	Р	15	. ELECTROMAGN	IETIC		PP	100	40	56	Р
06.	DIGITAL LOGIC DESIGN	PP	100	40	40	P C	16	. ELECTROMAGN	IETIC		TW	25	10	13	Р
07.	DIGITAL LOGIC DESIGN	PR	50	20	25	P C	17	. DATA STRUCT	URES		PP	100	40	62	Р
08.	POWER DEVICES AND MACHINES	PP	100	40	43	РС	18	. DATA STRUCT	URES		PR	50	20	43	Р
09.	NETWORK AND POWER LAB.	TW	50	20	33	РС	19	. COMMUNICATI	ON THEORY		PP	100	40	62	Р
10.	ELECTRONIC INSTRUMENTS AND	TOOLS TW	50	20	31	РС	20	. COMMUNICATI	ON THEORY		OR	50	20	21	Р
							21	. CIRCUIT SIM	ULATION AND	T00LS	TW	50	20	30	Р

GRAND TOTAL = 813/1500, RESULT: SECOND CLASS

ORDN. 1 MARKS:

S8053062 KHANDELWAL DIVYANG KA	MALKISH	OR		SANGEETA	, 71100863к		PICT	. s805
01. SIGNAL AND SYSTEMS	PP	100	40	43 P C	,	,		,
02. SIGNAL AND SYSTEMS	OR	50	20	20 P C				
03. SOLID STATES DEVICES AND CIRC	UITSPP	100	40	AA F				
04. SOLID STATES DEVICES AND CIRC	UITSPR	50	20	AA F				
05. NETWORK ANALYSIS	PP	100	40	10 F				
06. DIGITAL LOGIC DESIGN	PP	100	40	AA F				
07. DIGITAL LOGIC DESIGN	PR	50	20	29 P C				
08. POWER DEVICES AND MACHINES	PP	100	40	16 F				
09. NETWORK AND POWER LAB.	TW	50	20	21 P C				
10. ELECTRONIC INSTRUMENTS AND TO	OLS TW	50	20	23 P C				
FIRST TERM TOTAL = $162/750$.								

ORDN. 1 MARKS:

S8053064 KOLI VIJAYA VYANKATRAO SEETA . 71100866D PICT . S8053064

36033004	KULI VIJATA VTANKATI	KAU			SEE	LIA		, /11000000 , , ,	PI	LCI	, :	50035	004
01. SIGNAL	AND SYSTEMS	PP	100	40	40	P C	11.	ENGINEERING MATHEMATICS III	PP	100	40	06	F
02. SIGNAL	AND SYSTEMS	OR	50	20	22	P C	12.	ENGINEERING MATHEMATICS III	TW	25	10	17	Р
03. SOLID S	STATES DEVICES AND CI	RCUITSPP	100	40	24	F	13.	INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	40	Р
04. SOLID S	STATES DEVICES AND CI	RCUITSPR	50	20	20	P C	14.	INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	05	F
05. NETWORK	<pre>ANALYSIS</pre>	PP	100	40	17	F	15.	ELECTROMAGNETIC	PP	100	40	31	F
06. DIGITAL	LOGIC DESIGN	PP	100	40	15	F	16.	ELECTROMAGNETIC	TW	25	10	18	Р
07. DIGITAL	LOGIC DESIGN	PR	50	20	30	P C	17.	DATA STRUCTURES	PP	100	40	43	Р
08. POWER	DEVICES AND MACHINES	PP	100	40	31	F	18.	DATA STRUCTURES	PR	50	20	32	Р
09. NETWORK	AND POWER LAB.	TW	50	20	27	P C	19.	COMMUNICATION THEORY	PP	100	40	20	F
10. ELECTRO	ONIC INSTRUMENTS AND	TOOLS TW	50	20	30	P C	20.	COMMUNICATION THEORY	OR	50	20	12	F
							21.	CIRCUIT SIMULATION AND TOOLS	TW	50	20	32	Р

GRAND TOTAL = 512/1500, RESULT: FAILS

RESULT RESERVED FOR BKLG

ORDN. 1 MARKS:

DATE: 14 AUG. 2012 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 22 (393)

NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71100867B , , PICT , S8053065 RADHIKA S8053065 KRISHNAN SANTOSH KUMAR PP 100 40 54 P 01. SIGNAL AND SYSTEMS PP 100 40 58 P C 11. ENGINEERING MATHEMATICS III 02. SIGNAL AND SYSTEMS OR 50 20 41 P C 25 10 23 P 12. ENGINEERING MATHEMATICS III TW 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 56 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 52 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 34 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 35 P 05. NETWORK ANALYSIS PP 100 40 48 P C 15. ELECTROMAGNETIC PP 100 40 66 P PP 100 40 71 PC 25 10 06. DIGITAL LOGIC DESIGN 16. ELECTROMAGNETIC TW 21 P 50 20 27 P C 100 07. DIGITAL LOGIC DESIGN 40 55 P PR 17. DATA STRUCTURES PP 08. POWER DEVICES AND MACHINES PP 100 40 55 P C 50 20 47 P 18. DATA STRUCTURES PR 09. NETWORK AND POWER LAB. TW 50 20 41 P C 100 40 19. COMMUNICATION THEORY PP 66 P 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 43 P C 50 20 22 P 20. COMMUNICATION THEORY OR 50 20 43 P 21. CIRCUIT SIMULATION AND TOOLS TW GRAND TOTAL = 958/1500, RESULT: FIRST CLASS ORDN. 1 MARKS: , 71100871L , , , PICT , S8053066 S8053066 KULKARNI KRANTI MADHAV MADHURI PP 100 40 44 P C 11. ENGINEERING MATHEMATICS III PP 100 40 74 P 01. SIGNAL AND SYSTEMS OR 50 20 28 P.C. 02. SIGNAL AND SYSTEMS 12. ENGINEERING MATHEMATICS III TW 25 10 19 P 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 45 P C 50 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 32 P C 50 20 14. INTEGRATED CIRCUITS APPLICATIONS PR 34 P PP 100 PP 100 05. NETWORK ANALYSIS 40 40 P C 15. ELECTROMAGNETIC 40 53 P PP 100 40 64 P C 16. ELECTROMAGNETIC 25 19 P 06. DIGITAL LOGIC DESIGN TW 10 20 32 P 100 40 07. DIGITAL LOGIC DESIGN PR 50 17. DATA STRUCTURES PP 58 P 08. POWER DEVICES AND MACHINES PP 100 40 45 P C 18. DATA STRUCTURES PR 50 20 46 P 09. NETWORK AND POWER LAB. TW 50 20 38 P C 19. COMMUNICATION THEORY PP 100 40 52 P 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 36 P C 50 20 29 P 20. COMMUNICATION THEORY OR 50 20 34 P 21. CIRCUIT SIMULATION AND TOOLS TW GRAND TOTAL = 872/1500, RESULT: HIGHER SECOND CLASS ORDN. 1 MARKS: , 71100875C , , PICT , s8053067 S8053067 KUNAL AGARWAL POONAM DEVI 01. SIGNAL AND SYSTEMS PP 100 40 52 P C 11. ENGINEERING MATHEMATICS III PP 100 40 69 P 12. ENGINEERING MATHEMATICS III TW 25 10 02. SIGNAL AND SYSTEMS or 50 20 30 P C 23 P 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 51 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 51 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 35 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 39 P 05. NETWORK ANALYSIS PP 100 40 48 P C PP 100 40 79 P 15. ELECTROMAGNETIC 25 19 P 06. DIGITAL LOGIC DESIGN PP 100 40 57 P C TW 10 16. ELECTROMAGNETIC 07. DIGITAL LOGIC DESIGN 20 39 P C 100 40 60 P PR 50 17. DATA STRUCTURES PP 08. POWER DEVICES AND MACHINES PP 100 40 45 P C 18. DATA STRUCTURES PR 50 20 48 P 09. NETWORK AND POWER LAB. 50 20 36 P C 100 40 58 P TW 19. COMMUNICATION THEORY 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 36 P C 50 20 30 P 20. COMMUNICATION THEORY OR 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 38 P GRAND TOTAL = 943/1500, RESULT: FIRST CLASS ORDN. 1 MARKS:

DATE: 14 AUG. 2012 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 23 (394)

NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71100876M , , PICT , S8053068 SANGITA S8053068 KURHADE RUSHIKESH SAMBHAJI 01. SIGNAL AND SYSTEMS PP 100 40 59 P C 11. ENGINEERING MATHEMATICS III PP 100 40 88 P OR 50 20 38 P C 25 10 23 P 02. SIGNAL AND SYSTEMS 12. ENGINEERING MATHEMATICS III TW 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 51 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 53 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 44 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 40 P 05. NETWORK ANALYSIS PP 100 40 63 P C 15. ELECTROMAGNETIC PP 100 40 80 P 100 40 73 P C 25 10 23 P 06. DIGITAL LOGIC DESIGN PP 16. ELECTROMAGNETIC TW 100 07. DIGITAL LOGIC DESIGN 50 20 30 P C 40 55 P PR 17. DATA STRUCTURES PP 08. POWER DEVICES AND MACHINES PP 100 40 53 P C 50 20 18. DATA STRUCTURES PR 47 P 09. NETWORK AND POWER LAB. TW 50 20 43 P C 100 40 19. COMMUNICATION THEORY PP 62 P 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 44 P C 50 20 34 P 20. COMMUNICATION THEORY OR 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 44 P GRAND TOTAL = 1047/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS: S8053069 KUSH VARMA PARVEEN , s8053069 PP 100 40 69 P 01. SIGNAL AND SYSTEMS PP 100 40 64 P C 11. ENGINEERING MATHEMATICS III 20 39 P C 02. SIGNAL AND SYSTEMS OR 50 12. ENGINEERING MATHEMATICS III TW 25 10 22 P 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 72 P.C 60 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 43 P C 50 20 14. INTEGRATED CIRCUITS APPLICATIONS PR 30 P PP 100 PP 100 05. NETWORK ANALYSIS 40 48 P C 15. ELECTROMAGNETIC 40 57 P PP 100 40 75 P C 16. ELECTROMAGNETIC 25 19 P 06. DIGITAL LOGIC DESIGN TW 10 50 20 45 P C 100 73 P 07. DIGITAL LOGIC DESIGN PR 17. DATA STRUCTURES PP 40 08. POWER DEVICES AND MACHINES PP 100 40 71 P C 18. DATA STRUCTURES PR 50 20 48 P 09. NETWORK AND POWER LAB. TW 50 20 41 P C 19. COMMUNICATION THEORY PP 100 40 54 P 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 40 P C 50 20 37 P 20. COMMUNICATION THEORY OR 50 20 42 P 21. CIRCUIT SIMULATION AND TOOLS TW GRAND TOTAL = 1049/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS: , 71100885L , , PICT , s8053070 S8053070 M SUBRAMANIAN M KOHILA 01. SIGNAL AND SYSTEMS PP 100 40 43 P C 11. ENGINEERING MATHEMATICS III PP 100 40 52 P 12. ENGINEERING MATHEMATICS III TW 25 10 02. SIGNAL AND SYSTEMS or 50 20 43 P C 20 P 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 51 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 51 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 42 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 35 P 05. NETWORK ANALYSIS PP 100 40 56 P C PP 100 40 72 P 15. ELECTROMAGNETIC 25 06. DIGITAL LOGIC DESIGN PP 100 40 66 P C 16. ELECTROMAGNETIC TW 10 16 P 07. DIGITAL LOGIC DESIGN 20 35 P C 100 40 PR 50 17. DATA STRUCTURES PP 61 P 08. POWER DEVICES AND MACHINES PP 100 40 50 P C 18. DATA STRUCTURES PR 50 20 44 P 09. NETWORK AND POWER LAB. 50 20 40 P C 100 40 45 P TW 19. COMMUNICATION THEORY 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 39 P C 50 20 33 P 20. COMMUNICATION THEORY OR 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 39 P GRAND TOTAL = 933+05/1500, RESULT: FIRST CLASS[0.163] ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71100888E , , PICT , S8053071 S8053071 MAHAJAN PRATEEK PARAG PREETI 01. SIGNAL AND SYSTEMS PP 100 40 80 P C 11. ENGINEERING MATHEMATICS III PP 100 40 85 P or 50 20 36 P C 25 10 23 P 02. SIGNAL AND SYSTEMS 12. ENGINEERING MATHEMATICS III TW 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 40 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 43 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 36 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 37 P 05. NETWORK ANALYSIS PP 100 40 55 P C 15. ELECTROMAGNETIC PP 100 40 72 P PP 100 40 59 P C 25 10 22 P 06. DIGITAL LOGIC DESIGN 16. ELECTROMAGNETIC TW 100 07. DIGITAL LOGIC DESIGN 50 20 32 P C 40 71 P PR 17. DATA STRUCTURES PP 08. POWER DEVICES AND MACHINES PP 100 40 46 P C 50 20 18. DATA STRUCTURES PR 48 P 09. NETWORK AND POWER LAB. TW 50 20 38 P C 100 40 19. COMMUNICATION THEORY PP 62 P 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 43 P C 50 20 35 P 20. COMMUNICATION THEORY OR 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 39 P GRAND TOTAL = 1002/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS: S8053072 MAHANGARE BHAGYASHRI BABASAHEB SUREKHA , s8053072 11. ENGINEERING MATHEMATICS III PP 100 40 40 P 01. SIGNAL AND SYSTEMS PP 100 40 41 P C OR 50 20 29 P C 02. SIGNAL AND SYSTEMS 12. ENGINEERING MATHEMATICS III TW 25 10 22 P 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 42 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 30# P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 30 P C 50 20 25 P 14. INTEGRATED CIRCUITS APPLICATIONS PR PP 100 40 41 P PP 100 05. NETWORK ANALYSIS 15. ELECTROMAGNETIC 40 40 P PP 100 40 57 P C 16. ELECTROMAGNETIC 25 06. DIGITAL LOGIC DESIGN TW 10 17 P 50 20 29 P C 100 40 07. DIGITAL LOGIC DESIGN PR 17. DATA STRUCTURES PP 48 P 08. POWER DEVICES AND MACHINES PP 100 40 42 P C 18. DATA STRUCTURES PR 50 20 33 P 09. NETWORK AND POWER LAB. TW 50 20 40 P C 19. COMMUNICATION THEORY PP 100 40 50 P 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 37 P C 50 20 38 P 20. COMMUNICATION THEORY OR 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 42 P GRAND TOTAL = 773/1500, RESULT: SECOND CLASS # [0.4] ORDN. 1 MARKS: , 71100889C , , PICT , s8053073 S8053073 MALI RAKESH SAKHARAM REKHA 01. SIGNAL AND SYSTEMS PP 100 40 63 P C 11. ENGINEERING MATHEMATICS III PP 100 40 89 P 02. SIGNAL AND SYSTEMS 12. ENGINEERING MATHEMATICS III TW 25 10 OR 50 20 42 P C 22 P 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 53 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 51 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 22 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 28 P 05. NETWORK ANALYSIS PP 100 40 59 P C PP 100 40 64 P 15. ELECTROMAGNETIC 25 06. DIGITAL LOGIC DESIGN PP 100 40 55 P C TW 10 21 P 16. ELECTROMAGNETIC 07. DIGITAL LOGIC DESIGN 20 21 P C 100 40 72 P PR 50 17. DATA STRUCTURES PP 08. POWER DEVICES AND MACHINES PP 100 40 49 P C 18. DATA STRUCTURES PR 50 20 47 P 09. NETWORK AND POWER LAB. 50 20 34 P C 100 40 55 P TW 19. COMMUNICATION THEORY 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 38 P C 50 20 30 P 20. COMMUNICATION THEORY OR 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 37 P GRAND TOTAL = 952/1500, RESULT: FIRST CLASS ORDN. 1 MARKS:

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NOTE: FIRST LINE : SEAT NO., NAME							REG NO PREVIOUS SEAT NO C					
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S8053074 METHIKAR PRIYANKA JITEN					JSHMA			 Р:			S8053	
01. SIGNAL AND SYSTEMS	PP	100	40		РС	11.	ENGINEERING MATHEMATICS III	PP	100	40	86	
02. SIGNAL AND SYSTEMS	OR	50	20	36			ENGINEERING MATHEMATICS III	TW	25	10	21	
03. SOLID STATES DEVICES AND CIRCU	_	100	40	77			INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	62	
04. SOLID STATES DEVICES AND CIRCU	ETSPR	50	20		РC		INTEGRATED CIRCUITS APPLICATIONS		50	20	42	Р
05. NETWORK ANALYSIS	PP	100	40	66	РС		ELECTROMAGNETIC	PP	100	40	61	Р
06. DIGITAL LOGIC DESIGN	PP	100	40	76	РС	16.	ELECTROMAGNETIC	TW	25	10	18	Р
07. DIGITAL LOGIC DESIGN	PR	50	20	22	РС	17.	DATA STRUCTURES	PP	100	40	82	Р
08. POWER DEVICES AND MACHINES	PP	100	40	52	РС	18.	DATA STRUCTURES	PR	50	20	48	Р
09. NETWORK AND POWER LAB.	TW	50	20	42	РС	19.	COMMUNICATION THEORY	PP	100	40	69	Р
10. ELECTRONIC INSTRUMENTS AND TOOL	_S TW	50	20	42	РС	20.	COMMUNICATION THEORY	OR	50	20	33	Р
						21.	CIRCUIT SIMULATION AND TOOLS	TW	50	20	39	Р
GRAND TOTAL = 1098/1500, RESULT: FIRST	ST CLAS	SS WITH	H DIS	TINCT	ΓΙΟN							
ORDN. 1 MARKS :												
S8053075 MOHITE SHEETAL CHANDRA	KANT			SA	AVITA		, 71236319L , , ,	Ρ:	ICT	,	S8053	075
01. SIGNAL AND SYSTEMS	PP	100	40	64	РС	11.	ENGINEERING MATHEMATICS III	PP	100	40	93	Р
02. SIGNAL AND SYSTEMS	OR	50	20	42	РС	12.	ENGINEERING MATHEMATICS III	TW	25	10	21	Р
03. SOLID STATES DEVICES AND CIRCUI	ITSPP	100	40	68	РС	13.	INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	61	Р
04. SOLID STATES DEVICES AND CIRCUI	ITSPR	50	20	32	РС	14.	INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	35	Р
05. NETWORK ANALYSIS	PP	100	40	67	РС	15.	ELECTROMAGNETIC	PP	100	40	70	Р
06. DIGITAL LOGIC DESIGN	PP	100	40	77	PС	16.	ELECTROMAGNETIC	TW	25	10	20	Р
07. DIGITAL LOGIC DESIGN	PR	50	20	32	РС	17.	DATA STRUCTURES	PP	100	40	74	Р
08. POWER DEVICES AND MACHINES	PP	100	40	51	РС	18.	DATA STRUCTURES	PR	50	20	44	Р
09. NETWORK AND POWER LAB.	TW	50	20	40	РС	19.	COMMUNICATION THEORY	PP	100	40	64	Р
10. ELECTRONIC INSTRUMENTS AND TOOL	_S TW	50	20	40	РС	20.	COMMUNICATION THEORY	OR	50	20	34	Р
						21.	CIRCUIT SIMULATION AND TOOLS	TW	50	20	38	Р
GRAND TOTAL = $1067/1500$, RESULT: FIRS	ST CLAS	SS WITH	H DIS	TINCT	ΓΙΟΝ							
ORDN. 1 MARKS :												
S8053076 MORE ANKUR SURESHRAO		100	40		AXMI		, 71100900н , , ,				S8053	
	PP				P C		ENGINEERING MATHEMATICS III			40	44	
02. SIGNAL AND SYSTEMS	_	50	20		P _		ENGINEERING MATHEMATICS III		25	10		
03. SOLID STATES DEVICES AND CIRCU		100	40	11			INTEGRATED CIRCUITS APPLICATIONS		100	40	12	
04. SOLID STATES DEVICES AND CIRCU		50	20	20			INTEGRATED CIRCUITS APPLICATIONS		50	20	08	
05. NETWORK ANALYSIS	PP 	100	40	23			ELECTROMAGNETIC	PP	100	40	18	
06. DIGITAL LOGIC DESIGN		100	40	19			ELECTROMAGNETIC	TW	25	10	11	
07. DIGITAL LOGIC DESIGN		50	20		P C		DATA STRUCTURES	PP	100	40	17	
08. POWER DEVICES AND MACHINES		100	40	20			DATA STRUCTURES	PR	50 100	20		
09. NETWORK AND POWER LAB.		50	20		P C		COMMUNICATION THEORY	PP	100	40	19	
10. ELECTRONIC INSTRUMENTS AND TOOL	-9 IW	50	20	21	РС		COMMUNICATION THEORY	OR Tw	50 50	20		
GRAND TOTAL = 436/1500, RESULT: FAIL	c					۷1.	CIRCUIT SIMULATION AND TOOLS	TW	50	20	23	۲
ORDN. 1 MARKS:	_3											

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71236320D , , PICT , S8053077 S8053077 MUSALE VIVEK KISANRAO INDUBAI PP 100 40 55 P C 01. SIGNAL AND SYSTEMS 11. ENGINEERING MATHEMATICS III PP 100 40 43 P OR 50 20 28 P C 25 10 02. SIGNAL AND SYSTEMS 12. ENGINEERING MATHEMATICS III TW 18 P 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 56 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 40 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 33 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 38 P 05. NETWORK ANALYSIS PP 100 PP 100 40 40 P 15. ELECTROMAGNETIC 40 40 P PP 100 40 60 P C 25 10 19 P 06. DIGITAL LOGIC DESIGN 16. ELECTROMAGNETIC TW 100 07. DIGITAL LOGIC DESIGN 50 20 28 P C 40 53 P PR 17. DATA STRUCTURES PP 08. POWER DEVICES AND MACHINES PP 100 40 P C 50 20 32 P 18. DATA STRUCTURES PR 09. NETWORK AND POWER LAB. TW 50 20 36 P C 100 40 46 P 19. COMMUNICATION THEORY PP 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 39 P C 50 20 30 P 20. COMMUNICATION THEORY OR 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 38 P GRAND TOTAL = 812/1500, RESULT: SECOND CLASS ORDN. 1 MARKS: S8053078 NADEKAR SANJIV CHANDRAKANT ALKA , s8053078 PP 100 40 44 P C 11. ENGINEERING MATHEMATICS III PP 100 40 40 P 01. SIGNAL AND SYSTEMS 02. SIGNAL AND SYSTEMS OR 50 20 22 P.C. 12. ENGINEERING MATHEMATICS III TW 25 10 17 P 40 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 41 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 33 P C 50 14. INTEGRATED CIRCUITS APPLICATIONS PR 20 38 P 05. NETWORK ANALYSIS PP 100 PP 100 40 40 P C 15. ELECTROMAGNETIC 40 28 F PP 100 40 59 P C 16. ELECTROMAGNETIC 25 06. DIGITAL LOGIC DESIGN TW 10 16 P 50 20 30 P C 100 07. DIGITAL LOGIC DESIGN PR 17. DATA STRUCTURES PP 40 65 P 08. POWER DEVICES AND MACHINES PP 100 40 P C 18. DATA STRUCTURES PR 50 20 42 P 09. NETWORK AND POWER LAB. TW 50 20 37 P C 19. COMMUNICATION THEORY PP 100 40 65 P 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 38 P C 50 20 29 P 20. COMMUNICATION THEORY OR 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 37 P GRAND TOTAL = 801/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS: , 71100906G , , PICT , s8053079 S8053079 NAIK DIGJA ANKUSH VASANTI 01. SIGNAL AND SYSTEMS PP 100 40 72 P C 11. ENGINEERING MATHEMATICS III PP 100 40 82 P 02. SIGNAL AND SYSTEMS 12. ENGINEERING MATHEMATICS III TW 25 10 OR 50 20 35 P.C. 22 P 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 55 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 48 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 44 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 30 P 05. NETWORK ANALYSIS PP 100 40 58 P C PP 100 40 47 P 15. ELECTROMAGNETIC 25 06. DIGITAL LOGIC DESIGN PP 100 40 58 P C TW 10 18 P 16. ELECTROMAGNETIC 83 P 07. DIGITAL LOGIC DESIGN 20 22 P C 100 40 PR 50 17. DATA STRUCTURES PP 08. POWER DEVICES AND MACHINES PP 100 40 43 P C 18. DATA STRUCTURES PR 50 20 46 P 09. NETWORK AND POWER LAB. 50 20 42 P C 100 40 62 P TW 19. COMMUNICATION THEORY 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 41 P C 50 20 35 P 20. COMMUNICATION THEORY OR 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 38 P GRAND TOTAL = 981/1500, RESULT: FIRST CLASS ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71100909M , , PICT , S8053080 S8053080 NAKHATE ANIKET BHAGWANRAO ANITA PP 100 40 67 P C PP 100 40 74 P 01. SIGNAL AND SYSTEMS 11. ENGINEERING MATHEMATICS III or 50 20 44 P C 25 10 23 P 02. SIGNAL AND SYSTEMS 12. ENGINEERING MATHEMATICS III TW 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 48 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 42 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 35 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 31 P 05. NETWORK ANALYSIS PP 100 PP 100 40 56 P C 15. ELECTROMAGNETIC 40 46 P PP 100 40 43 P C 25 10 22 P 06. DIGITAL LOGIC DESIGN 16. ELECTROMAGNETIC TW 100 07. DIGITAL LOGIC DESIGN 50 20 25 P C 40 56 P PR 17. DATA STRUCTURES PP 08. POWER DEVICES AND MACHINES PP 100 40 P C 50 20 18. DATA STRUCTURES PR 44 P 09. NETWORK AND POWER LAB. TW 50 20 36 P C 100 40 54 P 19. COMMUNICATION THEORY PP 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 36 P C 50 20 29 P 20. COMMUNICATION THEORY OR 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 43 P GRAND TOTAL = 894+06/1500, RESULT: FIRST CLASS [0.2] ORDN. 1 MARKS : , 71100910E , , PICT S8053081 NANDNAWARE AKASH JAGNNATH DRAUPADA , s8053081 PP 100 40 15 F 11. ENGINEERING MATHEMATICS III PP 100 40 40 P 01. SIGNAL AND SYSTEMS 02. SIGNAL AND SYSTEMS OR 50 20 26 P C 12. ENGINEERING MATHEMATICS III TW 25 10 11 P 40 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 08 F 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 25 P C 50 20 05 F 14. INTEGRATED CIRCUITS APPLICATIONS PR PP 100 PP 100 05. NETWORK ANALYSIS 40 11 F 15. ELECTROMAGNETIC 40 12 F PP 100 49 P C 16. ELECTROMAGNETIC 25 10 P 06. DIGITAL LOGIC DESIGN 40 TW 10 50 20 05 F 100 40 07. DIGITAL LOGIC DESIGN PR 17. DATA STRUCTURES PP 40 P 08. POWER DEVICES AND MACHINES PP 100 40 22 F 18. DATA STRUCTURES PR 50 20 35 P 09. NETWORK AND POWER LAB. TW 50 20 30 P C 40 27 F 19. COMMUNICATION THEORY PP 100 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 28 P C 50 20 30 P 20. COMMUNICATION THEORY OR 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 25 P GRAND TOTAL = 494/1500, RESULT: FAILS ORDN. 1 MARKS: , 71100914H , , , , , , S8053082 S8053082 NARKAR PRAJAKTA SUHAS SUPRIYA 01. SIGNAL AND SYSTEMS PP 100 40 78 P C 11. ENGINEERING MATHEMATICS III PP 100 40 93 P OR 50 20 38 P C 02. SIGNAL AND SYSTEMS 12. ENGINEERING MATHEMATICS III TW 25 10 24 P 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 74 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 53 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 43 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 42 P 05. NETWORK ANALYSIS PP 100 40 83 P C PP 100 40 66 P 15. ELECTROMAGNETIC 25 06. DIGITAL LOGIC DESIGN PP 100 40 55 P C TW 10 24 P 16. ELECTROMAGNETIC 07. DIGITAL LOGIC DESIGN 20 43 P C 100 40 76 P PR 50 17. DATA STRUCTURES PP 08. POWER DEVICES AND MACHINES PP 100 40 59 P C 18. DATA STRUCTURES PR 50 20 48 P 09. NETWORK AND POWER LAB. TW 50 20 45 P C 100 40 63 P 19. COMMUNICATION THEORY 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 46 P C 50 20 42 P 20. COMMUNICATION THEORY OR 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 45 P GRAND TOTAL = 1140/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71100917B , , PICT , S8053083 CHITRA S8053083 NIKITA AVINASH ANGRE PP 100 40 72 P 01. SIGNAL AND SYSTEMS PP 100 40 40 P C 11. ENGINEERING MATHEMATICS III 02. SIGNAL AND SYSTEMS OR 50 20 42 P C 25 10 18 P 12. ENGINEERING MATHEMATICS III TW 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 42 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 30 F 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 32 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 22 P 05. NETWORK ANALYSIS PP 100 40 66 P C 15. ELECTROMAGNETIC PP 100 40 22 F PP 100 40 45 P C 25 10 15 P 06. DIGITAL LOGIC DESIGN 16. ELECTROMAGNETIC TW 100 07. DIGITAL LOGIC DESIGN 50 20 34 P C 40 53 P PR 17. DATA STRUCTURES PP 08. POWER DEVICES AND MACHINES PP 100 40 P 50 20 18. DATA STRUCTURES PR 42 P 09. NETWORK AND POWER LAB. TW 50 20 36 P C 100 40 40 P 19. COMMUNICATION THEORY PP 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 33 P C 50 20 40 P 20. COMMUNICATION THEORY OR 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 31 P GRAND TOTAL = 795/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS: , 71100920в , , РІСТ S8053084 PADGILWAR GAURAV VISHWAMBHAR SAVITA , s8053084 11. ENGINEERING MATHEMATICS III PP 100 40 80 P 01. SIGNAL AND SYSTEMS PP 100 40 49 P C 02. SIGNAL AND SYSTEMS OR 50 20 36 P C 12. ENGINEERING MATHEMATICS III TW 25 10 18 P 59 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 40 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 40 P C 50 20 14. INTEGRATED CIRCUITS APPLICATIONS PR 41 P PP 100 40 57 P C PP 100 49 P 05. NETWORK ANALYSIS 15. ELECTROMAGNETIC 40 PP 100 40 75 P C 16. ELECTROMAGNETIC 25 15 P 06. DIGITAL LOGIC DESIGN TW 10 50 20 30 P C 100 07. DIGITAL LOGIC DESIGN PR 17. DATA STRUCTURES PP 40 64 P 08. POWER DEVICES AND MACHINES PP 100 40 45 P C 18. DATA STRUCTURES PR 50 20 45 P 09. NETWORK AND POWER LAB. TW 50 20 36 P C 19. COMMUNICATION THEORY PP 100 40 48 P 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 35 P C 50 20 40 P 20. COMMUNICATION THEORY OR 50 20 35 P 21. CIRCUIT SIMULATION AND TOOLS TW GRAND TOTAL = 937/1500, RESULT: FIRST CLASS ORDN. 1 MARKS: , 71236322L , , PICT , s8053085 S8053085 PADHYE ABHINAV ANANT ANURADHA 01. SIGNAL AND SYSTEMS PP 100 40 65 P C 11. ENGINEERING MATHEMATICS III PP 100 40 49 P 02. SIGNAL AND SYSTEMS 12. ENGINEERING MATHEMATICS III TW 25 10 or 50 20 39 P C 20 P 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 63 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 56 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 36 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 40 P 05. NETWORK ANALYSIS PP 100 40 55 P C PP 100 40 45 P 15. ELECTROMAGNETIC 25 19 P 06. DIGITAL LOGIC DESIGN PP 100 40 71 P C TW 10 16. ELECTROMAGNETIC 07. DIGITAL LOGIC DESIGN 20 33 P C 100 40 65 P PR 50 17. DATA STRUCTURES PP 08. POWER DEVICES AND MACHINES PP 100 40 48 P C 18. DATA STRUCTURES PR 50 20 43 P 09. NETWORK AND POWER LAB. 50 20 44 P C 100 40 57 P TW 19. COMMUNICATION THEORY 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 43 P C 50 20 45 P 20. COMMUNICATION THEORY OR 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 40 P GRAND TOTAL = 976/1500, RESULT: FIRST CLASS ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71100921L , , PICT , S8053086 S8053086 PADOLE NEHA SUNIL NIRMALA 01. SIGNAL AND SYSTEMS PP 100 40 75 P C 11. ENGINEERING MATHEMATICS III PP 100 40 96 P 02. SIGNAL AND SYSTEMS OR 50 20 30 P C 25 10 22 P 12. ENGINEERING MATHEMATICS III TW 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 64 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 50 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 40 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 38 P 05. NETWORK ANALYSIS PP 100 40 40 P C 15. ELECTROMAGNETIC PP 100 40 PP 100 40 82 P C 25 10 22 P 06. DIGITAL LOGIC DESIGN 16. ELECTROMAGNETIC TW 100 07. DIGITAL LOGIC DESIGN 50 20 35 P C 40 81 P PR 17. DATA STRUCTURES PP 08. POWER DEVICES AND MACHINES PP 100 40 P C 50 20 18. DATA STRUCTURES PR 46 P 09. NETWORK AND POWER LAB. TW 50 20 39 P C 100 40 19. COMMUNICATION THEORY PP 62 P 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 38 P C 50 20 42 P 20. COMMUNICATION THEORY OR 50 20 41 P 21. CIRCUIT SIMULATION AND TOOLS TW GRAND TOTAL = 1027/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS: , 71100923G , , PICT S8053087 PAGARIYA BHUSHAN RAMESHCHAND MANJUSHRI , s8053087 11. ENGINEERING MATHEMATICS III PP 100 40 91 P 01. SIGNAL AND SYSTEMS PP 100 40 66 P C 02. SIGNAL AND SYSTEMS OR 50 20 38 P C 12. ENGINEERING MATHEMATICS III TW 25 10 22 P 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 71 P C 52 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 39 P C 50 20 42 P 14. INTEGRATED CIRCUITS APPLICATIONS PR PP 100 PP 100 05. NETWORK ANALYSIS 40 78 P C 15. ELECTROMAGNETIC 40 56 P PP 100 40 91 P C 16. ELECTROMAGNETIC 25 06. DIGITAL LOGIC DESIGN TW 10 21 P 50 20 42 P C 100 07. DIGITAL LOGIC DESIGN PR 17. DATA STRUCTURES PP 40 81 P 08. POWER DEVICES AND MACHINES PP 100 40 64 P C 18. DATA STRUCTURES PR 50 20 48 P 09. NETWORK AND POWER LAB. TW 50 20 42 P C 19. COMMUNICATION THEORY PP 100 40 66 P 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 42 P C 50 20 43 P 20. COMMUNICATION THEORY OR 50 20 42 P 21. CIRCUIT SIMULATION AND TOOLS TW GRAND TOTAL = 1137/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS: , 71100927к , , РІСТ S8053088 PANDEY JUHI SANJAY SUNITA , S8053088 01. SIGNAL AND SYSTEMS PP 100 40 75 P C 11. ENGINEERING MATHEMATICS III PP 100 40 91 P 12. ENGINEERING MATHEMATICS III TW 25 10 02. SIGNAL AND SYSTEMS OR 50 20 45 P C 24 P 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 53 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 48 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 45 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 45 P 05. NETWORK ANALYSIS PP 100 40 85 P C PP 100 40 71 P 15. ELECTROMAGNETIC 25 24 P 06. DIGITAL LOGIC DESIGN PP 100 40 73 P C 16. ELECTROMAGNETIC TW 10 07. DIGITAL LOGIC DESIGN 20 44 P C 100 40 PR 50 17. DATA STRUCTURES PP 61 P 08. POWER DEVICES AND MACHINES PP 100 40 59 P C 18. DATA STRUCTURES PR 50 20 48 P 09. NETWORK AND POWER LAB. 100 40 74 P TW 50 20 47 P C 19. COMMUNICATION THEORY 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 47 P C 50 20 40 P 20. COMMUNICATION THEORY OR 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 46 P GRAND TOTAL = 1145/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS:

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DATE : 14 AUG. 2012	CENT	RE : I	PUNE :	INSTI	TUTE	OF COMPUT	ITER	TECHNOLOGY, PUNE.	PAG	GE NO.	30	(4	01)
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NOTE: FIRST LINE : SEAT NO., NAME	OF THE	CAND	IDATE	, MC	THER,	, PERMANEN	NT I	REG. NO., PREVIOUS SEAT NO., CO	OLLEG	GE, S	EAT	NO.	
OTHER LINES: HEAD OF PASSING,	MAX.	MARKS	S, M	IN. P	PASS N	MARKS, MA	IARKS	S OBTAINED, P/F:PASS/FAIL, C:PI	REVIC	OUS CAR	RRY C	VER	
S8053089 PANDILWAR RAMESHWAR KIS	HORE			SU	JJATA			, 71100928н , ,	ΡI	ГСТ	,	S8053	089
01. SIGNAL AND SYSTEMS	PP	100	40	55	РС	11	1. 1	ENGINEERING MATHEMATICS III	PP	100	40	46	Р
02. SIGNAL AND SYSTEMS	OR	50	20	32	РС	12	.2. I	ENGINEERING MATHEMATICS III	TW	25	10	14	Р
03. SOLID STATES DEVICES AND CIRCUI	TSPP	100	40	40	Р	13	3. :	INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	32	F
04. SOLID STATES DEVICES AND CIRCUI	TSPR	50	20	28	Р			INTEGRATED CIRCUITS APPLICATIONS		50	20	20	
05. NETWORK ANALYSIS	PP	100	40	26				ELECTROMAGNETIC	PP	100	40	41	
06. DIGITAL LOGIC DESIGN	PP	100	40	58				ELECTROMAGNETIC	TW	25	10	12	
07. DIGITAL LOGIC DESIGN	PR	50	20	25				DATA STRUCTURES	PP	100	40	48	
08. POWER DEVICES AND MACHINES	PP	100	40	40	РС			DATA STRUCTURES	PR	50	20	31	
09. NETWORK AND POWER LAB.		50	20	27				COMMUNICATION THEORY	PP	100	40	50	
	TW					_	_				_		
10. ELECTRONIC INSTRUMENTS AND TOOL	SIW	50	20	23	РС			COMMUNICATION THEORY	OR	50	20	30	
704/4500						21	Ί. (CIRCUIT SIMULATION AND TOOLS	TW	50	20	24	Р
GRAND TOTAL = 704/1500, RESULT: FAIL	S A.T.	K.T.											
ORDN. 1 MARKS :													
							•						
S8053090 PARDESHI SAGAR RAJKUMAR					SHA			, 71100931н , ,	ΡI	ICT	,	S8053	
01. SIGNAL AND SYSTEMS	PP	100	40	66	РС	11	.1. 1	ENGINEERING MATHEMATICS III	PP	100	40	66	
02. SIGNAL AND SYSTEMS	OR	50	20	39	РС	12	.2. I	ENGINEERING MATHEMATICS III	TW	25	10	20	
03. SOLID STATES DEVICES AND CIRCUI	TSPP	100	40	55	РС	13	.3.	INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	40	Р
04. SOLID STATES DEVICES AND CIRCUI	TSPR	50	20	37	РС			INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	35	Р
05. NETWORK ANALYSIS	PP	100	40	54	РС	15	.5 . I	ELECTROMAGNETIC	PP	100	40	55	Р
06. DIGITAL LOGIC DESIGN	PP	100	40	47	РС	16	.6. I	ELECTROMAGNETIC	TW	25	10	17	Р
07. DIGITAL LOGIC DESIGN	PR	50	20	42	РС	17	.7 . [DATA STRUCTURES	PP	100	40	73	Р
08. POWER DEVICES AND MACHINES	PP	100	40	41	РС	18	.8. I	DATA STRUCTURES	PR	50	20	46	Р
09. NETWORK AND POWER LAB.	TW	50	20	43	РС	19	9. (COMMUNICATION THEORY	PP	100	40	63	Р
10. ELECTRONIC INSTRUMENTS AND TOOL	S TW	50	20	43	РС	20	0.0	COMMUNICATION THEORY	OR	50	20	38	Р
						21	1. (CIRCUIT SIMULATION AND TOOLS	TW	50	20	34	Р
GRAND TOTAL = 954/1500, RESULT: FIRS	T CLAS	SS											
ORDN. 1 MARKS :													
S8053091 PATHAK SWANAND SUDHIR				SC	NALI			, 71100936」 , , ,	ΡJ	СТ	,	S8053	091
01. SIGNAL AND SYSTEMS	PP	100	40	66	РС	11	1. 1	ENGINEERING MATHEMATICS III			40	87	Р
02. SIGNAL AND SYSTEMS	OR	50	20		РС			ENGINEERING MATHEMATICS III		25	10	23	
03. SOLID STATES DEVICES AND CIRCUI	_	100	40		PC			INTEGRATED CIRCUITS APPLICATIONS		100	40	40	
04. SOLID STATES DEVICES AND CIRCUI		50	20		P C			INTEGRATED CIRCUITS APPLICATIONS		50	20	32	
05. NETWORK ANALYSIS	PP	100	40		PC			ELECTROMAGNETIC	PP	100	40	61	
06. DIGITAL LOGIC DESIGN	PP	100	40		PC			ELECTROMAGNETIC	TW	25	10	20	
07. DIGITAL LOGIC DESIGN	PR	50 100	20 40	24 40	P C			DATA STRUCTURES	PP DB	100	40	67 48	
08. POWER DEVICES AND MACHINES		100	40					DATA STRUCTURES	PR	50 100	20	48	
09. NETWORK AND POWER LAB.		50	20		PC			COMMUNICATION THEORY	PP	100	40	43	
10. ELECTRONIC INSTRUMENTS AND TOOL	5 IW	50	20	40	РС			COMMUNICATION THEORY	OR	50	20	35	
CDAND TOTAL 030/4500	-					21	Ι. (CIRCUIT SIMULATION AND TOOLS	TW	50	20	44	Ч
GRAND TOTAL = 938/1500, RESULT: FIRS	r CLAS	S											
ORDN. 1 MARKS :													

DATE: 14 AUG. 2012 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 31 (402)

NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71100937G , , PICT , S8053092 LATA S8053092 PATIL AMOL HIRAMAN PP 100 40 47 P 01. SIGNAL AND SYSTEMS PP 100 40 44 P C 11. ENGINEERING MATHEMATICS III 02. SIGNAL AND SYSTEMS OR 50 20 29 P C 25 10 21 P 12. ENGINEERING MATHEMATICS III TW 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 40 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 40 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 27 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 30 P 05. NETWORK ANALYSIS PP 100 PP 100 40 44 P 15. ELECTROMAGNETIC 40 40 P PP 100 40 43 P C 25 10 20 P 06. DIGITAL LOGIC DESIGN 16. ELECTROMAGNETIC TW 100 07. DIGITAL LOGIC DESIGN 50 20 30 P C 40 70 P PR 17. DATA STRUCTURES PP 08. POWER DEVICES AND MACHINES PP 100 40 41 P C 50 20 18. DATA STRUCTURES PR 43 P 09. NETWORK AND POWER LAB. TW 50 20 39 P C 100 40 51 P 19. COMMUNICATION THEORY PP 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 37 P C 50 20 35 P 20. COMMUNICATION THEORY OR 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 37 P GRAND TOTAL = 808/1500, RESULT: SECOND CLASS ORDN. 1 MARKS: S8053093 PATIL ASHISH ISHWAR KUSUM , s8053093 PP 100 40 70 P C 11. ENGINEERING MATHEMATICS III PP 100 40 67 P 01. SIGNAL AND SYSTEMS 02. SIGNAL AND SYSTEMS OR 50 20 26 P C 12. ENGINEERING MATHEMATICS III TW 25 10 20 P 40 52 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 03. SOLID STATES DEVICES AND CIRCUITSPP 100 43 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 30 P C 50 35 P 14. INTEGRATED CIRCUITS APPLICATIONS PR 20 PP 100 PP 100 05. NETWORK ANALYSIS 40 49 P C 15. ELECTROMAGNETIC 40 40 P PP 100 40 72 P C 16. ELECTROMAGNETIC 25 06. DIGITAL LOGIC DESIGN TW 10 16 P 20 30 P C 100 40 07. DIGITAL LOGIC DESIGN PR 50 17. DATA STRUCTURES PP 59 P 08. POWER DEVICES AND MACHINES PP 100 40 P C 18. DATA STRUCTURES PR 50 20 40 P 09. NETWORK AND POWER LAB. TW 50 20 39 P C 19. COMMUNICATION THEORY PP 100 40 57 P 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 37 P C 50 20 38 P 20. COMMUNICATION THEORY OR 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 38 P GRAND TOTAL = 898+02/1500, RESULT: FIRST CLASS [0.2] ORDN. 1 MARKS: , s8053094 S8053094 PATIL PRADNYA RAJENDRA SNEHAL 01. SIGNAL AND SYSTEMS PP 100 40 65 P C 11. ENGINEERING MATHEMATICS III PP 100 40 69 P 02. SIGNAL AND SYSTEMS 12. ENGINEERING MATHEMATICS III TW 25 10 OR 50 20 32 P C 20 P 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 54 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 40 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 38 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 35 P 05. NETWORK ANALYSIS PP 100 40 49 P C PP 100 40 46 P 15. ELECTROMAGNETIC 25 06. DIGITAL LOGIC DESIGN PP 100 40 66 P C TW 10 19 P 16. ELECTROMAGNETIC 07. DIGITAL LOGIC DESIGN 20 34 P C 100 40 71 P PR 50 17. DATA STRUCTURES PP 08. POWER DEVICES AND MACHINES PP 100 40 44 P C 18. DATA STRUCTURES PR 50 20 42 P 09. NETWORK AND POWER LAB. 40 59 P TW 50 20 42 P C 100 19. COMMUNICATION THEORY 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 43 P C 50 20 35 P 20. COMMUNICATION THEORY OR 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 39 P GRAND TOTAL = 942/1500, RESULT: FIRST CLASS ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71100942C , , PICT , S8053095 S8053095 PATIL PUJA VIJAY MIRA 01. SIGNAL AND SYSTEMS PP 100 40 48 P C 11. ENGINEERING MATHEMATICS III PP 100 40 69 P or 50 20 25 P C 25 10 02. SIGNAL AND SYSTEMS 12. ENGINEERING MATHEMATICS III TW 16 P 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 40 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 45 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 32 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 33 P 05. NETWORK ANALYSIS PP 100 PP 100 40 56 P 15. ELECTROMAGNETIC 40 45 P PP 100 40 49 P C 25 10 15 P 06. DIGITAL LOGIC DESIGN 16. ELECTROMAGNETIC TW 50 20 25 P C 100 07. DIGITAL LOGIC DESIGN 40 64 P PR 17. DATA STRUCTURES PP 08. POWER DEVICES AND MACHINES PP 100 40 P C 50 20 33 P 18. DATA STRUCTURES PR 09. NETWORK AND POWER LAB. TW 50 20 32 P C 100 40 53 P 19. COMMUNICATION THEORY PP 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 33 P C 50 20 30 P 20. COMMUNICATION THEORY OR 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 31 P GRAND TOTAL = 814/1500, RESULT: SECOND CLASS ORDN. 1 MARKS: S8053096 PATIL SHIVRATNA BALASAHEB SHEELA , s8053096 11. ENGINEERING MATHEMATICS III PP 100 40 57 P 01. SIGNAL AND SYSTEMS PP 100 40 54 P C 02. SIGNAL AND SYSTEMS OR 50 20 30 P C 12. ENGINEERING MATHEMATICS III TW 25 10 16 P 49 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 61 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 39 P C 50 20 30 P 14. INTEGRATED CIRCUITS APPLICATIONS PR PP 100 PP 100 05. NETWORK ANALYSIS 40 44 P C 15. ELECTROMAGNETIC 40 40 P PP 100 40 16. ELECTROMAGNETIC 25 06. DIGITAL LOGIC DESIGN 68 P C TW 10 16 P 50 20 24 P C 100 40 07. DIGITAL LOGIC DESIGN PR 17. DATA STRUCTURES PP 56 P 08. POWER DEVICES AND MACHINES PP 100 40 P C 18. DATA STRUCTURES PR 50 20 34 P 09. NETWORK AND POWER LAB. TW 50 20 30 P C 19. COMMUNICATION THEORY PP 100 40 64 P 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 32 P C 50 20 30 P 20. COMMUNICATION THEORY OR 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 36 P GRAND TOTAL = 850/1500, RESULT: HIGHER SECOND CLASS ORDN. 1 MARKS: , 71100946F , , PICT , s8053097 S8053097 PAVAN TENKALE SHIVKANTA 01. SIGNAL AND SYSTEMS PP 100 40 54 P C 11. ENGINEERING MATHEMATICS III PP 100 40 61 P 12. ENGINEERING MATHEMATICS III TW 25 10 17 P 02. SIGNAL AND SYSTEMS or 50 20 32 P C 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 48 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 13 F 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 35 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 AA F 05. NETWORK ANALYSIS PP 100 40 42 P C PP 100 40 31 F 15. ELECTROMAGNETIC 25 06. DIGITAL LOGIC DESIGN PP 100 40 46 P C 16. ELECTROMAGNETIC TW 10 13 P 07. DIGITAL LOGIC DESIGN 20 23 P C 100 40 43 P PR 50 17. DATA STRUCTURES PP 08. POWER DEVICES AND MACHINES PP 100 40 P C 18. DATA STRUCTURES PR 50 20 25 P 09. NETWORK AND POWER LAB. 50 20 38 P C 100 40 53 P TW 19. COMMUNICATION THEORY 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 32 P C 50 20 23 P 20. COMMUNICATION THEORY OR 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 30 P GRAND TOTAL = 699/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71100948B , , PICT , S8053098 SUNITA S8053098 PAWAR AJAY BALNATH 01. SIGNAL AND SYSTEMS PP 100 40 65 P C 11. ENGINEERING MATHEMATICS III PP 100 40 83 P 02. SIGNAL AND SYSTEMS OR 50 20 42 P C 25 10 22 P 12. ENGINEERING MATHEMATICS III TW 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 56 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 48 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 39 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 30 P 05. NETWORK ANALYSIS PP 100 PP 100 40 57 P C 15. ELECTROMAGNETIC 40 40 P PP 100 40 63 P C 25 10 22 P 06. DIGITAL LOGIC DESIGN 16. ELECTROMAGNETIC TW 100 07. DIGITAL LOGIC DESIGN 50 20 38 P C 40 67 P PR 17. DATA STRUCTURES PP 08. POWER DEVICES AND MACHINES PP 100 40 52 P C 50 20 18. DATA STRUCTURES PR 46 P 09. NETWORK AND POWER LAB. TW 50 20 40 P C 100 40 56 P 19. COMMUNICATION THEORY PP 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 41 P C 50 20 30 P 20. COMMUNICATION THEORY OR 50 20 41 P 21. CIRCUIT SIMULATION AND TOOLS TW GRAND TOTAL = 978/1500, RESULT: FIRST CLASS ORDN. 1 MARKS: , 71236325E , , PICT S8053099 VANDANA PAWAR ASHWINI SUDHAKAR , s8053099 PP 100 40 53 P C 11. ENGINEERING MATHEMATICS III PP 100 40 62 P 01. SIGNAL AND SYSTEMS 02. SIGNAL AND SYSTEMS OR 50 20 32 P C 12. ENGINEERING MATHEMATICS III TW 25 10 22 P 54 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 40 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 34 P C 50 20 14. INTEGRATED CIRCUITS APPLICATIONS PR 38 P PP 100 PP 100 05. NETWORK ANALYSIS 40 40 P C 15. ELECTROMAGNETIC 40 52 P PP 100 40 46 P C 16. ELECTROMAGNETIC 25 17 P 06. DIGITAL LOGIC DESIGN TW 10 50 20 30 P C 100 40 07. DIGITAL LOGIC DESIGN PR 17. DATA STRUCTURES PP 59 P 08. POWER DEVICES AND MACHINES PP 100 40 41 P C 18. DATA STRUCTURES PR 50 20 36 P 09. NETWORK AND POWER LAB. TW 50 20 39 P C 40 56 P 19. COMMUNICATION THEORY PP 100 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 39 P C 50 20 40 P 20. COMMUNICATION THEORY OR 50 20 41 P 21. CIRCUIT SIMULATION AND TOOLS TW GRAND TOTAL = 871/1500, RESULT: HIGHER SECOND CLASS ORDN. 1 MARKS: , 71100955E , , PICT , s8053100 S8053100 PHAPALE MONIKA DILIP ALKA 01. SIGNAL AND SYSTEMS PP 100 40 53 P C 11. ENGINEERING MATHEMATICS III PP 100 40 65 P 02. SIGNAL AND SYSTEMS 12. ENGINEERING MATHEMATICS III TW 25 10 or 50 20 34 P C 19 P 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 42 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 24 F 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 33 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 35 P 05. NETWORK ANALYSIS PP 100 40 40 P C PP 100 40 40 P 15. ELECTROMAGNETIC 25 20 P 06. DIGITAL LOGIC DESIGN PP 100 40 48 P C 16. ELECTROMAGNETIC TW 10 07. DIGITAL LOGIC DESIGN 20 26 P C 100 40 63 P PR 50 17. DATA STRUCTURES PP 08. POWER DEVICES AND MACHINES PP 100 40 56 P 18. DATA STRUCTURES PR 50 20 35 P 09. NETWORK AND POWER LAB. 50 20 40 P C 100 40 53 P TW 19. COMMUNICATION THEORY 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 37 P C 50 20 30 P 20. COMMUNICATION THEORY OR 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 40 P GRAND TOTAL = 833/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71100958K , , PICT , S8053101 VARSHA S8053101 PRAGATI VASANT SHAH 11. ENGINEERING MATHEMATICS III PP 100 40 77 P 01. SIGNAL AND SYSTEMS PP 100 40 59 P C 02. SIGNAL AND SYSTEMS OR 50 20 37 P C 12. ENGINEERING MATHEMATICS III TW 25 10 21 P 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 50 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 40 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 28 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 28 P 05. NETWORK ANALYSIS PP 100 PP 100 40 55 P C 15. ELECTROMAGNETIC 40 52 P PP 100 40 54 P C 25 10 20 P 06. DIGITAL LOGIC DESIGN 16. ELECTROMAGNETIC TW 100 07. DIGITAL LOGIC DESIGN 50 20 39 P PP 40 58 P PR 17. DATA STRUCTURES 08. POWER DEVICES AND MACHINES PP 100 40 P C 50 20 18. DATA STRUCTURES PR 48 P 09. NETWORK AND POWER LAB. TW 50 20 41 P C 100 40 64 P 19. COMMUNICATION THEORY PP 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 41 P C 50 20 40 P 20. COMMUNICATION THEORY OR 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 39 P GRAND TOTAL = 931/1500, RESULT: FIRST CLASS ORDN. 1 MARKS: S8053102 PRAMOD KUMAR VERMA SUMAN DEVI , s8053102 PP 100 40 58 P C 11. ENGINEERING MATHEMATICS III PP 100 40 91 P 01. SIGNAL AND SYSTEMS 02. SIGNAL AND SYSTEMS OR 50 20 38 P C 12. ENGINEERING MATHEMATICS III TW 25 10 21 P 40 50 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 03. SOLID STATES DEVICES AND CIRCUITSPP 100 45 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 35 P C 50 20 14. INTEGRATED CIRCUITS APPLICATIONS PR 42 P 05. NETWORK ANALYSIS PP 100 PP 100 40 65 P C 15. ELECTROMAGNETIC 40 71 P PP 100 40 60 P C 16. ELECTROMAGNETIC 25 18 P 06. DIGITAL LOGIC DESIGN TW 10 50 20 28 P 100 74 P 07. DIGITAL LOGIC DESIGN PR 17. DATA STRUCTURES PP 40 08. POWER DEVICES AND MACHINES PP 100 40 51 P C 18. DATA STRUCTURES PR 50 20 44 P 09. NETWORK AND POWER LAB. TW 50 20 42 P C 19. COMMUNICATION THEORY PP 100 40 63 P 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 39 P C 50 20 39 P 20. COMMUNICATION THEORY OR 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 40 P GRAND TOTAL = 1014/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS: , 71100960M , , PICT , s8053103 S8053103 PRAYAG SHREYAS NAGNATH JAYASHRI 01. SIGNAL AND SYSTEMS PP 100 40 58 P C 11. ENGINEERING MATHEMATICS III PP 100 40 73 P OR 50 20 34 P C 02. SIGNAL AND SYSTEMS 12. ENGINEERING MATHEMATICS III TW 25 10 19 P 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 61 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 43 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 33 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 32 P 05. NETWORK ANALYSIS PP 100 40 51 P C PP 100 40 40 P 15. ELECTROMAGNETIC 25 06. DIGITAL LOGIC DESIGN PP 100 40 57 P C 16. ELECTROMAGNETIC TW 10 15 P 07. DIGITAL LOGIC DESIGN 20 34 P C 100 40 63 P PR 50 17. DATA STRUCTURES PP 08. POWER DEVICES AND MACHINES PP 100 40 46 P C 18. DATA STRUCTURES PR 50 20 46 P 09. NETWORK AND POWER LAB. 50 20 35 P C 100 40 57 P TW 19. COMMUNICATION THEORY 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 38 P C 20. COMMUNICATION THEORY 50 20 38 P OR 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 30 P GRAND TOTAL = 903+05/1500, RESULT: FIRST CLASS[0.163] ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71100979B , , PICT , S8053110 SHOBHA S8053110 RUDRAWAR SOURABH ASHWIN 11. ENGINEERING MATHEMATICS III PP 100 40 47 P 01. SIGNAL AND SYSTEMS PP 100 40 59 P C 02. SIGNAL AND SYSTEMS OR 50 20 35 P C 25 10 12. ENGINEERING MATHEMATICS III TW 14 P 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 49 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 52 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 31 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 28 P 05. NETWORK ANALYSIS PP 100 PP 100 40 40 P C 15. ELECTROMAGNETIC 40 PP 100 40 59 P C 25 10 14 P 06. DIGITAL LOGIC DESIGN 16. ELECTROMAGNETIC TW 100 07. DIGITAL LOGIC DESIGN 50 20 24 P C PP 40 55 P PR 17. DATA STRUCTURES 08. POWER DEVICES AND MACHINES PP 100 40 48 P C 50 20 18. DATA STRUCTURES PR 42 P 09. NETWORK AND POWER LAB. TW 50 20 35 P C 100 40 53 P 19. COMMUNICATION THEORY PP 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 33 P C 50 20 29 P 20. COMMUNICATION THEORY OR 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 32 P GRAND TOTAL = 837/1500, RESULT: HIGHER SECOND CLASS ORDN. 1 MARKS: S8053111 SALUNKE SAYALI RAJENDRA SANGEETA , s8053111 PP 100 40 60 P C 11. ENGINEERING MATHEMATICS III PP 100 40 79 P 01. SIGNAL AND SYSTEMS OR 50 20 40 P.C. 02. SIGNAL AND SYSTEMS 12. ENGINEERING MATHEMATICS III TW 25 10 22 P 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 57 P C 63 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 38 P C 50 20 14. INTEGRATED CIRCUITS APPLICATIONS PR 30 P 05. NETWORK ANALYSIS PP 100 PP 100 40 60 P C 15. ELECTROMAGNETIC 40 78 P PP 100 40 60 P C 16. ELECTROMAGNETIC 25 20 P 06. DIGITAL LOGIC DESIGN TW 10 50 20 38 P C 100 40 07. DIGITAL LOGIC DESIGN PR 17. DATA STRUCTURES PP 67 P 08. POWER DEVICES AND MACHINES PP 100 40 64 P C 18. DATA STRUCTURES PR 50 20 42 P 09. NETWORK AND POWER LAB. TW 50 20 32 P C 19. COMMUNICATION THEORY PP 100 40 63 P 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 34 P C 50 20 34 P 20. COMMUNICATION THEORY OR 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 38 P GRAND TOTAL = 1019/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS: , s8053112 S8053112 SALUNKHE MITALI KISHOR BHARATI 01. SIGNAL AND SYSTEMS PP 100 40 56 P C 11. ENGINEERING MATHEMATICS III PP 100 40 61 P OR 50 20 24 P C 02. SIGNAL AND SYSTEMS 12. ENGINEERING MATHEMATICS III TW 25 10 21 P 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 42 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 57 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 36 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 20 P 05. NETWORK ANALYSIS PP 100 40 42 P C PP 100 40 42 P 15. ELECTROMAGNETIC 25 06. DIGITAL LOGIC DESIGN PP 100 40 60 P C 16. ELECTROMAGNETIC TW 10 16 P 07. DIGITAL LOGIC DESIGN 20 26 P C 100 40 70 P PR 50 17. DATA STRUCTURES PP 08. POWER DEVICES AND MACHINES PP 100 40 46 P C 18. DATA STRUCTURES PR 50 20 35 P 09. NETWORK AND POWER LAB. 50 20 41 P C 100 40 51 P TW 19. COMMUNICATION THEORY 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 40 P C 20. COMMUNICATION THEORY 50 20 34 P OR 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 40 P GRAND TOTAL = 860/1500, RESULT: HIGHER SECOND CLASS ORDN. 1 MARKS:

UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(ELECTRONICS & TELECOM.)

DATE: 14 AUG. 2012 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 38 (409) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71236328K , , , PICT , s8053113 S8053113 SARODE BHUSHAN CHANDRAKANT ALKA PP 100 40 22 F 01. SIGNAL AND SYSTEMS PP 100 40 48 P C 11. ENGINEERING MATHEMATICS III 02. SIGNAL AND SYSTEMS OR 50 20 37 P C 25 10 20 P 12. ENGINEERING MATHEMATICS III TW 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 40 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 63 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 35 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 38 P PP 100 05. NETWORK ANALYSIS 40 40 P C 15. ELECTROMAGNETIC PP 100 40 PP 100 40 69 P C 25 10 19 P 06. DIGITAL LOGIC DESIGN 16. ELECTROMAGNETIC TW 50 20 22 P C PP 100 07. DIGITAL LOGIC DESIGN 40 67 P PR 17. DATA STRUCTURES 08. POWER DEVICES AND MACHINES PP 100 40 54 P C 50 20 45 P 18. DATA STRUCTURES PR 09. NETWORK AND POWER LAB. TW 50 20 41 P C 100 40 70 P 19. COMMUNICATION THEORY PP 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 42 P C 50 20 35 P 20. COMMUNICATION THEORY OR 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 38 P GRAND TOTAL = 885/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS: , 71100988M , , PICT S8053114 SARWAR ABHIJIT ASHOK MEERA , s8053114 PP 100 40 40 P C 11. ENGINEERING MATHEMATICS III PP 100 40 27 F 01. SIGNAL AND SYSTEMS OR 50 20 20 P C 02. SIGNAL AND SYSTEMS 12. ENGINEERING MATHEMATICS III TW 25 10 12 P 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 46 P.C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 45 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 33 P C 50 20 30 P 14. INTEGRATED CIRCUITS APPLICATIONS PR PP 100 40 40 P C PP 100 05. NETWORK ANALYSIS 15. ELECTROMAGNETIC 40 25 F PP 100 40 52 P C 16. ELECTROMAGNETIC 25 10 12 P 06. DIGITAL LOGIC DESIGN TW 50 20 32 P C 100 40 51 P 07. DIGITAL LOGIC DESIGN PR 17. DATA STRUCTURES PP 08. POWER DEVICES AND MACHINES PP 100 40 P C 18. DATA STRUCTURES PR 50 20 34 P 09. NETWORK AND POWER LAB. 40 46 P TW 50 20 39 P C 19. COMMUNICATION THEORY PP 100 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 35 P C 50 20 30 P 20. COMMUNICATION THEORY OR 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 30 P GRAND TOTAL = 719/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS: , 71100997L , , PICT , S8053115 S8053115 SHAHA SANKET MANOJKUMAR NEETA 01. SIGNAL AND SYSTEMS PP 100 40 51 P C 02. SIGNAL AND SYSTEMS OR 50 20 24 P C 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 40 P C 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 22 P C PP 100 40 58 P 05. NETWORK ANALYSIS PP 100 40 48 P C 06. DIGITAL LOGIC DESIGN 07. DIGITAL LOGIC DESIGN 20 15 F PR 50 08. POWER DEVICES AND MACHINES PP 100 40 P C TW 50 20 20 P C 09. NETWORK AND POWER LAB. 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 22 P C FIRST TERM TOTAL = 340/750. ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71100998J , , PICT , S8053116 S8053116 SHAIKH MAAZ FAYYAZ SHAHEEN 11. ENGINEERING MATHEMATICS III PP 100 40 76 P 01. SIGNAL AND SYSTEMS PP 100 40 67 P C 02. SIGNAL AND SYSTEMS OR 50 20 33 P C 25 10 23 P 12. ENGINEERING MATHEMATICS III TW 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 65 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 67 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 35 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 37 P 05. NETWORK ANALYSIS PP 100 PP 100 40 53 P C 15. ELECTROMAGNETIC 40 PP 100 40 64 P C 25 10 19 P 06. DIGITAL LOGIC DESIGN 16. ELECTROMAGNETIC TW 100 07. DIGITAL LOGIC DESIGN 50 20 33 P C PP 40 56 P PR 17. DATA STRUCTURES 08. POWER DEVICES AND MACHINES PP 100 40 56 P C 50 20 18. DATA STRUCTURES PR 45 P 09. NETWORK AND POWER LAB. TW 50 20 42 P C 100 40 63 P 19. COMMUNICATION THEORY PP 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 38 P C 50 20 40 P 20. COMMUNICATION THEORY OR 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 42 P GRAND TOTAL = 1017/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS: , 71101001D , , PICT S8053117 SHELKE ISHWAR ANKUSH MUKTA , s8053117 PP 100 40 65 P C 11. ENGINEERING MATHEMATICS III PP 100 40 84 P 01. SIGNAL AND SYSTEMS 02. SIGNAL AND SYSTEMS OR 50 20 35 P C 12. ENGINEERING MATHEMATICS III TW 25 10 22 P 60 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 69 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 38 P C 50 20 35 P 14. INTEGRATED CIRCUITS APPLICATIONS PR 05. NETWORK ANALYSIS PP 100 PP 100 40 76 PC 15. ELECTROMAGNETIC 40 86 P PP 100 40 76 P C 16. ELECTROMAGNETIC 25 06. DIGITAL LOGIC DESIGN TW 10 22 P 50 20 38 P C 100 71 P 07. DIGITAL LOGIC DESIGN PR 17. DATA STRUCTURES PP 40 08. POWER DEVICES AND MACHINES PP 100 40 66 P C 18. DATA STRUCTURES PR 50 20 48 P 09. NETWORK AND POWER LAB. TW 50 20 39 P C 19. COMMUNICATION THEORY PP 100 40 70 P 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 37 P C 50 20 36 P 20. COMMUNICATION THEORY OR 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 43 P GRAND TOTAL = 1116/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS: , 71101004j , , PICT , s8053118 S8053118 SHIKHA SUMAN SINHA SAVITA 01. SIGNAL AND SYSTEMS PP 100 40 42 P C 11. ENGINEERING MATHEMATICS III PP 100 40 24 F 12. ENGINEERING MATHEMATICS III TW 25 10 19 P 02. SIGNAL AND SYSTEMS OR 50 20 33 P C 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 40 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 47 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 25 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 20 P 05. NETWORK ANALYSIS PP 100 40 40 P C PP 100 40 28 F 15. ELECTROMAGNETIC 25 06. DIGITAL LOGIC DESIGN PP 100 40 61 P C 16. ELECTROMAGNETIC TW 10 17 P 07. DIGITAL LOGIC DESIGN 20 24 P 100 40 PR 50 17. DATA STRUCTURES PP 61 P 08. POWER DEVICES AND MACHINES PP 100 40 P C 18. DATA STRUCTURES PR 50 20 40 P 09. NETWORK AND POWER LAB. TW 50 20 39 P C 100 40 51 P 19. COMMUNICATION THEORY 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 35 P C 20. COMMUNICATION THEORY 50 20 24 P OR 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 36 P GRAND TOTAL = 746/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS:

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OTHER LINES: HEAD OF PASSING	, MAX	. MARK	S, M	IN. F	PASS MARKS	S, MAR	KS OBTAINED, P/F:PASS/FAIL, C:P	REVI	OUS CAI	RRY C	VER	
S8053122 SIDDHANT KHURANA				SA	ROJ		, 71101016в , ,	Ρ.	ICT	,	S8053	12
01. SIGNAL AND SYSTEMS	PP	100	40	43	PC	11.	ENGINEERING MATHEMATICS III	PP	100	40	44	Р
02. SIGNAL AND SYSTEMS	OR	50	20	36	PC	12.	ENGINEERING MATHEMATICS III	TW	25	10	17	Р
03. SOLID STATES DEVICES AND CIRCU	ITSPP	100	40	40	PC	13.	INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	15	
04. SOLID STATES DEVICES AND CIRCU	ITSPR	50	20	34	PC	14.	INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	20	Р
05. NETWORK ANALYSIS	PP	100	40	40	PC	15.	ELECTROMAGNETIC	PP	100	40	22	F
06. DIGITAL LOGIC DESIGN	PP	100	40	40	PC	16.	ELECTROMAGNETIC	TW	25	10	14	
07. DIGITAL LOGIC DESIGN	PR	50	20	25	Р	17.	DATA STRUCTURES	PP	100	40	53	Р
08. POWER DEVICES AND MACHINES	PP	100	40	40	Р	18.	DATA STRUCTURES	PR	50	20	46	Ρ
09. NETWORK AND POWER LAB.	TW	50	20	38	PC	19.	COMMUNICATION THEORY	PP	100	40	40	Ρ
10. ELECTRONIC INSTRUMENTS AND TOO	LS TW	50	20	35	P C	20.	COMMUNICATION THEORY	OR	50	20	AA	F
						21.	CIRCUIT SIMULATION AND TOOLS	TW	50	20	33	Р
RAND TOTAL = 675/1500, RESULT: FAI RDN. 1 MARKS :	LS A.T	.K.T.										
S8053123 SONAWANE ANUJA GIRIDHA					 JNITA		, 71101019G , , ,		 ICT		 S8053	
01. SIGNAL AND SYSTEMS	PP	100	40	46	РС	11.	ENGINEERING MATHEMATICS III	PP	100	40	42	Р
02. SIGNAL AND SYSTEMS	OR	50	20	40	РС	12.	ENGINEERING MATHEMATICS III	TW	25	10	22	F
03. SOLID STATES DEVICES AND CIRCU	ITSPP	100	40	43	Р	13.	INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	44	F
04. SOLID STATES DEVICES AND CIRCU	ITSPR	50	20	22	РС	14.	INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	22	F
05. NETWORK ANALYSIS	PP	100	40	40	РС	15.	ELECTROMAGNETIC	PP	100	40	40	F
06. DIGITAL LOGIC DESIGN	PP	100	40	51	PC	16.	ELECTROMAGNETIC	TW	25	10	19	P
07. DIGITAL LOGIC DESIGN	PR	50	20	30	PC	17.	DATA STRUCTURES	PP	100	40	55	P
08. POWER DEVICES AND MACHINES	PP	100	40	45	PC	18.	DATA STRUCTURES	PR	50	20	42	P
09. NETWORK AND POWER LAB.	TW	50	20	35	P C	19.	COMMUNICATION THEORY	PP	100	40	51	F
10. ELECTRONIC INSTRUMENTS AND TOO	LS TW	50	20	37	PC	20.	COMMUNICATION THEORY	OR	50	20	37	F
						21.	CIRCUIT SIMULATION AND TOOLS	TW	50	20	38	F
RAND TOTAL = $801/1500$, RESULT: SEC	OND CLA	ASS										
RDN. 1 MARKS :												
S8053124 SONOO KUMAR					URADEVI		, 71101020L , , ,					
01. SIGNAL AND SYSTEMS		100	40		PC		ENGINEERING MATHEMATICS III			40	70	
02. SIGNAL AND SYSTEMS	OR	50	20	30	PC	12.	ENGINEERING MATHEMATICS III	TW	25	10	15	F
03. SOLID STATES DEVICES AND CIRCU	ITSPP	100	40	53	PC	13.	INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	53	F
04. SOLID STATES DEVICES AND CIRCU	ITSPR	50	20	28	P C	14.	INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	20	F
05. NETWORK ANALYSIS	PP	100	40	63	PC	15.	ELECTROMAGNETIC	PP	100	40	63	F
06. DIGITAL LOGIC DESIGN	PP	100	40	69	PC	16.	ELECTROMAGNETIC	TW	25	10	14	F
07. DIGITAL LOGIC DESIGN	PR	50	20	32	PC	17.	DATA STRUCTURES	PP	100	40	59	F
08. POWER DEVICES AND MACHINES	PP	100	40	52	PC	18.	DATA STRUCTURES	PR	50	20	34	F
09. NETWORK AND POWER LAB.	TW	50	20	34	PC	19.	COMMUNICATION THEORY	PP	100	40	50	F
10. ELECTRONIC INSTRUMENTS AND TOO	LS TW	50	20	30	PC	20.	COMMUNICATION THEORY	OR	50	20	35	F
						21.	CIRCUIT SIMULATION AND TOOLS	TW	50	20	30	ŗ

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71101021J , , PICT , S8053125 SEETABAI S8053125 SONTAKKE BASVESHWAR RAOSAHEB PP 100 40 47 P C PP 100 40 79 P 01. SIGNAL AND SYSTEMS 11. ENGINEERING MATHEMATICS III 50 20 33 P C 25 10 19 P 02. SIGNAL AND SYSTEMS 12. ENGINEERING MATHEMATICS III TW 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 57 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 56 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 30 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 20 P 05. NETWORK ANALYSIS PP 100 40 50 P C 15. ELECTROMAGNETIC PP 100 40 51 P PP 100 40 44 P C 25 10 15 P 06. DIGITAL LOGIC DESIGN 16. ELECTROMAGNETIC TW 100 07. DIGITAL LOGIC DESIGN 50 20 30 P C 40 50 P PR 17. DATA STRUCTURES PP 08. POWER DEVICES AND MACHINES PP 100 40 P C 50 20 18. DATA STRUCTURES PR 37 P 09. NETWORK AND POWER LAB. TW 50 20 31 P C 100 40 42 P 19. COMMUNICATION THEORY PP 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 36 P C 50 20 26 P 20. COMMUNICATION THEORY OR 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 31 P GRAND TOTAL = 824+05/1500, RESULT: HIGHER SECOND CLASS [o.163] ORDN. 1 MARKS: , 71101023E , , PICT , S8053126 S8053126 SUMIT SINGH USHA 63 P C 11. ENGINEERING MATHEMATICS III PP 100 40 70 P 01. SIGNAL AND SYSTEMS PP 100 40 42 P C 02. SIGNAL AND SYSTEMS OR 50 20 12. ENGINEERING MATHEMATICS III TW 25 10 21 P 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 74 P C 76 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 42 P C 50 20 32 P 14. INTEGRATED CIRCUITS APPLICATIONS PR PP 100 40 75 P C PP 100 05. NETWORK ANALYSIS 15. ELECTROMAGNETIC 40 70 P PP 100 40 73 P C 16. ELECTROMAGNETIC 25 19 P 06. DIGITAL LOGIC DESIGN TW 10 50 20 24 P C 100 71 P 07. DIGITAL LOGIC DESIGN PR 17. DATA STRUCTURES PP 40 08. POWER DEVICES AND MACHINES PP 100 40 76 P C 18. DATA STRUCTURES PR 50 20 42 P 09. NETWORK AND POWER LAB. TW 50 20 42 P C 19. COMMUNICATION THEORY PP 100 40 69 P 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 39 P C 50 20 39 P 20. COMMUNICATION THEORY OR 50 20 42 P 21. CIRCUIT SIMULATION AND TOOLS TW GRAND TOTAL = 1101/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS: , 71101025M , , PICT , S8053127 S8053127 TANK HIMANSHU SHARAD GEETA 01. SIGNAL AND SYSTEMS PP 100 40 63 P C 11. ENGINEERING MATHEMATICS III PP 100 40 42 P 02. SIGNAL AND SYSTEMS 12. ENGINEERING MATHEMATICS III TW 25 10 16 P or 50 20 40 P C 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 61 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 45 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 40 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 10 F 05. NETWORK ANALYSIS PP 100 40 47 P C PP 100 40 52 P 15. ELECTROMAGNETIC 25 06. DIGITAL LOGIC DESIGN PP 100 40 70 P C 16. ELECTROMAGNETIC TW 10 14 P 07. DIGITAL LOGIC DESIGN 20 25 P C 100 40 57 P PR 50 17. DATA STRUCTURES PP 08. POWER DEVICES AND MACHINES PP 100 40 P C 18. DATA STRUCTURES PR 50 20 44 P 09. NETWORK AND POWER LAB. 50 20 33 P C 100 40 44 P TW 19. COMMUNICATION THEORY 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 35 P C 50 20 35 P 20. COMMUNICATION THEORY OR 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 30 P GRAND TOTAL = 843/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71236329н , , , рІСТ , S8053128 S8053128 TANNA VATSAL AJAY VAISHALI PP 100 40 40 P C 11. ENGINEERING MATHEMATICS III PP 100 40 14 F 01. SIGNAL AND SYSTEMS 02. SIGNAL AND SYSTEMS OR 50 20 30 P C 12. ENGINEERING MATHEMATICS III TW 25 10 16 P 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 46 P 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 40 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 39 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 27 P 05. NETWORK ANALYSIS PP 100 PP 100 40 43 P C 15. ELECTROMAGNETIC 40 19 F PP 100 40 53 P C 25 10 13 P 06. DIGITAL LOGIC DESIGN 16. ELECTROMAGNETIC TW PP 100 07. DIGITAL LOGIC DESIGN 50 20 23 P C 40 54 P PR 17. DATA STRUCTURES 08. POWER DEVICES AND MACHINES PP 100 40 44 P C 50 20 33 P 18. DATA STRUCTURES PR 09. NETWORK AND POWER LAB. TW 50 20 37 P C 100 40 48 P 19. COMMUNICATION THEORY PP 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 36 P C 50 20 40 P 20. COMMUNICATION THEORY OR 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 32 P GRAND TOTAL = 727/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS: , 71101026к , , , ріст , s8053129 S8053129 TANYA SINGHAL SUSHMA PP 100 40 56 P C 11. ENGINEERING MATHEMATICS III PP 100 40 81 P 01. SIGNAL AND SYSTEMS 02. SIGNAL AND SYSTEMS OR 50 20 35 P C 12. ENGINEERING MATHEMATICS III TW 25 10 19 P 48 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 61 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 20 P C 50 20 35 P 14. INTEGRATED CIRCUITS APPLICATIONS PR 05. NETWORK ANALYSIS PP 100 40 62 P C PP 100 15. ELECTROMAGNETIC 40 58 P PP 100 40 63 P C 16. ELECTROMAGNETIC 25 19 P 06. DIGITAL LOGIC DESIGN TW 10 50 20 30 P C 100 40 07. DIGITAL LOGIC DESIGN PR 17. DATA STRUCTURES PP 62 P 08. POWER DEVICES AND MACHINES PP 100 40 43 P C 18. DATA STRUCTURES PR 50 20 42 P 09. NETWORK AND POWER LAB. TW 50 20 32 P C 19. COMMUNICATION THEORY PP 100 40 61 P 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 36 P C 50 20 28 P 20. COMMUNICATION THEORY OR 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 35 P GRAND TOTAL = 926/1500, RESULT: FIRST CLASS ORDN. 1 MARKS: , 71236330M , , PICT , s8053130 S8053130 TAWATE PRIYA GIRISH SNEHAPRABHA 01. SIGNAL AND SYSTEMS PP 100 40 46 P C 11. ENGINEERING MATHEMATICS III PP 100 40 81 P 02. SIGNAL AND SYSTEMS OR 50 20 37 P C 12. ENGINEERING MATHEMATICS III TW 25 10 23 P 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 60 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 75 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 40 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 40 P 05. NETWORK ANALYSIS PP 100 40 64 P C PP 100 40 65 P 15. ELECTROMAGNETIC 25 06. DIGITAL LOGIC DESIGN PP 100 40 70 P C 16. ELECTROMAGNETIC TW 10 20 P 07. DIGITAL LOGIC DESIGN 20 34 P C 100 40 79 P PR 50 17. DATA STRUCTURES PP 08. POWER DEVICES AND MACHINES PP 100 40 63 P C 18. DATA STRUCTURES PR 50 20 48 P 09. NETWORK AND POWER LAB. TW 50 20 43 P C 40 75 P 100 19. COMMUNICATION THEORY 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 43 P C 20. COMMUNICATION THEORY 50 20 34 P OR 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 44 P GRAND TOTAL = 1084/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71101030H , , PICT , S8053131 ASHA S8053131 TERKAR PRASAD VITTHALRAO 11. ENGINEERING MATHEMATICS III PP 100 40 40 P 01. SIGNAL AND SYSTEMS PP 100 40 49 P 02. SIGNAL AND SYSTEMS OR 50 20 20 P C 25 10 12 P 12. ENGINEERING MATHEMATICS III TW 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 22 F 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 43 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 20 P 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 06 F 05. NETWORK ANALYSIS PP 100 PP 100 40 19 F 15. ELECTROMAGNETIC 40 16 F PP 100 40 43 P C 25 10 12 P 06. DIGITAL LOGIC DESIGN 16. ELECTROMAGNETIC TW 50 20 25 P C 100 07. DIGITAL LOGIC DESIGN 40 40 P PR 17. DATA STRUCTURES PP 08. POWER DEVICES AND MACHINES PP 100 40 25 F 50 20 18. DATA STRUCTURES PR 42 P 09. NETWORK AND POWER LAB. TW 50 20 33 P C 100 40 40 P 19. COMMUNICATION THEORY PP 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 28 P C 50 20 33 P 20. COMMUNICATION THEORY OR 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 27 P GRAND TOTAL = 595/1500, RESULT: FAILS ORDN. 1 MARKS : S8053132 THAKARE APURV VIJAY MEENA , s8053132 PP 100 40 31 F 11. ENGINEERING MATHEMATICS III PP 100 40 20 F 01. SIGNAL AND SYSTEMS 02. SIGNAL AND SYSTEMS OR 50 20 26 P 12. ENGINEERING MATHEMATICS III TW 25 10 11 P 28 F 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 40 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 21 P C 50 20 14. INTEGRATED CIRCUITS APPLICATIONS PR 05 F 05. NETWORK ANALYSIS PP 100 40 21 F PP 100 15. ELECTROMAGNETIC 40 12 F PP 100 40 40 P C 16. ELECTROMAGNETIC 25 06. DIGITAL LOGIC DESIGN TW 10 11 P 50 20 20 P 100 40 07. DIGITAL LOGIC DESIGN PR 17. DATA STRUCTURES PP 46 P 08. POWER DEVICES AND MACHINES PP 100 40 P 18. DATA STRUCTURES PR 50 20 33 P 09. NETWORK AND POWER LAB. TW 50 20 P C 19. COMMUNICATION THEORY PP 100 40 28 F 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 22 P C 50 20 26 P 20. COMMUNICATION THEORY OR 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 23 P GRAND TOTAL = 524/1500, RESULT: FAILS ORDN. 1 MARKS: , 71101036G , , PICT , s8053133 S8053133 TILAK AMEY CHANDRASHEKHAR APARNA 01. SIGNAL AND SYSTEMS PP 100 40 55 P C 11. ENGINEERING MATHEMATICS III PP 100 40 79 P 02. SIGNAL AND SYSTEMS 12. ENGINEERING MATHEMATICS III TW 25 10 24 P OR 50 20 38 P C 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 57 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 65 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 39 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 38 P 05. NETWORK ANALYSIS PP 100 40 60 P C PP 100 40 61 P 15. ELECTROMAGNETIC 25 06. DIGITAL LOGIC DESIGN PP 100 40 74 P C 16. ELECTROMAGNETIC TW 10 21 P 07. DIGITAL LOGIC DESIGN 20 40 P C 100 40 60 P PR 50 17. DATA STRUCTURES PP 08. POWER DEVICES AND MACHINES PP 100 40 47 P C 18. DATA STRUCTURES PR 50 20 48 P 09. NETWORK AND POWER LAB. TW 50 20 44 P C 100 40 63 P 19. COMMUNICATION THEORY 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 44 P C 20. COMMUNICATION THEORY 50 20 40 P OR 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 47 P GRAND TOTAL = 1044/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS:

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DATE . 14 AUG. 2012	CENT	KE . P	UNE .	TN21T	IUIE	OF COMPOS	ER TECHNOLOGY, PUNE.	PAC	JE NU.	43	(41	0)
NOTE: FIRST LINE : SEAT NO., NAME				-	-				-			
OTHER LINES: HEAD OF PASSING,	MAX.	MARKS	, M	IN. P	ASS M	ARKS, MAI	RKS OBTAINED, P/F:PASS/FAIL, C:F	PREVIO	DUS CA	RRY C	VER	
							71101027-					
S8053134 TIWARI BHAVYA YOGESH		100	40		:HA	11	, 71101037E , ,			-	S80531	
01. SIGNAL AND SYSTEMS	PP		40		P C		. ENGINEERING MATHEMATICS III		100	40	75	
02. SIGNAL AND SYSTEMS	OR	50 100	20	_	P C			TW	25	10	14	
03. SOLID STATES DEVICES AND CIRCUI		100	40		P C		. INTEGRATED CIRCUITS APPLICATIONS		100	40	65 26	
04. SOLID STATES DEVICES AND CIRCUI		50 100	20	35			. INTEGRATED CIRCUITS APPLICATIONS		50 100	20	36 72	
05. NETWORK ANALYSIS	PP	100	40		P C		. ELECTROMAGNETIC	PP Tw	100	40	72 1 E	
06. DIGITAL LOGIC DESIGN	PP	100 50	40	62			. ELECTROMAGNETIC	TW	25 100	10 40	15 52	
07. DIGITAL LOGIC DESIGN 08. POWER DEVICES AND MACHINES		30 100	20	39 _{E1}	P C P C		. DATA STRUCTURES . DATA STRUCTURES	PP	50		52 45	
	PP Tw		40	_	_			PR		20		
09. NETWORK AND POWER LAB.		50	20		P C		. COMMUNICATION THEORY	PP	100	40	53	
10. ELECTRONIC INSTRUMENTS AND TOOL	.S IW	50	20	36	РС		. COMMUNICATION THEORY	OR Tu	50	20	28	
CDAND TOTAL 040/1500 DECULT, ETDS	T CLAS	c				21	. CIRCUIT SIMULATION AND TOOLS	TW	50	20	37	Р
GRAND TOTAL = 940/1500, RESULT: FIRS	I CLAS	5										
ORDN. 1 MARKS :												
					 DMA		, 71101038C , , , ,				s80531	
01. SIGNAL AND SYSTEMS	DD	100	40		P C	11	. ENGINEERING MATHEMATICS III		100	, 40	40	
02. SIGNAL AND SYSTEMS	OR	50	20		PC		. ENGINEERING MATHEMATICS III	TW	25	10	14	
03. SOLID STATES DEVICES AND CIRCUI		100	40		PC		. INTEGRATED CIRCUITS APPLICATIONS		100	40	40	
04. SOLID STATES DEVICES AND CIRCUI		50	20		P C		. INTEGRATED CIRCUITS APPLICATIONS		50	20	06	
05. NETWORK ANALYSIS	PP	100	40		P C		ELECTROMAGNETIC	PP	100	40	21	
06. DIGITAL LOGIC DESIGN	PP	100	40	60			. ELECTROMAGNETIC	TW	25	10	13	
07. DIGITAL LOGIC DESIGN		50	20		P C		. DATA STRUCTURES	PP	100	40	56	-
08. POWER DEVICES AND MACHINES	PP	100	40	44	P C		. DATA STRUCTURES	PR	50	20	32	
09. NETWORK AND POWER LAB.	TW	50	20	25	P C		. COMMUNICATION THEORY	PP	100	40	42	
10. ELECTRONIC INSTRUMENTS AND TOOL					P C		. COMMUNICATION THEORY		50	20	35	
							. CIRCUIT SIMULATION AND TOOLS		50	20	25	
GRAND TOTAL = 699/1500, RESULT: FAIL	S A.T.	к.т.										
ORDN. 1 MARKS :												
S8053136 VAIBHAV RAMDAS BORUDE					NDAKI		, 71101043к , ,				s80531	
01. SIGNAL AND SYSTEMS	PP	100	40	71	РС	11	. ENGINEERING MATHEMATICS III		100	40	66	
02. SIGNAL AND SYSTEMS	OR	50	20		РС		. ENGINEERING MATHEMATICS III		25	10	16	
03. SOLID STATES DEVICES AND CIRCUI	TSPP	100	40	58	РС	13	. INTEGRATED CIRCUITS APPLICATIONS	S PP	100	40	63	Р
04. SOLID STATES DEVICES AND CIRCUI		50	20		РС		. INTEGRATED CIRCUITS APPLICATIONS		50	20	38	
05. NETWORK ANALYSIS	PP	100	40		РС		. ELECTROMAGNETIC	PP	100	40	77	Р
06. DIGITAL LOGIC DESIGN	PP	100	40	62	РС	16	. ELECTROMAGNETIC	TW	25	10	15	Р
07. DIGITAL LOGIC DESIGN		50	20		РС		. DATA STRUCTURES	PP	100	40	67	Р
	PP	100	40		РС		. DATA STRUCTURES	PR	50	20	46	
09. NETWORK AND POWER LAB.		50	20		РС		. COMMUNICATION THEORY	PP	100	40	61	
10. ELECTRONIC INSTRUMENTS AND TOOL	S TW	50	20		РС		. COMMUNICATION THEORY	OR	50	20	35	
							. CIRCUIT SIMULATION AND TOOLS		50	20	35	
GRAND TOTAL = 1035/1500, RESULT: FIRS	T CLAS	S WITH	DIS	TINCT	ION							
ORDN. 1 MARKS :												

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER SANGEETA S8053137 VAIDYA SANDIP MILIND PP 100 40 51 P C PP 100 40 56 P 01. SIGNAL AND SYSTEMS 11. ENGINEERING MATHEMATICS III 02. SIGNAL AND SYSTEMS OR 50 20 41 P C 12. ENGINEERING MATHEMATICS III TW 25 10 23 P 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 52 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 62 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 39 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 40 P 05. NETWORK ANALYSIS PP 100 PP 100 40 55 P C 15. ELECTROMAGNETIC 40 32# P PP 100 40 66 P C 25 10 06. DIGITAL LOGIC DESIGN 16. ELECTROMAGNETIC TW 21 P PP 100 07. DIGITAL LOGIC DESIGN 50 20 28 P 40 59 P PR 17. DATA STRUCTURES 08. POWER DEVICES AND MACHINES PP 100 40 52 P C 50 20 18. DATA STRUCTURES PR 47 P 09. NETWORK AND POWER LAB. TW 50 20 34 P C 100 40 52 P 19. COMMUNICATION THEORY PP 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 39 P C 50 20 35 P 20. COMMUNICATION THEORY OR 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 43 P GRAND TOTAL = 927/1500, RESULT: FIRST CLASS # [0.4] ORDN. 1 MARKS: , 71101045F , , PICT , S8053138 S8053138 VARADE RUSHIKESH DILIP CHHAYA PP 100 40 42 P C 11. ENGINEERING MATHEMATICS III PP 100 40 27 F 01. SIGNAL AND SYSTEMS OR 50 20 30 P 02. SIGNAL AND SYSTEMS 12. ENGINEERING MATHEMATICS III TW 25 10 12 P 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 49 P.C 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 51 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 36 P C 50 20 33 P 14. INTEGRATED CIRCUITS APPLICATIONS PR 05. NETWORK ANALYSIS PP 100 40 30 F PP 100 15. ELECTROMAGNETIC 40 22 F PP 100 40 53 P C 16. ELECTROMAGNETIC 25 06. DIGITAL LOGIC DESIGN TW 10 11 P 50 20 32 P C 100 07. DIGITAL LOGIC DESIGN PR 17. DATA STRUCTURES PP 40 41 P 08. POWER DEVICES AND MACHINES PP 100 40 46 P C 18. DATA STRUCTURES PR 50 20 32 P 09. NETWORK AND POWER LAB. TW 50 20 28 P C 19. COMMUNICATION THEORY PP 100 40 40 P 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 27 P C 50 20 28 P 20. COMMUNICATION THEORY OR 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 25 P GRAND TOTAL = 695/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS: , s8053139 S8053139 VASAGADEKAR SWATI PRADEEP SUPRIYA 01. SIGNAL AND SYSTEMS PP 100 40 81 P C 11. ENGINEERING MATHEMATICS III PP 100 40 77 P 02. SIGNAL AND SYSTEMS 12. ENGINEERING MATHEMATICS III TW 25 10 22 P OR 50 20 35 P C 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 61 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 73 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 40 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 39 P 05. NETWORK ANALYSIS PP 100 40 64 P C PP 100 40 62 P 15. ELECTROMAGNETIC 25 06. DIGITAL LOGIC DESIGN PP 100 40 65 P C 16. ELECTROMAGNETIC TW 10 18 P 07. DIGITAL LOGIC DESIGN 20 38 P C 100 40 77 P PR 50 17. DATA STRUCTURES PP 08. POWER DEVICES AND MACHINES PP 100 40 49 P C 18. DATA STRUCTURES PR 50 20 44 P 09. NETWORK AND POWER LAB. TW 50 20 43 P C 100 40 56 P 19. COMMUNICATION THEORY 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 43 P C 50 20 28 P 20. COMMUNICATION THEORY OR 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 42 P GRAND TOTAL = 1057/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS:

DATE: 14 AUG. 2012 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 47 (418)

NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71101048L , , PICT , S8053140 VARSHA S8053140 VIDHATE TRUPTI SOPAN 11. ENGINEERING MATHEMATICS III PP 100 40 48 P 01. SIGNAL AND SYSTEMS PP 100 40 40 P C 02. SIGNAL AND SYSTEMS OR 50 20 37 P C 25 10 22 P 12. ENGINEERING MATHEMATICS III TW 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 42 P 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 50 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 38 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 40 P 05. NETWORK ANALYSIS PP 100 PP 100 40 48 P C 15. ELECTROMAGNETIC 40 43 P PP 100 40 67 P C 25 10 19 P 06. DIGITAL LOGIC DESIGN 16. ELECTROMAGNETIC TW PP 100 07. DIGITAL LOGIC DESIGN 50 20 35 P C 40 48 P PR 17. DATA STRUCTURES 08. POWER DEVICES AND MACHINES PP 100 40 P C 50 20 18. DATA STRUCTURES PR 35 P 09. NETWORK AND POWER LAB. TW 50 20 29 P C 100 40 56 P 19. COMMUNICATION THEORY PP 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 37 P C 50 20 35 P 20. COMMUNICATION THEORY OR 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 39 P GRAND TOTAL = 848/1500, RESULT: HIGHER SECOND CLASS ORDN. 1 MARKS : , 71101055C , , PICT S8053141 WANI ANIKET ARUN BHARTI , s8053141 PP 100 40 80 P C 11. ENGINEERING MATHEMATICS III PP 100 40 74 P 01. SIGNAL AND SYSTEMS 02. SIGNAL AND SYSTEMS OR 50 20 37 P C 12. ENGINEERING MATHEMATICS III TW 25 10 16 P 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 56 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 69 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 37 P C 50 20 14. INTEGRATED CIRCUITS APPLICATIONS PR 38 P 05. NETWORK ANALYSIS PP 100 PP 100 40 61 P C 15. ELECTROMAGNETIC 40 80 P PP 100 40 76 P C 16. ELECTROMAGNETIC 25 15 P 06. DIGITAL LOGIC DESIGN TW 10 50 20 36 P C 100 40 07. DIGITAL LOGIC DESIGN PR 17. DATA STRUCTURES PP 62 P 08. POWER DEVICES AND MACHINES PP 100 40 62 P C 18. DATA STRUCTURES PR 50 20 34 P 09. NETWORK AND POWER LAB. TW 50 20 39 P C 19. COMMUNICATION THEORY PP 100 40 49 P 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 37 P C 50 20 37 P 20. COMMUNICATION THEORY OR 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 36 P GRAND TOTAL = 1031/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS: , 71101057к , , РІСТ UMA , s8053142 S8053142 WANKHEDE ROHIT NITIN 01. SIGNAL AND SYSTEMS PP 100 40 89 P C 11. ENGINEERING MATHEMATICS III PP 100 40 63 P OR 50 20 38 P C 02. SIGNAL AND SYSTEMS 12. ENGINEERING MATHEMATICS III TW 25 10 22 P 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 69 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 75 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 30 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 35 P 05. NETWORK ANALYSIS PP 100 40 58 P C PP 100 40 78 P 15. ELECTROMAGNETIC 25 06. DIGITAL LOGIC DESIGN PP 100 40 74 P C 16. ELECTROMAGNETIC TW 10 18 P 07. DIGITAL LOGIC DESIGN 20 37 P C 100 40 66 P PR 50 17. DATA STRUCTURES PP 08. POWER DEVICES AND MACHINES PP 100 40 58 P C 18. DATA STRUCTURES PR 50 20 48 P 09. NETWORK AND POWER LAB. TW 50 20 35 P C 100 40 53 P 19. COMMUNICATION THEORY 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 38 P C 20. COMMUNICATION THEORY 50 20 40 P OR 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 38 P GRAND TOTAL = 1062/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS:

UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(ELECTRONICS & TELECOM.)

DATE: 14 AUG. 2012 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 48 (419) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER SANDHYA , s8053143 S8053143 YAWALKAR SIDDHESH SHRIPAD 01. SIGNAL AND SYSTEMS PP 100 40 76 P C 11. ENGINEERING MATHEMATICS III PP 100 40 80 P or 50 20 33 P C 25 10 22 P 02. SIGNAL AND SYSTEMS 12. ENGINEERING MATHEMATICS III TW 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 68 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 72 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 38 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 36 P PP 100 05. NETWORK ANALYSIS 40 60 P C 15. ELECTROMAGNETIC PP 100 40 PP 100 40 71 P C 25 10 19 P 06. DIGITAL LOGIC DESIGN 16. ELECTROMAGNETIC TW 100 07. DIGITAL LOGIC DESIGN 50 20 35 P C PP 40 62 P PR 17. DATA STRUCTURES 08. POWER DEVICES AND MACHINES PP 100 40 P C 50 20 47 P 18. DATA STRUCTURES PR 09. NETWORK AND POWER LAB. TW 50 20 40 P C 100 40 46 P 19. COMMUNICATION THEORY PP 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 39 P C 50 20 40 P 20. COMMUNICATION THEORY OR 50 20 41 P 21. CIRCUIT SIMULATION AND TOOLS TW GRAND TOTAL = 1033/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS: , 71236332н , , , ріст S8053144 YEILE PRAJKT PRAMOD JYOTI , s8053144 PP 100 40 41 P 11. ENGINEERING MATHEMATICS III PP 100 40 18 F 01. SIGNAL AND SYSTEMS or 50 20 23 P C 02. SIGNAL AND SYSTEMS 12. ENGINEERING MATHEMATICS III TW 25 10 15 P 29 F 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 40 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 26 P C 50 20 20 P 14. INTEGRATED CIRCUITS APPLICATIONS PR PP 100 40 27 F PP 100 05. NETWORK ANALYSIS 15. ELECTROMAGNETIC 40 15 F PP 100 40 31 F 16. ELECTROMAGNETIC 25 10 12 P 06. DIGITAL LOGIC DESIGN TW 50 20 30 P C 100 40 40 P 07. DIGITAL LOGIC DESIGN PR 17. DATA STRUCTURES PP 08. POWER DEVICES AND MACHINES PP 100 40 41 P 18. DATA STRUCTURES PR 50 20 35 P 09. NETWORK AND POWER LAB. TW 50 20 35 P C 19. COMMUNICATION THEORY PP 100 40 17 F 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 29 P C 50 20 28 P 20. COMMUNICATION THEORY OR 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 31 P GRAND TOTAL = 583/1500, RESULT: FAILS ORDN. 1 MARKS: , 70801362н , , , РІСТ , S8053146 S8053146 BHANDALKAR VISHAL MAHADEO LAXMI 01. SIGNAL AND SYSTEMS PP 100 40 AA F 02. SIGNAL AND SYSTEMS or 50 20 AA F 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 AA F 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 AA F PP 100 40 AA F 05. NETWORK ANALYSIS PP 100 06. DIGITAL LOGIC DESIGN 40 AA F 07. DIGITAL LOGIC DESIGN 20 AA F PR 50 08. POWER DEVICES AND MACHINES PP 100 40 AA F 50 20 24 P C 09. NETWORK AND POWER LAB. TW 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 P C FIRST TERM TOTAL = 44/750. ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 70925442D , , PICT , S8053148 KAMAL S8053148 ISHTE PRAKASH BHAGVAN PP 100 40 40 P C 01. SIGNAL AND SYSTEMS PP 100 40 40 P C 11. ENGINEERING MATHEMATICS III or 50 20 24 P C 25 10 02. SIGNAL AND SYSTEMS 12. ENGINEERING MATHEMATICS III TW 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 40 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 P C 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 20 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 25 P C 05. NETWORK ANALYSIS PP 100 PP 100 40 46 P C 15. ELECTROMAGNETIC 40 57 P.C. PP 100 40 48 P 25 10 10 P C 06. DIGITAL LOGIC DESIGN 16. ELECTROMAGNETIC TW 100 07. DIGITAL LOGIC DESIGN 50 20 24 P C PP 40 46 P C PR 17. DATA STRUCTURES 08. POWER DEVICES AND MACHINES PP 100 40 30# P 50 20 43 P C 18. DATA STRUCTURES PR 09. NETWORK AND POWER LAB. TW 50 20 32 P C 100 40 40 P C 19. COMMUNICATION THEORY PP 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 35 P C 50 20 22 P C 20. COMMUNICATION THEORY OR 50 20 26 P C 21. CIRCUIT SIMULATION AND TOOLS TW GRAND TOTAL = 706/1500, RESULT: PASS CLASS # [0.4] ORDN. 1 MARKS: , 71045527F , , PICT , S8053151 S8053151 MORE VIKAS CHANDRABHAN SUMAN PP 100 40 44 P C 11. ENGINEERING MATHEMATICS III PP 100 40 40 PC 01. SIGNAL AND SYSTEMS OR 50 20 22 P.C. 02. SIGNAL AND SYSTEMS 12. ENGINEERING MATHEMATICS III TW 25 10 15 P C 40 P C 40 42 P C 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 21 P C 50 20 20 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 05. NETWORK ANALYSIS PP 100 PP 100 40 27 F 40 40 P C 15. ELECTROMAGNETIC PP 100 40 40 P C 16. ELECTROMAGNETIC 25 06. DIGITAL LOGIC DESIGN TW 10 17 PC 50 20 25 P C 100 40 07. DIGITAL LOGIC DESIGN PR 17. DATA STRUCTURES PP 47 P 08. POWER DEVICES AND MACHINES PP 100 40 42 P C 18. DATA STRUCTURES PR 50 20 32 P C 09. NETWORK AND POWER LAB. TW 50 20 39 P C 40 40 P C 19. COMMUNICATION THEORY PP 100 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 38 P C 50 20 29 P C 20. COMMUNICATION THEORY OR 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 33 P C GRAND TOTAL = 693/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS: , 71045546B , , PICT SUGANDHA S8053153 NERLEKAR SHWETA PRAKASH , S8053153 01. SIGNAL AND SYSTEMS PP 100 40 46 P C 11. ENGINEERING MATHEMATICS III PP 100 40 66 P C 12. ENGINEERING MATHEMATICS III TW 25 10 21 P C 02. SIGNAL AND SYSTEMS OR 50 20 22 P C 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 40 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 51 P C 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 25 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 35 P C 05. NETWORK ANALYSIS PP 100 40 40 P C PP 100 40 49 P 15. ELECTROMAGNETIC 25 06. DIGITAL LOGIC DESIGN PP 100 40 65 P C 16. ELECTROMAGNETIC TW 10 19 P C 07. DIGITAL LOGIC DESIGN 20 25 P C 100 40 50 P C PR 50 17. DATA STRUCTURES PP 08. POWER DEVICES AND MACHINES PP 100 40 P C 18. DATA STRUCTURES PR 50 20 36 P C 09. NETWORK AND POWER LAB. 50 20 40 P C PP 100 40 56 P C TW 19. COMMUNICATION THEORY 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 41 P C 20. COMMUNICATION THEORY 50 20 27 P C OR 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 37 P C GRAND TOTAL = 831/1500, RESULT: HIGHER SECOND CLASS ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER SANGITA , 71045547L , , PICT , S8053155 S8053155 NIKAM JEEVAN SHANKAR PP 100 40 72 P PP 100 40 40 P 01. SIGNAL AND SYSTEMS 11. ENGINEERING MATHEMATICS III 02. SIGNAL AND SYSTEMS OR 50 20 20 P C 25 10 23 P C 12. ENGINEERING MATHEMATICS III TW 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 40 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 43 P C 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 20 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 25 P C 05. NETWORK ANALYSIS PP 100 PP 100 40 47 P 15. ELECTROMAGNETIC 40 41 P 100 40 40 P C 25 10 06. DIGITAL LOGIC DESIGN PP 16. ELECTROMAGNETIC TW 15 P C 100 07. DIGITAL LOGIC DESIGN 50 20 29 P C 40 40 P C PR 17. DATA STRUCTURES PP 08. POWER DEVICES AND MACHINES PP 100 40 P C 50 18. DATA STRUCTURES PR 20 28 P C 09. NETWORK AND POWER LAB. TW 50 20 37 P C 100 40 40 P C 19. COMMUNICATION THEORY PP 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 39 P C 50 20 23 P C 20. COMMUNICATION THEORY OR 50 20 37 P C 21. CIRCUIT SIMULATION AND TOOLS TW GRAND TOTAL = 739/1500, RESULT: PASS CLASS ORDN. 1 MARKS: , 70925557J , , PICT , S8053156 S8053156 PAWAR SACHIN VINAYAKRAO SAVITA PP 100 40 AA F 11. ENGINEERING MATHEMATICS III PP 100 40 AA F 01. SIGNAL AND SYSTEMS 02. SIGNAL AND SYSTEMS OR 50 20 22 P 12. ENGINEERING MATHEMATICS III TW 25 10 15 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 AA F 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 AA F 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 20 P 50 20 40 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR PP 100 PP 100 05. NETWORK ANALYSIS 40 AA F 15. ELECTROMAGNETIC 40 AA F PP 100 16. ELECTROMAGNETIC 25 06. DIGITAL LOGIC DESIGN 40 AA F TW 10 18 P C 50 20 AA F 100 40 07. DIGITAL LOGIC DESIGN PR 17. DATA STRUCTURES PP AA F 08. POWER DEVICES AND MACHINES PP 100 40 AA F 18. DATA STRUCTURES PR 50 20 34 P 09. NETWORK AND POWER LAB. 40 AA F TW 50 20 33 P C 19. COMMUNICATION THEORY PP 100 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 P C 50 20 21 P C 20. COMMUNICATION THEORY OR 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 32 P C GRAND TOTAL = 255/1500, RESULT: FAILS RESULT RESERVED FOR BKLG ORDN. 1 MARKS: , 71070203F , , PICT S8053158 PAWAR VISHAL ALIAS DIGVIJAY VIJAYANTI , S8053158 01. SIGNAL AND SYSTEMS PP 100 40 40 P C 11. ENGINEERING MATHEMATICS III PP 100 40 AA F 02. SIGNAL AND SYSTEMS OR 12. ENGINEERING MATHEMATICS III TW 25 10 14 P C 50 20 20 P C 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 50 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 59 P C 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 20 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 20 P.C 05. NETWORK ANALYSIS PP 100 40 AA F PP 100 40 40 P C 15. ELECTROMAGNETIC 25 06. DIGITAL LOGIC DESIGN PP 100 40 AA F 16. ELECTROMAGNETIC TW 10 12 P C 07. DIGITAL LOGIC DESIGN 20 20 P C 100 40 40 P C PR 50 17. DATA STRUCTURES PP 08. POWER DEVICES AND MACHINES PP 100 40 56 P C 18. DATA STRUCTURES PR 50 20 28 P C 09. NETWORK AND POWER LAB. 50 20 34 P C PP 100 40 53 P C TW 19. COMMUNICATION THEORY 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 30 P C 20. COMMUNICATION THEORY 50 20 24 P C OR 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 37 P C GRAND TOTAL = 597/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS:

DATE: 14 AUG. 2012 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 51 (422)

DATE . 14 AUG. 2012	CENT	KE . P	UNE	TN211	IUIE	OF COMPUTE	R TECHNOLOGY, PUNE.	PAC	JE NO.	ЭТ	(4	22)
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			-			•	KS OBTAINED, P/F:PASS/FAIL, C:P					
S8053159 PITRODA UTSAV RAJENDRA					· · · ILASH						 s8053	
01. SIGNAL AND SYSTEMS		100	40		P C				100	, 40	36033 22	
02. SIGNAL AND SYSTEMS	OR	50	20	_	PC				25	10		РC
03. SOLID STATES DEVICES AND CIRCU	_	100	40		PC		ENGINEERING MATHEMATICS III INTEGRATED CIRCUITS APPLICATIONS		100	40		PC
04. SOLID STATES DEVICES AND CIRCU	_	50	20		PC		INTEGRATED CIRCUITS APPLICATIONS INTEGRATED CIRCUITS APPLICATIONS		50	20		PC
05. NETWORK ANALYSIS	PP	100	40	40	_		ELECTROMAGNETIC	PR PP	100	40		PC
06. DIGITAL LOGIC DESIGN	PP	100	40		P C		ELECTROMAGNETIC	TW	25	10	_	РС
07. DIGITAL LOGIC DESIGN	PR	50	20	_	PC		DATA STRUCTURES	PP	100	40	_	PC
	PR PP	100	40		PC		DATA STRUCTURES	PR	50	20		PC
		50	20		PC	_	COMMUNICATION THEORY	PR PP	100	40		PC
09. NETWORK AND POWER LAB.		50 50	20		PC					20		
10. ELECTRONIC INSTRUMENTS AND TOO	LS IW	30	20	30	PC		COMMUNICATION THEORY	OR Tw	50 50			P C
CDAND TOTAL 726/1500 DECULT. FAT	1.C. A. T.	и т				21.	CIRCUIT SIMULATION AND TOOLS	TW	50	20	42	РС
GRAND TOTAL = 736/1500, RESULT: FAI	LS A.I.	K.I.										
ORDN. 1 MARKS :												
CONTRACT CONTRACT NEW TITLE BUTTORA												
S8053162 SONAWANE NIKHIL BHIMRAG		100	40		NANDA		, 71045632J , , , ,			-	S8053	
01. SIGNAL AND SYSTEMS		100	40		F		ENGINEERING MATHEMATICS III			40	16	
02. SIGNAL AND SYSTEMS	OR	50	20		P C		ENGINEERING MATHEMATICS III	TW	25	10	-	P C
03. SOLID STATES DEVICES AND CIRCU		100	40	13			INTEGRATED CIRCUITS APPLICATIONS		100	40	14	
04. SOLID STATES DEVICES AND CIRCU		50	20	AA			INTEGRATED CIRCUITS APPLICATIONS		50	20	AA	
05. NETWORK ANALYSIS	PP 	100	40	09			ELECTROMAGNETIC	PP —	100	40	07	
06. DIGITAL LOGIC DESIGN	PP 	100	40		P C		ELECTROMAGNETIC	TW	25	10	-	P C
07. DIGITAL LOGIC DESIGN		50	20		P C		DATA STRUCTURES	PP 	100	40	30	
	PP	100	40	40			DATA STRUCTURES	PR	50	20	AA	
09. NETWORK AND POWER LAB.	TW	50	20		РС		COMMUNICATION THEORY	PP	100	40	10	
10. ELECTRONIC INSTRUMENTS AND TOO	LS TW	50	20	30	РС		COMMUNICATION THEORY	OR	50	20	AA	
//						21.	CIRCUIT SIMULATION AND TOOLS	TW	50	20	20	РС
GRAND TOTAL = 350/1500, RESULT: FAI	LS											
ORDN. 1 MARKS :												
S8053163 SONU SINGH					HA		, 71045633G , ,				S8053	
01. SIGNAL AND SYSTEMS		100	40		РС		ENGINEERING MATHEMATICS III	PP	100	40		РС
02. SIGNAL AND SYSTEMS	OR	50	20		РС		ENGINEERING MATHEMATICS III		25	10		РС
03. SOLID STATES DEVICES AND CIRCU	ITSPP	100	40	40	Р	13.	INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	43	РС
04. SOLID STATES DEVICES AND CIRCU	ITSPR	50	20		РС	14.	INTEGRATED CIRCUITS APPLICATIONS	PR	50	20		РС
05. NETWORK ANALYSIS	PP	100	40	40		15.	ELECTROMAGNETIC	PP	100	40	40	
06. DIGITAL LOGIC DESIGN	PP	100	40		РС		ELECTROMAGNETIC	TW	25	10		РС
07. DIGITAL LOGIC DESIGN	PR	50	20		P C	17.	DATA STRUCTURES	PP	100	40	41	РС
08. POWER DEVICES AND MACHINES	PP	100	40	30#		18.	DATA STRUCTURES	PR	50	20	35	P C
09. NETWORK AND POWER LAB.	TW	50	20	35	P C	19.	COMMUNICATION THEORY	PP	100	40	40	РС
10. ELECTRONIC INSTRUMENTS AND TOO	LS TW	50	20	33	P C	20.	COMMUNICATION THEORY	OR	50	20		P C
						21.	CIRCUIT SIMULATION AND TOOLS	TW	50	20	35	P C
GRAND TOTAL = 709/1500, RESULT: PASS	S CLASS	# [0.4]									
ORDN. 1 MARKS :												

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DATE : 14 AUG. 2012	CENTI	RE : P	UNE I	NSTI	TUTE OF CO	MPUTER	R TECHNOLOGY, PUNE.	PAG	SE NO.	52	(4	23)
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S8053164 THOKALE PRIYANKA MADHAV				 MA	 NDAKINI				 :CT		 s8053	 164
01. SIGNAL AND SYSTEMS	PP	100	40	53	P C	11.	ENGINEERING MATHEMATICS III	PP	100	40	40	PC
02. SIGNAL AND SYSTEMS	OR	50	20	32	P C	12.	ENGINEERING MATHEMATICS III	TW	25	10	18	P C
03. SOLID STATES DEVICES AND CIRCUITS	SPP	100	40	AA	F	13.	INTEGRATED CIRCUITS APPLICATION	ONS PP	100	40	AA	F
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	29	P C	14.	INTEGRATED CIRCUITS APPLICATION	ONS PR	50	20	AA	F
05. NETWORK ANALYSIS	PP	100	40	AA	F	15.	ELECTROMAGNETIC	PP	100	40	AA	F
06. DIGITAL LOGIC DESIGN	PP	100	40	43	P C	16.	ELECTROMAGNETIC	TW	25	10	13	РС
07. DIGITAL LOGIC DESIGN	PR	50	20	20	P C	17.	DATA STRUCTURES	PP	100	40	50	РС
08. POWER DEVICES AND MACHINES	PP	100	40	AA	F	18.	DATA STRUCTURES	PR	50	20	AA	F
09. NETWORK AND POWER LAB.	TW	50	20	32	P C	19.	COMMUNICATION THEORY	PP	100	40	AA	F
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	32	P C	20.	COMMUNICATION THEORY	OR	50	20	AA	F
						21.	CIRCUIT SIMULATION AND TOOLS	TW	50	20	41	РС
GRAND TOTAL = 403/1500, RESULT: FAILS												
ORDN. 1 MARKS :												

DATE: 14 AUG. 2012 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 01 (424)

NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , s8054201 S8054201 AAMIR AHMED ABOOBAKAR SADIQUE YASMEEN PP 100 40 45 P C 11. ENGINEERING MATHEMATICS III PP 100 40 67 P 01. DISCRETE STRUCTURES 02. PROGRAMMING & PROBLEM SOLVING PP 100 40 53 P C 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 46 P 100 40 66 P C 100 40 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 13. DATA STRUCTURES PP 63 P 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 62 P C PP 100 40 60 P 14. COMPUTER GRAPHICS 05. HUMANITIES AND SOCIAL SCIENCE PP 100 40 48 P C 15. COMPUTER ORGANIZATION 100 40 55 P 25 10 19 P C 50 20 30 P 06. PROGRAMMING LABORATORY 16. O. O. PROG. & COMP. GRAPH. LAB TW TW 07. PROGRAMMING LABORATORY 50 20 28 P C 50 20 34 P PR 17. O. O. PROG. & COMP. GRAPH. LAB PR 25 10 18 P C 18. MICROPROCESSORS & INTERFACING LABTW 50 20 37 P 08. DIGITAL ELECTRONICS LABORATORY TW 50 20 25 P C 50 20 23 P 09. DIGITAL ELECTRONICS LABORATORY PR 19. MICROPROCESSORS & INTERFACING LABPR 50 20 32 P C 10. SOFT SKILLS 50 20 36 P TW 20. DATA STRUCTURES LABORATORY TW PR 50 20 38 P 21. DATA STRUCTURES LABORATORY GRAND TOTAL = 885/1500, RESULT: HIGHER SECOND CLASS ORDN. 1 MARKS: S8054202 ABHISHEK KUMAR SAURAV SUMAN , s8054202 01. DISCRETE STRUCTURES 11. ENGINEERING MATHEMATICS III PP 100 40 40 P PP 100 40 40 P C 32 F 02. PROGRAMMING & PROBLEM SOLVING PP 100 40 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 16 F 13. DATA STRUCTURES 43 P 100 40 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 40 43 P PP 100 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 41 P 14. COMPUTER GRAPHICS PP 40 30 F 100 40 30 F 05. HUMANITIES AND SOCIAL SCIENCE PP 100 40 41 P 15. COMPUTER ORGANIZATION 25 10 10 P C 50 20 38 P 06. PROGRAMMING LABORATORY 16. O. O. PROG. & COMP. GRAPH. LAB TW TW 50 20 06 F 50 20 14 F 07. PROGRAMMING LABORATORY PR 17. O. O. PROG. & COMP. GRAPH. LAB PR 25 10 50 20 22 P 08. DIGITAL ELECTRONICS LABORATORY TW 10 P C 18. MICROPROCESSORS & INTERFACING LABTW 09. DIGITAL ELECTRONICS LABORATORY PR 50 20 05 F 19. MICROPROCESSORS & INTERFACING LABPR 50 20 AA F 10. SOFT SKILLS 50 20 20 P C 20. DATA STRUCTURES LABORATORY 50 20 20 P TW PR 50 20 10 F 21. DATA STRUCTURES LABORATORY GRAND TOTAL = 511/1500, RESULT: FAILS RESULT RESERVED FOR BKLG ORDN. 1 MARKS: , 71100725L , , PICT S8054203 AMAN TEWARY SUNANDA , S8054203 01. DISCRETE STRUCTURES PP 100 40 29 F 11. ENGINEERING MATHEMATICS III PP 100 40 14 F AA F 02. PROGRAMMING & PROBLEM SOLVING PP 100 40 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 AA F 100 40 AA F PP 100 40 18 F 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 13. DATA STRUCTURES 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 14. COMPUTER GRAPHICS 100 40 18 F AA F 05. HUMANITIES AND SOCIAL SCIENCE PP 100 40 40 P C 15. COMPUTER ORGANIZATION 100 40 PP AA F 20 30 P 06. PROGRAMMING LABORATORY 25 10 17 P C 50 TW 16. O. O. PROG. & COMP. GRAPH. LAB TW 07. PROGRAMMING LABORATORY 20 17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 25 P PR 50 AA F 08. DIGITAL ELECTRONICS LABORATORY TW 25 10 17 P C 18. MICROPROCESSORS & INTERFACING LABTW 50 20 21 P 50 20 34 P C 09. DIGITAL ELECTRONICS LABORATORY PR 50 20 21 P 19. MICROPROCESSORS & INTERFACING LABPR 10. SOFT SKILLS 50 20 33 P C 20. DATA STRUCTURES LABORATORY TW 50 20 22 P TW 21. DATA STRUCTURES LABORATORY PR 50 20 AA F GRAND TOTAL = 339/1500, RESULT: FAILS ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71100728E , , PICT JYOTI , s8054204 S8054204 AMRUTKAR SNEHAL RAJENDRA 01. DISCRETE STRUCTURES 11. ENGINEERING MATHEMATICS III PP 100 40 62 P PP 100 40 51 P C 40 62 P C 02. PROGRAMMING & PROBLEM SOLVING PP 100 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 46 P 100 40 65 P C 100 40 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 13. DATA STRUCTURES PP 71 p 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 74 P C PP 100 40 64 P 14. COMPUTER GRAPHICS 05. HUMANITIES AND SOCIAL SCIENCE PP 100 40 58 P C 15. COMPUTER ORGANIZATION 100 40 71 P 25 10 22 P C 50 20 38 P 06. PROGRAMMING LABORATORY 16. O. O. PROG. & COMP. GRAPH. LAB TW TW 07. PROGRAMMING LABORATORY 50 20 40 P C 50 20 31 P PR 17. O. O. PROG. & COMP. GRAPH. LAB PR 25 10 19 P C 18. MICROPROCESSORS & INTERFACING LABTW 50 20 41 P 08. DIGITAL ELECTRONICS LABORATORY TW 50 20 38 P C 50 20 32 P 09. DIGITAL ELECTRONICS LABORATORY PR 19. MICROPROCESSORS & INTERFACING LABPR 10. SOFT SKILLS 50 20 40 P C 20. DATA STRUCTURES LABORATORY 50 20 41 P TW TW PR 50 20 40 P 21. DATA STRUCTURES LABORATORY GRAND TOTAL = 1006/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS: , 71100735н , , РІСТ S8054205 AVHAD PRIYA SANTOSH MADHURI , s8054205 01. DISCRETE STRUCTURES PP 100 40 75 P C 11. ENGINEERING MATHEMATICS III PP 100 40 92 P 02. PROGRAMMING & PROBLEM SOLVING PP 100 40 61 P C 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 57 P 100 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 40 75 P.C 13. DATA STRUCTURES PP 40 72 P 40 72 P C 100 67 P 04. DATA STRUCTURES AND ALGORITHMS PP 100 14. COMPUTER GRAPHICS PP 40 40 100 05. HUMANITIES AND SOCIAL SCIENCE PP 100 60 P C 15. COMPUTER ORGANIZATION 40 63 P 25 10 20 P C 50 20 40 P 06. PROGRAMMING LABORATORY 16. O. O. PROG. & COMP. GRAPH. LAB TW TW 50 20 41 P C 20 07. PROGRAMMING LABORATORY PR 17. O. O. PROG. & COMP. GRAPH. LAB PR 50 42 P 25 10 17 P C 40 P 08. DIGITAL ELECTRONICS LABORATORY TW 18. MICROPROCESSORS & INTERFACING LABTW 50 20 50 20 30 P 09. DIGITAL ELECTRONICS LABORATORY PR 50 20 37 P C 19. MICROPROCESSORS & INTERFACING LABPR 10. SOFT SKILLS 50 20 32 P C 20. DATA STRUCTURES LABORATORY 50 20 36 P TW TW PR 50 20 41 P 21. DATA STRUCTURES LABORATORY GRAND TOTAL = 1070/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS: , 71100740D , , PICT , S8054206 S8054206 BALI SONIYA NARAYAN SHOBHA 01. DISCRETE STRUCTURES PP 100 40 58 P C 11. ENGINEERING MATHEMATICS III PP 100 40 21 F 02. PROGRAMMING & PROBLEM SOLVING PP 100 40 42 P C 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 40 P 100 40 57 P C PP 100 40 50 P 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 13. DATA STRUCTURES 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 40 P C 14. COMPUTER GRAPHICS 100 40 55 P PP 05. HUMANITIES AND SOCIAL SCIENCE PP 100 40 50 P C 15. COMPUTER ORGANIZATION 100 40 58 P PP 25 10 18 P C 20 33 P 06. PROGRAMMING LABORATORY 50 TW 16. O. O. PROG. & COMP. GRAPH. LAB TW 07. PROGRAMMING LABORATORY 50 20 35 P 17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 32 P PR 50 20 08. DIGITAL ELECTRONICS LABORATORY TW 25 10 18 P C 18. MICROPROCESSORS & INTERFACING LABTW 35 P 50 20 25 P C 50 20 25 P 09. DIGITAL ELECTRONICS LABORATORY PR 19. MICROPROCESSORS & INTERFACING LABPR 10. SOFT SKILLS 50 20 38 P C TW 50 20 32 P TW 20. DATA STRUCTURES LABORATORY 21. DATA STRUCTURES LABORATORY PR 50 20 32 P GRAND TOTAL = 794/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER S8054207 BALIWANT SOURABH SANJAY KALPANA , s8054207 01. DISCRETE STRUCTURES 11. ENGINEERING MATHEMATICS III PP 100 40 40 P PP 100 40 40 P C 40 40 P C 43 P 02. PROGRAMMING & PROBLEM SOLVING PP 100 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 100 40 44 P C 100 40 54 P 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 13. DATA STRUCTURES PP 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 63 P C PP 100 40 56 P 14. COMPUTER GRAPHICS 05. HUMANITIES AND SOCIAL SCIENCE PP 100 40 58 P C 15. COMPUTER ORGANIZATION 100 40 48 P 25 10 21 PC 50 20 33 P 06. PROGRAMMING LABORATORY 16. O. O. PROG. & COMP. GRAPH. LAB TW TW 07. PROGRAMMING LABORATORY 50 20 33 P 50 20 32 P PR 17. O. O. PROG. & COMP. GRAPH. LAB PR 25 10 17 P C 18. MICROPROCESSORS & INTERFACING LABTW 50 20 30 P 08. DIGITAL ELECTRONICS LABORATORY TW 50 20 30 P C 50 20 06 F 09. DIGITAL ELECTRONICS LABORATORY PR 19. MICROPROCESSORS & INTERFACING LABPR TW 50 20 32 P C 10. SOFT SKILLS 20. DATA STRUCTURES LABORATORY 50 20 30 P TW PR 50 20 27 P 21. DATA STRUCTURES LABORATORY GRAND TOTAL = 777/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS: S8054208 BAPAT RUTUGANDHA SUNIL ANAGHA , s8054208 01. DISCRETE STRUCTURES PP 100 40 45 P C 11. ENGINEERING MATHEMATICS III PP 100 40 27 F 02. PROGRAMMING & PROBLEM SOLVING PP 100 40 54 P C 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 46 P 13. DATA STRUCTURES 40 50 P C 100 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 PP 40 71 P 100 59 P 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 54 P C 14. COMPUTER GRAPHICS PP 40 100 05. HUMANITIES AND SOCIAL SCIENCE PP 100 40 61 P C 15. COMPUTER ORGANIZATION 40 61 P 19 P C 25 10 50 20 38 P 06. PROGRAMMING LABORATORY 16. O. O. PROG. & COMP. GRAPH. LAB TW TW 50 20 10 F 20 07. PROGRAMMING LABORATORY PR 17. O. O. PROG. & COMP. GRAPH. LAB PR 50 36 P 25 10 22 P C 08. DIGITAL ELECTRONICS LABORATORY TW 18. MICROPROCESSORS & INTERFACING LABTW 50 20 44 P 50 20 07 F 09. DIGITAL ELECTRONICS LABORATORY PR 50 20 32 P C 19. MICROPROCESSORS & INTERFACING LABPR 10. SOFT SKILLS 50 20 40 P C 20. DATA STRUCTURES LABORATORY 50 20 37 P TW TW PR 50 20 24 P 21. DATA STRUCTURES LABORATORY GRAND TOTAL = 837/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS : , 71100742L , , PICT , s8054209 S8054209 BAPAT SWARNADEE SANDEEP RIJUTA 01. DISCRETE STRUCTURES PP 100 40 53 P C 11. ENGINEERING MATHEMATICS III PP 100 40 66 P 02. PROGRAMMING & PROBLEM SOLVING PP 100 40 58 P C 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 57 P 100 40 65 P C PP 100 40 75 P 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 13. DATA STRUCTURES 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 65 P C 14. COMPUTER GRAPHICS 100 40 56 P PP 100 40 60 P C 15. COMPUTER ORGANIZATION 100 40 54 P 05. HUMANITIES AND SOCIAL SCIENCE PP PP 06. PROGRAMMING LABORATORY 25 10 23 P C 50 20 41 P TW 16. O. O. PROG. & COMP. GRAPH. LAB TW 07. PROGRAMMING LABORATORY 50 20 31 P C 17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 40 P PR 50 20 08. DIGITAL ELECTRONICS LABORATORY TW 25 10 20 P C 18. MICROPROCESSORS & INTERFACING LABTW 42 P 09. DIGITAL ELECTRONICS LABORATORY PR 50 20 35 P C 50 20 42 P 19. MICROPROCESSORS & INTERFACING LABPR 10. SOFT SKILLS 50 20 40 P C TW 50 20 43 P ΤW 20. DATA STRUCTURES LABORATORY 21. DATA STRUCTURES LABORATORY PR 50 20 34 P GRAND TOTAL = 1000/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71239287E , , PICT , S8054210 S8054210 BARDE KAJAL NARAYAN SULOCHANA 01. DISCRETE STRUCTURES PP 100 40 43 P C 11. ENGINEERING MATHEMATICS III PP 100 40 43 P 40 59 P C 51 P 02. PROGRAMMING & PROBLEM SOLVING PP 100 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 100 40 70 P C PP 100 40 70 P 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 13. DATA STRUCTURES 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 69 P C PP 100 40 50 P 14. COMPUTER GRAPHICS 05. HUMANITIES AND SOCIAL SCIENCE PP 100 40 53 P C 15. COMPUTER ORGANIZATION 100 40 25 10 23 P C 50 20 45 P 06. PROGRAMMING LABORATORY 16. O. O. PROG. & COMP. GRAPH. LAB TW TW 07. PROGRAMMING LABORATORY 50 20 44 P C 50 20 44 P PR 17. O. O. PROG. & COMP. GRAPH. LAB PR 25 10 21 P C 18. MICROPROCESSORS & INTERFACING LABTW 50 20 08. DIGITAL ELECTRONICS LABORATORY TW 46 P 09. DIGITAL ELECTRONICS LABORATORY PR 50 20 42 P C 50 20 39 P 19. MICROPROCESSORS & INTERFACING LABPR 20. DATA STRUCTURES LABORATORY 10. SOFT SKILLS TW 50 20 43 P C TW 50 20 45 P 21. DATA STRUCTURES LABORATORY PR 50 20 44 P GRAND TOTAL = 1008/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS: , 71239288C , , PICT S8054211 BARDE KOMAL NARAYAN SULOCHANA , s8054211 01. DISCRETE STRUCTURES PP 100 40 42 P C 11. ENGINEERING MATHEMATICS III PP 100 40 43 P 71 P C 02. PROGRAMMING & PROBLEM SOLVING PP 100 40 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 51 P 13. DATA STRUCTURES 100 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 40 69 P C PP 40 70 P 100 57 P 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 68 P C 14. COMPUTER GRAPHICS PP 40 100 05. HUMANITIES AND SOCIAL SCIENCE PP 100 40 57 P C 15. COMPUTER ORGANIZATION 40 63 P 50 20 45 P 06. PROGRAMMING LABORATORY 25 10 23 P C 16. O. O. PROG. & COMP. GRAPH. LAB TW TW 50 20 42 P C 20 07. PROGRAMMING LABORATORY PR 17. O. O. PROG. & COMP. GRAPH. LAB PR 50 41 P 25 10 20 P C 08. DIGITAL ELECTRONICS LABORATORY TW 18. MICROPROCESSORS & INTERFACING LABTW 50 20 44 P 09. DIGITAL ELECTRONICS LABORATORY PR 50 20 42 P C 19. MICROPROCESSORS & INTERFACING LABPR 50 20 25 P 10. SOFT SKILLS TW 50 20 43 P C 20. DATA STRUCTURES LABORATORY TW 50 20 42 P 21. DATA STRUCTURES LABORATORY PR 50 20 38 P GRAND TOTAL = 996/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS: , s8054212 S8054212 BASAPURE SHRAVYA RAMESH VIJAYA 01. DISCRETE STRUCTURES PP 100 40 26 F 11. ENGINEERING MATHEMATICS III PP 100 40 04 F 02. PROGRAMMING & PROBLEM SOLVING PP 100 40 15 F 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 03 F 100 40 13 F PP 100 40 22 F 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 13. DATA STRUCTURES 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 18 F 14. COMPUTER GRAPHICS 100 40 23 F PP 100 40 23 F 15. COMPUTER ORGANIZATION 100 40 14 F 05. HUMANITIES AND SOCIAL SCIENCE PP PP 32 P 06. PROGRAMMING LABORATORY 25 10 21 P C 50 20 TW 16. O. O. PROG. & COMP. GRAPH. LAB TW 07. PROGRAMMING LABORATORY 50 20 17 F 17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 10 F PR 08. DIGITAL ELECTRONICS LABORATORY TW 25 10 18 P C 18. MICROPROCESSORS & INTERFACING LABTW 50 20 21 P 50 20 25 P C 09. DIGITAL ELECTRONICS LABORATORY PR 19. MICROPROCESSORS & INTERFACING LABPR 50 20 04 F 10. SOFT SKILLS 50 20 36 P C 20. DATA STRUCTURES LABORATORY TW 50 20 26 P TW 21. DATA STRUCTURES LABORATORY PR 50 20 05 F GRAND TOTAL = 376/1500, RESULT: FAILS RESULT RESERVED FOR BKLG ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER SANDHYA S8054213 BAWALE DEVEN ABHAY , s8054213 01. DISCRETE STRUCTURES 11. ENGINEERING MATHEMATICS III PP 100 40 40 P PP 100 40 42 P C 02. PROGRAMMING & PROBLEM SOLVING PP 100 40 47 P C 40 P 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 100 40 42 P C 100 40 63 P 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 13. DATA STRUCTURES PP 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 40 P C PP 100 40 50 P 14. COMPUTER GRAPHICS 05. HUMANITIES AND SOCIAL SCIENCE PP 100 40 46 P 15. COMPUTER ORGANIZATION 100 40 42 P 25 10 21 PC 50 20 45 P 06. PROGRAMMING LABORATORY 16. O. O. PROG. & COMP. GRAPH. LAB TW TW 07. PROGRAMMING LABORATORY 50 20 36 P C 50 20 40 P PR 17. O. O. PROG. & COMP. GRAPH. LAB PR 25 10 22 P C 18. MICROPROCESSORS & INTERFACING LABTW 50 20 42 P 08. DIGITAL ELECTRONICS LABORATORY TW 50 20 38 P C 50 20 29 P 09. DIGITAL ELECTRONICS LABORATORY PR 19. MICROPROCESSORS & INTERFACING LABPR 10. SOFT SKILLS 50 20 42 P C 20. DATA STRUCTURES LABORATORY 50 20 42 P TW TW PR 50 20 32 P 21. DATA STRUCTURES LABORATORY GRAND TOTAL = 841/1500, RESULT: HIGHER SECOND CLASS ORDN. 1 MARKS: S8054214 BELOTE AMOL PRABHAKAR GANGUBAI , s8054214 01. DISCRETE STRUCTURES 57 P C 11. ENGINEERING MATHEMATICS III PP 100 40 40 P PP 100 40 02. PROGRAMMING & PROBLEM SOLVING PP 100 40 44 P C 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 49 P 13. DATA STRUCTURES 40 100 40 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 49 P C PP 62 P 100 57 P 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 59 P C 14. COMPUTER GRAPHICS PP 40 100 05. HUMANITIES AND SOCIAL SCIENCE PP 100 40 51 P C 15. COMPUTER ORGANIZATION 40 67 P 25 10 17 P C 50 20 35 P 06. PROGRAMMING LABORATORY 16. O. O. PROG. & COMP. GRAPH. LAB TW TW 50 20 28 P 20 07. PROGRAMMING LABORATORY PR 17. O. O. PROG. & COMP. GRAPH. LAB PR 50 38 P 25 10 18 P C 08. DIGITAL ELECTRONICS LABORATORY TW 18. MICROPROCESSORS & INTERFACING LABTW 50 20 32 P 50 20 39 P 09. DIGITAL ELECTRONICS LABORATORY PR 50 20 21 P C 19. MICROPROCESSORS & INTERFACING LABPR 10. SOFT SKILLS 50 20 34 P C 20. DATA STRUCTURES LABORATORY 50 20 31 P TW TW 21. DATA STRUCTURES LABORATORY PR 50 20 28 P GRAND TOTAL = 856/1500, RESULT: HIGHER SECOND CLASS ORDN. 1 MARKS: , 71239289M , , PICT , s8054215 S8054215 BHADKE ASHUTOSH CHANDRAKANT SUNANDA 01. DISCRETE STRUCTURES 100 40 60 P C 11. ENGINEERING MATHEMATICS III PP 100 40 52 P 02. PROGRAMMING & PROBLEM SOLVING PP 100 40 60 P C 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 60 P 100 40 72 P C 13. DATA STRUCTURES PP 100 40 74 P 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 74 P.C 14. COMPUTER GRAPHICS 100 40 53 P PP 05. HUMANITIES AND SOCIAL SCIENCE PP 100 40 54 P C 15. COMPUTER ORGANIZATION 100 40 71 P PP 20 37 P 06. PROGRAMMING LABORATORY 25 10 21 P C 50 TW 16. O. O. PROG. & COMP. GRAPH. LAB TW 07. PROGRAMMING LABORATORY 50 20 43 P C 17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 42 P PR 50 20 08. DIGITAL ELECTRONICS LABORATORY TW 25 10 19 P C 18. MICROPROCESSORS & INTERFACING LABTW 45 P 09. DIGITAL ELECTRONICS LABORATORY PR 50 20 35 P C 50 20 42 P 19. MICROPROCESSORS & INTERFACING LABPR 10. SOFT SKILLS 50 20 34 P C TW 50 20 39 P ΤW 20. DATA STRUCTURES LABORATORY 21. DATA STRUCTURES LABORATORY PR 50 20 30 P GRAND TOTAL = 1017/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71239290E , , PICT , S8054216 S8054216 BHANDARI RUHI BABU SMITA 01. DISCRETE STRUCTURES 11. ENGINEERING MATHEMATICS III PP 100 40 25 F PP 100 40 41 P C 40 57 P C 49 P 02. PROGRAMMING & PROBLEM SOLVING PP 100 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 100 40 40 P C PP 100 40 72 P 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 13. DATA STRUCTURES 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 49 P C PP 100 40 57 P 14. COMPUTER GRAPHICS 05. HUMANITIES AND SOCIAL SCIENCE PP 100 40 58 P C 15. COMPUTER ORGANIZATION 100 40 54 P 25 10 17 P C 50 20 39 P 06. PROGRAMMING LABORATORY 16. O. O. PROG. & COMP. GRAPH. LAB TW TW 07. PROGRAMMING LABORATORY 50 20 20 P C 50 20 42 P PR 17. O. O. PROG. & COMP. GRAPH. LAB PR 25 10 18 P C 50 20 41 P 08. DIGITAL ELECTRONICS LABORATORY TW 18. MICROPROCESSORS & INTERFACING LABTW 50 20 33 P C 50 20 10 F 09. DIGITAL ELECTRONICS LABORATORY PR 19. MICROPROCESSORS & INTERFACING LABPR 20. DATA STRUCTURES LABORATORY 10. SOFT SKILLS TW 50 20 31 P C TW 50 20 33 P PR 50 20 30 P 21. DATA STRUCTURES LABORATORY GRAND TOTAL = 816/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS: S8054217 BHANDARI YASHASHRI VASANT RANJANA , s8054217 01. DISCRETE STRUCTURES 11. ENGINEERING MATHEMATICS III PP 100 40 40 P PP 100 40 43 P C 02. PROGRAMMING & PROBLEM SOLVING PP 100 40 47 P C 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 55 P 13. DATA STRUCTURES 40 100 75 P 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 48 P C PP 40 100 59 P 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 56 P C 14. COMPUTER GRAPHICS PP 40 05. HUMANITIES AND SOCIAL SCIENCE PP 100 40 44 P C 15. COMPUTER ORGANIZATION 100 40 65 P 25 10 20 36 P 06. PROGRAMMING LABORATORY 19 P C 16. O. O. PROG. & COMP. GRAPH. LAB TW 50 TW 50 20 20 P C 20 07. PROGRAMMING LABORATORY PR 17. O. O. PROG. & COMP. GRAPH. LAB PR 50 43 P 25 10 21 P C 08. DIGITAL ELECTRONICS LABORATORY TW 18. MICROPROCESSORS & INTERFACING LABTW 50 20 43 P 50 20 23 P 09. DIGITAL ELECTRONICS LABORATORY PR 50 20 35 P C 19. MICROPROCESSORS & INTERFACING LABPR 10. SOFT SKILLS 50 20 37 P C 20. DATA STRUCTURES LABORATORY TW 50 20 37 P TW 21. DATA STRUCTURES LABORATORY PR 50 20 22 P GRAND TOTAL = 868/1500, RESULT: HIGHER SECOND CLASS ORDN. 1 MARKS: , S8054218 S8054218 BHOIR RUPALI LAXMAN VANITA 01. DISCRETE STRUCTURES PP 100 40 42 P 11. ENGINEERING MATHEMATICS III PP 100 40 40 P 02. PROGRAMMING & PROBLEM SOLVING PP 100 40 40 P C 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 26 F 24 F 100 40 13. DATA STRUCTURES PP 100 40 49 P 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 31 F 14. COMPUTER GRAPHICS 100 40 42 P PP 100 40 40 P C 15. COMPUTER ORGANIZATION 100 40 23 F 05. HUMANITIES AND SOCIAL SCIENCE PP PP 25 10 06. PROGRAMMING LABORATORY 19 P C 50 20 43 P TW 16. O. O. PROG. & COMP. GRAPH. LAB TW 07. PROGRAMMING LABORATORY 50 20 24 P C 17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 23 P PR 08. DIGITAL ELECTRONICS LABORATORY TW 25 10 18 P C 18. MICROPROCESSORS & INTERFACING LABTW 50 20 39 P 50 20 27 P C 09. DIGITAL ELECTRONICS LABORATORY PR 19. MICROPROCESSORS & INTERFACING LABPR 50 20 21 P 10. SOFT SKILLS 50 20 34 P C 20. DATA STRUCTURES LABORATORY TW 50 20 38 P TW 21. DATA STRUCTURES LABORATORY PR 50 20 10 F GRAND TOTAL = 653/1500, RESULT: FAILS RESULT RESERVED FOR BKLG ORDN. 1 MARKS:

DATE: 14 AUG. 2012 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 07 (430)

NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER S8054219 BHURKE SHWETA MILIND , s8054219 MADHURA 01. DISCRETE STRUCTURES 11. ENGINEERING MATHEMATICS III PP 100 40 78 P PP 100 40 66 P C 40 52 P C 02. PROGRAMMING & PROBLEM SOLVING PP 100 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 100 40 60 P C PP 100 40 69 P 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 13. DATA STRUCTURES 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 65 P C PP 100 40 55 P 14. COMPUTER GRAPHICS 05. HUMANITIES AND SOCIAL SCIENCE PP 100 40 60 P C 15. COMPUTER ORGANIZATION 100 40 59 P 25 10 22 P C 50 20 44 P 06. PROGRAMMING LABORATORY 16. O. O. PROG. & COMP. GRAPH. LAB TW TW 07. PROGRAMMING LABORATORY 50 20 21 P C 50 20 40 P PR 17. O. O. PROG. & COMP. GRAPH. LAB PR 25 10 22 P C 18. MICROPROCESSORS & INTERFACING LABTW 50 20 45 P 08. DIGITAL ELECTRONICS LABORATORY TW 50 20 38 P C 50 20 43 P 09. DIGITAL ELECTRONICS LABORATORY PR 19. MICROPROCESSORS & INTERFACING LABPR 20. DATA STRUCTURES LABORATORY 10. SOFT SKILLS TW 50 20 43 P C TW 50 20 43 P PR 50 20 30 P 21. DATA STRUCTURES LABORATORY GRAND TOTAL = 999/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS : , 71100762E , , PICT S8054221 CHAUDHAR VISHAL SHESHRAO SAVITA , s8054221 01. DISCRETE STRUCTURES PP 100 40 45 P C 11. ENGINEERING MATHEMATICS III PP 100 40 40 P 02. PROGRAMMING & PROBLEM SOLVING PP 100 40 41 P C 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 42 P 13. DATA STRUCTURES 100 40 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 40 47 P C PP 63 P 100 52 P 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 48 P C 14. COMPUTER GRAPHICS PP 40 40 47 P C 100 54 P 05. HUMANITIES AND SOCIAL SCIENCE PP 100 15. COMPUTER ORGANIZATION 40 25 10 17 P C 50 20 29 P 06. PROGRAMMING LABORATORY 16. O. O. PROG. & COMP. GRAPH. LAB TW TW 50 20 21 P C 20 07. PROGRAMMING LABORATORY PR 17. O. O. PROG. & COMP. GRAPH. LAB PR 50 16 F 25 10 15 P C 08. DIGITAL ELECTRONICS LABORATORY TW 18. MICROPROCESSORS & INTERFACING LABTW 50 20 30 P 50 20 31 P C 50 20 11 F 09. DIGITAL ELECTRONICS LABORATORY PR 19. MICROPROCESSORS & INTERFACING LABPR 10. SOFT SKILLS TW 50 20 29 P C 20. DATA STRUCTURES LABORATORY 50 20 30 P TW 21. DATA STRUCTURES LABORATORY PR 50 20 28 P GRAND TOTAL = 736/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS: DNYNESHWARI , 71100763C , PICT , s8054222 S8054222 CHAUDHARI POOJA DHANRAJ 01. DISCRETE STRUCTURES PP 100 40 40 P C 11. ENGINEERING MATHEMATICS III PP 100 40 85 P 02. PROGRAMMING & PROBLEM SOLVING PP 100 40 52 P 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 53 P 100 40 62 P C PP 100 40 71 P 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 13. DATA STRUCTURES 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 75 P C 14. COMPUTER GRAPHICS 100 40 63 P PP 05. HUMANITIES AND SOCIAL SCIENCE PP 100 40 59 P C 15. COMPUTER ORGANIZATION 100 40 65 P PP 06. PROGRAMMING LABORATORY 25 10 19 P C 50 20 42 P TW 16. O. O. PROG. & COMP. GRAPH. LAB TW 07. PROGRAMMING LABORATORY 50 20 21 P C 17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 30 P PR 08. DIGITAL ELECTRONICS LABORATORY TW 25 10 21 P C 18. MICROPROCESSORS & INTERFACING LABTW 50 20 39 P 50 20 33 P C 09. DIGITAL ELECTRONICS LABORATORY PR 50 20 27 P 19. MICROPROCESSORS & INTERFACING LABPR 10. SOFT SKILLS 50 20 38 P C 20. DATA STRUCTURES LABORATORY TW 50 20 40 P TW 21. DATA STRUCTURES LABORATORY PR 50 20 35 P GRAND TOTAL = 970/1500, RESULT: FIRST CLASS ORDN. 1 MARKS:

DATE: 14 AUG. 2012 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 08 (431)

NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71239292M , , PICT , s8054223 S8054223 CHAVAN KARISHMA SANJAY SHARADA 01. DISCRETE STRUCTURES 11. ENGINEERING MATHEMATICS III PP 100 40 50 P PP 100 40 40 P C 40 63 P C 57 P 02. PROGRAMMING & PROBLEM SOLVING PP 100 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 100 40 60 P C PP 100 40 74 P 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 13. DATA STRUCTURES 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 67 P C PP 100 40 47 P 14. COMPUTER GRAPHICS 05. HUMANITIES AND SOCIAL SCIENCE PP 100 40 60 P C 15. COMPUTER ORGANIZATION 100 40 25 10 21 PC 50 20 39 P 06. PROGRAMMING LABORATORY 16. O. O. PROG. & COMP. GRAPH. LAB TW TW 07. PROGRAMMING LABORATORY 50 20 38 P C 50 20 38 P PR 17. O. O. PROG. & COMP. GRAPH. LAB PR 25 10 19 P C 18. MICROPROCESSORS & INTERFACING LABTW 50 20 43 P 08. DIGITAL ELECTRONICS LABORATORY TW 09. DIGITAL ELECTRONICS LABORATORY PR 50 20 35 P C 50 20 35 P 19. MICROPROCESSORS & INTERFACING LABPR 10. SOFT SKILLS TW 50 20 40 P C 20. DATA STRUCTURES LABORATORY 50 20 42 P TW PR 50 20 40 P 21. DATA STRUCTURES LABORATORY GRAND TOTAL = 973/1500, RESULT: FIRST CLASS ORDN. 1 MARKS: S8054224 CHIYA IFFA IMTIAZ FAMIDA , s8054224 01. DISCRETE STRUCTURES 11. ENGINEERING MATHEMATICS III PP 100 40 65 P PP 100 40 40 P C 02. PROGRAMMING & PROBLEM SOLVING PP 100 40 42 P C 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 54 P 13. DATA STRUCTURES 40 60 P C 100 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 PP 40 74 P 40 77 P C 100 04. DATA STRUCTURES AND ALGORITHMS PP 100 14. COMPUTER GRAPHICS PP 40 49 P 05. HUMANITIES AND SOCIAL SCIENCE PP 100 40 58 P C 15. COMPUTER ORGANIZATION 100 40 52 P 25 10 22 P C 50 20 06. PROGRAMMING LABORATORY 16. O. O. PROG. & COMP. GRAPH. LAB TW 41 P TW 50 20 43 P C 20 07. PROGRAMMING LABORATORY PR 17. O. O. PROG. & COMP. GRAPH. LAB PR 50 43 P 25 10 18 P C 08. DIGITAL ELECTRONICS LABORATORY TW 18. MICROPROCESSORS & INTERFACING LABTW 50 20 44 P 50 20 30 P 09. DIGITAL ELECTRONICS LABORATORY PR 50 20 41 P C 19. MICROPROCESSORS & INTERFACING LABPR 10. SOFT SKILLS TW 50 20 35 P C 20. DATA STRUCTURES LABORATORY 50 20 45 P TW 21. DATA STRUCTURES LABORATORY PR 50 20 41 P GRAND TOTAL = 974/1500, RESULT: FIRST CLASS ORDN. 1 MARKS: , 71100771D , , PICT , s8054225 S8054225 DAMLE RUCHA JAYANT VANDANA 01. DISCRETE STRUCTURES PP 100 40 64 P C 11. ENGINEERING MATHEMATICS III PP 100 40 79 P 02. PROGRAMMING & PROBLEM SOLVING PP 100 40 60 P C 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 55 P 100 40 66 P C PP 100 40 71 P 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 13. DATA STRUCTURES 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 74 P C 14. COMPUTER GRAPHICS 100 40 58 P PP 05. HUMANITIES AND SOCIAL SCIENCE PP 100 40 56 P C 15. COMPUTER ORGANIZATION 100 40 73 P PP 38 P 06. PROGRAMMING LABORATORY 25 10 22 P C 50 20 TW 16. O. O. PROG. & COMP. GRAPH. LAB TW 07. PROGRAMMING LABORATORY 50 20 38 P C 17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 40 P PR 08. DIGITAL ELECTRONICS LABORATORY TW 25 10 21 P C 18. MICROPROCESSORS & INTERFACING LABTW 50 20 37 P 09. DIGITAL ELECTRONICS LABORATORY PR 50 20 42 P C 50 20 43 P 19. MICROPROCESSORS & INTERFACING LABPR 10. SOFT SKILLS 50 20 38 P C TW 50 20 37 P ΤW 20. DATA STRUCTURES LABORATORY 21. DATA STRUCTURES LABORATORY PR 50 20 37 P GRAND TOTAL = 1049/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS:

DATE: 14 AUG. 2012 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 09 (432)

NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71100772в , , рІСТ , s8054226 S8054226 DAMLE VISHAKHA RAVINDRA VIJAYANTI 01. DISCRETE STRUCTURES PP 100 40 74 P C 11. ENGINEERING MATHEMATICS III PP 100 40 95 P 40 64 P C 02. PROGRAMMING & PROBLEM SOLVING PP 100 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 100 40 76 P C 100 40 75 P 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 13. DATA STRUCTURES PP 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 69 P C PP 100 40 61 P 14. COMPUTER GRAPHICS 05. HUMANITIES AND SOCIAL SCIENCE PP 100 40 59 P C 15. COMPUTER ORGANIZATION 100 40 72 P 25 10 20 P C 50 20 41 P 06. PROGRAMMING LABORATORY 16. O. O. PROG. & COMP. GRAPH. LAB TW TW 07. PROGRAMMING LABORATORY 50 20 45 P C 50 20 45 P PR 17. O. O. PROG. & COMP. GRAPH. LAB PR 25 10 18 P C 18. MICROPROCESSORS & INTERFACING LABTW 50 20 39 P 08. DIGITAL ELECTRONICS LABORATORY TW 50 20 38 P C 50 20 40 P 09. DIGITAL ELECTRONICS LABORATORY PR 19. MICROPROCESSORS & INTERFACING LABPR 20. DATA STRUCTURES LABORATORY 10. SOFT SKILLS 50 20 37 P C 50 20 36 P TW TW PR 50 20 40 P 21. DATA STRUCTURES LABORATORY GRAND TOTAL = 1104/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS: , 71100773L , , PICT S8054227 DAMLE VISHWAS MILIND MUGDHA , s8054227 01. DISCRETE STRUCTURES 11. ENGINEERING MATHEMATICS III PP 100 40 63 P PP 100 40 62 P C 40 59 P C 02. PROGRAMMING & PROBLEM SOLVING PP 100 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 53 P 100 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 40 81 P C 13. DATA STRUCTURES PP 40 69 P 83 P C 100 58 P 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 14. COMPUTER GRAPHICS PP 40 100 05. HUMANITIES AND SOCIAL SCIENCE PP 100 40 56 P C 15. COMPUTER ORGANIZATION 40 69 P 25 10 20 P C 50 20 45 P 06. PROGRAMMING LABORATORY 16. O. O. PROG. & COMP. GRAPH. LAB TW TW 50 20 44 P C 20 07. PROGRAMMING LABORATORY PR 17. O. O. PROG. & COMP. GRAPH. LAB PR 50 45 P 25 10 22 P C 08. DIGITAL ELECTRONICS LABORATORY TW 18. MICROPROCESSORS & INTERFACING LABTW 50 20 43 P 50 20 44 P 09. DIGITAL ELECTRONICS LABORATORY PR 50 20 46 P C 19. MICROPROCESSORS & INTERFACING LABPR 10. SOFT SKILLS 50 20 40 P C 20. DATA STRUCTURES LABORATORY 50 20 44 P TW TW 21. DATA STRUCTURES LABORATORY PR 50 20 43 P GRAND TOTAL = 1089/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS: , 71100774j , , PICT , S8054228 S8054228 DAMSE SAYALI LAXMAN LATA 01. DISCRETE STRUCTURES PP 100 40 40 P C 11. ENGINEERING MATHEMATICS III PP 100 40 16 F 02. PROGRAMMING & PROBLEM SOLVING PP 100 40 40 P C 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 40 P 100 40 40 P 13. DATA STRUCTURES PP 100 40 58 P 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 40 P C 14. COMPUTER GRAPHICS 100 40 41 P PP 05. HUMANITIES AND SOCIAL SCIENCE PP 100 40 40 P C 15. COMPUTER ORGANIZATION 100 40 40 P PP 25 10 18 P C 20 34 P 06. PROGRAMMING LABORATORY 50 TW 16. O. O. PROG. & COMP. GRAPH. LAB TW 07. PROGRAMMING LABORATORY 50 20 05 F 17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 10 F PR 50 20 08. DIGITAL ELECTRONICS LABORATORY TW 25 10 17 P C 18. MICROPROCESSORS & INTERFACING LABTW 36 P 50 20 21 P C 09. DIGITAL ELECTRONICS LABORATORY PR 50 20 27 P 19. MICROPROCESSORS & INTERFACING LABPR 10. SOFT SKILLS 50 20 34 P C TW 50 20 34 P TW 20. DATA STRUCTURES LABORATORY 21. DATA STRUCTURES LABORATORY PR 50 20 10 F GRAND TOTAL = 641/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS:

DATE: 14 AUG. 2012 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 10 (433)

				-	-		KS OBTAINED, P/F:PASS/FAIL, C:		•			
S8054229 DEBOJEET CHATTERJEE		100	40		IIKHA	11		-	ICT 100	-	S8054	
01. DISCRETE STRUCTURES		100	40		P C		ENGINEERING MATHEMATICS III	PP	100	40	71	
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40		P C		MICROPROC. & INTERFACING TECHNI	•	100	40	40	
03. DIGIT. ELECTRONICS & LOGIC DESI		100	40	66	PC		DATA STRUCTURES	PP	100	40	65	
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40		P C		COMPUTER GRAPHICS		100	40	52	
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	58	PC		COMPUTER ORGANIZATION	PP	100	40	67 40	
06. PROGRAMMING LABORATORY	TW	25	10		P C			TW	50	20	40	
07. PROGRAMMING LABORATORY		50	20	43	P C		O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	37	-
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	20	P C		MICROPROCESSORS & INTERFACING L		50	20	43	
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	38			MICROPROCESSORS & INTERFACING L		50	20	40	
10. SOFT SKILLS	TW	50	20	38	РС		DATA STRUCTURES LABORATORY		50	20	40	
1027/1500						21.	DATA STRUCTURES LABORATORY	PR	50	20	44	Р
GRAND TOTAL = 1037/1500, RESULT: FIRS ORDN. 1 MARKS:	T CLAS	SS WIT	H DIS	TINCT	ION							
S8054230 DEEPANSHU JOSHI				LA	ALITA		, 71100776E ,	, P	ICT	,	S8054	230
01. DISCRETE STRUCTURES	PP	100	40	40	РС	11.	ENGINEERING MATHEMATICS III	PP	100	40	40	Р
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	41	РС	12.	MICROPROC. & INTERFACING TECHNI	Q.PP	100	40	20	F
03. DIGIT. ELECTRONICS & LOGIC DESI	GNPP	100	40	40	РС	13.	DATA STRUCTURES	PP	100	40	49	Р
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	44	Р	14.	COMPUTER GRAPHICS	PP	100	40	40	Р
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	50	РС	15.	COMPUTER ORGANIZATION	PP	100	40	40	Р
06. PROGRAMMING LABORATORY	TW	25	10	20	РС	16.	O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	30	Р
07. PROGRAMMING LABORATORY	PR	50	20	22	РС	17.	O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	30	Р
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	18	РС	18.	MICROPROCESSORS & INTERFACING L	ABTW	50	20	28	Р
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	34	РС	19.	MICROPROCESSORS & INTERFACING L	ABPR	50	20	04	F
10. SOFT SKILLS	TW	50	20	30	РС	20.	DATA STRUCTURES LABORATORY	TW	50	20	23	Р
						21.	DATA STRUCTURES LABORATORY	PR	50	20	08	F
GRAND TOTAL = 651/1500, RESULT: FAIL	S A.T	.к.т.										
ORDN. 1 MARKS :												
S8054231 DESALE VRUSHALI SANJEEV					ENAKS			 , P			s8054	
01. DISCRETE STRUCTURES	PP	100	40	53	РС	11.	ENGINEERING MATHEMATICS III	PP	100	40	49	Р
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	65	РС	12.	MICROPROC. & INTERFACING TECHNI	Q.PP	100	40	62	Р
03. DIGIT. ELECTRONICS & LOGIC DESI	GNPP	100	40	67	РС	13.	DATA STRUCTURES	PP	100	40	82	Р
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	68	РС	14.	COMPUTER GRAPHICS	PP	100	40	71	Р
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	68	РС	15.	COMPUTER ORGANIZATION	PP	100	40	72	Р
06. PROGRAMMING LABORATORY	TW	25	10	22	РС	16.	O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	39	Р
07. PROGRAMMING LABORATORY	PR	50	20	40	РС	17.	O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	39	Р
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	19	РС	18.	MICROPROCESSORS & INTERFACING L	ABTW	50	20	41	Р
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	37	РС	19.	MICROPROCESSORS & INTERFACING L	ABPR	50	20	24	Р
10. SOFT SKILLS	TW	50	20	32	РС	20.	DATA STRUCTURES LABORATORY	TW	50	20	46	Р
						21.	DATA STRUCTURES LABORATORY	PR	50	20	35	Р
GRAND TOTAL = 1031/1500, RESULT: FIRSORDN. 1 MARKS:	T CLAS	SS WIT	H DIS	TINCT	ION							

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER S8054232 DESHMUKH AKSHAY ANIL JYOTI , s8054232 01. DISCRETE STRUCTURES 11. ENGINEERING MATHEMATICS III PP 100 40 70 P PP 100 40 46 P C 40 49 P C 45 P 02. PROGRAMMING & PROBLEM SOLVING PP 100 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 100 40 44 P C 100 40 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 13. DATA STRUCTURES PP 68 P 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 61 P C PP 100 40 56 P 14. COMPUTER GRAPHICS 05. HUMANITIES AND SOCIAL SCIENCE PP 100 40 41 P C 15. COMPUTER ORGANIZATION 100 40 61 P 25 10 18 P C 50 20 33 P 06. PROGRAMMING LABORATORY 16. O. O. PROG. & COMP. GRAPH. LAB TW TW 50 20 25 P 07. PROGRAMMING LABORATORY 50 20 42 P C PR 17. O. O. PROG. & COMP. GRAPH. LAB PR 25 10 17 P C 18. MICROPROCESSORS & INTERFACING LABTW 50 20 35 P 08. DIGITAL ELECTRONICS LABORATORY TW 50 20 36 P C 50 20 40 P 09. DIGITAL ELECTRONICS LABORATORY PR 19. MICROPROCESSORS & INTERFACING LABPR 50 20 32 P C 10. SOFT SKILLS 20. DATA STRUCTURES LABORATORY TW 50 20 32 P TW PR 50 20 34 P 21. DATA STRUCTURES LABORATORY GRAND TOTAL = 885/1500, RESULT: HIGHER SECOND CLASS ORDN. 1 MARKS: S8054233 DESHPANDE ANUJ ABHAY KALPANA , s8054233 01. DISCRETE STRUCTURES 11. ENGINEERING MATHEMATICS III PP 100 40 70 P PP 100 40 53 P C 53 P C 02. PROGRAMMING & PROBLEM SOLVING PP 100 40 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 40 P 52 P C 100 40 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 40 13. DATA STRUCTURES PP 55 P 100 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 44 P C 14. COMPUTER GRAPHICS PP 40 49 P 100 05. HUMANITIES AND SOCIAL SCIENCE PP 100 40 53 P C 15. COMPUTER ORGANIZATION 40 58 P 50 20 35 P 06. PROGRAMMING LABORATORY 25 10 24 P C 16. O. O. PROG. & COMP. GRAPH. LAB TW TW 50 20 38 P C 20 07. PROGRAMMING LABORATORY PR 17. O. O. PROG. & COMP. GRAPH. LAB PR 50 41 P 25 10 23 P C 08. DIGITAL ELECTRONICS LABORATORY TW 18. MICROPROCESSORS & INTERFACING LABTW 50 20 23 P 50 20 40 P 09. DIGITAL ELECTRONICS LABORATORY PR 50 20 24 P C 19. MICROPROCESSORS & INTERFACING LABPR 10. SOFT SKILLS 50 20 35 P C 20. DATA STRUCTURES LABORATORY 50 20 30 P TW TW PR 50 20 35 P 21. DATA STRUCTURES LABORATORY GRAND TOTAL = 875+05/1500, RESULT: HIGHER SECOND CLASS[0.163] ORDN. 1 MARKS: , s8054234 S8054234 DHAPTE OMKAR HANUMANT MINAXI 01. DISCRETE STRUCTURES PP 100 40 40 P C 11. ENGINEERING MATHEMATICS III PP 100 40 62 P 02. PROGRAMMING & PROBLEM SOLVING PP 100 40 52 P C 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 57 P 100 40 46 P C PP 100 40 76 P 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 13. DATA STRUCTURES 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 63 P C 14. COMPUTER GRAPHICS 100 40 56 P PP 05. HUMANITIES AND SOCIAL SCIENCE PP 100 40 51 P C 15. COMPUTER ORGANIZATION 100 40 64 P PP 20 34 P 06. PROGRAMMING LABORATORY 25 10 19 P C 50 TW 16. O. O. PROG. & COMP. GRAPH. LAB TW 07. PROGRAMMING LABORATORY 50 20 34 P C 17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 34 P PR 08. DIGITAL ELECTRONICS LABORATORY TW 25 10 14 P C 18. MICROPROCESSORS & INTERFACING LABTW 50 20 25 P 50 20 26 P C 09. DIGITAL ELECTRONICS LABORATORY PR 50 20 36 P 19. MICROPROCESSORS & INTERFACING LABPR 10. SOFT SKILLS 50 20 24 P C TW 50 20 32 P TW 20. DATA STRUCTURES LABORATORY 21. DATA STRUCTURES LABORATORY PR 50 20 39 P GRAND TOTAL = 884/1500, RESULT: HIGHER SECOND CLASS ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71100787L , , PICT , S8054235 S8054235 DIPESH SINGH REEN KOMAL KAUR PP 100 40 64 P C 11. ENGINEERING MATHEMATICS III PP 100 40 40 P 01. DISCRETE STRUCTURES 02. PROGRAMMING & PROBLEM SOLVING PP 100 40 57 P C 40 P 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 100 40 49 P C PP 100 40 65 P 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 13. DATA STRUCTURES 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 58 P C PP 100 40 53 P 14. COMPUTER GRAPHICS 05. HUMANITIES AND SOCIAL SCIENCE PP 100 40 54 P C 15. COMPUTER ORGANIZATION 100 40 47 P 25 10 20 P C 50 20 36 P 06. PROGRAMMING LABORATORY 16. O. O. PROG. & COMP. GRAPH. LAB TW TW 07. PROGRAMMING LABORATORY 50 20 21 P C 50 20 37 P PR 17. O. O. PROG. & COMP. GRAPH. LAB PR 25 10 21 P C 18. MICROPROCESSORS & INTERFACING LABTW 50 20 43 P 08. DIGITAL ELECTRONICS LABORATORY TW 50 20 30 P C 50 20 39 P 09. DIGITAL ELECTRONICS LABORATORY PR 19. MICROPROCESSORS & INTERFACING LABPR 10. SOFT SKILLS 50 20 39 P C 20. DATA STRUCTURES LABORATORY 50 20 39 P TW TW PR 50 20 30 P 21. DATA STRUCTURES LABORATORY GRAND TOTAL = 882/1500, RESULT: HIGHER SECOND CLASS ORDN. 1 MARKS: S8054236 DIXIT PRATIKSHA SUBHASHCHANDRA POONAM , s8054236 PP 100 40 61 P C 11. ENGINEERING MATHEMATICS III PP 100 40 48 P 01. DISCRETE STRUCTURES 02. PROGRAMMING & PROBLEM SOLVING PP 100 40 62 P C 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 54 P 13. DATA STRUCTURES 100 40 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 40 60 P C PP 73 P 100 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 63 P C 14. COMPUTER GRAPHICS PP 40 63 P 59 P C 05. HUMANITIES AND SOCIAL SCIENCE PP 100 40 15. COMPUTER ORGANIZATION 100 40 62 P 25 10 20 P C 50 20 06. PROGRAMMING LABORATORY 16. O. O. PROG. & COMP. GRAPH. LAB TW 41 P TW 50 20 25 P C 20 07. PROGRAMMING LABORATORY PR 17. O. O. PROG. & COMP. GRAPH. LAB PR 50 38 P 25 10 18 P C 08. DIGITAL ELECTRONICS LABORATORY TW 18. MICROPROCESSORS & INTERFACING LABTW 50 20 43 P 50 20 39 P 09. DIGITAL ELECTRONICS LABORATORY PR 50 20 39 P C 19. MICROPROCESSORS & INTERFACING LABPR 10. SOFT SKILLS TW 50 20 38 P C 20. DATA STRUCTURES LABORATORY 50 20 42 P TW PR 50 20 24 P 21. DATA STRUCTURES LABORATORY GRAND TOTAL = 972/1500, RESULT: FIRST CLASS ORDN. 1 MARKS: , s8054237 S8054237 DOIPHODE NISHIGANDHA SANJAY MADHUBALA 01. DISCRETE STRUCTURES 100 40 40 P C 11. ENGINEERING MATHEMATICS III PP 100 40 25 F 02. PROGRAMMING & PROBLEM SOLVING PP 100 40 48 P C 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 27 F 100 40 40 P C PP 100 40 49 P 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 13. DATA STRUCTURES 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 40 P C 14. COMPUTER GRAPHICS 100 40 41 P PP 05. HUMANITIES AND SOCIAL SCIENCE PP 100 40 51 P C 15. COMPUTER ORGANIZATION 100 40 40 P PP 25 10 06. PROGRAMMING LABORATORY 18 P C 50 20 31 P TW 16. O. O. PROG. & COMP. GRAPH. LAB TW 07. PROGRAMMING LABORATORY 50 20 22 P C 17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 20 P PR 08. DIGITAL ELECTRONICS LABORATORY TW 25 10 16 P C 18. MICROPROCESSORS & INTERFACING LABTW 50 20 34 P 09. DIGITAL ELECTRONICS LABORATORY PR 50 20 27 P C 50 20 07 F 19. MICROPROCESSORS & INTERFACING LABPR 10. SOFT SKILLS 50 20 37 P C TW 50 20 32 P TW 20. DATA STRUCTURES LABORATORY 21. DATA STRUCTURES LABORATORY PR 50 20 12 F GRAND TOTAL = 657/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71100790L , , PICT ARCHANA , S8054238 S8054238 DOSHI ARCHIT RAVINDRA 01. DISCRETE STRUCTURES 11. ENGINEERING MATHEMATICS III PP 100 40 52 P PP 100 40 40 P C 40 48 P C 40 P 02. PROGRAMMING & PROBLEM SOLVING PP 100 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 100 40 41 P C PP 100 40 49 P 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 13. DATA STRUCTURES 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 53 P C PP 100 40 40 P 14. COMPUTER GRAPHICS 05. HUMANITIES AND SOCIAL SCIENCE PP 100 40 54 P C 15. COMPUTER ORGANIZATION 100 40 43 P 25 10 17 P C 50 20 34 P 06. PROGRAMMING LABORATORY 16. O. O. PROG. & COMP. GRAPH. LAB TW TW 07. PROGRAMMING LABORATORY 50 20 20 P C 50 20 28 P PR 17. O. O. PROG. & COMP. GRAPH. LAB PR 25 10 20 P C 18. MICROPROCESSORS & INTERFACING LABTW 50 20 33 P 08. DIGITAL ELECTRONICS LABORATORY TW 50 20 31 P C 50 20 27 P 09. DIGITAL ELECTRONICS LABORATORY PR 19. MICROPROCESSORS & INTERFACING LABPR TW 50 20 40 P C 10. SOFT SKILLS 20. DATA STRUCTURES LABORATORY TW 50 20 30 P PR 50 20 30 P 21. DATA STRUCTURES LABORATORY GRAND TOTAL = 770/1500, RESULT: SECOND CLASS ORDN. 1 MARKS: , 71100791j , , PICT S8054239 EKHANDE ATUL ANIL TRISHALA , s8054239 PP 100 40 51 P C 11. ENGINEERING MATHEMATICS III PP 100 40 59 P 01. DISCRETE STRUCTURES 53 P C 02. PROGRAMMING & PROBLEM SOLVING PP 100 40 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 40 P 13. DATA STRUCTURES 42 P C 100 40 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 40 PP 57 P 51 P C 100 53 P 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 14. COMPUTER GRAPHICS PP 40 100 05. HUMANITIES AND SOCIAL SCIENCE PP 100 40 53 P C 15. COMPUTER ORGANIZATION 40 25 10 17 P C 50 20 29 P 06. PROGRAMMING LABORATORY 16. O. O. PROG. & COMP. GRAPH. LAB TW TW 50 20 30 P C 50 20 26 P 07. PROGRAMMING LABORATORY PR 17. O. O. PROG. & COMP. GRAPH. LAB PR 25 10 18 P C 08. DIGITAL ELECTRONICS LABORATORY TW 18. MICROPROCESSORS & INTERFACING LABTW 50 20 32 P 09. DIGITAL ELECTRONICS LABORATORY PR 50 20 35 P C 19. MICROPROCESSORS & INTERFACING LABPR 50 20 36 P 10. SOFT SKILLS 50 20 35 P C 20. DATA STRUCTURES LABORATORY 50 20 32 P TW TW 21. DATA STRUCTURES LABORATORY PR 50 20 31 P GRAND TOTAL = 824+01/1500, RESULT: HIGHER SECOND CLASS [0.2] ORDN. 1 MARKS: , 71045426M , , PICT , s8054240 S8054240 GABHALE VARSHA LIMBAJI JANABAI 01. DISCRETE STRUCTURES PP 100 40 52 P 11. ENGINEERING MATHEMATICS III PP 100 40 40 P 02. PROGRAMMING & PROBLEM SOLVING PP 100 40 44 P C 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 40 P 100 40 40 P C PP 100 40 53 P 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 13. DATA STRUCTURES 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 48 P C 14. COMPUTER GRAPHICS 100 40 27 F PP 100 40 40 P 15. COMPUTER ORGANIZATION 100 40 23 F 05. HUMANITIES AND SOCIAL SCIENCE PP PP 25 10 20 39 P 06. PROGRAMMING LABORATORY 21 P C 50 TW 16. O. O. PROG. & COMP. GRAPH. LAB TW 07. PROGRAMMING LABORATORY 50 20 30 P C 17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 34 P PR 08. DIGITAL ELECTRONICS LABORATORY TW 25 10 20 P C 18. MICROPROCESSORS & INTERFACING LABTW 50 20 39 P 50 20 27 P C 09. DIGITAL ELECTRONICS LABORATORY PR 50 20 07 F 19. MICROPROCESSORS & INTERFACING LABPR 10. SOFT SKILLS 50 20 35 P C TW 50 20 36 P TW 20. DATA STRUCTURES LABORATORY 21. DATA STRUCTURES LABORATORY PR 50 20 10 F GRAND TOTAL = 705/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , s8054241 S8054241 GALGALI VAISHNAVI VITHAL VAISHALI 01. DISCRETE STRUCTURES PP 100 40 53 P C 11. ENGINEERING MATHEMATICS III PP 100 40 62 P 40 56 P C 57 P 02. PROGRAMMING & PROBLEM SOLVING PP 100 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 100 40 68 P C PP 100 40 69 P 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 13. DATA STRUCTURES 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 66 P C PP 100 40 53 P 14. COMPUTER GRAPHICS 05. HUMANITIES AND SOCIAL SCIENCE PP 100 40 47 P C 15. COMPUTER ORGANIZATION 100 40 47 P 25 10 20 P C 50 20 30 P 06. PROGRAMMING LABORATORY 16. O. O. PROG. & COMP. GRAPH. LAB TW TW 50 20 21 P 07. PROGRAMMING LABORATORY 50 20 38 P PR 17. O. O. PROG. & COMP. GRAPH. LAB PR 25 10 17 P C 18. MICROPROCESSORS & INTERFACING LABTW 50 20 36 P 08. DIGITAL ELECTRONICS LABORATORY TW 50 20 38 P C 50 20 05 F 09. DIGITAL ELECTRONICS LABORATORY PR 19. MICROPROCESSORS & INTERFACING LABPR 10. SOFT SKILLS TW 50 20 33 P C 20. DATA STRUCTURES LABORATORY 50 20 39 P TW PR 50 20 31 P 21. DATA STRUCTURES LABORATORY GRAND TOTAL = 886/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS : , 71100797н , , РІСТ S8054242 GANVIR AJAY SUDHAKAR SUREKHA , s8054242 01. DISCRETE STRUCTURES 11. ENGINEERING MATHEMATICS III PP 100 40 41 P PP 100 40 58 P C 50 P C 02. PROGRAMMING & PROBLEM SOLVING PP 100 40 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 40 P 13. DATA STRUCTURES 40 100 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 66 P C PP 40 71 P 63 P C 100 40 P 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 14. COMPUTER GRAPHICS PP 40 50 P C 100 05. HUMANITIES AND SOCIAL SCIENCE PP 100 40 15. COMPUTER ORGANIZATION 40 41 P 25 10 50 20 26 P 06. PROGRAMMING LABORATORY 18 P C 16. O. O. PROG. & COMP. GRAPH. LAB TW TW 50 20 07 F 20 28 P 07. PROGRAMMING LABORATORY PR 17. O. O. PROG. & COMP. GRAPH. LAB PR 50 25 10 14 P C 08. DIGITAL ELECTRONICS LABORATORY TW 18. MICROPROCESSORS & INTERFACING LABTW 50 20 28 P 50 20 09. DIGITAL ELECTRONICS LABORATORY PR 50 20 23 P C 19. MICROPROCESSORS & INTERFACING LABPR 06 F 10. SOFT SKILLS 50 20 32 P C 20. DATA STRUCTURES LABORATORY 50 20 20 P TW TW PR 50 20 20 P 21. DATA STRUCTURES LABORATORY GRAND TOTAL = 742/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS: , 71100799D , , PICT , S8054243 S8054243 GAVALI TANUJA KASHINATH PARVATA 01. DISCRETE STRUCTURES PP 100 40 46 P 11. ENGINEERING MATHEMATICS III PP 100 40 40 P 02. PROGRAMMING & PROBLEM SOLVING PP 100 40 41 P C 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 40 P 100 40 44 P C PP 100 40 61 P 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 13. DATA STRUCTURES 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 40 P C 14. COMPUTER GRAPHICS 100 40 40 P PP 100 40 46 P C 15. COMPUTER ORGANIZATION 100 40 32 F 05. HUMANITIES AND SOCIAL SCIENCE PP PP 25 10 21 P C 35 P 06. PROGRAMMING LABORATORY 50 20 TW 16. O. O. PROG. & COMP. GRAPH. LAB TW 07. PROGRAMMING LABORATORY 50 20 07 F 17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 06 F PR 08. DIGITAL ELECTRONICS LABORATORY TW 25 10 17 P C 18. MICROPROCESSORS & INTERFACING LABTW 50 20 31 P 50 20 21 P C 09. DIGITAL ELECTRONICS LABORATORY PR 50 20 21 P 19. MICROPROCESSORS & INTERFACING LABPR 10. SOFT SKILLS 50 20 31 P C TW 50 20 37 P TW 20. DATA STRUCTURES LABORATORY 21. DATA STRUCTURES LABORATORY PR 50 20 05 F GRAND TOTAL = 662/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS:

DATE: 14 AUG. 2012 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 15 (438)

NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , s8054244 S8054244 GEET KAUR GIRVAR SINGH SUKHMANI PAVANJEET PP 100 40 54 P C 11. ENGINEERING MATHEMATICS III PP 100 40 40 P 01. DISCRETE STRUCTURES 02. PROGRAMMING & PROBLEM SOLVING PP 100 40 56 P C 40 P 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 100 40 53 P C 100 40 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 13. DATA STRUCTURES PP 68 P 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 57 P C PP 100 40 50 P 14. COMPUTER GRAPHICS 05. HUMANITIES AND SOCIAL SCIENCE PP 100 40 59 P C 15. COMPUTER ORGANIZATION 100 40 41 P 25 10 21 P C 50 20 34 P 06. PROGRAMMING LABORATORY 16. O. O. PROG. & COMP. GRAPH. LAB TW TW 50 20 22 P 07. PROGRAMMING LABORATORY 50 20 23 P C PR 17. O. O. PROG. & COMP. GRAPH. LAB PR 25 10 19 P C 18. MICROPROCESSORS & INTERFACING LABTW 50 20 40 P 08. DIGITAL ELECTRONICS LABORATORY TW 50 20 33 P C 50 20 32 P 09. DIGITAL ELECTRONICS LABORATORY PR 19. MICROPROCESSORS & INTERFACING LABPR 10. SOFT SKILLS TW 50 20 37 P C 20. DATA STRUCTURES LABORATORY 50 20 30 P TW PR 50 20 30 P 21. DATA STRUCTURES LABORATORY GRAND TOTAL = 839/1500, RESULT: HIGHER SECOND CLASS ORDN. 1 MARKS : , 71100803F , , PICT S8054245 GHULE RUTUJA POPAT SANGEETA , s8054245 01. DISCRETE STRUCTURES 11. ENGINEERING MATHEMATICS III PP 100 40 59 P PP 100 40 68 P C 47 P C 02. PROGRAMMING & PROBLEM SOLVING PP 100 40 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 40 P 13. DATA STRUCTURES 40 100 40 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 67 P C PP 60 P 100 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 56 P C 14. COMPUTER GRAPHICS PP 40 41 P 100 47 P 05. HUMANITIES AND SOCIAL SCIENCE PP 100 40 41 P C 15. COMPUTER ORGANIZATION 40 25 22 P C 50 20 06. PROGRAMMING LABORATORY 10 16. O. O. PROG. & COMP. GRAPH. LAB TW 41 P TW 50 20 38 P C 20 42 P 07. PROGRAMMING LABORATORY PR 17. O. O. PROG. & COMP. GRAPH. LAB PR 50 25 10 23 P C 08. DIGITAL ELECTRONICS LABORATORY TW 18. MICROPROCESSORS & INTERFACING LABTW 50 20 37 P 50 20 29 P 09. DIGITAL ELECTRONICS LABORATORY PR 50 20 43 P C 19. MICROPROCESSORS & INTERFACING LABPR 10. SOFT SKILLS 50 20 30 P C 20. DATA STRUCTURES LABORATORY 50 20 32 P TW TW PR 50 20 35 P 21. DATA STRUCTURES LABORATORY GRAND TOTAL = 898+02/1500, RESULT: FIRST CLASS [0.2] ORDN. 1 MARKS: , 71100804D , , PICT , s8054246 S8054246 GINA RIGZIN CHETEN ARCHANA 01. DISCRETE STRUCTURES PP 100 40 58 P C 11. ENGINEERING MATHEMATICS III PP 100 40 93 P 02. PROGRAMMING & PROBLEM SOLVING PP 100 40 56 P C 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 51 P 100 40 76 P C PP 100 40 64 P 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 13. DATA STRUCTURES 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 60 P C 14. COMPUTER GRAPHICS 100 40 48 P PP 05. HUMANITIES AND SOCIAL SCIENCE PP 100 40 56 P C 15. COMPUTER ORGANIZATION 100 40 61 P PP 06. PROGRAMMING LABORATORY 25 10 24 P C 50 20 40 P TW 16. O. O. PROG. & COMP. GRAPH. LAB TW 07. PROGRAMMING LABORATORY 50 20 43 P C 17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 46 P PR 50 20 08. DIGITAL ELECTRONICS LABORATORY TW 25 10 22 P C 18. MICROPROCESSORS & INTERFACING LABTW 44 P 09. DIGITAL ELECTRONICS LABORATORY PR 50 20 42 P C 50 20 42 P 19. MICROPROCESSORS & INTERFACING LABPR 10. SOFT SKILLS 50 20 43 P C TW 50 20 43 P TW 20. DATA STRUCTURES LABORATORY 21. DATA STRUCTURES LABORATORY PR 50 20 41 P GRAND TOTAL = 1053/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71239298L , , PICT S8054257 JADHAV ROHINI DAGADU , s8054257 RANJANA 01. DISCRETE STRUCTURES 11. ENGINEERING MATHEMATICS III PP 100 40 62 P PP 100 40 41 P C 40 62 P C 48 P 02. PROGRAMMING & PROBLEM SOLVING PP 100 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 100 40 58 P C 100 40 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 13. DATA STRUCTURES PP 67 P 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 58 P C PP 100 40 56 P 14. COMPUTER GRAPHICS 05. HUMANITIES AND SOCIAL SCIENCE PP 100 40 59 P C 15. COMPUTER ORGANIZATION 100 40 48 P 25 10 20 P C 50 20 36 P 06. PROGRAMMING LABORATORY 16. O. O. PROG. & COMP. GRAPH. LAB TW TW 07. PROGRAMMING LABORATORY 50 20 23 P C 50 20 30 P PR 17. O. O. PROG. & COMP. GRAPH. LAB PR 25 10 18 P C 50 20 40 P 08. DIGITAL ELECTRONICS LABORATORY TW 18. MICROPROCESSORS & INTERFACING LABTW 50 20 24 P C 50 20 31 P 09. DIGITAL ELECTRONICS LABORATORY PR 19. MICROPROCESSORS & INTERFACING LABPR 50 20 32 P C 10. SOFT SKILLS 20. DATA STRUCTURES LABORATORY 50 20 40 P TW TW PR 50 20 25 P 21. DATA STRUCTURES LABORATORY GRAND TOTAL = 878/1500, RESULT: HIGHER SECOND CLASS ORDN. 1 MARKS: S8054258 JANTRE NEHA ASHOK ARUNA , s8054258 46 P C 11. ENGINEERING MATHEMATICS III PP 100 40 46 P 01. DISCRETE STRUCTURES PP 100 40 53 P C 02. PROGRAMMING & PROBLEM SOLVING PP 100 40 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 49 P 54 P 100 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 40 13. DATA STRUCTURES PP 40 66 P 100 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 40 P C 14. COMPUTER GRAPHICS PP 40 60 P 57 P C 100 05. HUMANITIES AND SOCIAL SCIENCE PP 100 40 15. COMPUTER ORGANIZATION 40 52 P 22 P C 50 20 38 P 06. PROGRAMMING LABORATORY 25 10 16. O. O. PROG. & COMP. GRAPH. LAB TW TW 50 20 26 P C 20 07. PROGRAMMING LABORATORY PR 17. O. O. PROG. & COMP. GRAPH. LAB PR 50 40 P 25 10 19 P C 08. DIGITAL ELECTRONICS LABORATORY TW 18. MICROPROCESSORS & INTERFACING LABTW 50 20 41 P 09. DIGITAL ELECTRONICS LABORATORY PR 50 20 38 P C 19. MICROPROCESSORS & INTERFACING LABPR 50 20 25 P 10. SOFT SKILLS 50 20 36 P C 20. DATA STRUCTURES LABORATORY 50 20 42 P TW TW PR 50 20 30 P 21. DATA STRUCTURES LABORATORY GRAND TOTAL = 880/1500, RESULT: HIGHER SECOND CLASS ORDN. 1 MARKS: , 71100830C , , PICT , s8054259 S8054259 JASIM MAIMUNA 01. DISCRETE STRUCTURES PP 100 40 55 P C 11. ENGINEERING MATHEMATICS III PP 100 40 61 P 02. PROGRAMMING & PROBLEM SOLVING PP 100 40 52 P C 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 46 P 100 40 56 P C PP 100 40 63 P 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 13. DATA STRUCTURES 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 59 P C 14. COMPUTER GRAPHICS 100 40 47 P PP 05. HUMANITIES AND SOCIAL SCIENCE PP 100 40 49 P C 15. COMPUTER ORGANIZATION 100 40 40 P PP 06. PROGRAMMING LABORATORY 25 10 24 P C 50 20 38 P TW 16. O. O. PROG. & COMP. GRAPH. LAB TW 07. PROGRAMMING LABORATORY 50 20 45 P C 17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 38 P PR 50 20 08. DIGITAL ELECTRONICS LABORATORY TW 25 10 20 P C 18. MICROPROCESSORS & INTERFACING LABTW 41 P 50 20 34 P 50 20 40 P C 09. DIGITAL ELECTRONICS LABORATORY PR 19. MICROPROCESSORS & INTERFACING LABPR 10. SOFT SKILLS 50 20 38 P C TW 50 20 42 P TW 20. DATA STRUCTURES LABORATORY 21. DATA STRUCTURES LABORATORY PR 50 20 42 P GRAND TOTAL = 930/1500, RESULT: FIRST CLASS ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71100832к , , , РІСТ POOJA , s8054260 S8054260 JOGLEKAR SPHOORTI CHANDRASHEKHAR PP 100 40 70 P C 11. ENGINEERING MATHEMATICS III PP 100 40 85 P 01. DISCRETE STRUCTURES 02. PROGRAMMING & PROBLEM SOLVING PP 100 40 71 P C 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 100 40 79 P C 100 40 70 P 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 13. DATA STRUCTURES PP 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 73 P C PP 100 40 71 P 14. COMPUTER GRAPHICS 05. HUMANITIES AND SOCIAL SCIENCE PP 100 40 61 P C 15. COMPUTER ORGANIZATION 100 40 55 P 25 10 24 P C 50 20 42 P 06. PROGRAMMING LABORATORY 16. O. O. PROG. & COMP. GRAPH. LAB TW TW 07. PROGRAMMING LABORATORY 50 20 47 P C 50 20 45 P PR 17. O. O. PROG. & COMP. GRAPH. LAB PR 25 10 19 P C 18. MICROPROCESSORS & INTERFACING LABTW 50 20 29 P 08. DIGITAL ELECTRONICS LABORATORY TW 50 20 36 P C 50 20 43 P 09. DIGITAL ELECTRONICS LABORATORY PR 19. MICROPROCESSORS & INTERFACING LABPR TW 50 20 32 P C 10. SOFT SKILLS 20. DATA STRUCTURES LABORATORY 50 20 33 P TW PR 50 20 40 P 21. DATA STRUCTURES LABORATORY GRAND TOTAL = 1089/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS: , 71100834F , , PICT S8054261 JOSHI ANUJA RAMKRUSHNA VEENA , s8054261 01. DISCRETE STRUCTURES 11. ENGINEERING MATHEMATICS III PP 100 40 81 P PP 100 40 80 P C 02. PROGRAMMING & PROBLEM SOLVING PP 100 40 61 P C 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 55 P 100 40 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 40 58 P C 13. DATA STRUCTURES PP 71 P 100 72 P 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 64 P C 14. COMPUTER GRAPHICS PP 40 05. HUMANITIES AND SOCIAL SCIENCE PP 100 40 58 P C 15. COMPUTER ORGANIZATION 100 40 53 P 25 10 22 P C 50 20 38 P 06. PROGRAMMING LABORATORY 16. O. O. PROG. & COMP. GRAPH. LAB TW TW 50 20 37 P C 20 35 P 07. PROGRAMMING LABORATORY PR 17. O. O. PROG. & COMP. GRAPH. LAB PR 50 25 10 20 P C 08. DIGITAL ELECTRONICS LABORATORY TW 18. MICROPROCESSORS & INTERFACING LABTW 50 20 39 P 50 20 28 P 09. DIGITAL ELECTRONICS LABORATORY PR 50 20 35 P C 19. MICROPROCESSORS & INTERFACING LABPR 10. SOFT SKILLS 50 20 40 P C 20. DATA STRUCTURES LABORATORY 50 20 34 P TW TW PR 50 20 35 P 21. DATA STRUCTURES LABORATORY GRAND TOTAL = 1016/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS: , 71100837L , , PICT , s8054262 S8054262 JOSHI ESHAN CHANDRASHEKHAR MEDHAVINI 01. DISCRETE STRUCTURES PP 100 40 72 P C 11. ENGINEERING MATHEMATICS III PP 100 40 64 P 02. PROGRAMMING & PROBLEM SOLVING PP 100 40 63 P C 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 44 P 100 40 62 P C PP 100 40 63 P 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 13. DATA STRUCTURES 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 63 P C 14. COMPUTER GRAPHICS 100 40 51 P PΡ 05. HUMANITIES AND SOCIAL SCIENCE PP 100 40 57 P C 15. COMPUTER ORGANIZATION 100 40 43 P PP 35 P 06. PROGRAMMING LABORATORY 25 10 24 P C 50 20 TW 16. O. O. PROG. & COMP. GRAPH. LAB TW 07. PROGRAMMING LABORATORY 50 20 37 P C 17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 44 P PR 50 20 08. DIGITAL ELECTRONICS LABORATORY TW 25 10 22 P C 18. MICROPROCESSORS & INTERFACING LABTW 35 P 50 20 29 P 50 20 38 P C 09. DIGITAL ELECTRONICS LABORATORY PR 19. MICROPROCESSORS & INTERFACING LABPR 10. SOFT SKILLS 50 20 29 P C TW 50 20 32 P TW 20. DATA STRUCTURES LABORATORY 21. DATA STRUCTURES LABORATORY PR 50 20 34 P GRAND TOTAL = 941/1500, RESULT: FIRST CLASS ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71045479B , , PICT , S8054269 S8054269 KEDARE SWAPNIL PANDIT NIRMALA 01. DISCRETE STRUCTURES 11. ENGINEERING MATHEMATICS III PP 100 40 05 F PP 100 40 40 P 02. PROGRAMMING & PROBLEM SOLVING PP 100 40 18 F 23 F 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 100 40 40 P PP 100 40 40 P 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 13. DATA STRUCTURES 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 26 F PP 100 40 43 P 14. COMPUTER GRAPHICS 05. HUMANITIES AND SOCIAL SCIENCE PP 100 40 40 P C 15. COMPUTER ORGANIZATION 100 40 31 F 25 10 10 P C 50 20 20 P 06. PROGRAMMING LABORATORY 16. O. O. PROG. & COMP. GRAPH. LAB TW TW 50 20 10 F 07. PROGRAMMING LABORATORY 17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 PR AA F 25 10 10 P C 18. MICROPROCESSORS & INTERFACING LABTW 50 20 20 P 08. DIGITAL ELECTRONICS LABORATORY TW 50 20 08 F 50 20 03 F 09. DIGITAL ELECTRONICS LABORATORY PR 19. MICROPROCESSORS & INTERFACING LABPR 50 20 20 P C 10. SOFT SKILLS 20. DATA STRUCTURES LABORATORY 50 20 20 P TW TW PR 50 20 09 F 21. DATA STRUCTURES LABORATORY GRAND TOTAL = 436/1500, RESULT: FAILS ORDN. 1 MARKS: , 71100854L , , PICT S8054270 KEDIYA RIDDHI PRAVIN MANJUSHA , s8054270 01. DISCRETE STRUCTURES 11. ENGINEERING MATHEMATICS III PP 100 40 44 P PP 100 40 60 P C 02. PROGRAMMING & PROBLEM SOLVING PP 100 40 54 P C 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 40 P 13. DATA STRUCTURES 40 49 P C 100 40 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 PP 56 P 100 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 64 P C 14. COMPUTER GRAPHICS PP 40 40 P 49 P C 100 40 P 05. HUMANITIES AND SOCIAL SCIENCE PP 100 40 15. COMPUTER ORGANIZATION 40 25 10 19 P C 50 20 37 P 06. PROGRAMMING LABORATORY 16. O. O. PROG. & COMP. GRAPH. LAB TW TW 50 20 31 P C 20 07. PROGRAMMING LABORATORY PR 17. O. O. PROG. & COMP. GRAPH. LAB PR 50 16# P 25 10 18 P C 08. DIGITAL ELECTRONICS LABORATORY TW 18. MICROPROCESSORS & INTERFACING LABTW 50 20 37 P 50 20 34 P 09. DIGITAL ELECTRONICS LABORATORY PR 50 20 32 P C 19. MICROPROCESSORS & INTERFACING LABPR 10. SOFT SKILLS 50 20 36 P C 20. DATA STRUCTURES LABORATORY 50 20 35 P TW TW PR 50 20 20 P 21. DATA STRUCTURES LABORATORY GRAND TOTAL = 811/1500, RESULT: SECOND CLASS # [0.4] ORDN. 1 MARKS : , 71100858C , , PICT , s8054271 S8054271 KHAIRNAR APOORV DATTATRAYA PRAGATI 01. DISCRETE STRUCTURES PP 100 40 67 P C 11. ENGINEERING MATHEMATICS III PP 100 40 69 P 02. PROGRAMMING & PROBLEM SOLVING PP 100 40 66 P C 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 52 P 100 40 62 P C PP 100 40 65 P 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 13. DATA STRUCTURES 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 70 P C 14. COMPUTER GRAPHICS 100 40 62 P PP 05. HUMANITIES AND SOCIAL SCIENCE PP 100 40 47 P C 15. COMPUTER ORGANIZATION 100 40 49 P PP 25 10 23 P C 06. PROGRAMMING LABORATORY 50 20 40 P TW 16. O. O. PROG. & COMP. GRAPH. LAB TW 07. PROGRAMMING LABORATORY 50 20 45 P C 17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 37 P PR 08. DIGITAL ELECTRONICS LABORATORY TW 25 10 22 P C 18. MICROPROCESSORS & INTERFACING LABTW 50 20 33 P 50 20 25 P C 09. DIGITAL ELECTRONICS LABORATORY PR 50 20 37 P 19. MICROPROCESSORS & INTERFACING LABPR 10. SOFT SKILLS 50 20 28 P C TW 50 20 32 P TW 20. DATA STRUCTURES LABORATORY 21. DATA STRUCTURES LABORATORY PR 50 20 34 P GRAND TOTAL = 965/1500, RESULT: FIRST CLASS ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71100861C , , PICT ASHWINI , s8054272 S8054272 KHANDAGALE RAVEE SANJEEV 01. DISCRETE STRUCTURES 11. ENGINEERING MATHEMATICS III PP 100 40 40 P PP 100 40 55 P C 40 55 P C 02. PROGRAMMING & PROBLEM SOLVING PP 100 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 100 40 40 P C PP 100 40 49 P 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 13. DATA STRUCTURES 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 48 P C PP 100 40 14. COMPUTER GRAPHICS 46 P 05. HUMANITIES AND SOCIAL SCIENCE PP 100 40 52 P C 15. COMPUTER ORGANIZATION 100 40 32# P 25 10 17 P C 50 20 35 P 06. PROGRAMMING LABORATORY 16. O. O. PROG. & COMP. GRAPH. LAB TW TW 07. PROGRAMMING LABORATORY 50 20 35 P 50 20 39 P PR 17. O. O. PROG. & COMP. GRAPH. LAB PR 25 10 14 P C 18. MICROPROCESSORS & INTERFACING LABTW 50 20 08. DIGITAL ELECTRONICS LABORATORY TW 38 P 50 20 29 P C 50 20 23 P 09. DIGITAL ELECTRONICS LABORATORY PR 19. MICROPROCESSORS & INTERFACING LABPR 10. SOFT SKILLS TW 50 20 32 P C 20. DATA STRUCTURES LABORATORY 50 20 23 P TW PR 50 20 30 P 21. DATA STRUCTURES LABORATORY GRAND TOTAL = 772/1500, RESULT: SECOND CLASS # [0.4] ORDN. 1 MARKS: , 71100862M , , PICT S8054273 KHANDEKAR GAUTAMI MILIND VAISHALI , s8054273 01. DISCRETE STRUCTURES 11. ENGINEERING MATHEMATICS III PP 100 40 77 P PP 100 40 62 P C 02. PROGRAMMING & PROBLEM SOLVING PP 100 40 66 P C 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 50 P 13. DATA STRUCTURES 40 100 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 60 P C PP 40 62 P 100 67 P 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 68 P C 14. COMPUTER GRAPHICS PP 40 53 P C 100 59 P 05. HUMANITIES AND SOCIAL SCIENCE PP 100 40 15. COMPUTER ORGANIZATION 40 25 10 22 P C 50 20 43 P 06. PROGRAMMING LABORATORY 16. O. O. PROG. & COMP. GRAPH. LAB TW TW 50 20 42 P C 20 07. PROGRAMMING LABORATORY PR 17. O. O. PROG. & COMP. GRAPH. LAB PR 50 40 P 25 10 23 P C 08. DIGITAL ELECTRONICS LABORATORY TW 18. MICROPROCESSORS & INTERFACING LABTW 50 20 42 P 50 20 09. DIGITAL ELECTRONICS LABORATORY PR 50 20 45 P C 19. MICROPROCESSORS & INTERFACING LABPR 38 P 10. SOFT SKILLS 50 20 45 P C 20. DATA STRUCTURES LABORATORY 50 20 43 P TW TW 21. DATA STRUCTURES LABORATORY PR 50 20 40 P GRAND TOTAL = 1047/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS: , s8054274 S8054274 KHARADE TUSHAR DILIP **PURNIMA** 01. DISCRETE STRUCTURES PP 100 40 60 P C 11. ENGINEERING MATHEMATICS III PP 100 40 70 P 02. PROGRAMMING & PROBLEM SOLVING PP 100 40 64 P C 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 46 P 100 40 58 P C 13. DATA STRUCTURES PP 100 40 58 P 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 53 P C 14. COMPUTER GRAPHICS 100 40 57 P PP 100 40 44 P C 15. COMPUTER ORGANIZATION 100 40 55 P 05. HUMANITIES AND SOCIAL SCIENCE PP PP 06. PROGRAMMING LABORATORY 25 10 20 P C 50 20 44 P TW 16. O. O. PROG. & COMP. GRAPH. LAB TW 07. PROGRAMMING LABORATORY 50 20 36 P C 17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 41 P PR 50 20 08. DIGITAL ELECTRONICS LABORATORY TW 25 10 20 P C 18. MICROPROCESSORS & INTERFACING LABTW 39 P 50 20 36 P C 09. DIGITAL ELECTRONICS LABORATORY PR 50 20 21 P 19. MICROPROCESSORS & INTERFACING LABPR 10. SOFT SKILLS 50 20 39 P C TW 50 20 40 P TW 20. DATA STRUCTURES LABORATORY 21. DATA STRUCTURES LABORATORY PR 50 20 39 P GRAND TOTAL = 940/1500, RESULT: FIRST CLASS ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER SAROJ S8054281 LAJPAT BISHNOI , S8054281 11. ENGINEERING MATHEMATICS III PP 100 40 83 P 01. DISCRETE STRUCTURES PP 100 40 49 P C 02. PROGRAMMING & PROBLEM SOLVING PP 100 40 57 P C 40 P 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 100 40 73 P C PP 100 40 60 P 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 13. DATA STRUCTURES 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 63 P C PP 100 40 48 P 14. COMPUTER GRAPHICS 05. HUMANITIES AND SOCIAL SCIENCE PP 100 40 42 P C 15. COMPUTER ORGANIZATION 100 40 56 P 25 10 24 P C 50 20 43 P 06. PROGRAMMING LABORATORY 16. O. O. PROG. & COMP. GRAPH. LAB TW TW 07. PROGRAMMING LABORATORY 50 20 40 P C 50 20 45 P PR 17. O. O. PROG. & COMP. GRAPH. LAB PR 25 10 22 P C 18. MICROPROCESSORS & INTERFACING LABTW 50 20 42 P 08. DIGITAL ELECTRONICS LABORATORY TW 50 20 42 P C 50 20 41 P 09. DIGITAL ELECTRONICS LABORATORY PR 19. MICROPROCESSORS & INTERFACING LABPR 10. SOFT SKILLS TW 50 20 40 P C 20. DATA STRUCTURES LABORATORY 50 20 40 P TW PR 50 20 39 P 21. DATA STRUCTURES LABORATORY GRAND TOTAL = 989/1500, RESULT: FIRST CLASS ORDN. 1 MARKS: , 71100880K , , PICT S8054282 LALANI SANIA SALIM NASHINA , s8054282 01. DISCRETE STRUCTURES 65 P C 11. ENGINEERING MATHEMATICS III PP 100 40 90 P PP 100 40 02. PROGRAMMING & PROBLEM SOLVING PP 100 40 65 P C 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 41 P 13. DATA STRUCTURES 100 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 40 68 P C PP 40 55 P 100 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 47 P.C 14. COMPUTER GRAPHICS PP 40 49 P 05. HUMANITIES AND SOCIAL SCIENCE PP 100 40 51 P C 15. COMPUTER ORGANIZATION 100 40 60 P 20 P C 50 20 32 P 06. PROGRAMMING LABORATORY 25 10 16. O. O. PROG. & COMP. GRAPH. LAB TW TW 50 20 38 P C 20 42 P 07. PROGRAMMING LABORATORY PR 17. O. O. PROG. & COMP. GRAPH. LAB PR 50 25 10 20 P C 08. DIGITAL ELECTRONICS LABORATORY TW 18. MICROPROCESSORS & INTERFACING LABTW 50 20 35 P 09. DIGITAL ELECTRONICS LABORATORY PR 50 20 38 P C 19. MICROPROCESSORS & INTERFACING LABPR 50 20 36 P 10. SOFT SKILLS 50 20 33 P C 20. DATA STRUCTURES LABORATORY 50 20 34 P TW TW PR 50 20 38 P 21. DATA STRUCTURES LABORATORY GRAND TOTAL = 957/1500, RESULT: FIRST CLASS ORDN. 1 MARKS: , 71100882F , , PICT , s8054283 S8054283 LIMAYE ARNAV ABHAY VEENA 01. DISCRETE STRUCTURES PP 100 40 66 P C 11. ENGINEERING MATHEMATICS III PP 100 40 81 P 02. PROGRAMMING & PROBLEM SOLVING PP 100 40 62 P C 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 40 P 100 40 74 P C PP 100 40 68 P 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 13. DATA STRUCTURES 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 53 P C 14. COMPUTER GRAPHICS 100 40 51 P PP 100 40 50 P C 15. COMPUTER ORGANIZATION 100 40 45 P 05. HUMANITIES AND SOCIAL SCIENCE PP PP 36 P 06. PROGRAMMING LABORATORY 25 10 22 P C 50 20 TW 16. O. O. PROG. & COMP. GRAPH. LAB TW 07. PROGRAMMING LABORATORY 50 20 30 P C 17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 40 P PR 08. DIGITAL ELECTRONICS LABORATORY TW 25 10 17 P C 18. MICROPROCESSORS & INTERFACING LABTW 50 20 35 P 50 20 35 P C 09. DIGITAL ELECTRONICS LABORATORY PR 50 20 40 P 19. MICROPROCESSORS & INTERFACING LABPR 10. SOFT SKILLS 50 20 32 P C TW 50 20 40 P TW 20. DATA STRUCTURES LABORATORY 21. DATA STRUCTURES LABORATORY PR 50 20 36 P GRAND TOTAL = 953/1500, RESULT: FIRST CLASS ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , s8054284 S8054284 LOYA SHRUTI SHAMSUNDER PREMLATA 01. DISCRETE STRUCTURES PP 100 40 65 P C 11. ENGINEERING MATHEMATICS III PP 100 40 87 P 40 72 P C 56 P 02. PROGRAMMING & PROBLEM SOLVING PP 100 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 100 40 55 P C 100 40 63 P 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 13. DATA STRUCTURES PP 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 68 P C PP 100 40 66 P 14. COMPUTER GRAPHICS 05. HUMANITIES AND SOCIAL SCIENCE PP 100 40 54 P C 15. COMPUTER ORGANIZATION 100 40 65 P 25 10 18 P C 50 20 31 P 06. PROGRAMMING LABORATORY 16. O. O. PROG. & COMP. GRAPH. LAB TW TW 07. PROGRAMMING LABORATORY 50 20 28 P C 50 20 42 P PR 17. O. O. PROG. & COMP. GRAPH. LAB PR 25 10 19 P C 18. MICROPROCESSORS & INTERFACING LABTW 50 20 35 P 08. DIGITAL ELECTRONICS LABORATORY TW 50 20 24 P C 50 20 21 P 09. DIGITAL ELECTRONICS LABORATORY PR 19. MICROPROCESSORS & INTERFACING LABPR 10. SOFT SKILLS TW 50 20 33 P C 20. DATA STRUCTURES LABORATORY 50 20 36 P TW PR 50 20 34 P 21. DATA STRUCTURES LABORATORY GRAND TOTAL = 972/1500, RESULT: FIRST CLASS ORDN. 1 MARKS: S8054285 MAHAJAN ABHIJEET SHIVDAS PRABHAVATI , s8054285 01. DISCRETE STRUCTURES 60 P C 11. ENGINEERING MATHEMATICS III PP 100 40 40 P PP 100 40 19 F 02. PROGRAMMING & PROBLEM SOLVING PP 100 40 40 P C 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 13. DATA STRUCTURES 40 100 40 59 P 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 44 P C PP 40 100 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 P C 14. COMPUTER GRAPHICS PP 40 44 P 40 40 P C 100 05. HUMANITIES AND SOCIAL SCIENCE PP 100 15. COMPUTER ORGANIZATION 40 25 10 20 P C 50 20 29 P 06. PROGRAMMING LABORATORY 16. O. O. PROG. & COMP. GRAPH. LAB TW TW 50 20 27 P C 20 21 P 07. PROGRAMMING LABORATORY PR 17. O. O. PROG. & COMP. GRAPH. LAB PR 50 25 10 16 P C 08. DIGITAL ELECTRONICS LABORATORY TW 18. MICROPROCESSORS & INTERFACING LABTW 50 20 30 P 50 20 28 P 09. DIGITAL ELECTRONICS LABORATORY PR 50 20 30 P C 19. MICROPROCESSORS & INTERFACING LABPR 10. SOFT SKILLS 50 20 30 P C 20. DATA STRUCTURES LABORATORY 50 20 35 P TW TW PR 50 20 25 P 21. DATA STRUCTURES LABORATORY GRAND TOTAL = 721/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS: , 71239301D , , PICT , s8054286 S8054286 MARATHE SONALI ASHOK ARUNA 01. DISCRETE STRUCTURES PP 100 40 41 P C 11. ENGINEERING MATHEMATICS III PP 100 40 46 P 02. PROGRAMMING & PROBLEM SOLVING PP 100 40 64 P C 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 40 P 100 40 52 P C PP 100 40 64 P 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 13. DATA STRUCTURES 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 49 P C 14. COMPUTER GRAPHICS 100 40 54 P PP 100 40 41 P C 15. COMPUTER ORGANIZATION 100 40 44 P 05. HUMANITIES AND SOCIAL SCIENCE PP PP 22 P C 06. PROGRAMMING LABORATORY 25 10 50 20 42 P TW 16. O. O. PROG. & COMP. GRAPH. LAB TW 07. PROGRAMMING LABORATORY 50 20 22 P C 17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 35 P PR 50 20 08. DIGITAL ELECTRONICS LABORATORY TW 25 10 23 P C 18. MICROPROCESSORS & INTERFACING LABTW 44 P 50 20 46 P C 09. DIGITAL ELECTRONICS LABORATORY PR 50 20 32 P 19. MICROPROCESSORS & INTERFACING LABPR 10. SOFT SKILLS 50 20 45 P C TW 50 20 40 P TW 20. DATA STRUCTURES LABORATORY 21. DATA STRUCTURES LABORATORY PR 50 20 28 P GRAND TOTAL = 874/1500, RESULT: HIGHER SECOND CLASS ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER S8054287 MARUDA PRAVIN POPATLAL NANDA , s8054287 01. DISCRETE STRUCTURES 11. ENGINEERING MATHEMATICS III PP 100 40 40 P PP 100 40 48 P 40 65 P C 40 P 02. PROGRAMMING & PROBLEM SOLVING PP 100 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 100 40 40 P C PP 100 40 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 13. DATA STRUCTURES 66 P 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 42 P C PP 100 40 55 P 14. COMPUTER GRAPHICS 05. HUMANITIES AND SOCIAL SCIENCE PP 100 40 56 P C 15. COMPUTER ORGANIZATION 100 40 53 P 25 10 21 P C 50 20 42 P 06. PROGRAMMING LABORATORY 16. O. O. PROG. & COMP. GRAPH. LAB TW TW 50 20 25 P 07. PROGRAMMING LABORATORY 50 20 21 P PR 17. O. O. PROG. & COMP. GRAPH. LAB PR 25 10 17 P C 50 20 42 P 08. DIGITAL ELECTRONICS LABORATORY TW 18. MICROPROCESSORS & INTERFACING LABTW 50 20 37 P 50 20 08 F 09. DIGITAL ELECTRONICS LABORATORY PR 19. MICROPROCESSORS & INTERFACING LABPR 10. SOFT SKILLS TW 50 20 30 P C 20. DATA STRUCTURES LABORATORY 50 20 43 P TW PR 50 20 37 P 21. DATA STRUCTURES LABORATORY GRAND TOTAL = 828/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS : , 71100894к , , , ріст S8054288 MEHTA MIHIR ARPIT FALGUNI , s8054288 11. ENGINEERING MATHEMATICS III PP 100 40 82 P 01. DISCRETE STRUCTURES PP 100 40 48 P C 02. PROGRAMMING & PROBLEM SOLVING PP 100 40 50 P C 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 51 P 40 100 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 64 P C 13. DATA STRUCTURES PP 40 62 P 100 57 P 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 70 P C 14. COMPUTER GRAPHICS PP 40 05. HUMANITIES AND SOCIAL SCIENCE PP 100 40 48 P C 15. COMPUTER ORGANIZATION 100 40 58 P 25 10 20 06. PROGRAMMING LABORATORY 19 P C 16. O. O. PROG. & COMP. GRAPH. LAB TW 50 41 P TW 50 20 41 P C 20 39 P 07. PROGRAMMING LABORATORY PR 17. O. O. PROG. & COMP. GRAPH. LAB PR 50 25 10 21 P C 08. DIGITAL ELECTRONICS LABORATORY TW 18. MICROPROCESSORS & INTERFACING LABTW 50 20 38 P 50 20 40 P 09. DIGITAL ELECTRONICS LABORATORY PR 50 20 41 P C 19. MICROPROCESSORS & INTERFACING LABPR 10. SOFT SKILLS 50 20 38 P C 20. DATA STRUCTURES LABORATORY 50 20 38 P TW TW PR 50 20 39 P 21. DATA STRUCTURES LABORATORY GRAND TOTAL = 985/1500, RESULT: FIRST CLASS ORDN. 1 MARKS: , 71100896F , , PICT , s8054289 S8054289 MISTRY HASIT NIPUN SONAL 01. DISCRETE STRUCTURES PP 100 40 40 P C 11. ENGINEERING MATHEMATICS III PP 100 40 40 P 02. PROGRAMMING & PROBLEM SOLVING PP 100 40 52 P C 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 40 P 100 40 40 P C PP 100 40 52 P 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 13. DATA STRUCTURES 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 45 P C 14. COMPUTER GRAPHICS 100 40 43 P PP 100 40 45 P C 15. COMPUTER ORGANIZATION 100 40 41 P 05. HUMANITIES AND SOCIAL SCIENCE PP PP 32 P 06. PROGRAMMING LABORATORY 25 10 23 P C 50 20 TW 16. O. O. PROG. & COMP. GRAPH. LAB TW 07. PROGRAMMING LABORATORY 50 20 36 P C 17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 32 P PR 50 20 08. DIGITAL ELECTRONICS LABORATORY TW 25 10 18 P C 18. MICROPROCESSORS & INTERFACING LABTW 40 P 50 20 36 P C 09. DIGITAL ELECTRONICS LABORATORY PR 50 20 31 P 19. MICROPROCESSORS & INTERFACING LABPR 10. SOFT SKILLS 50 20 34 P C TW 50 20 33 P TW 20. DATA STRUCTURES LABORATORY 21. DATA STRUCTURES LABORATORY PR 50 20 30 P GRAND TOTAL = 783/1500, RESULT: SECOND CLASS ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER S8054293 NAGRE KARTIK ATUL MANISHA , s8054293 01. DISCRETE STRUCTURES 11. ENGINEERING MATHEMATICS III PP 100 40 57 P PP 100 40 41 P C 02. PROGRAMMING & PROBLEM SOLVING PP 100 40 62 P C 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 100 40 65 P C PP 100 40 57 P 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 13. DATA STRUCTURES 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 60 P C PP 100 40 57 P 14. COMPUTER GRAPHICS 05. HUMANITIES AND SOCIAL SCIENCE PP 100 40 43 P C 15. COMPUTER ORGANIZATION 100 40 25 10 19 P C 50 20 29 P 06. PROGRAMMING LABORATORY 16. O. O. PROG. & COMP. GRAPH. LAB TW TW 07. PROGRAMMING LABORATORY 50 20 28 P C 50 20 40 P PR 17. O. O. PROG. & COMP. GRAPH. LAB PR 25 10 17 P C 18. MICROPROCESSORS & INTERFACING LABTW 50 20 35 P 08. DIGITAL ELECTRONICS LABORATORY TW 50 20 33 P C 50 20 33 P 09. DIGITAL ELECTRONICS LABORATORY PR 19. MICROPROCESSORS & INTERFACING LABPR 20. DATA STRUCTURES LABORATORY 10. SOFT SKILLS TW 50 20 35 P C TW 50 20 35 P PR 50 20 35 P 21. DATA STRUCTURES LABORATORY GRAND TOTAL = 892+08/1500, RESULT: FIRST CLASS [0.2] ORDN. 1 MARKS : , 71100908C , , PICT S8054294 NAIK SANDESH TARSING KAMAL , s8054294 01. DISCRETE STRUCTURES 11. ENGINEERING MATHEMATICS III PP 100 40 45 P PP 100 40 40 P C 02. PROGRAMMING & PROBLEM SOLVING PP 100 40 60 P C 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 40 P 13. DATA STRUCTURES 40 49 P 100 40 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 PP 59 P 100 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 63 P C 14. COMPUTER GRAPHICS PP 40 43 P 100 05. HUMANITIES AND SOCIAL SCIENCE PP 100 40 43 P C 15. COMPUTER ORGANIZATION 40 47 P 25 10 50 20 27 P 06. PROGRAMMING LABORATORY 19 P C 16. O. O. PROG. & COMP. GRAPH. LAB TW TW 50 20 25 P C 20 07. PROGRAMMING LABORATORY PR 17. O. O. PROG. & COMP. GRAPH. LAB PR 50 38 P 25 10 15 P C 08. DIGITAL ELECTRONICS LABORATORY TW 18. MICROPROCESSORS & INTERFACING LABTW 50 20 27 P 50 20 23 P 09. DIGITAL ELECTRONICS LABORATORY PR 50 20 24 P C 19. MICROPROCESSORS & INTERFACING LABPR 10. SOFT SKILLS TW 50 20 32 P C 20. DATA STRUCTURES LABORATORY 50 20 36 P TW PR 50 20 25 P 21. DATA STRUCTURES LABORATORY GRAND TOTAL = 780/1500, RESULT: SECOND CLASS ORDN. 1 MARKS: , 71100913K , , PICT , s8054295 S8054295 NARGUNDE TUSHAR VYANKATESH ANJALI 01. DISCRETE STRUCTURES PP 100 40 71 P C 11. ENGINEERING MATHEMATICS III PP 100 40 81 P 02. PROGRAMMING & PROBLEM SOLVING PP 100 40 57 P C 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 44 P 100 40 50 P C PP 100 40 62 P 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 13. DATA STRUCTURES 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 71 P C 14. COMPUTER GRAPHICS 100 40 58 P PP 05. HUMANITIES AND SOCIAL SCIENCE PP 100 40 54 P C 15. COMPUTER ORGANIZATION 100 40 61 P PP 25 10 22 P C 38 P 06. PROGRAMMING LABORATORY 50 20 TW 16. O. O. PROG. & COMP. GRAPH. LAB TW 07. PROGRAMMING LABORATORY 50 20 38 P C 17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 38 P PR 08. DIGITAL ELECTRONICS LABORATORY TW 25 10 22 P C 18. MICROPROCESSORS & INTERFACING LABTW 50 20 41 P 09. DIGITAL ELECTRONICS LABORATORY PR 50 20 36 P C 50 20 42 P 19. MICROPROCESSORS & INTERFACING LABPR 10. SOFT SKILLS 50 20 38 P C TW 50 20 39 P TW 20. DATA STRUCTURES LABORATORY 21. DATA STRUCTURES LABORATORY PR 50 20 43 P GRAND TOTAL = 1006/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71100947D , , PICT , s8054305 S8054305 PAWAR ABHIJEET SADANAND RENUKA 01. DISCRETE STRUCTURES 11. ENGINEERING MATHEMATICS III PP 100 40 72 P PP 100 40 57 P C 40 57 P C 40 P 02. PROGRAMMING & PROBLEM SOLVING PP 100 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 100 40 68 P C 100 40 60 P 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 13. DATA STRUCTURES PP 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 67 P C PP 100 40 62 P 14. COMPUTER GRAPHICS 05. HUMANITIES AND SOCIAL SCIENCE PP 100 40 40 P C 15. COMPUTER ORGANIZATION 100 40 56 P 25 10 23 P C 50 20 41 P 06. PROGRAMMING LABORATORY 16. O. O. PROG. & COMP. GRAPH. LAB TW TW 07. PROGRAMMING LABORATORY 50 20 44 P C 50 20 43 P PR 17. O. O. PROG. & COMP. GRAPH. LAB PR 25 10 22 P C 50 20 42 P 08. DIGITAL ELECTRONICS LABORATORY TW 18. MICROPROCESSORS & INTERFACING LABTW 50 20 35 P C 50 20 36 P 09. DIGITAL ELECTRONICS LABORATORY PR 19. MICROPROCESSORS & INTERFACING LABPR 10. SOFT SKILLS TW 50 20 40 P C 20. DATA STRUCTURES LABORATORY 50 20 42 P TW PR 50 20 38 P 21. DATA STRUCTURES LABORATORY GRAND TOTAL = 985/1500, RESULT: FIRST CLASS ORDN. 1 MARKS: S8054306 PAWAR NAYAN SUNIL NALINI , s8054306 PP 100 40 40 P C 11. ENGINEERING MATHEMATICS III PP 100 40 21 F 01. DISCRETE STRUCTURES 02. PROGRAMMING & PROBLEM SOLVING PP 100 40 65 P C 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 50 P 40 100 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 46 P.C 13. DATA STRUCTURES PP 40 58 P 55 P C 100 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 14. COMPUTER GRAPHICS PP 40 63 P 59 P C 05. HUMANITIES AND SOCIAL SCIENCE PP 100 40 15. COMPUTER ORGANIZATION 100 40 61 P 25 10 50 20 37 P 06. PROGRAMMING LABORATORY 18 P C 16. O. O. PROG. & COMP. GRAPH. LAB TW TW 50 20 30 P 20 37 P 07. PROGRAMMING LABORATORY PR 17. O. O. PROG. & COMP. GRAPH. LAB PR 50 25 10 19 P C 08. DIGITAL ELECTRONICS LABORATORY TW 18. MICROPROCESSORS & INTERFACING LABTW 50 20 43 P 50 20 23 P 09. DIGITAL ELECTRONICS LABORATORY PR 50 20 33 P C 19. MICROPROCESSORS & INTERFACING LABPR 10. SOFT SKILLS 50 20 30 P C 20. DATA STRUCTURES LABORATORY 50 20 36 P TW TW PR 50 20 05 F 21. DATA STRUCTURES LABORATORY GRAND TOTAL = 829/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS: , 71100950D , , PICT , s8054307 S8054307 PAWAR OM DILIP MADHURI 01. DISCRETE STRUCTURES PP 100 40 50 P C 11. ENGINEERING MATHEMATICS III PP 100 40 50 P 02. PROGRAMMING & PROBLEM SOLVING PP 100 40 69 P C 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 40 P 100 40 59 P C PP 100 40 54 P 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 13. DATA STRUCTURES 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 82 P C 14. COMPUTER GRAPHICS 100 40 56 P PP 100 40 54 P C 15. COMPUTER ORGANIZATION 100 40 47 P 05. HUMANITIES AND SOCIAL SCIENCE PP PP 06. PROGRAMMING LABORATORY 25 10 20 P C 50 20 37 P TW 16. O. O. PROG. & COMP. GRAPH. LAB TW 07. PROGRAMMING LABORATORY 50 20 38 P C 17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 40 P PR 50 20 08. DIGITAL ELECTRONICS LABORATORY TW 25 10 18 P C 18. MICROPROCESSORS & INTERFACING LABTW 41 P 50 20 40 P C 09. DIGITAL ELECTRONICS LABORATORY PR 50 20 40 P 19. MICROPROCESSORS & INTERFACING LABPR 10. SOFT SKILLS 50 20 36 P C TW 50 20 40 P TW 20. DATA STRUCTURES LABORATORY 21. DATA STRUCTURES LABORATORY PR 50 20 35 P GRAND TOTAL = 946/1500, RESULT: FIRST CLASS ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER S8054317 SAMRIDDH HADA MAHESH RUBY , s8054317 01. DISCRETE STRUCTURES 11. ENGINEERING MATHEMATICS III PP 100 40 40 P PP 100 40 40 P 40 44 P C 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 09 F 02. PROGRAMMING & PROBLEM SOLVING PP 100 100 40 43 P C 100 40 42 P 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 13. DATA STRUCTURES PP 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 44 P PP 100 40 50 P 14. COMPUTER GRAPHICS 05. HUMANITIES AND SOCIAL SCIENCE PP 100 40 47 P C 15. COMPUTER ORGANIZATION 100 40 30 F 25 10 18 P C 50 20 21 P 06. PROGRAMMING LABORATORY 16. O. O. PROG. & COMP. GRAPH. LAB TW TW 07. PROGRAMMING LABORATORY 50 20 20 P C 50 20 08 F PR 17. O. O. PROG. & COMP. GRAPH. LAB PR 25 10 15 P C 18. MICROPROCESSORS & INTERFACING LABTW 50 20 28 P 08. DIGITAL ELECTRONICS LABORATORY TW 50 20 30 P 50 20 02 F 09. DIGITAL ELECTRONICS LABORATORY PR 19. MICROPROCESSORS & INTERFACING LABPR TW 50 20 28 P C 50 20 25 P 10. SOFT SKILLS 20. DATA STRUCTURES LABORATORY TW PR 50 20 00 F 21. DATA STRUCTURES LABORATORY GRAND TOTAL = 584/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS : S8054318 SARAF HRISHIKESH SHRIRAM VRUSHALI , s8054318 01. DISCRETE STRUCTURES PP 100 40 70 P C 11. ENGINEERING MATHEMATICS III PP 100 40 58 P 02. PROGRAMMING & PROBLEM SOLVING PP 100 40 65 P C 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 44 P 40 100 40 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 67 P C 13. DATA STRUCTURES PP 57 P 40 71 PC 100 62 P 04. DATA STRUCTURES AND ALGORITHMS PP 100 14. COMPUTER GRAPHICS PP 40 59 P C 05. HUMANITIES AND SOCIAL SCIENCE PP 100 40 15. COMPUTER ORGANIZATION 100 40 56 P 25 10 23 P C 06. PROGRAMMING LABORATORY 16. O. O. PROG. & COMP. GRAPH. LAB TW 50 20 36 P TW 50 20 40 P C 20 07. PROGRAMMING LABORATORY PR 17. O. O. PROG. & COMP. GRAPH. LAB PR 50 40 P 25 10 18 P C 08. DIGITAL ELECTRONICS LABORATORY TW 18. MICROPROCESSORS & INTERFACING LABTW 50 20 36 P 50 20 09. DIGITAL ELECTRONICS LABORATORY PR 50 20 36 P C 19. MICROPROCESSORS & INTERFACING LABPR 35 P 10. SOFT SKILLS 50 20 35 P C 20. DATA STRUCTURES LABORATORY 50 20 41 P TW TW 21. DATA STRUCTURES LABORATORY PR 50 20 41 P GRAND TOTAL = 990/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS: , 71239306E , , PICT , s8054319 S8054319 SATHE DEEPASHREE CHANDRAKANT CHITRA 01. DISCRETE STRUCTURES 100 40 40 P C 11. ENGINEERING MATHEMATICS III PP 100 40 67 P 02. PROGRAMMING & PROBLEM SOLVING PP 100 40 60 P C 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 49 P 100 40 70 P C PP 100 40 68 P 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 13. DATA STRUCTURES 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 89 P C 14. COMPUTER GRAPHICS 100 40 66 P PΡ 05. HUMANITIES AND SOCIAL SCIENCE PP 100 40 51 P C 15. COMPUTER ORGANIZATION 100 40 58 P PP 25 10 24 P C 06. PROGRAMMING LABORATORY 50 20 44 P TW 16. O. O. PROG. & COMP. GRAPH. LAB TW 07. PROGRAMMING LABORATORY 50 20 44 P C 17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 44 P PR 50 20 08. DIGITAL ELECTRONICS LABORATORY TW 25 10 20 P C 18. MICROPROCESSORS & INTERFACING LABTW 46 P 09. DIGITAL ELECTRONICS LABORATORY PR 50 20 38 P C 50 20 40 P 19. MICROPROCESSORS & INTERFACING LABPR 10. SOFT SKILLS 50 20 39 P C TW 50 20 44 P TW 20. DATA STRUCTURES LABORATORY 21. DATA STRUCTURES LABORATORY PR 50 20 39 P GRAND TOTAL = 1040/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER VIJAYASHANTHI , 71100990C , , PICT , s8054321 S8054321 SATYANARAYAN RAJAGOPALAN 01. DISCRETE STRUCTURES PP 100 40 45 P C 11. ENGINEERING MATHEMATICS III PP 100 40 58 P 40 44 P C 02. PROGRAMMING & PROBLEM SOLVING PP 100 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 100 40 40 P C PP 100 40 45 P 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 13. DATA STRUCTURES 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 45 P C PP 100 40 14. COMPUTER GRAPHICS 41 P 05. HUMANITIES AND SOCIAL SCIENCE PP 100 40 46 P C 15. COMPUTER ORGANIZATION 100 40 40 P 25 10 17 P C 50 20 39 P 06. PROGRAMMING LABORATORY 16. O. O. PROG. & COMP. GRAPH. LAB TW TW 07. PROGRAMMING LABORATORY 50 20 25 P C 50 20 38 P PR 17. O. O. PROG. & COMP. GRAPH. LAB PR 25 10 18 P C 18. MICROPROCESSORS & INTERFACING LABTW 50 20 39 P 08. DIGITAL ELECTRONICS LABORATORY TW 50 20 31 P C 50 20 07 F 09. DIGITAL ELECTRONICS LABORATORY PR 19. MICROPROCESSORS & INTERFACING LABPR 10. SOFT SKILLS TW 50 20 33 P C 20. DATA STRUCTURES LABORATORY 50 20 35 P TW PR 50 20 33 P 21. DATA STRUCTURES LABORATORY GRAND TOTAL = 759/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS: , 71100991M , , , , , S8054322 SAVAJI ADITYA JAYANT SHRUTI , s8054322 01. DISCRETE STRUCTURES 11. ENGINEERING MATHEMATICS III PP 100 40 53 P PP 100 40 54 P C 02. PROGRAMMING & PROBLEM SOLVING PP 100 40 63 P C 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 40 P 13. DATA STRUCTURES 100 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 40 51 P C PP 40 60 P 100 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 66 P C 14. COMPUTER GRAPHICS PP 40 56 P 100 40 P 05. HUMANITIES AND SOCIAL SCIENCE PP 100 40 50 P C 15. COMPUTER ORGANIZATION 40 25 10 50 20 36 P 06. PROGRAMMING LABORATORY 21 P C 16. O. O. PROG. & COMP. GRAPH. LAB TW TW 50 20 22 P C 20 37 P 07. PROGRAMMING LABORATORY 17. O. O. PROG. & COMP. GRAPH. LAB PR 50 25 10 21 P C 08. DIGITAL ELECTRONICS LABORATORY TW 18. MICROPROCESSORS & INTERFACING LABTW 50 20 38 P 50 20 27 P 09. DIGITAL ELECTRONICS LABORATORY PR 50 20 39 P C 19. MICROPROCESSORS & INTERFACING LABPR 10. SOFT SKILLS 50 20 40 P C 20. DATA STRUCTURES LABORATORY 50 20 38 P TW TW 21. DATA STRUCTURES LABORATORY PR 50 20 P GRAND TOTAL = 872/1500, RESULT: HIGHER SECOND CLASS ORDN. 1 MARKS: , 71100992K , , PICT , S8054323 S8054323 SAWANT TEJASWINI NANASAHEB SUNITA 01. DISCRETE STRUCTURES PP 100 40 48 P 11. ENGINEERING MATHEMATICS III PP 100 40 40 P 02. PROGRAMMING & PROBLEM SOLVING PP 100 40 47 P C 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 21 F 100 40 40 P C PP 100 40 57 P 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 13. DATA STRUCTURES 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 40 P C 14. COMPUTER GRAPHICS 100 40 42 P PP 100 40 58 P C 15. COMPUTER ORGANIZATION 100 40 30 F 05. HUMANITIES AND SOCIAL SCIENCE PP PP 25 10 06. PROGRAMMING LABORATORY 19 P C 50 20 38 P TW 16. O. O. PROG. & COMP. GRAPH. LAB TW 07. PROGRAMMING LABORATORY 50 20 21 P 17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 34 P PR 50 20 08. DIGITAL ELECTRONICS LABORATORY TW 25 10 16 P C 18. MICROPROCESSORS & INTERFACING LABTW 38 P 50 20 29 P C 09. DIGITAL ELECTRONICS LABORATORY PR 50 20 05 F 19. MICROPROCESSORS & INTERFACING LABPR 10. SOFT SKILLS 50 20 29 P C TW 50 20 37 P TW 20. DATA STRUCTURES LABORATORY 21. DATA STRUCTURES LABORATORY PR 50 20 04 F GRAND TOTAL = 693/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS:

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NOTE: FIRST LINE : SEAT NO., NAME OTHER LINES: HEAD OF PASSING,				-	-		REG. NO., PREVIOUS SEAT NO., RKS OBTAINED, P/F:PASS/FAIL, C:		•			
S8054327 SHAH SAMKIT UPESHKUMAR					AGRUTI		, 71100996в ,				S8054	
01. DISCRETE STRUCTURES	PP	100	40	_	P C	11.	ENGINEERING MATHEMATICS III		100	40	54	
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40		P C		MICROPROC. & INTERFACING TECHNI		100	40	48	
03. DIGIT. ELECTRONICS & LOGIC DESI		100	40		P C		DATA STRUCTURES	PP	100	40	67	
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40		PC		COMPUTER GRAPHICS	PP	100	40	66	
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	57			COMPUTER ORGANIZATION	PP	100	40	51	
06. PROGRAMMING LABORATORY	TW	25	10	24	РC		O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	36	Р
07. PROGRAMMING LABORATORY	PR	50	20		PC		O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	44	P
08. DIGITAL ELECTRONICS LABORATORY		25	10	23			MICROPROCESSORS & INTERFACING L		50	20	44	
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20		P C		MICROPROCESSORS & INTERFACING L		50	20	44	
10. SOFT SKILLS	TW	50	20		PC		DATA STRUCTURES LABORATORY	TW	50	20	44	
201 5011 511225		30		.5			DATA STRUCTURES LABORATORY		50	20	40	
GRAND TOTAL = 1047/1500, RESULT: FIRS	T CLAS	SS WTTI	H DTS	TTNCT	LTON		DATA STRUCTURES EASON TON		30			•
ORDN. 1 MARKS :												
S8054328 SHELKANDE AKASH DAGADU					EETA		, 71101000F ,		 ICT		s8054	
01. DISCRETE STRUCTURES	PP	100	40	26	F	11.	ENGINEERING MATHEMATICS III	PP	100	40	15	F
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	40	Р	12.	MICROPROC. & INTERFACING TECHNI	Q.PP	100	40	17	F
03. DIGIT. ELECTRONICS & LOGIC DESI	GNPP	100	40	21	F	13.	DATA STRUCTURES	PP	100	40	23	F
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	43	РС	14.	COMPUTER GRAPHICS	PP	100	40	31	F
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	40	РС		COMPUTER ORGANIZATION	PP	100	40	26	F
06. PROGRAMMING LABORATORY	TW	25	10	17	РС		O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	26	Р
07. PROGRAMMING LABORATORY	PR	50	20	21	Р	17.	O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	08	F
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	17	РС	18.	MICROPROCESSORS & INTERFACING L	.ABTW	50	20	33	Р
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20				MICROPROCESSORS & INTERFACING L		50	20		F
10. SOFT SKILLS	TW	50			РС		DATA STRUCTURES LABORATORY		50	20	32	
							DATA STRUCTURES LABORATORY			20		
GRAND TOTAL = 498/1500, RESULT: FAIL	.S											
ORDN. 1 MARKS :												
S8054329 SHENOY ROHIT VEDVYAS					EENA		, 71101002в ,				S8054	
01. DISCRETE STRUCTURES	PP	100	40			11.	ENGINEERING MATHEMATICS III		100	40		
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40		РС		MICROPROC. & INTERFACING TECHNI			40		
03. DIGIT. ELECTRONICS & LOGIC DESI		100	40		PC		DATA STRUCTURES	PP	100	40		
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40		P C		COMPUTER GRAPHICS		100	40		
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40		P C		COMPUTER ORGANIZATION	PP	100	40		
06. PROGRAMMING LABORATORY		25	10		Р С		O. O. PROG. & COMP. GRAPH. LAB		50	20	33	
07. PROGRAMMING LABORATORY		50	20		PC		O. O. PROG. & COMP. GRAPH. LAB	PR	50	20		
08. DIGITAL ELECTRONICS LABORATORY		25	10		PC		MICROPROCESSORS & INTERFACING L		50	20		
09. DIGITAL ELECTRONICS LABORATORY		50	20		PC		MICROPROCESSORS & INTERFACING L		50	20		
10. SOFT SKILLS	TW	50	20		Р С		DATA STRUCTURES LABORATORY		50	20		
		50	_5	٥.	. •		DATA STRUCTURES LABORATORY			20		
GRAND TOTAL = 881/1500, RESULT: HIGH	IER SFO	כטאט כו	LASS			21.	Z STREET ENDOUGTORT	1 11	50	-0	<i>32</i>	•
ORDN. 1 MARKS :	5	-0.10	_, .55									
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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , s8054330 S8054330 SHIKHA AGARWAL RITA 11. ENGINEERING MATHEMATICS III PP 100 40 52 P 01. DISCRETE STRUCTURES PP 100 40 58 P C 02. PROGRAMMING & PROBLEM SOLVING PP 100 40 59 P C 40 P 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 100 40 46 P C PP 100 40 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 13. DATA STRUCTURES 66 P 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 69 P C PP 100 40 47 P 14. COMPUTER GRAPHICS 05. HUMANITIES AND SOCIAL SCIENCE PP 100 40 43 P C 15. COMPUTER ORGANIZATION 100 40 40 P 25 10 18 P C 50 20 30 P 06. PROGRAMMING LABORATORY 16. O. O. PROG. & COMP. GRAPH. LAB TW TW 07. PROGRAMMING LABORATORY 50 20 15 F 50 20 12 F PR 17. O. O. PROG. & COMP. GRAPH. LAB PR 25 10 17 P C 50 20 32 P 08. DIGITAL ELECTRONICS LABORATORY TW 18. MICROPROCESSORS & INTERFACING LABTW 50 20 27 P C 50 20 05 F 09. DIGITAL ELECTRONICS LABORATORY PR 19. MICROPROCESSORS & INTERFACING LABPR 10. SOFT SKILLS 50 20 30 P C 20. DATA STRUCTURES LABORATORY 50 20 28 P TW TW PR 50 20 03 F 21. DATA STRUCTURES LABORATORY GRAND TOTAL = 737/1500, RESULT: FAILS ORDN. 1 MARKS: , 71101007C , , PICT S8054331 SHINDE SHWETA SANJAY SNEHA , s8054331 01. DISCRETE STRUCTURES 11. ENGINEERING MATHEMATICS III PP 100 40 60 P PP 100 40 50 P C 71 P C 02. PROGRAMMING & PROBLEM SOLVING PP 100 40 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 42 P 13. DATA STRUCTURES 100 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 40 63 P C PP 40 71 P 82 P C 100 63 P 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 14. COMPUTER GRAPHICS PP 40 100 05. HUMANITIES AND SOCIAL SCIENCE PP 100 40 55 P C 15. COMPUTER ORGANIZATION 40 54 P 19 P C 50 20 39 P 06. PROGRAMMING LABORATORY 25 10 16. O. O. PROG. & COMP. GRAPH. LAB TW TW 50 20 32 P C 20 36 P 07. PROGRAMMING LABORATORY PR 17. O. O. PROG. & COMP. GRAPH. LAB PR 50 25 10 18 P C 08. DIGITAL ELECTRONICS LABORATORY TW 18. MICROPROCESSORS & INTERFACING LABTW 50 20 38 P 50 20 28 P 09. DIGITAL ELECTRONICS LABORATORY PR 50 20 44 P C 19. MICROPROCESSORS & INTERFACING LABPR 10. SOFT SKILLS 50 20 36 P C 20. DATA STRUCTURES LABORATORY 50 20 40 P TW TW PR 50 20 33 P 21. DATA STRUCTURES LABORATORY GRAND TOTAL = 974/1500, RESULT: FIRST CLASS ORDN. 1 MARKS : , 71101008M , , PICT , s8054332 S8054332 SHINDE VIRENDRA BIJWANT SEEMA 01. DISCRETE STRUCTURES PP 100 40 42 P C 11. ENGINEERING MATHEMATICS III PP 100 40 53 P 02. PROGRAMMING & PROBLEM SOLVING PP 100 40 62 P C 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 41 P 100 40 65 P C PP 100 40 68 P 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 13. DATA STRUCTURES 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 54 P C 14. COMPUTER GRAPHICS 100 40 57 P PP 100 40 50 P C 15. COMPUTER ORGANIZATION 100 40 47 P 05. HUMANITIES AND SOCIAL SCIENCE PP PP 06. PROGRAMMING LABORATORY 25 10 23 P C 50 20 40 P TW 16. O. O. PROG. & COMP. GRAPH. LAB TW 07. PROGRAMMING LABORATORY 50 20 35 P C 17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 34 P PR 50 20 08. DIGITAL ELECTRONICS LABORATORY TW 25 10 23 P C 18. MICROPROCESSORS & INTERFACING LABTW 45 P 50 20 39 P C 09. DIGITAL ELECTRONICS LABORATORY PR 50 20 33 P 19. MICROPROCESSORS & INTERFACING LABPR 10. SOFT SKILLS 50 20 40 P C TW 50 20 44 P TW 20. DATA STRUCTURES LABORATORY 21. DATA STRUCTURES LABORATORY PR 50 20 43 P GRAND TOTAL = 938/1500, RESULT: FIRST CLASS ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , s8054342 S8054342 YADAV PRAKHAR SUNILKUMAR CHHAYA 01. DISCRETE STRUCTURES 11. ENGINEERING MATHEMATICS III PP 100 40 AA F PP 100 40 40 P C 40 41 P C 02. PROGRAMMING & PROBLEM SOLVING PP 100 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 AA F 100 40 40 P C PP 100 40 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 13. DATA STRUCTURES ΔΔ F 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 PP 100 40 AA F AA F 14. COMPUTER GRAPHICS 05. HUMANITIES AND SOCIAL SCIENCE PP 100 40 55 P C 15. COMPUTER ORGANIZATION 100 40 AA F 25 10 15 P C 50 20 20 P 06. PROGRAMMING LABORATORY 16. O. O. PROG. & COMP. GRAPH. LAB TW TW 07. PROGRAMMING LABORATORY 50 20 10 F 50 20 20 P PR 17. O. O. PROG. & COMP. GRAPH. LAB PR 25 10 13 P C 50 20 21 P 08. DIGITAL ELECTRONICS LABORATORY TW 18. MICROPROCESSORS & INTERFACING LABTW 50 20 35 P C 50 20 01 F 09. DIGITAL ELECTRONICS LABORATORY PR 19. MICROPROCESSORS & INTERFACING LABPR 10. SOFT SKILLS 50 20 24 P C 20. DATA STRUCTURES LABORATORY 50 20 20 P TW TW PR 50 20 00 F 21. DATA STRUCTURES LABORATORY GRAND TOTAL = 355/1500, RESULT: FAILS ORDN. 1 MARKS: S8054343 YARDI JUHI PRAKASH PRIYA , s8054343 01. DISCRETE STRUCTURES 11. ENGINEERING MATHEMATICS III PP 100 40 89 P PP 100 40 63 P C 02. PROGRAMMING & PROBLEM SOLVING PP 100 40 63 P C 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 50 P 13. DATA STRUCTURES 100 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 40 76 P.C PP 40 60 P 75 P C 100 67 P 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 14. COMPUTER GRAPHICS PP 40 40 100 05. HUMANITIES AND SOCIAL SCIENCE PP 100 60 P C 15. COMPUTER ORGANIZATION 40 60 P 25 10 50 20 43 P 06. PROGRAMMING LABORATORY 24 P C 16. O. O. PROG. & COMP. GRAPH. LAB TW TW 50 20 20 07. PROGRAMMING LABORATORY PR 46 P C 17. O. O. PROG. & COMP. GRAPH. LAB PR 50 45 P 25 10 24 P C 08. DIGITAL ELECTRONICS LABORATORY TW 18. MICROPROCESSORS & INTERFACING LABTW 50 20 45 P 50 20 09. DIGITAL ELECTRONICS LABORATORY PR 50 20 46 P C 19. MICROPROCESSORS & INTERFACING LABPR 38 P 10. SOFT SKILLS 50 20 43 P C 20. DATA STRUCTURES LABORATORY 50 20 46 P TW TW PR 50 20 45 P 21. DATA STRUCTURES LABORATORY GRAND TOTAL = 1108/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS: , 71101062F , , PICT , s8054344 S8054344 YEMUL SANDEEP PRAKASH BHULAXMI 01. DISCRETE STRUCTURES PP 100 40 50 P C 11. ENGINEERING MATHEMATICS III PP 100 40 57 P 02. PROGRAMMING & PROBLEM SOLVING PP 100 40 55 P C 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 40 P 100 40 55 P C PP 100 40 53 P 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 13. DATA STRUCTURES 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 58 P C 14. COMPUTER GRAPHICS 100 40 53 P PP 100 40 43 P C 15. COMPUTER ORGANIZATION 100 40 53 P 05. HUMANITIES AND SOCIAL SCIENCE PP PP 36 P 06. PROGRAMMING LABORATORY 25 10 22 P C 50 20 TW 16. O. O. PROG. & COMP. GRAPH. LAB TW 07. PROGRAMMING LABORATORY 50 20 35 P C 17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 36 P PR 50 20 08. DIGITAL ELECTRONICS LABORATORY TW 25 10 20 P C 18. MICROPROCESSORS & INTERFACING LABTW 37 P 09. DIGITAL ELECTRONICS LABORATORY PR 50 20 35 P C 50 20 35 P 19. MICROPROCESSORS & INTERFACING LABPR 10. SOFT SKILLS 50 20 34 P C TW 50 20 34 P TW 20. DATA STRUCTURES LABORATORY 21. DATA STRUCTURES LABORATORY PR 50 20 35 P GRAND TOTAL = 876/1500, RESULT: HIGHER SECOND CLASS ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71101064в , , , РІСТ RANJANA , s8054345 S8054345 YEREKAR KARISHMA TUKARAM 01. DISCRETE STRUCTURES PP 100 40 60 P 11. ENGINEERING MATHEMATICS III PP 100 40 63 P 40 47 P C 54 P 02. PROGRAMMING & PROBLEM SOLVING PP 100 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 100 40 59 P C PP 100 40 55 P 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 13. DATA STRUCTURES 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 61 P C PP 100 40 60 P 14. COMPUTER GRAPHICS 05. HUMANITIES AND SOCIAL SCIENCE PP 100 40 55 P C 15. COMPUTER ORGANIZATION 100 40 25 10 17 P C 50 20 38 P 06. PROGRAMMING LABORATORY 16. O. O. PROG. & COMP. GRAPH. LAB TW TW 07. PROGRAMMING LABORATORY 50 20 39 P C 50 20 30 P PR 17. O. O. PROG. & COMP. GRAPH. LAB PR 25 10 17 P C 18. MICROPROCESSORS & INTERFACING LABTW 50 20 33 P 08. DIGITAL ELECTRONICS LABORATORY TW 50 20 25 P C 50 20 06 F 09. DIGITAL ELECTRONICS LABORATORY PR 19. MICROPROCESSORS & INTERFACING LABPR 10. SOFT SKILLS TW 50 20 46 P C 20. DATA STRUCTURES LABORATORY 50 20 33 P TW PR 50 20 31 P 21. DATA STRUCTURES LABORATORY GRAND TOTAL = 892/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS: S8054346 YEWALE SHUBHANGI KAILAS MANISHA , s8054346 01. DISCRETE STRUCTURES 40 P 11. ENGINEERING MATHEMATICS III PP 100 40 17 F PP 100 40 02. PROGRAMMING & PROBLEM SOLVING PP 100 40 41 P C 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 15 F 13. DATA STRUCTURES 40 40 P C 100 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 PP 40 40 P 40 100 40 22 F 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 P C 14. COMPUTER GRAPHICS PP 40 49 P C 05. HUMANITIES AND SOCIAL SCIENCE PP 100 15. COMPUTER ORGANIZATION 100 40 24 F 25 10 22 P C 50 20 33 P 06. PROGRAMMING LABORATORY 16. O. O. PROG. & COMP. GRAPH. LAB TW TW 50 20 20 P C 20 12 F 07. PROGRAMMING LABORATORY PR 17. O. O. PROG. & COMP. GRAPH. LAB PR 50 25 10 21 P C 08. DIGITAL ELECTRONICS LABORATORY TW 18. MICROPROCESSORS & INTERFACING LABTW 50 20 39 P 50 20 04 F 09. DIGITAL ELECTRONICS LABORATORY PR 50 20 30 P C 19. MICROPROCESSORS & INTERFACING LABPR 10. SOFT SKILLS 50 20 37 P C 20. DATA STRUCTURES LABORATORY 50 20 36 P TW TW PR 50 20 10 F 21. DATA STRUCTURES LABORATORY GRAND TOTAL = 592/1500, RESULT: FAILS ORDN. 1 MARKS: , 71132426D , , PICT , s8054352 S8054352 BHOSALE SHITAL MANOHAR JAYA 01. DISCRETE STRUCTURES PP 100 40 40 P C 11. ENGINEERING MATHEMATICS III PP 100 40 25 F 02. PROGRAMMING & PROBLEM SOLVING PP 100 40 44 P C 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 40 P C 100 40 41 P C PP 100 40 40 P C 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 13. DATA STRUCTURES 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 47 P C 14. COMPUTER GRAPHICS 100 40 46 P.C. PP 100 40 40 P C 15. COMPUTER ORGANIZATION 100 40 40 P C 05. HUMANITIES AND SOCIAL SCIENCE PP PP 06. PROGRAMMING LABORATORY 25 10 17 P C 50 20 39 P C TW 16. O. O. PROG. & COMP. GRAPH. LAB TW 07. PROGRAMMING LABORATORY 20 25 P C 17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 20 P C PR 50 08. DIGITAL ELECTRONICS LABORATORY TW 25 10 19 P C 18. MICROPROCESSORS & INTERFACING LABTW 50 20 34 P C 50 20 20 P C 09. DIGITAL ELECTRONICS LABORATORY PR 50 20 28 P C 19. MICROPROCESSORS & INTERFACING LABPR 10. SOFT SKILLS 50 20 34 P C TW 50 20 32 P C TW 20. DATA STRUCTURES LABORATORY 21. DATA STRUCTURES LABORATORY PR 50 20 20 P C GRAND TOTAL = 691/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS:

DATE: 14 AUG. 2012 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 49 (472)

NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , s8054356 S8054356 DURUGKAR SURAJ ANIL NIRMALA 01. DISCRETE STRUCTURES 11. ENGINEERING MATHEMATICS III PP 100 40 61 P C PP 100 40 40 P C 40 45 P C 02. PROGRAMMING & PROBLEM SOLVING PP 100 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 100 40 44 P C 100 40 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 13. DATA STRUCTURES PP 40 P C 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 70 P C PP 100 40 58 P C 14. COMPUTER GRAPHICS 05. HUMANITIES AND SOCIAL SCIENCE PP 100 40 40 P C 15. COMPUTER ORGANIZATION 100 40 46 P.C 25 10 11 PC 50 20 22 P C 06. PROGRAMMING LABORATORY 16. O. O. PROG. & COMP. GRAPH. LAB TW TW 07. PROGRAMMING LABORATORY 50 20 34 P C 20 26 P C PR 17. O. O. PROG. & COMP. GRAPH. LAB PR 50 25 10 14 P C 18. MICROPROCESSORS & INTERFACING LABTW 50 20 21 P C 08. DIGITAL ELECTRONICS LABORATORY TW 50 20 20 P C 50 20 24 P 09. DIGITAL ELECTRONICS LABORATORY PR 19. MICROPROCESSORS & INTERFACING LABPR 10. SOFT SKILLS TW 50 20 26 P C 20. DATA STRUCTURES LABORATORY 50 20 21 P C TW PR 50 20 20 P 21. DATA STRUCTURES LABORATORY GRAND TOTAL = 691/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS: S8054357 GADODIA ANUSHREE RAJENDRA **GEETA** , s8054357 01. DISCRETE STRUCTURES PP 100 40 40 P C 11. ENGINEERING MATHEMATICS III PP 100 40 40 PC 02. PROGRAMMING & PROBLEM SOLVING PP 100 40 40 P C 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 42 P C 13. DATA STRUCTURES 40 100 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 49 P C PP 40 49 P C 100 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 43 P C 14. COMPUTER GRAPHICS PP 40 49 P C 100 05. HUMANITIES AND SOCIAL SCIENCE PP 100 40 43 P C 15. COMPUTER ORGANIZATION 40 41 P C 25 50 20 06. PROGRAMMING LABORATORY 10 19 P C 16. O. O. PROG. & COMP. GRAPH. LAB TW 36 P C TW 50 20 20 07. PROGRAMMING LABORATORY PR 30 P C 17. O. O. PROG. & COMP. GRAPH. LAB PR 50 28 P C 25 10 19 P C 08. DIGITAL ELECTRONICS LABORATORY TW 18. MICROPROCESSORS & INTERFACING LABTW 50 20 33 P C 09. DIGITAL ELECTRONICS LABORATORY PR 50 20 29 P C 19. MICROPROCESSORS & INTERFACING LABPR 50 20 20 P C 10. SOFT SKILLS 50 20 42 P C 20. DATA STRUCTURES LABORATORY 50 20 32 P C TW TW PR 50 20 23 P 21. DATA STRUCTURES LABORATORY GRAND TOTAL = 747+03/1500, RESULT: SECOND CLASS [0.2] ORDN. 1 MARKS : , s8054358 S8054358 GAIKWAD MAHESH EKNATH LATA 01. DISCRETE STRUCTURES PP 100 40 40 P C 11. ENGINEERING MATHEMATICS III PP 100 40 40 P C 02. PROGRAMMING & PROBLEM SOLVING PP 100 40 40 P C 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 40 P C 100 40 47 P C PP 100 40 45 P 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 13. DATA STRUCTURES 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 42 P C 14. COMPUTER GRAPHICS 100 40 46 P.C PP 100 40 40 P C 15. COMPUTER ORGANIZATION 100 40 40 P C 05. HUMANITIES AND SOCIAL SCIENCE PP PP 06. PROGRAMMING LABORATORY 25 10 14 P C 50 20 27 P C TW 16. O. O. PROG. & COMP. GRAPH. LAB TW 07. PROGRAMMING LABORATORY 50 20 26 P 17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 35 P PR 08. DIGITAL ELECTRONICS LABORATORY TW 25 10 16 P C 18. MICROPROCESSORS & INTERFACING LABTW 50 20 26 P C 50 20 20 P C 09. DIGITAL ELECTRONICS LABORATORY PR 50 20 22 P C 19. MICROPROCESSORS & INTERFACING LABPR 10. SOFT SKILLS 50 20 31 P C TW 50 20 26 P C TW 20. DATA STRUCTURES LABORATORY 21. DATA STRUCTURES LABORATORY PR 50 20 25 P C GRAND TOTAL = 688/1500, RESULT: PASS CLASS ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71132428L , , PICT SWATI , s8054359 S8054359 GOKHALE GAURAV SUHAS 01. DISCRETE STRUCTURES 11. ENGINEERING MATHEMATICS III PP 100 40 40 PC PP 100 40 40 P C 40 48 P C 42 P C 02. PROGRAMMING & PROBLEM SOLVING PP 100 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 100 40 40 P C 100 40 51 P 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 13. DATA STRUCTURES PP 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 66 P C PP 100 40 57 P C 14. COMPUTER GRAPHICS 05. HUMANITIES AND SOCIAL SCIENCE PP 100 40 58 P C 15. COMPUTER ORGANIZATION 100 40 50 P.C 25 10 16 P C 50 20 24 P C 06. PROGRAMMING LABORATORY 16. O. O. PROG. & COMP. GRAPH. LAB TW TW 07. PROGRAMMING LABORATORY 50 20 22 P C 20 22 P C PR 17. O. O. PROG. & COMP. GRAPH. LAB PR 50 25 10 18 P C 18. MICROPROCESSORS & INTERFACING LABTW 20 20 P C 08. DIGITAL ELECTRONICS LABORATORY TW 50 50 20 27 P C 50 20 20 P C 09. DIGITAL ELECTRONICS LABORATORY PR 19. MICROPROCESSORS & INTERFACING LABPR 10. SOFT SKILLS 50 20 29 P C 20. DATA STRUCTURES LABORATORY 50 20 20 P C TW TW PR 50 20 27 P C 21. DATA STRUCTURES LABORATORY GRAND TOTAL = 737/1500, RESULT: PASS CLASS ORDN. 1 MARKS: S8054361 JADHAV PRASHANT UTTAM ANITA , s8054361 01. DISCRETE STRUCTURES 40 P C 11. ENGINEERING MATHEMATICS III PP 100 40 40 PC PP 100 40 02. PROGRAMMING & PROBLEM SOLVING PP 100 40 40 P C 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 40 P 40 40 P C 100 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 13. DATA STRUCTURES PP 40 40 P C 100 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 53 P C 14. COMPUTER GRAPHICS PP 40 49 P C 100 05. HUMANITIES AND SOCIAL SCIENCE PP 100 40 40 P C 15. COMPUTER ORGANIZATION 40 40 P C 50 20 28 P C 06. PROGRAMMING LABORATORY 25 10 13 P C 16. O. O. PROG. & COMP. GRAPH. LAB TW TW 50 20 35 P C 20 07. PROGRAMMING LABORATORY 17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 P C 08. DIGITAL ELECTRONICS LABORATORY TW 25 10 11 P C 18. MICROPROCESSORS & INTERFACING LABTW 50 20 22 P C 09. DIGITAL ELECTRONICS LABORATORY PR 50 20 21 P C 19. MICROPROCESSORS & INTERFACING LABPR 50 20 26 P 10. SOFT SKILLS 50 20 30 P C 20. DATA STRUCTURES LABORATORY 50 20 21 P C TW TW PR 50 20 33 P C 21. DATA STRUCTURES LABORATORY GRAND TOTAL = 682/1500, RESULT: PASS CLASS ORDN. 1 MARKS: , 71045460M , , PICT , s8054364 S8054364 JAMES DADO ANIA 01. DISCRETE STRUCTURES PP 100 40 40 P C 11. ENGINEERING MATHEMATICS III PP 100 40 40 P C 02. PROGRAMMING & PROBLEM SOLVING PP 100 40 40 P C 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 40 P C 100 40 57 P C PP 100 40 47 P C 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 13. DATA STRUCTURES 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 65 P C 14. COMPUTER GRAPHICS 100 40 53 P C PΡ 05. HUMANITIES AND SOCIAL SCIENCE PP 100 40 64 P C 15. COMPUTER ORGANIZATION 100 40 47 P.C PP 06. PROGRAMMING LABORATORY 25 10 16 P C 50 20 28 P C TW 16. O. O. PROG. & COMP. GRAPH. LAB TW 07. PROGRAMMING LABORATORY 20 25 P C 17. O. O. PROG. & COMP. GRAPH. LAB PR 20 26 P C PR 50 50 50 20 26 P C 08. DIGITAL ELECTRONICS LABORATORY TW 25 10 16 P C 18. MICROPROCESSORS & INTERFACING LABTW 50 20 24 P C 09. DIGITAL ELECTRONICS LABORATORY PR 50 20 28 P C 19. MICROPROCESSORS & INTERFACING LABPR 10. SOFT SKILLS 50 20 37 P C 20. DATA STRUCTURES LABORATORY TW 50 20 22 P C TW 21. DATA STRUCTURES LABORATORY PR 50 20 24 P GRAND TOTAL = 765/1500, RESULT: SECOND CLASS ORDN. 1 MARKS:

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DATE . 14 AUG. 2012	CEN	IKE .	PUNE	TIV211	IUIE	OF COMPOSE	R TECHNOLOGY, PUNE.	PA	GE NO.	ЭТ	(4	74)
NOTE: FIRST LINE : SEAT NO., NAME				-	-				•			
OTHER LINES: HEAD OF PASSING,	MAX	. MARK	S, M	IN. P	PASS M	IARKS, MAR	KS OBTAINED, P/F:PASS/FAIL, C:	PREVI	OUS CA	RRY O	VER	
S8054367 KARAD NEHA DASHRATH				US	SHA		, 71045471G ,	, P			S8054	
01. DISCRETE STRUCTURES	PP	100	40	66	РС		ENGINEERING MATHEMATICS III		100	40	50	Р
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	46	РС	12.	MICROPROC. & INTERFACING TECHNIC	Q.PP	100	40	40	РС
03. DIGIT. ELECTRONICS & LOGIC DESI	GNPP	100	40	60	РС	13.	DATA STRUCTURES	PP	100	40	42	PС
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	57	РС	14.	COMPUTER GRAPHICS	PP	100	40	49	P C
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	40	РС	15.	COMPUTER ORGANIZATION	PP	100	40	50	P C
06. PROGRAMMING LABORATORY	TW	25	10	19	РС	16.	O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	33	PС
07. PROGRAMMING LABORATORY	PR	50	20	38	РС	17.	O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	33	P C
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	20	РС	18.	MICROPROCESSORS & INTERFACING L	ABTW	50	20	29	РС
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	21	РС	19.	MICROPROCESSORS & INTERFACING L	ABPR	50	20	21	P C
10. SOFT SKILLS	TW	50	20	38	РС	20.	DATA STRUCTURES LABORATORY	TW	50	20	26	РС
						21.	DATA STRUCTURES LABORATORY	PR	50	20	34	Р
GRAND TOTAL = $812/1500$, RESULT: SECO	ND CL	ASS						RES	ULT RE	SERVE	D FOR	BKL
ORDN. 1 MARKS :												
S8054368 KARANDE RAHUL MADAN				SU	JNITA		, 71045472E ,	, P	ICT	,	S8054	368
01. DISCRETE STRUCTURES	PP	100	40	49	РС	11.	ENGINEERING MATHEMATICS III	PP	100	40	40	РС
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	40	РС	12.	MICROPROC. & INTERFACING TECHNIC	Q.PP	100	40	09	F
03. DIGIT. ELECTRONICS & LOGIC DESI	GNPP	100	40	54	РС	13.	DATA STRUCTURES	PP	100	40	42	Р
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	44	РС	14.	COMPUTER GRAPHICS	PP	100	40	60	РС
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	40	РС	15.	COMPUTER ORGANIZATION	PP	100	40	41	РС
06. PROGRAMMING LABORATORY	TW	25	10	15	РС	16.	O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	24	РС
07. PROGRAMMING LABORATORY	PR	50	20	26	Р	17.	O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	35	Р
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	16	РС	18.	MICROPROCESSORS & INTERFACING L	ABTW	50	20	30	РС
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	24	РС	19.	MICROPROCESSORS & INTERFACING L.	ABPR	50	20	21	Р
10. SOFT SKILLS	TW	50	20	35	РС	20.	DATA STRUCTURES LABORATORY	TW	50	20	30	РС
						21.	DATA STRUCTURES LABORATORY	PR	50	20	27	Р
GRAND TOTAL = 702/1500, RESULT: FAIL	S A.T	.к.т.										
ORDN. 1 MARKS :												
S8054372 KHALID RAZA KHAN				AY	'ESHA		, 70925473D ,	, P	ICT	,	S8054	372
01. DISCRETE STRUCTURES	PP	100	40	40	РС	11.	ENGINEERING MATHEMATICS III	PP	100	40	09	F
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	40	РС	12.	MICROPROC. & INTERFACING TECHNIC	Q.PP	100	40	15	F
03. DIGIT. ELECTRONICS & LOGIC DESI	GNPP	100	40	40	Р	13.	DATA STRUCTURES	PP	100	40	29	F
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	40	РС	14.	COMPUTER GRAPHICS	PP	100	40	43	Р
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	42	Р	15.	COMPUTER ORGANIZATION	PP	100	40	40	Р
06. PROGRAMMING LABORATORY	TW	25	10	10	РС	16.	O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	20	Р
07. PROGRAMMING LABORATORY	PR	50	20	25	РС	17.	O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	07	F
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	10	РС	18.	MICROPROCESSORS & INTERFACING L	ABTW	50	20	20	Р
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	23	РС	19.	MICROPROCESSORS & INTERFACING L	ABPR	50	20	02	F
10. SOFT SKILLS	TW	50	20		РС		DATA STRUCTURES LABORATORY	TW	50	20	20	
							DATA STRUCTURES LABORATORY	PR	50	20	02	
GRAND TOTAL = 497/1500, RESULT: FAIL	.S							-	- -	-		
ORDN. 1 MARKS :	-											
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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71045501B , , PICT , S8054375 SARASWATHI S8054375 KUNTALWAD GNYANESH GAJJARAM 01. DISCRETE STRUCTURES PP 100 40 40 P C 11. ENGINEERING MATHEMATICS III PP 100 40 40 PC 51 P C 02. PROGRAMMING & PROBLEM SOLVING PP 100 40 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 100 40 48 P C 100 40 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 13. DATA STRUCTURES PP 41 P C 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 40 P C PP 100 40 57 P C 14. COMPUTER GRAPHICS 05. HUMANITIES AND SOCIAL SCIENCE PP 100 40 41 P C 15. COMPUTER ORGANIZATION 100 40 40 P.C 25 10 16 P C 50 20 22 P C 06. PROGRAMMING LABORATORY 16. O. O. PROG. & COMP. GRAPH. LAB TW TW 07. PROGRAMMING LABORATORY 50 20 20 P C 20 23 P C PR 17. O. O. PROG. & COMP. GRAPH. LAB PR 50 25 10 14 P C 18. MICROPROCESSORS & INTERFACING LABTW 50 20 25 P C 08. DIGITAL ELECTRONICS LABORATORY TW 50 20 30 P C 50 20 21 P 09. DIGITAL ELECTRONICS LABORATORY PR 19. MICROPROCESSORS & INTERFACING LABPR 10. SOFT SKILLS 50 20 38 P C 20. DATA STRUCTURES LABORATORY 50 20 34 P C TW TW PR 50 20 12 F 21. DATA STRUCTURES LABORATORY GRAND TOTAL = 693/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS : NGURTHANKIMI , 71045503J , PICT S8054376 LALHMANGAIHA , s8054376 11. ENGINEERING MATHEMATICS III PP 100 40 AA F 01. DISCRETE STRUCTURES 100 40 40 P C 02. PROGRAMMING & PROBLEM SOLVING PP 100 40 AA F 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 AA F 40 100 40 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 40 P C 13. DATA STRUCTURES PP AA F 100 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 AA F 14. COMPUTER GRAPHICS PP 40 AA F 05. HUMANITIES AND SOCIAL SCIENCE PP 100 40 45 P C 15. COMPUTER ORGANIZATION 100 40 AA F 50 20 20 P C 06. PROGRAMMING LABORATORY 25 10 14 P C 16. O. O. PROG. & COMP. GRAPH. LAB TW TW 50 20 26 P 20 07. PROGRAMMING LABORATORY 17. O. O. PROG. & COMP. GRAPH. LAB PR 50 AA F 08. DIGITAL ELECTRONICS LABORATORY TW 25 10 10 P C 18. MICROPROCESSORS & INTERFACING LABTW 50 20 20 P C 20 P C 50 20 02 F 09. DIGITAL ELECTRONICS LABORATORY PR 50 20 19. MICROPROCESSORS & INTERFACING LABPR 10. SOFT SKILLS 50 20 24 P C 20. DATA STRUCTURES LABORATORY 50 20 20 P C TW TW PR 50 20 AA F 21. DATA STRUCTURES LABORATORY GRAND TOTAL = 281/1500, RESULT: FAILS ORDN. 1 MARKS: , 71045540C , , PICT , s8054380 S8054380 NAJAN KAUSTUBH GORAKSHANATH MEERA 01. DISCRETE STRUCTURES 100 40 42 P C 11. ENGINEERING MATHEMATICS III PP 100 40 54 P C 02. PROGRAMMING & PROBLEM SOLVING PP 100 40 40 P C 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 40 P 100 40 47 P C PP 100 40 40 P C 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 13. DATA STRUCTURES 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 48 P C 14. COMPUTER GRAPHICS 100 40 41 P.C PΡ 05. HUMANITIES AND SOCIAL SCIENCE PP 100 40 42 P C 15. COMPUTER ORGANIZATION 100 40 40 P C PP 06. PROGRAMMING LABORATORY 25 10 17 P C 50 20 41 P C TW 16. O. O. PROG. & COMP. GRAPH. LAB TW 07. PROGRAMMING LABORATORY 20 30 P C 17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 30 P C PR 50 40 P C 08. DIGITAL ELECTRONICS LABORATORY TW 25 10 18 P C 18. MICROPROCESSORS & INTERFACING LABTW 50 20 50 20 20 P C 09. DIGITAL ELECTRONICS LABORATORY PR 50 20 36 P C 19. MICROPROCESSORS & INTERFACING LABPR 10. SOFT SKILLS 50 20 34 P C 20. DATA STRUCTURES LABORATORY TW 50 20 35 P C TW 21. DATA STRUCTURES LABORATORY PR 50 20 32 P C GRAND TOTAL = 767/1500, RESULT: SECOND CLASS ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER S8054382 PANDYA PARTH VIPUL , S8054382 ASHA 01. DISCRETE STRUCTURES 11. ENGINEERING MATHEMATICS III PP 100 40 40 PC PP 100 40 51 P C 40 40 P C 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 02. PROGRAMMING & PROBLEM SOLVING PP 100 100 40 40 P C 100 40 40 P C 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 13. DATA STRUCTURES PP 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 46 P C PP 100 40 58 P C 14. COMPUTER GRAPHICS 05. HUMANITIES AND SOCIAL SCIENCE PP 100 40 49 P C 15. COMPUTER ORGANIZATION 100 40 50 P.C 25 10 19 P C 50 20 33 P C 06. PROGRAMMING LABORATORY 16. O. O. PROG. & COMP. GRAPH. LAB TW TW 07. PROGRAMMING LABORATORY 50 20 22 P C 20 20 P C PR 17. O. O. PROG. & COMP. GRAPH. LAB PR 50 25 10 16 P C 18. MICROPROCESSORS & INTERFACING LABTW 20 27 P C 08. DIGITAL ELECTRONICS LABORATORY TW 50 50 20 21 P C 50 20 21 P C 09. DIGITAL ELECTRONICS LABORATORY PR 19. MICROPROCESSORS & INTERFACING LABPR 10. SOFT SKILLS 50 20 34 P C 20. DATA STRUCTURES LABORATORY 50 20 33 P C TW TW PR 50 20 20 P 21. DATA STRUCTURES LABORATORY GRAND TOTAL = 720/1500, RESULT: PASS CLASS ORDN. 1 MARKS: , 70925566н , , ріст S8054386 ROHINI POORVI ARVIND DHARWAD , s8054386 01. DISCRETE STRUCTURES 11. ENGINEERING MATHEMATICS III PP 100 40 44 P PP 100 40 40 P C 02. PROGRAMMING & PROBLEM SOLVING PP 100 40 40 P C 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 40 P C 40 40 P C 100 40 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 13. DATA STRUCTURES PP 40 P C 40 100 04. DATA STRUCTURES AND ALGORITHMS PP 100 41 P C 14. COMPUTER GRAPHICS PP 40 40 P C 40 40 P C 100 05. HUMANITIES AND SOCIAL SCIENCE PP 100 15. COMPUTER ORGANIZATION 40 41 P C 50 20 06. PROGRAMMING LABORATORY 25 10 14 P C 16. O. O. PROG. & COMP. GRAPH. LAB TW 23 P C TW 50 20 28 P C 20 07. PROGRAMMING LABORATORY PR 17. O. O. PROG. & COMP. GRAPH. LAB PR 50 24 P C 25 10 19 P C 08. DIGITAL ELECTRONICS LABORATORY TW 18. MICROPROCESSORS & INTERFACING LABTW 50 20 25 P C 09. DIGITAL ELECTRONICS LABORATORY PR 50 20 26 P C 19. MICROPROCESSORS & INTERFACING LABPR 50 20 30 P C 10. SOFT SKILLS 50 20 32 P C 20. DATA STRUCTURES LABORATORY 50 20 22 P C TW TW 50 20 12 F 21. DATA STRUCTURES LABORATORY PR GRAND TOTAL = 661/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS: , s8054387 S8054387 RAWALE SUHAS SUDAMRAO RATNMALA 01. DISCRETE STRUCTURES PP 100 40 49 P C 11. ENGINEERING MATHEMATICS III PP 100 40 63 P 02. PROGRAMMING & PROBLEM SOLVING PP 100 40 52 P C 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 40 P C 100 40 54 P C PP 100 40 49 P C 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 13. DATA STRUCTURES 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 53 P C 14. COMPUTER GRAPHICS 100 40 40 P.C PP 100 40 40 P C 15. COMPUTER ORGANIZATION 100 40 44 P C 05. HUMANITIES AND SOCIAL SCIENCE PP PP 06. PROGRAMMING LABORATORY 25 10 20 P C 50 20 32 P C TW 16. O. O. PROG. & COMP. GRAPH. LAB TW 07. PROGRAMMING LABORATORY 50 20 24 P C 17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 29 P C PR 32 P C 08. DIGITAL ELECTRONICS LABORATORY TW 25 10 19 P C 18. MICROPROCESSORS & INTERFACING LABTW 50 20 50 20 22 P C 09. DIGITAL ELECTRONICS LABORATORY PR 50 20 28 P C 19. MICROPROCESSORS & INTERFACING LABPR 10. SOFT SKILLS 50 20 34 P C 20. DATA STRUCTURES LABORATORY TW 50 20 27 P C TW 21. DATA STRUCTURES LABORATORY PR 50 20 32 P C GRAND TOTAL = 783/1500, RESULT: SECOND CLASS ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER S8054389 SANDEEP AGARWAL , s8054389 RITA 11. ENGINEERING MATHEMATICS III PP 100 40 53 P C 01. DISCRETE STRUCTURES PP 100 40 59 P C 02. PROGRAMMING & PROBLEM SOLVING PP 100 40 40 P C 27 F 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 100 40 47 P C 100 40 43 P.C 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 13. DATA STRUCTURES PP 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 40 P C PP 100 40 40 P 14. COMPUTER GRAPHICS 05. HUMANITIES AND SOCIAL SCIENCE PP 100 40 52 P C 15. COMPUTER ORGANIZATION 100 40 43 P 25 10 20 P C 50 20 30 P C 06. PROGRAMMING LABORATORY 16. O. O. PROG. & COMP. GRAPH. LAB TW TW 07. PROGRAMMING LABORATORY 50 20 30 P C 20 24 P C PR 17. O. O. PROG. & COMP. GRAPH. LAB PR 50 25 10 17 P C 18. MICROPROCESSORS & INTERFACING LABTW 50 20 32 P C 08. DIGITAL ELECTRONICS LABORATORY TW 50 20 25 P C 50 20 20 P C 09. DIGITAL ELECTRONICS LABORATORY PR 19. MICROPROCESSORS & INTERFACING LABPR 10. SOFT SKILLS 50 20 40 P C 20. DATA STRUCTURES LABORATORY 50 20 22 P C TW TW PR 50 20 15 F 21. DATA STRUCTURES LABORATORY GRAND TOTAL = 719/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS: S8054391 SAURAV JAUHARI POONAM , s8054391 11. ENGINEERING MATHEMATICS III PP 100 40 00 F 01. DISCRETE STRUCTURES PP 100 40 AA F 40 P C 02. PROGRAMMING & PROBLEM SOLVING PP 100 40 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 11 F 31 F 100 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 40 13. DATA STRUCTURES PP 40 03 F 14 F 100 11 F 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 14. COMPUTER GRAPHICS PP 40 05. HUMANITIES AND SOCIAL SCIENCE PP 100 40 47 P 15. COMPUTER ORGANIZATION 100 40 28 F 25 50 20 06. PROGRAMMING LABORATORY 10 13 P C 16. O. O. PROG. & COMP. GRAPH. LAB TW 20 P C TW 50 20 08 F 20 20 P C 07. PROGRAMMING LABORATORY PR 17. O. O. PROG. & COMP. GRAPH. LAB PR 50 25 10 10 P C 08. DIGITAL ELECTRONICS LABORATORY TW 18. MICROPROCESSORS & INTERFACING LABTW 50 20 20 P C 09. DIGITAL ELECTRONICS LABORATORY PR 50 20 22 P 19. MICROPROCESSORS & INTERFACING LABPR 50 20 20 P 10. SOFT SKILLS 50 20 26 P C 20. DATA STRUCTURES LABORATORY 50 20 20 P C TW TW PR 50 20 AA F 21. DATA STRUCTURES LABORATORY GRAND TOTAL = 364/1500, RESULT: FAILS ORDN. 1 MARKS : , 71045614L , , PICT , s8054394 S8054394 SHINDE AMIT KEDARNATH SANGEETA 01. DISCRETE STRUCTURES PP 100 40 53 P C 11. ENGINEERING MATHEMATICS III PP 100 40 40 P C 02. PROGRAMMING & PROBLEM SOLVING PP 100 40 40 P C 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 27 F 100 40 49 P C PP 100 40 54 P C 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 13. DATA STRUCTURES 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 40 P C 14. COMPUTER GRAPHICS 100 40 46 P.C PΡ 05. HUMANITIES AND SOCIAL SCIENCE PP 100 40 40 P C 15. COMPUTER ORGANIZATION 100 40 54 P C PP 06. PROGRAMMING LABORATORY 25 10 17 P C 50 20 32 P C TW 16. O. O. PROG. & COMP. GRAPH. LAB TW 07. PROGRAMMING LABORATORY 20 35 P C 17. O. O. PROG. & COMP. GRAPH. LAB PR 20 32 P C PR 50 50 50 20 08. DIGITAL ELECTRONICS LABORATORY TW 25 10 16 P C 18. MICROPROCESSORS & INTERFACING LABTW 36 P C 50 20 21 P C 09. DIGITAL ELECTRONICS LABORATORY PR 50 20 38 P C 19. MICROPROCESSORS & INTERFACING LABPR 10. SOFT SKILLS 50 20 34 P C TW 50 20 30 P C TW 20. DATA STRUCTURES LABORATORY 21. DATA STRUCTURES LABORATORY PR 50 20 21 P C GRAND TOTAL = 755/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , s8054395 S8054395 SIDDHARTH BATRA SANGEETA PP 100 40 71 P C 11. ENGINEERING MATHEMATICS III PP 100 40 27 F 01. DISCRETE STRUCTURES 02. PROGRAMMING & PROBLEM SOLVING PP 100 40 40 P C 15 F 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 100 40 40 P C PP 100 40 40 P 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 13. DATA STRUCTURES 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 48 P C PP 100 40 43 P 14. COMPUTER GRAPHICS 05. HUMANITIES AND SOCIAL SCIENCE PP 100 40 43 P C 15. COMPUTER ORGANIZATION 100 40 61 P 25 10 14 P C 50 20 21 P 06. PROGRAMMING LABORATORY 16. O. O. PROG. & COMP. GRAPH. LAB TW TW 07. PROGRAMMING LABORATORY 50 20 24 P C 50 20 30 P PR 17. O. O. PROG. & COMP. GRAPH. LAB PR 25 10 10 P C 18. MICROPROCESSORS & INTERFACING LABTW 50 20 24 P 08. DIGITAL ELECTRONICS LABORATORY TW 50 20 20 P C 50 20 20 P 09. DIGITAL ELECTRONICS LABORATORY PR 19. MICROPROCESSORS & INTERFACING LABPR TW 50 20 23 P C 10. SOFT SKILLS 20. DATA STRUCTURES LABORATORY 50 20 26 P TW PR 50 20 08 F 21. DATA STRUCTURES LABORATORY GRAND TOTAL = 648/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS : , 70925632K , , PICT S8054399 THAKARE SAGAR SATISH JAYASHRI , s8054399 01. DISCRETE STRUCTURES 11. ENGINEERING MATHEMATICS III PP 100 40 40 P PP 100 40 40 P C 02. PROGRAMMING & PROBLEM SOLVING PP 100 40 40 P C 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 40 P C 13. DATA STRUCTURES 40 40 P 100 40 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 PP 40 P C 40 100 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 P C 14. COMPUTER GRAPHICS PP 40 40 P C 40 100 40 P C 05. HUMANITIES AND SOCIAL SCIENCE PP 100 48 P C 15. COMPUTER ORGANIZATION 40 25 10 50 20 32 P C 06. PROGRAMMING LABORATORY 17 P C 16. O. O. PROG. & COMP. GRAPH. LAB TW TW 50 20 20 P C 20 07. PROGRAMMING LABORATORY PR 17. O. O. PROG. & COMP. GRAPH. LAB PR 50 28 P C 25 10 19 P C 08. DIGITAL ELECTRONICS LABORATORY TW 18. MICROPROCESSORS & INTERFACING LABTW 50 20 33 P C 09. DIGITAL ELECTRONICS LABORATORY PR 50 20 21 P C 19. MICROPROCESSORS & INTERFACING LABPR 50 20 25 P C 10. SOFT SKILLS 50 20 25 P C 20. DATA STRUCTURES LABORATORY 50 20 28 P C TW TW PR 50 20 06 F 21. DATA STRUCTURES LABORATORY GRAND TOTAL = 662/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS: , 71045648E , , PICT , s8054400 S8054400 THAKUR DHIRAJSING UDAYSING SAROJADEVI 01. DISCRETE STRUCTURES PP 100 40 48 P C 11. ENGINEERING MATHEMATICS III PP 100 40 AA F 02. PROGRAMMING & PROBLEM SOLVING PP 100 40 40 P C 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 13 F 100 40 40 P C PP 100 40 40 P C 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 13. DATA STRUCTURES 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 45 P C 14. COMPUTER GRAPHICS 100 40 23 F PΡ 100 40 40 P C 15. COMPUTER ORGANIZATION 100 40 40 P C 05. HUMANITIES AND SOCIAL SCIENCE PP PP 20 21 P C 06. PROGRAMMING LABORATORY 25 10 13 P C 50 TW 16. O. O. PROG. & COMP. GRAPH. LAB TW 07. PROGRAMMING LABORATORY 50 20 37 P C 17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 35 P PR 25 P C 08. DIGITAL ELECTRONICS LABORATORY TW 25 10 12 P C 18. MICROPROCESSORS & INTERFACING LABTW 50 20 09. DIGITAL ELECTRONICS LABORATORY PR 50 20 20 P C 50 20 21 P 19. MICROPROCESSORS & INTERFACING LABPR 10. SOFT SKILLS 50 20 29 P C TW 50 20 28 P C TW 20. DATA STRUCTURES LABORATORY 21. DATA STRUCTURES LABORATORY PR 50 20 02 F GRAND TOTAL = 572/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME				-	•				-			
			•			•	<pre>KS OBTAINED, P/F:PASS/FAIL, C:</pre>					
S8054401 UIKEY ANURAG VINAYAK		• •		 JY0			, 70925640L ,				 s8054	
01. DISCRETE STRUCTURES	PP	100	40	40		11	ENGINEERING MATHEMATICS III		100	40	AA	
	PP	100	40	40	_		MICROPROC. & INTERFACING TECHNI		100	40	17	
03. DIGIT. ELECTRONICS & LOGIC DESI		100	40	47			DATA STRUCTURES	PP	100	40	AA	
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	40	_	_	COMPUTER GRAPHICS	PP	100	40	52	
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	62	_		COMPUTER GRAPHICS COMPUTER ORGANIZATION		100	40	40	-
06. PROGRAMMING LABORATORY		25	10	15	_		O. O. PROG. & COMP. GRAPH. LAB		50	20	24	
07. PROGRAMMING LABORATORY		50	20	AA	_		O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	20	
	PR	25	10				MICROPROCESSORS & INTERFACING L					-
08. DIGITAL ELECTRONICS LABORATORY				11	_				50	20	30	
09. DIGITAL ELECTRONICS LABORATORY		50	20	24			MICROPROCESSORS & INTERFACING L		50	20	30	
10. SOFT SKILLS	TW	50	20	38	PC		DATA STRUCTURES LABORATORY	TW	50	20	20	
500 /1500 BEGULT. 5471						21.	DATA STRUCTURES LABORATORY	PR	50	20	10	
RAND TOTAL = 560/1500, RESULT: FAIL RDN. 1 MARKS :	S A.I.	K.I.						KES	JLT RE	SERVE) FOR	. В
S8054403 VAIRAGE ROHIT VISHNU					 ASHRI		, 70925643E ,				 s8054	
01. DISCRETE STRUCTURES	PP	100	40	_	P C	11	ENGINEERING MATHEMATICS III			40	AA	
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	40			MICROPROC. & INTERFACING TECHNI		100	40	AA	
03. DIGIT. ELECTRONICS & LOGIC DESI		100	40	48	_		DATA STRUCTURES	PP	100	40	41	
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	40			COMPUTER GRAPHICS	PP	100	40	46	
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	55			COMPUTER ORGANIZATION		100	40	AA	
	TW	25	10	16	_	_	O. O. PROG. & COMP. GRAPH. LAB		50	20	34	-
07. PROGRAMMING LABORATORY		50	20	24		_	O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	20	
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	16	_		MICROPROCESSORS & INTERFACING L		50	20	20	
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	31			MICROPROCESSORS & INTERFACING L		50	20	AA	
10. SOFT SKILLS	TW	50	20	40			DATA STRUCTURES LABORATORY	TW	50	20	20	
10. 3011 SKILLS	1 00	30	20	40	r C		DATA STRUCTURES LABORATORY	PR	50	20	AA	
RAND TOTAL = 542/1500, RESULT: FAIL	S A.T.	K.T.				21.	DATA STRUCTURES EABORATORY	FK	50	20	AA	'
RDN. 1 MARKS :												
S8054405 ABNANE OMKAR RAJENDRA				SHO	ВНА		, 71045358C ,	, P	ICT	,	s8054	40!
01. DISCRETE STRUCTURES	PP	100	40	40	P C	11.	ENGINEERING MATHEMATICS III	PP	100	40	42	Р
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	40	P C	12.	MICROPROC. & INTERFACING TECHNI	Q.PP	100	40	43	Р
03. DIGIT. ELECTRONICS & LOGIC DESI	GNPP	100	40	40	P C	13.	DATA STRUCTURES	PP	100	40	40	Р
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	40	P C	14.	COMPUTER GRAPHICS	PP	100	40	57	Р
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	41	P C	15.	COMPUTER ORGANIZATION	PP	100	40	45	Р
06. PROGRAMMING LABORATORY	TW	25	10	13	РС	16.	O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	23	Р
07. PROGRAMMING LABORATORY	PR	50	20	AA	F	17.	O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	AA	F
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	13	РС	18.	MICROPROCESSORS & INTERFACING L	ABTW	50	20	21	Р
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	23	РС	19.	MICROPROCESSORS & INTERFACING L	ABPR	50	20	20	Р
10. SOFT SKILLS	TW	50	20	22	РС	20.	DATA STRUCTURES LABORATORY	TW	50	20	22	Р
						21.	DATA STRUCTURES LABORATORY	PR	50	20	AA	F
RAND TOTAL = 585/1500, RESULT: FAIL	S A.T.	K.T.										
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DATE: 14 AUG. 2012 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 57 (480)

NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , s8054406 S8054406 SHUBHRANK GUPTA INDIRA 11. ENGINEERING MATHEMATICS III PP 100 40 AA F 01. DISCRETE STRUCTURES PP 100 40 40 P C 02. PROGRAMMING & PROBLEM SOLVING PP 100 40 40 P C 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 AA F 100 40 40 P 100 40 40 P 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 13. DATA STRUCTURES PP 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 40 P PP 100 40 14. COMPUTER GRAPHICS AA F 05. HUMANITIES AND SOCIAL SCIENCE PP 100 40 40 P C 15. COMPUTER ORGANIZATION 100 40 $\Delta\Delta$ F 25 10 13 P C 50 20 22 P 06. PROGRAMMING LABORATORY 16. O. O. PROG. & COMP. GRAPH. LAB TW TW 07. PROGRAMMING LABORATORY 50 20 17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 28 P PR AA F 25 10 10 P C 50 20 20 P 08. DIGITAL ELECTRONICS LABORATORY TW 18. MICROPROCESSORS & INTERFACING LABTW 50 20 AA F 50 20 AA F 09. DIGITAL ELECTRONICS LABORATORY PR 19. MICROPROCESSORS & INTERFACING LABPR 10. SOFT SKILLS 50 20 24 P C 20. DATA STRUCTURES LABORATORY TW 50 20 24 P TW PR 50 20 27 P 21. DATA STRUCTURES LABORATORY GRAND TOTAL = 408/1500, RESULT: FAILS RESULT RESERVED FOR BKLG ORDN. 1 MARKS: , 70818756M , , PICT S8054407 KULKARNI VARAD SURESH TILOTTAMA , s8054407 01. DISCRETE STRUCTURES 11. ENGINEERING MATHEMATICS III PP 100 40 03 F 100 40 AA F 02. PROGRAMMING & PROBLEM SOLVING PP 100 40 AA F 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 40 P AA F 100 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 40 13. DATA STRUCTURES PP 40 42 P 100 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 AA F 14. COMPUTER GRAPHICS PP 40 46 P 100 05. HUMANITIES AND SOCIAL SCIENCE PP 100 40 AA F 15. COMPUTER ORGANIZATION 40 40 P 06. PROGRAMMING LABORATORY 25 50 42 P 10 AA F 16. O. O. PROG. & COMP. GRAPH. LAB TW 20 TW 50 20 20 07. PROGRAMMING LABORATORY AA F 17. O. O. PROG. & COMP. GRAPH. LAB PR 50 46 P 25 10 AA F 08. DIGITAL ELECTRONICS LABORATORY TW 18. MICROPROCESSORS & INTERFACING LABTW 50 20 39 P 50 20 40 P 09. DIGITAL ELECTRONICS LABORATORY PR 50 20 AA F 19. MICROPROCESSORS & INTERFACING LABPR 10. SOFT SKILLS 50 20 AA F 20. DATA STRUCTURES LABORATORY 50 20 40 P TW 21. DATA STRUCTURES LABORATORY PR 50 20 41 P GRAND TOTAL = 419/1500, RESULT: FAILS RESULT RESERVED FOR OTHR ORDN. 1 MARKS: , 70701614C , S8054352 , PICT S8054408 SALUNKE DEVENDRAKUMAR ASHOK VIJAYA , S8054408 01. DISCRETE STRUCTURES 100 40 AA F 11. ENGINEERING MATHEMATICS III PP 100 40 10 F AA F 02. PROGRAMMING & PROBLEM SOLVING PP 100 40 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 03 F 100 40 AA F PP 100 40 46 P 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 13. DATA STRUCTURES 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 14. COMPUTER GRAPHICS 100 40 24 F AA F PP 100 40 AA F 15. COMPUTER ORGANIZATION 100 40 41 P 05. HUMANITIES AND SOCIAL SCIENCE PP PP 23 P 06. PROGRAMMING LABORATORY 25 10 50 20 TW AA F 16. O. O. PROG. & COMP. GRAPH. LAB TW 07. PROGRAMMING LABORATORY 50 20 17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 08 F PR AA F 50 20 08. DIGITAL ELECTRONICS LABORATORY TW 25 10 AA F 18. MICROPROCESSORS & INTERFACING LABTW 24 P 50 20 09. DIGITAL ELECTRONICS LABORATORY PR 19. MICROPROCESSORS & INTERFACING LABPR 50 20 02 F AA F 10. SOFT SKILLS 50 20 AA F 50 20 22 P TW 20. DATA STRUCTURES LABORATORY TW 50 20 AA F 21. DATA STRUCTURES LABORATORY PR GRAND TOTAL = 203/1500, RESULT: FAILS RESULT RESERVED FOR BKLG + OTHR ORDN. 1 MARKS:

DATE: 14 AUG. 2012 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 01 (481)

NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

								, ,			,		
01.	DISCRETE STRUCTURES	PP	100	40	74	P C	11.	ENG MATHS III	PP	100	40	54	Р
02.	COMPUTER ORGANIZATION	PP	100	40	54	РС	12.	COMPUTER GRAPHICS	PP	100	40	62	Р
03.	DIGITAL ELECTRONICS & LOGIC DESIGNATION	GPP	100	40	68	РС	13.	PROCESSOR ARCHITECTURE & INTER.	PP	100	40	64	Р
04.	FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	58	РС	14.	DATA STRUCTURES AND FILES	PP	100	40	49	Р
05.	HUMANITIES AND SOCIAL SCIENCES	PP	100	40	54	РС	15.	DATA COMMUNICATION	PP	100	40	46	Р
06.	DIGITAL LABORATORY	TW	50	20	33	РС	16.	PROCESSOR INTERFACING LABORATORY	TW	25	10	17	Р
07.	DIGITAL LABORATORY	PR	50	20	35	РС	17.	PROCESSOR INTERFACING LABORATORY	OR	50	20	29	Р
08.	PROGRAMMING LABORATORY	TW	50	20	38	P C	18.	DATA STRUCTURES AND FILES LAB	TW	25	10	16	Р
09.	PROGRAMMING LABORATORY	PR	50	20	23	РС	19.	DATA STRUCTURES AND FILES LAB	PR	50	20	35	Р

GRAND TOTAL = 916/1500, RESULT: FIRST CLASS

10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 39 P C

ORDN. 1 MARKS:

20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 33 P 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 35 P

21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 32 P

S8058502 ABHISHEK BHATTACHARJEE				MOL	JSUMI		, 71100713G , , ,	ΡI	СТ	, 5	8058	502
01. DISCRETE STRUCTURES	PP	100	40	43	PС	11.	ENG MATHS III	PP	100	40	79	Р
02. COMPUTER ORGANIZATION	PP	100	40	67	РС	12.	COMPUTER GRAPHICS	PP	100	40	57	Р
03. DIGITAL ELECTRONICS & LOGIC DESI	GPP	100	40	51	РС	13.	PROCESSOR ARCHITECTURE & INTER.	PP	100	40	60	Р
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	65	РС	14.	DATA STRUCTURES AND FILES	PP	100	40	65	Р
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	49	РС	15.	DATA COMMUNICATION	PP	100	40	65	Р
06. DIGITAL LABORATORY	TW	50	20	37	РС	16.	PROCESSOR INTERFACING LABORATORY	TW	25	10	17	Р
07. DIGITAL LABORATORY	PR	50	20	40	РС	17.	PROCESSOR INTERFACING LABORATORY	OR	50	20	33	Р
08. PROGRAMMING LABORATORY	TW	50	20	34	РС	18.	DATA STRUCTURES AND FILES LAB	TW	25	10	19	Р
09. PROGRAMMING LABORATORY	PR	50	20	38	РС	19.	DATA STRUCTURES AND FILES LAB	PR	50	20	40	Р
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	37	РС	20.	OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	43	Р
						21.	OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	30	Р

GRAND TOTAL = 969/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS:

S8058503	ADEP AKASH RAJENDRA				KAI	_PANA		, 71100717к , ,	PI	CT	, ,	58058	503
01. DISCRET	E STRUCTURES	PP	100	40	40	Р	11.	ENG MATHS III	PP	100	40	26	F
02. COMPUTE	R ORGANIZATION	PP	100	40	40	P C	12.	COMPUTER GRAPHICS	PP	100	40	57	Р
03. DIGITAL	ELECTRONICS & LOGIC DESI	GPP	100	40	40	P C	13.	PROCESSOR ARCHITECTURE & INTER.	PP	100	40	31	F
04. FUNDAME	NTAL OF DATA STRUCTURES	PP	100	40	40	P C	14.	DATA STRUCTURES AND FILES	PP	100	40	40	Р
05. HUMANIT	TIES AND SOCIAL SCIENCES	PP	100	40	40	P C	15.	DATA COMMUNICATION	PP	100	40	28	F
06. DIGITAL	LABORATORY	TW	50	20	31	P C	16.	PROCESSOR INTERFACING LABORATORY	TW	25	10	10	Р
07. DIGITAL	LABORATORY	PR	50	20	23	P C	17.	PROCESSOR INTERFACING LABORATORY	OR	50	20	05	F
08. PROGRAM	MING LABORATORY	TW	50	20	27	P C	18.	DATA STRUCTURES AND FILES LAB	TW	25	10	11	Р
09. PROGRAM	MING LABORATORY	PR	50	20	20	Р	19.	DATA STRUCTURES AND FILES LAB	PR	50	20	38	Р
10. COMMUNI	CATION AND LANGUAGE LAB.	TW	50	20	35	P C	20.	OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	33	Р

GRAND TOTAL = 647/1500, RESULT: FAILS A.T.K.T.

DATE: 14 AUG. 2012 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 02 (482)

NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER DIMPLE , S8058504 S8058504 ADITYA SINGH SOLANKI 01. DISCRETE STRUCTURES PP 100 40 53 P 11. ENG MATHS III PP 100 40 40 P 02. COMPUTER ORGANIZATION PP 100 40 51 P C 12. COMPUTER GRAPHICS 100 40 43 P 100 40 40 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 40 P 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 42 P C PP 100 40 40 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 46 P C 100 40 41 P 15. DATA COMMUNICATION 50 20 30 P C 25 10 18 P 06. DIGITAL LABORATORY 16. PROCESSOR INTERFACING LABORATORY TW TW 07. DIGITAL LABORATORY 50 20 30 P C 50 20 20 P PR 17. PROCESSOR INTERFACING LABORATORY OR 08. PROGRAMMING LABORATORY 50 20 34 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 17 P TW 09. PROGRAMMING LABORATORY PR 50 20 23 P C 19. DATA STRUCTURES AND FILES LAB PR 50 20 09 F 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 36 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 36 P 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 10 F GRAND TOTAL = 699/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS: S8058505 AGARWAL RUHI DINESH RADHA , S8058505 01. DISCRETE STRUCTURES PP 100 40 72 P C 11. ENG MATHS III 90 P PP 100 40 40 58 P C PP 100 02. COMPUTER ORGANIZATION PP 100 12. COMPUTER GRAPHICS 40 64 P 40 70 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 64 P 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 62 P C 100 79 P 100 40 PP 40 40 44 P C 100 05. HUMANITIES AND SOCIAL SCIENCES PP 100 15. DATA COMMUNICATION 40 68 P 50 20 43 P C 24 P 06. DIGITAL LABORATORY 25 10 TW 16. PROCESSOR INTERFACING LABORATORY TW 50 20 42 P C 50 20 07. DIGITAL LABORATORY PR 17. PROCESSOR INTERFACING LABORATORY OR 42 P 50 20 42 P C 08. PROGRAMMING LABORATORY TW 18. DATA STRUCTURES AND FILES LAB TW 25 10 20 P 09. PROGRAMMING LABORATORY PR 50 20 40 P C 19. DATA STRUCTURES AND FILES LAB PR 50 20 45 P 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 43 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 46 P 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 40 P GRAND TOTAL = 1098/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS: , 71100722F , , PICT MANGALA , s8058506 S8058506 AHERKAR SHRIJEET CHANDRASHEKHAR 01. DISCRETE STRUCTURES PP 100 40 43 P C 11. ENG MATHS III 100 40 85 P 02. COMPUTER ORGANIZATION PP 100 40 42 P C 12. COMPUTER GRAPHICS PP 100 40 48 P 100 40 48 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 49 P 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 63 P C 14. DATA STRUCTURES AND FILES PP 100 40 55 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 41 P C 100 40 51 P 15. DATA COMMUNICATION PP 20 P 06. DIGITAL LABORATORY 50 20 35 P C 25 10 TW 16. PROCESSOR INTERFACING LABORATORY TW 50 20 37 P C 17. PROCESSOR INTERFACING LABORATORY OR 50 20 39 P 07. DIGITAL LABORATORY PR 18. DATA STRUCTURES AND FILES LAB TW 25 10 19 P 08. PROGRAMMING LABORATORY TW 50 20 36 P C 50 20 26 P C 19. DATA STRUCTURES AND FILES LAB PR 50 20 28 P 09. PROGRAMMING LABORATORY PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 41 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 42 P 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 39 P GRAND TOTAL = 887/1500, RESULT: HIGHER SECOND CLASS ORDN. 1 MARKS:

DATE: 14 AUG. 2012 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 03 (483)

NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER BUDDHO , 71241751G , , PICT , s8058507 S8058507 ALAMWALE SAMEER GAMA 01. DISCRETE STRUCTURES PP 100 40 45 P 11. ENG MATHS III PP 100 40 27 F 02. COMPUTER ORGANIZATION PP 100 40 57 P C 12. COMPUTER GRAPHICS 100 40 100 40 42 P C 100 40 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 13. PROCESSOR ARCHITECTURE & INTER. PP 58 P 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 61 P C 100 40 26 F PP 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 45 P C PP 100 40 41 P 15. DATA COMMUNICATION 50 20 36 P.C 25 10 16 P 06. DIGITAL LABORATORY 16. PROCESSOR INTERFACING LABORATORY TW TW 50 20 25 P C 07. DIGITAL LABORATORY 50 20 28 P PR 17. PROCESSOR INTERFACING LABORATORY OR 08. PROGRAMMING LABORATORY 50 20 36 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 17 P TW 09. PROGRAMMING LABORATORY PR 50 20 24 P C 19. DATA STRUCTURES AND FILES LAB PR 50 20 P 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 41 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 39 P 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 40 P GRAND TOTAL = 768/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS: S8058508 ALHAT ANIKETH KANILAL MANGAL , s8058508 01. DISCRETE STRUCTURES PP 100 40 59 P PP 100 40 63 P C 11. ENG MATHS III 40 02. COMPUTER ORGANIZATION PP 100 40 65 P C 12. COMPUTER GRAPHICS PP 100 70 P 40 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 67 P C 63 P 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 40 71 P C 100 100 PP 40 48 P 100 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 52 P C 15. DATA COMMUNICATION 40 66 P 50 20 43 P C 23 P 06. DIGITAL LABORATORY 25 10 TW 16. PROCESSOR INTERFACING LABORATORY TW 50 20 40 P C 20 07. DIGITAL LABORATORY PR 17. PROCESSOR INTERFACING LABORATORY OR 50 40 P 50 20 44 P C 08. PROGRAMMING LABORATORY TW 18. DATA STRUCTURES AND FILES LAB TW 25 10 20 P 09. PROGRAMMING LABORATORY PR 50 20 28 P C 19. DATA STRUCTURES AND FILES LAB PR 50 20 43 P 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 43 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 46 P 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 43 P GRAND TOTAL = 1037/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS: , 71100729C , , PICT , s8058509 S8058509 ANIL BAJAJ KAMAL 01. DISCRETE STRUCTURES PP 100 40 60 P C 11. ENG MATHS III 100 40 40 P 02. COMPUTER ORGANIZATION PP 100 40 43 P C 12. COMPUTER GRAPHICS PP 100 40 57 P 100 40 57 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 48 P 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 40 P C 14. DATA STRUCTURES AND FILES PP 100 40 59 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 51 P C 100 40 43 P 15. DATA COMMUNICATION PP 10 13 P 06. DIGITAL LABORATORY 50 20 36 P C 25 TW 16. PROCESSOR INTERFACING LABORATORY TW 50 20 33 P C 17. PROCESSOR INTERFACING LABORATORY OR 50 20 29 P 07. DIGITAL LABORATORY PR 18. DATA STRUCTURES AND FILES LAB TW 25 10 15 P 08. PROGRAMMING LABORATORY TW 50 20 33 P C 50 20 37 P 19. DATA STRUCTURES AND FILES LAB PR 50 20 43 P 09. PROGRAMMING LABORATORY PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 36 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 42 P 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 34 P GRAND TOTAL = 849/1500, RESULT: HIGHER SECOND CLASS ORDN. 1 MARKS:

UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(INFORMATION TECHNOLOGY) DATE: 14 AUG. 2012 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 04 (484) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER VIMLESH S8058510 ANIRUDH SHISHODIA , 71100730G , , PICT , S8058510 01. DISCRETE STRUCTURES 11. ENG MATHS III PP 100 40 61 P PP 100 40 68 P C 12. COMPUTER GRAPHICS 02. COMPUTER ORGANIZATION PP 100 40 49 P C PP 100 40 51 P 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 40 54 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 40 P 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 56 P C 14. DATA STRUCTURES AND FILES PP 100 40 63 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 43 PC 15. DATA COMMUNICATION PP 100 40 44 P TW 06. DIGITAL LABORATORY 50 20 36 P C 25 10 21 P 16. PROCESSOR INTERFACING LABORATORY TW

10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 43 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 42 P 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 38 P

50 20 34 P C

TW 50 20 41 P C

PR

09. PROGRAMMING LABORATORY PR 50 20 22 P C

GRAND TOTAL = 870/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS:

07. DIGITAL LABORATORY

08. PROGRAMMING LABORATORY

17. PROCESSOR INTERFACING LABORATORY OR

18. DATA STRUCTURES AND FILES LAB TW 25 10 20 P

19. DATA STRUCTURES AND FILES LAB PR 50 20 10 F

50 20 34 P

S8058511 ANKIT V BANSAL				URI	MILA		, 71045371L , , ,	ΡI	CT	, 5	8058	511
01. DISCRETE STRUCTURES	PP	100	40	40	Р	11.	ENG MATHS III	PP	100	40	AA	F
02. COMPUTER ORGANIZATION	PP	100	40	40	P C	12.	COMPUTER GRAPHICS	PP	100	40	40	Р
03. DIGITAL ELECTRONICS & LOGIC DES	IGPP	100	40	40	Р	13.	PROCESSOR ARCHITECTURE & INTER.	PP	100	40	21	F
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	27	F	14.	DATA STRUCTURES AND FILES	PP	100	40	29	F
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	40	P C	15.	DATA COMMUNICATION	PP	100	40	40	Р
06. DIGITAL LABORATORY	TW	50	20	25	P C	16.	PROCESSOR INTERFACING LABORATORY	′ TW	25	10	10	Р
07. DIGITAL LABORATORY	PR	50	20	21	Р	17.	PROCESSOR INTERFACING LABORATORY	OR	50	20	06	F
08. PROGRAMMING LABORATORY	TW	50	20	39	P C	18.	DATA STRUCTURES AND FILES LAB	TW	25	10	10	Р
09. PROGRAMMING LABORATORY	PR	50	20	12	F	19.	DATA STRUCTURES AND FILES LAB	PR	50	20	AA	F
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	28	P C	20.	OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	30	Р
						21.	OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	80	F

GRAND TOTAL = 506/1500, RESULT: FAILS RESULT RESERVED FOR BKLG

ORDN. 1 MARKS:

S8058512 ANUPAMA TRIVEDI				SANGITA	, 71100732C , ,	PI	СТ	,	s80585	512
01. DISCRETE STRUCTURES	PP	100	40	57 P C	11. ENG MATHS III	PP	100	40	42	Р
02. COMPUTER ORGANIZATION	PP	100	40	65 P C	12. COMPUTER GRAPHICS	PP	100	40	59	Р
03. DIGITAL ELECTRONICS & LOGIC DES	IGPP	100	40	51 P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	64	Р
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	56 P C	14. DATA STRUCTURES AND FILES	PP	100	40	64	Р
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	52 P C	15. DATA COMMUNICATION	PP	100	40	67	Р
06. DIGITAL LABORATORY	TW	50	20	39 P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	20	Р
07. DIGITAL LABORATORY	PR	50	20	43 P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	28	Р
08. PROGRAMMING LABORATORY	TW	50	20	38 P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	18	Р
09. PROGRAMMING LABORATORY	PR	50	20	36 P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	38	Р
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	39 P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	41	Р
					21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	42	Р

GRAND TOTAL = 959/1500, RESULT: FIRST CLASS

DATE: 14 AUG. 2012 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 05 (485)

NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER NIVEDITA , 70925337M , , PICT , s8058513 S8058513 ANUSHKA GHOGALE 01. DISCRETE STRUCTURES PP 100 40 13 F 11. ENG MATHS III PP 100 40 00 F 02. COMPUTER ORGANIZATION PP 100 40 40 P C 12. COMPUTER GRAPHICS PP 100 40 31 F 100 40 15 F 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 16 F 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 40 P 14. DATA STRUCTURES AND FILES PP 100 40 40 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 40 P C 15. DATA COMMUNICATION PP 100 40 23 F 50 20 23 P C 25 10 16 P 06. DIGITAL LABORATORY 16. PROCESSOR INTERFACING LABORATORY TW TW 07. DIGITAL LABORATORY 50 20 32 P C 50 20 25 P PR 17. PROCESSOR INTERFACING LABORATORY OR 08. PROGRAMMING LABORATORY 50 20 27 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 12 P TW 09. PROGRAMMING LABORATORY PR 50 20 20 P C 19. DATA STRUCTURES AND FILES LAB PR 50 20 07 F 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 31 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 34 P 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 12 F GRAND TOTAL = 497/1500, RESULT: FAILS ORDN. 1 MARKS : S8058514 ASHUTOSH PANDEY MAMTA , s8058514 PP 100 40 57 P 11. ENG MATHS III PP 100 40 40 P 01. DISCRETE STRUCTURES PP 100 40 59 P 02. COMPUTER ORGANIZATION PP 100 40 61 P C 12. COMPUTER GRAPHICS 40 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 45 P C 22 F 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 P 100 40 59 P C PP 40 40 PP 100 05. HUMANITIES AND SOCIAL SCIENCES PP 100 44 P C 15. DATA COMMUNICATION 40 40 P 50 20 34 P C 10 13 P 06. DIGITAL LABORATORY 25 TW 16. PROCESSOR INTERFACING LABORATORY TW 50 20 38 P C 50 20 20 P 07. DIGITAL LABORATORY PR 17. PROCESSOR INTERFACING LABORATORY OR 50 20 29 P C 08. PROGRAMMING LABORATORY TW 18. DATA STRUCTURES AND FILES LAB TW 25 10 16 P 09. PROGRAMMING LABORATORY PR 50 20 26 P 19. DATA STRUCTURES AND FILES LAB PR 50 20 35 P 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 38 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 40 P 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 30 P GRAND TOTAL = 786/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS : , 71100734K , , PICT S8058515 ASHWIN AJAY HABBU AARTI , s8058515 01. DISCRETE STRUCTURES PP 100 40 56 P C 11. ENG MATHS III PP 100 40 72 P 02. COMPUTER ORGANIZATION PP 100 40 61 P C 12. COMPUTER GRAPHICS PP 100 40 49 P 100 40 58 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 58 P C 14. DATA STRUCTURES AND FILES PP 100 40 50 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 50 P C PP 100 40 50 P 15. DATA COMMUNICATION 10 15 P 06. DIGITAL LABORATORY 50 20 28 P C 25 TW 16. PROCESSOR INTERFACING LABORATORY TW 50 20 32 P C 17. PROCESSOR INTERFACING LABORATORY OR 50 20 20 P 07. DIGITAL LABORATORY PR 18. DATA STRUCTURES AND FILES LAB TW 25 10 14 P 08. PROGRAMMING LABORATORY TW 50 20 35 P C 50 20 38 P C 19. DATA STRUCTURES AND FILES LAB PR 50 20 40 P 09. PROGRAMMING LABORATORY PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 35 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 30 P 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 28 P GRAND TOTAL = 863/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS:

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8058516 AYMAN MUSHTAQ AHMAD				VI	KAR UN NISA		, 71100736F , , ,	PΙ	CT	,	S8058	516
01. DISCRETE STRUCTURES	PP	100	40	40	P C	11.	ENG MATHS III	PP	100	40	40	Р
02. COMPUTER ORGANIZATION	PP	100	40	53	P C	12.	COMPUTER GRAPHICS	PP	100	40	62	Р
03. DIGITAL ELECTRONICS & LOGIC DES	IGPP	100	40	57	P C	13.	PROCESSOR ARCHITECTURE & INTER.	PP	100	40	45	Р
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	50	P C	14.	DATA STRUCTURES AND FILES	PP	100	40	55	Р
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	50	P C	15.	DATA COMMUNICATION	PP	100	40	40	Р
06. DIGITAL LABORATORY	TW	50	20	35	P C	16.	PROCESSOR INTERFACING LABORATORY	TW	25	10	14	Р
07. DIGITAL LABORATORY	PR	50	20	36	P C	17.	PROCESSOR INTERFACING LABORATORY	OR	50	20	30	Р
08. PROGRAMMING LABORATORY	TW	50	20	29	P C	18.	DATA STRUCTURES AND FILES LAB	TW	25	10	18	Р
09. PROGRAMMING LABORATORY	PR	50	20	40	Р	19.	DATA STRUCTURES AND FILES LAB	PR	50	20	35	Р
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	39	P C	20.	OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	38	Р
						21.	OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	35	Р

GRAND TOTAL = 841/1500, RESULT: HIGHER SECOND CLASS ORDN. 1 MARKS:

S8058517 BAHETI MAHESH RAJE	INDRA			CHA	ANDA		, 71100739L	, ,	ΡI	.CT	, 9	8058	517
01. DISCRETE STRUCTURES	PP	100	40	54	РС	11.	ENG MATHS III		PP	100	40	40	Р
02. COMPUTER ORGANIZATION	PP	100	40	43	РС	12.	COMPUTER GRAPHICS		PP	100	40	44	Р
03. DIGITAL ELECTRONICS & LOGI	C DESIGPP	100	40	54	PC	13.	PROCESSOR ARCHITE	CTURE & INTER.	PP	100	40	29	F
04. FUNDAMENTAL OF DATA STRUCT	URES PP	100	40	70	PC	14.	DATA STRUCTURES A	ND FILES	PP	100	40	50	Р
05. HUMANITIES AND SOCIAL SCIE	NCES PP	100	40	40	PC	15.	DATA COMMUNICATIO	N	PP	100	40	45	Р
06. DIGITAL LABORATORY	TW	50	20	30	PC	16.	PROCESSOR INTERFA	CING LABORATORY	TW	25	10	21	Р
07. DIGITAL LABORATORY	PR	50	20	43	РС	17.	PROCESSOR INTERFA	CING LABORATORY	OR	50	20	31	Р
08. PROGRAMMING LABORATORY	TW	50	20	46	PC	18.	DATA STRUCTURES A	ND FILES LAB	TW	25	10	18	Р
09. PROGRAMMING LABORATORY	PR	50	20	45	РС	19.	DATA STRUCTURES A	ND FILES LAB	PR	50	20	42	Р
10. COMMUNICATION AND LANGUAGE	LAB. TW	50	20	36	PC	20.	OBJECT ORIENTED P	ROGRAMMING LAB	TW	50	20	41	Р
						21.	OBJECT ORIENTED P	ROGRAMMING LAB	PR	50	20	33	Р

GRAND TOTAL = 855/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS:

S8058518 BHAKKAD MOHIT KISHANLAL	JI			PO	OJA	, 71100747M , , PICT , S8058518
01. DISCRETE STRUCTURES	PP	100	40	61	РС	11. ENG MATHS III PP 100 40 50 P
02. COMPUTER ORGANIZATION	PP	100	40	59	PС	12. COMPUTER GRAPHICS PP 100 40 59 P
03. DIGITAL ELECTRONICS & LOGIC DES	IGPP	100	40	64	PС	13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 58 P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	79	PС	14. DATA STRUCTURES AND FILES PP 100 40 70 P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	52	PС	15. DATA COMMUNICATION PP 100 40 51 P
06. DIGITAL LABORATORY	TW	50	20	41	PС	16. PROCESSOR INTERFACING LABORATORY TW 25 10 19 P
07. DIGITAL LABORATORY	PR	50	20	40	PС	17. PROCESSOR INTERFACING LABORATORY OR 50 20 30 P
08. PROGRAMMING LABORATORY	TW	50	20	39	PС	18. DATA STRUCTURES AND FILES LAB TW 25 10 21 P
09. PROGRAMMING LABORATORY	PR	50	20	45	PС	19. DATA STRUCTURES AND FILES LAB PR 50 20 39 P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	43	PС	20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 46 P
						21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 44 P

GRAND TOTAL = 1010/1500, RESULT: FIRST CLASS WITH DISTINCTION

DATE: 14 AUG. 2012 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 07 (487)

NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER S8058519 BHAND PRITI PARAJI RUKHMINI , 71100748K , , PICT , s8058519 PP 100 40 57 P 01. DISCRETE STRUCTURES 11. ENG MATHS III PP 100 40 46 P 02. COMPUTER ORGANIZATION PP 100 40 47 P C 12. COMPUTER GRAPHICS PP 100 40 100 40 40 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 25 F 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 45 P C 14. DATA STRUCTURES AND FILES PP 100 40 61 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 42 P C PP 100 40 42 P 15. DATA COMMUNICATION 50 20 30 P C 25 10 17 P 06. DIGITAL LABORATORY 16. PROCESSOR INTERFACING LABORATORY TW TW 50 20 32 P C 07. DIGITAL LABORATORY 50 20 20 P PR 17. PROCESSOR INTERFACING LABORATORY OR 08. PROGRAMMING LABORATORY 50 20 37 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 16 P TW 09. PROGRAMMING LABORATORY PR 50 20 20\$ P 19. DATA STRUCTURES AND FILES LAB PR 50 20 10 F 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 38 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 33 P 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 30 P GRAND TOTAL = 732/1500, RESULT: FAILS A.T.K.T. [\$ 0.1] ORDN. 1 MARKS: (09)2. S8058520 BHANDWALKAR ONKAR MADHUKAR PRABHAVATI , s8058520 01. DISCRETE STRUCTURES PP 100 40 67 P C 11. ENG MATHS III PP 100 40 58 P 60 P C PP 100 40 02. COMPUTER ORGANIZATION PP 100 40 12. COMPUTER GRAPHICS 55 P 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 40 65 P C 60 P 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 72 P 100 40 68 P C PP 40 40 42 P C PP 100 40 57 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 15. DATA COMMUNICATION 50 20 41 P C 20 P 06. DIGITAL LABORATORY 25 10 TW 16. PROCESSOR INTERFACING LABORATORY TW 50 20 40 P C 50 20 07. DIGITAL LABORATORY PR 17. PROCESSOR INTERFACING LABORATORY OR 42 P 50 20 34 P C 08. PROGRAMMING LABORATORY TW 18. DATA STRUCTURES AND FILES LAB TW 25 10 18 P 09. PROGRAMMING LABORATORY PR 50 20 33 P C 19. DATA STRUCTURES AND FILES LAB PR 50 20 35 P 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 44 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 43 P 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 43 P GRAND TOTAL = 997/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS: , 71241753C , , PICT S8058521 BHOIR KIRTI RAMCHANDRA ROHINI , s8058521 01. DISCRETE STRUCTURES PP 100 40 40 P C 11. ENG MATHS III PP 100 40 40 P 02. COMPUTER ORGANIZATION PP 100 40 56 P C 12. COMPUTER GRAPHICS PP 100 40 56 P 100 40 66 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 56 P 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 50 P C 14. DATA STRUCTURES AND FILES PP 100 40 50 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 46 P C PP 100 40 68 P 15. DATA COMMUNICATION 50 20 40 P C 25 10 19 P 06. DIGITAL LABORATORY TW 16. PROCESSOR INTERFACING LABORATORY TW 50 20 40 P C 17. PROCESSOR INTERFACING LABORATORY OR 50 20 25 P 07. DIGITAL LABORATORY PR 18. DATA STRUCTURES AND FILES LAB TW 25 10 22 P 08. PROGRAMMING LABORATORY TW 50 20 42 P C 50 20 22 P C 19. DATA STRUCTURES AND FILES LAB PR 50 20 20 P 09. PROGRAMMING LABORATORY PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 45 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 45 P 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 15# P GRAND TOTAL = 863/1500, RESULT: HIGHER SECOND CLASS # [0.4] ORDN. 1 MARKS:

UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(INFORMATION TECHNOLOGY)

DATE : 14 AUG. 2012 CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 08 (488)

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8058522 BORHADE BHUSHAN MALHARI				SU	REKHA		, 71241754M	,	, P	ICT	,	S8058	522
01. DISCRETE STRUCTURES	PP	100	40	40	РС	11.	ENG MATHS III		PP	100	40	40	Р
02. COMPUTER ORGANIZATION	PP	100	40	66	РС	12.	COMPUTER GRAPHIC	CS	PP	100	40	65	Р
03. DIGITAL ELECTRONICS & LOGIC DESI	GPP	100	40	40	РС	13.	PROCESSOR ARCHI	TECTURE & INTER.	PP	100	40	62	Р
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	71	РС	14.	DATA STRUCTURES	AND FILES	PP	100	40	69	Р
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	50	РС	15.	DATA COMMUNICAT	ION	PP	100	40	56	Р
06. DIGITAL LABORATORY	TW	50	20	37	РС	16.	PROCESSOR INTER	FACING LABORATOR	Y TW	25	10	21	Р
07. DIGITAL LABORATORY	PR	50	20	40	РС	17.	PROCESSOR INTER	FACING LABORATOR	Y OR	50	20	33	Р
08. PROGRAMMING LABORATORY	TW	50	20	39	РС	18.	DATA STRUCTURES	AND FILES LAB	TW	25	10	21	Р
09. PROGRAMMING LABORATORY	PR	50	20	35	РС	19.	DATA STRUCTURES	AND FILES LAB	PR	50	20	32	Р
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	42	РС	20.	OBJECT ORIENTED	PROGRAMMING LAB	TW	50	20	45	Р
						21.	OBJECT ORIENTED	PROGRAMMING LAB	PR	50	20	43	Р

GRAND TOTAL = 947/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS:

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S8058523 BOTHARA PRIYANKA RAMESH				VI	JAYA	, 71100755B , , PICT , S8058523	
01. DISCRETE STRUCTURES	PP	100	40	60	P C	11. ENG MATHS III PP 100 40 80 P	
02. COMPUTER ORGANIZATION	PP	100	40	67	P C	12. COMPUTER GRAPHICS PP 100 40 74 P	
03. DIGITAL ELECTRONICS & LOGIC DESI	GPP	100	40	74	P C	13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 61 P	
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	75	P C	14. DATA STRUCTURES AND FILES PP 100 40 65 P	
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	44	P C	15. DATA COMMUNICATION PP 100 40 66 P	
06. DIGITAL LABORATORY	TW	50	20	40	P C	16. PROCESSOR INTERFACING LABORATORY TW 25 10 21 P	
07. DIGITAL LABORATORY	PR	50	20	42	P C	17. PROCESSOR INTERFACING LABORATORY OR 50 20 41 P	
08. PROGRAMMING LABORATORY	TW	50	20	45	P C	18. DATA STRUCTURES AND FILES LAB TW 25 10 18 P	
09. PROGRAMMING LABORATORY	PR	50	20	44	P C	19. DATA STRUCTURES AND FILES LAB PR 50 20 42 P	
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	45	P C	20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 39 P	
						21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 46 P	

GRAND TOTAL = 1089/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS:

, 71100756L , , PICT S8058524 BOTHRA PAYAL SUMATILAL SANDHYA , s8058524 01. DISCRETE STRUCTURES PP 100 40 63 P C 11. ENG MATHS III 100 40 81 P 02. COMPUTER ORGANIZATION PP 100 40 61 P C 12. COMPUTER GRAPHICS 100 40 55 P 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 40 86 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 46 P 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 72 P C 14. DATA STRUCTURES AND FILES 100 40 62 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 40 P C 15. DATA COMMUNICATION 100 40 58 P 06. DIGITAL LABORATORY 50 20 36 P C 25 21 P 16. PROCESSOR INTERFACING LABORATORY TW 10 TW 07. DIGITAL LABORATORY PR 50 20 42 P C 17. PROCESSOR INTERFACING LABORATORY OR 50 20 43 P 08. PROGRAMMING LABORATORY TW 50 20 29 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 20 P 50 20 40 P C 09. PROGRAMMING LABORATORY PR 19. DATA STRUCTURES AND FILES LAB PR 50 20 38 P 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 43 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 42 P 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 37 P

GRAND TOTAL = 1015/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS:

UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(INFORMATION TECHNOLOGY)

DATE : 14 AUG. 2012 CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 09 (489)

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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	S8058525	BURSE RASHMI KIRAN				MAI	DHURI		,	71100	758G	,		, PI	[CT	,	58058	525
	01. DISCF	ETE STRUCTURES	PP	100	40	84	РС	11	. ENG	MATHS	III			PP	100	40	79	Р
	02. COMPL	TER ORGANIZATION	PP	100	40	59	РС	12	. COMP	UTER G	RAPHICS	;		PP	100	40	71	Р
	03. DIGIT	AL ELECTRONICS & LOGIC DESI	GPP	100	40	67	РС	13	. PROC	ESSOR	ARCHITE	CTURE &	& INTER.	PP	100	40	55	Р
	04. FUNDA	MENTAL OF DATA STRUCTURES	PP	100	40	74	РС	14	. DATA	STRUC	TURES A	ND FIL	ES	PP	100	40	65	Р
	05. HUMAN	ITIES AND SOCIAL SCIENCES	PP	100	40	50	РС	15	. DATA	COMMU	NICATIO	N		PP	100	40	56	Р
	06. DIGIT	AL LABORATORY	TW	50	20	41	РС	16	. PROC	ESSOR	INTERFA	CING L	ABORATOR	Y TW	25	10	20	Р
	07. DIGIT	AL LABORATORY	PR	50	20	41	РС	17	. PROC	ESSOR	INTERFA	CING L	ABORATOR	Y OR	50	20	34	Р
	08. PROGE	AMMING LABORATORY	TW	50	20	39	РС	18	. DATA	STRUC	TURES A	ND FIL	ES LAB	TW	25	10	17	Р
	09. PROGE	AMMING LABORATORY	PR	50	20	33	РС	19	. DATA	STRUC	TURES A	ND FIL	ES LAB	PR	50	20	32	Р
	10. COMMU	NICATION AND LANGUAGE LAB.	TW	50	20	43	РС	20	. OBJE	CT ORI	ENTED P	ROGRAM	MING LAB	TW	50	20	42	Р
								21	. OBJE	CT ORI	ENTED P	ROGRAM	MING LAB	PR	50	20	39	Р

GRAND TOTAL = 1041/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS :

S8058526 CHANDAN PRITI DILIP				MA	LATI	, 71241755К , , , РІСТ , S8058526
01. DISCRETE STRUCTURES	PP	100	40	47	РС	11. ENG MATHS III PP 100 40 46 P
02. COMPUTER ORGANIZATION	PP	100	40	58	РС	12. COMPUTER GRAPHICS PP 100 40 54 P
03. DIGITAL ELECTRONICS & LOGIC DESI	[GPP	100	40	43	РС	13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 58 P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	63	РС	14. DATA STRUCTURES AND FILES PP 100 40 51 P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	50	РС	15. DATA COMMUNICATION PP 100 40 57 P
06. DIGITAL LABORATORY	TW	50	20	42	РС	16. PROCESSOR INTERFACING LABORATORY TW 25 10 23 P
07. DIGITAL LABORATORY	PR	50	20	39	РС	17. PROCESSOR INTERFACING LABORATORY OR 50 20 39 P
08. PROGRAMMING LABORATORY	TW	50	20	45	РС	18. DATA STRUCTURES AND FILES LAB TW 25 10 19 P
09. PROGRAMMING LABORATORY	PR	50	20	38	РС	19. DATA STRUCTURES AND FILES LAB PR 50 20 37 P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	43	РС	20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 40 P
						21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 45 P

GRAND TOTAL = 937/1500, RESULT: FIRST CLASS ORDN. 1 MARKS:

S8058527 CHAUDHARI PRANITA RAVISH	IANKAR			ME	ENA	, 71100764M , , PICT , S8058527
01. DISCRETE STRUCTURES	PP	100	40	45	Р	11. ENG MATHS III PP 100 40 40 P
02. COMPUTER ORGANIZATION	PP	100	40	40	РС	12. COMPUTER GRAPHICS PP 100 40 45 P
03. DIGITAL ELECTRONICS & LOGIC DESI	[GPP	100	40	49	РС	13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 32 F
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	44	РС	14. DATA STRUCTURES AND FILES PP 100 40 32 F
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	40	Р	15. DATA COMMUNICATION PP 100 40 20 F
06. DIGITAL LABORATORY	TW	50	20	36	РС	16. PROCESSOR INTERFACING LABORATORY TW 25 10 14 P
07. DIGITAL LABORATORY	PR	50	20	35	РС	17. PROCESSOR INTERFACING LABORATORY OR 50 20 AA F
08. PROGRAMMING LABORATORY	TW	50	20	37	РС	18. DATA STRUCTURES AND FILES LAB TW 25 10 10 P
09. PROGRAMMING LABORATORY	PR	50	20	26	Р	19. DATA STRUCTURES AND FILES LAB PR 50 20 20 P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	37	РС	20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 36 P
						21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 35 P

GRAND TOTAL = 673/1500, RESULT: FAILS A.T.K.T.

UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(INFORMATION TECHNOLOGY) DATE: 14 AUG. 2012 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 10 (490) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71241756н , , ріст , S8058528 S8058528 CHAVAN SHRIKANT SHIVAJI MALAN 01. DISCRETE STRUCTURES PP 100 40 53 P C 11. ENG MATHS III PP 100 40 54 P 02. COMPUTER ORGANIZATION PP 100 40 50 P C 12. COMPUTER GRAPHICS 100 40 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 40 59 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 66 P 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 71 P C 14. DATA STRUCTURES AND FILES PP 100 40 62 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 44 P C PP 100 40 15. DATA COMMUNICATION 50 20 36 P C 25 10 06. DIGITAL LABORATORY 16. PROCESSOR INTERFACING LABORATORY TW 21 P TW 07. DIGITAL LABORATORY 50 20 32 P C 50 20 37 P PR 17. PROCESSOR INTERFACING LABORATORY OR 08. PROGRAMMING LABORATORY 50 20 34 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 19 P TW 09. PROGRAMMING LABORATORY PR 50 20 20 P C 19. DATA STRUCTURES AND FILES LAB PR 50 20 42 P 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 40 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 40 P 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 40 P GRAND TOTAL = 934/1500, RESULT: FIRST CLASS ORDN. 1 MARKS: S8058529 CHOKHAR PRITHVIRAJ BALASAHEB VIMAL , s8058529 01. DISCRETE STRUCTURES 23 F PP 100 40 05 F PP 100 40 11. ENG MATHS III 22 F 40 19 F 02. COMPUTER ORGANIZATION PP 100 40 12. COMPUTER GRAPHICS PP 100 13. PROCESSOR ARCHITECTURE & INTER. PP 100 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 40 25 F 40 16 F 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 100 40 40 P PP 40 40 P PP 100 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 40 P C 15. DATA COMMUNICATION 40 17 F 50 20 15 P 06. DIGITAL LABORATORY 23 P C 25 10 TW 16. PROCESSOR INTERFACING LABORATORY TW 50 20 22 P C 50 20 07. DIGITAL LABORATORY PR 17. PROCESSOR INTERFACING LABORATORY OR 05 F 50 20 29 P C 08. PROGRAMMING LABORATORY TW 18. DATA STRUCTURES AND FILES LAB TW 25 10 15 P 09. PROGRAMMING LABORATORY 50 20 30 P 19. DATA STRUCTURES AND FILES LAB PR 50 20 09 F PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 31 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 29 P 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 10 F GRAND TOTAL = 465/1500, RESULT: FAILS RESULT RESERVED FOR BKLG ORDN. 1 MARKS: , 71241757F , , PICT S8058530 DAHAWAD SUNIL NAMDEO VANITA , s8058530 01. DISCRETE STRUCTURES PP 100 40 23 F 11. ENG MATHS III 100 40 10 F 02. COMPUTER ORGANIZATION PP 100 40 57 P C 12. COMPUTER GRAPHICS PP 100 40 40 P 100 40 40 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 54 P 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 54 P 14. DATA STRUCTURES AND FILES PP 100 40 40 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 42 P C PP 100 40 43 P 15. DATA COMMUNICATION 06. DIGITAL LABORATORY 50 20 35 P C 25 10 13 P TW 16. PROCESSOR INTERFACING LABORATORY TW 50 20 22 P C 17. PROCESSOR INTERFACING LABORATORY OR 50 20 07. DIGITAL LABORATORY PR 06 F 18. DATA STRUCTURES AND FILES LAB TW 25 10 08. PROGRAMMING LABORATORY TW 50 20 38 P C 15 P 05 F 50 20 32 P 19. DATA STRUCTURES AND FILES LAB PR 50 20 09. PROGRAMMING LABORATORY PR

GRAND TOTAL = 659/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS:

10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 39 P C

20. OBJECT ORIENTED PROGRAMMING LAB TW

21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 16 F

50 20 35 P

DATE: 14 AUG. 2012 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 11 (491)

NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8058531 DARDA MAYOOR MONISH				KΑ	LPANA	, 71045410E ,	, P.	ICT	, 9	805853	31
01. DISCRETE STRUCTURES	PP	100	40	40	Р	11. ENG MATHS III	PP	100	40	40 F	P
02. COMPUTER ORGANIZATION	PP	100	40	41	РС	12. COMPUTER GRAPHICS	PP	100	40	49 F	Ρ
03. DIGITAL ELECTRONICS & LOGIC DE	SIGPP	100	40	44	РС	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	40 F	Ρ

04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 46 P C 14. DATA STRUCTURES AND FILES PP 100 40 43 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 P C 15. DATA COMMUNICATION PP 100 40 42 P

06. DIGITAL LABORATORY TW 50 20 23 P C 16. PROCESSOR INTERFACING LABORATORY TW 25 10 10 P 07. DIGITAL LABORATORY PR 50 20 40 P C 17. PROCESSOR INTERFACING LABORATORY OR 50 20 07 F 08. PROGRAMMING LABORATORY TW 50 20 24 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 10 P 09. PROGRAMMING LABORATORY PR 50 20 23 P C 19. DATA STRUCTURES AND FILES LAB PR 50 20 AA F

10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 27 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 23 P 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 07 F

GRAND TOTAL = 619/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS:

S80)58532 DHANAVE PRATIK BALASAHEB	;			UJ.	JWALLA		, 71100783н	, ,	ΡI	CT.	, :	8058	532
01.	DISCRETE STRUCTURES	PP	100	40	54	Р	11.	ENG MATHS III		PP	100	40	43	Р
02.	COMPUTER ORGANIZATION	PP	100	40	47	PC	12.	COMPUTER GRAPHICS	5	PP	100	40	50	Р
03.	DIGITAL ELECTRONICS & LOGIC DESI	GPP	100	40	54	PC	13.	PROCESSOR ARCHITE	CTURE & INTER.	PP	100	40	52	Р
04.	FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	57	PC	14.	DATA STRUCTURES A	AND FILES	PP	100	40	46	Р
05.	HUMANITIES AND SOCIAL SCIENCES	PP	100	40	42	PC	15.	DATA COMMUNICATIO	N	PP	100	40	49	Р
06.	DIGITAL LABORATORY	TW	50	20	30	PC	16.	PROCESSOR INTERFA	CING LABORATORY	′ TW	25	10	10	Р
07.	DIGITAL LABORATORY	PR	50	20	40	PC	17.	PROCESSOR INTERFA	CING LABORATORY	OR	50	20	32	Р
08.	PROGRAMMING LABORATORY	TW	50	20	30	PC	18.	DATA STRUCTURES A	AND FILES LAB	TW	25	10	12	Р
09.	PROGRAMMING LABORATORY	PR	50	20	28	P C	19.	DATA STRUCTURES A	AND FILES LAB	PR	50	20	20	Р
10.	COMMUNICATION AND LANGUAGE LAB.	TW	50	20	35	P C	20.	OBJECT ORIENTED F	ROGRAMMING LAB	TW	50	20	38	Р
							21.	OBJECT ORIENTED F	PROGRAMMING LAB	PR	50	20	33	Р

GRAND TOTAL = 802/1500, RESULT: SECOND CLASS

ORDN. 1 MARKS:

S8058533 DHAWALE PRACHI PRAMOD				ME	ENA	, 71100784F , , PICT , S805	8533
01. DISCRETE STRUCTURES	PP	100	40	40	РС	11. ENG MATHS III PP 100 40 55	Р
02. COMPUTER ORGANIZATION	PP	100	40	58	РС	12. COMPUTER GRAPHICS PP 100 40 53	Р
03. DIGITAL ELECTRONICS & LOGIC DESI	GPP	100	40	74	РС	13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 49	Р
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	65	РС	14. DATA STRUCTURES AND FILES PP 100 40 69	Р
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	46	РС	15. DATA COMMUNICATION PP 100 40 55	Р
06. DIGITAL LABORATORY	TW	50	20	33	РС	16. PROCESSOR INTERFACING LABORATORY TW 25 10 20	Р
07. DIGITAL LABORATORY	PR	50	20	40	РС	17. PROCESSOR INTERFACING LABORATORY OR 50 20 40	Р
08. PROGRAMMING LABORATORY	TW	50	20	38	РС	18. DATA STRUCTURES AND FILES LAB TW 25 10 18	Р
09. PROGRAMMING LABORATORY	PR	50	20	28	РС	19. DATA STRUCTURES AND FILES LAB PR 50 20 32	Р
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	38	P C	20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 36	Р

GRAND TOTAL = 929/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS:

21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 42 P

UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(INFORMATION TECHNOLOGY)

DATE : 14 AUG. 2012 CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 12 (492)

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8058534 DHEPLE ARCHANA HARIBHAU MEENA , 71100785D , PICT , S8058534

5805	08534 DHEPLE ARCHANA HARIBHAU				MEI	ENA		, /1100/85D	,	, P	ICT	,	S8058	534
01.	DISCRETE STRUCTURES	PP	100	40	40	РС	11.	ENG MATHS III		PP	100	40	40	Р
02.	COMPUTER ORGANIZATION	PP	100	40	52	РС	12.	COMPUTER GRAPHICS	S	PP	100	40	49	Р
03.	DIGITAL ELECTRONICS & LOGIC DESI	GPP	100	40	42	РС	13.	PROCESSOR ARCHITI	ECTURE & INTER.	PP	100	40	51	Р
04.	FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	54	РС	14.	DATA STRUCTURES	AND FILES	PP	100	40	51	Р
05.	HUMANITIES AND SOCIAL SCIENCES	PP	100	40	53	РС	15.	DATA COMMUNICATIO	ON	PP	100	40	47	Р
06.	DIGITAL LABORATORY	TW	50	20	36	P C	16.	PROCESSOR INTERFA	ACING LABORATOR	Y TW	25	10	15	Р
07.	DIGITAL LABORATORY	PR	50	20	35	P C	17.	PROCESSOR INTERFA	ACING LABORATOR	Y OR	50	20	22	Р
08.	PROGRAMMING LABORATORY	TW	50	20	33	P C	18.	DATA STRUCTURES	AND FILES LAB	TW	25	10	15	Р
09.	PROGRAMMING LABORATORY	PR	50	20	22	P C	19.	DATA STRUCTURES	AND FILES LAB	PR	50	20	25	Р
10.	COMMUNICATION AND LANGUAGE LAB.	TW	50	20	39	P C	20.	OBJECT ORIENTED I	PROGRAMMING LAB	TW	50	20	36	Р
							21.	OBJECT ORIENTED I	PROGRAMMING LAB	PR	50	20	30	Р

GRAND TOTAL = 787/1500, RESULT: SECOND CLASS

ORDN. 1 MARKS :

S8058535 GAIKWAD NETRANJALI SHAMRAO MOHINI , 71241758D , , PICT , S8058535 01. DISCRETE STRUCTURES PP 100 40 56 P C 11. ENG MATHS III 100 40 55 P PP 02. COMPUTER ORGANIZATION PP 100 40 63 P C PP 100 40 12. COMPUTER GRAPHICS 40 74 P.C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 57 P 14. DATA STRUCTURES AND FILES PP 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 79 P C 100 40 67 P 15. DATA COMMUNICATION 40 40 P C PP 100 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 62 P 50 20 40 P C 22 P 06. DIGITAL LABORATORY 25 10 TW 16. PROCESSOR INTERFACING LABORATORY TW 07. DIGITAL LABORATORY 50 20 43 P C 20 PR 17. PROCESSOR INTERFACING LABORATORY OR 50 38 P 50 20 42 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 19 P 08. PROGRAMMING LABORATORY TW 09. PROGRAMMING LABORATORY PR 50 20 34 P C 19. DATA STRUCTURES AND FILES LAB PR 50 20 35 P 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 42 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 43 P 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 41 P

GRAND TOTAL = 1016/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS:

, 71100793E , , PICT , s8058536 S8058536 GAIKWAD PIYUSH GHANASHYAM VASUDHA 01. DISCRETE STRUCTURES PP 100 40 46 P C 11. ENG MATHS III PP 100 40 40 P 02. COMPUTER ORGANIZATION PP 100 40 48 P C 12. COMPUTER GRAPHICS PP 100 40 49 P 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 40 56 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 42 P 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 51 P C 14. DATA STRUCTURES AND FILES PP 100 40 51 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 59 P C 15. DATA COMMUNICATION PP 100 40 53 P TW 50 20 34 P C 06. DIGITAL LABORATORY 25 10 14 P 16. PROCESSOR INTERFACING LABORATORY TW 07. DIGITAL LABORATORY 50 20 22 P 17. PROCESSOR INTERFACING LABORATORY OR 50 20 25 P PR 18. DATA STRUCTURES AND FILES LAB TW 25 10 15 P 08. PROGRAMMING LABORATORY TW 50 20 39 P C PR 50 20 20 P C 19. DATA STRUCTURES AND FILES LAB PR 50 20 38 P 09. PROGRAMMING LABORATORY 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 40 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 31 P 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 38 P

GRAND TOTAL = 811/1500, RESULT: SECOND CLASS

DATE: 14 AUG. 2012 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 13 (493)

NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER PRAMILA , 71100796к , , ріст , s8058537 S8058537 GANGARDE MAYURI MUKUND 01. DISCRETE STRUCTURES PP 100 40 53 P C 11. ENG MATHS III PP 100 40 40 P 02. COMPUTER ORGANIZATION PP 100 40 40 P C 12. COMPUTER GRAPHICS PP 100 40 100 40 55 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 49 P 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 48 P C 14. DATA STRUCTURES AND FILES PP 100 40 43 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 59 P C PP 100 40 45 P 15. DATA COMMUNICATION 50 20 42 P C 25 10 16 P 06. DIGITAL LABORATORY 16. PROCESSOR INTERFACING LABORATORY TW TW 07. DIGITAL LABORATORY 50 20 40 P C 50 20 30 P PR 17. PROCESSOR INTERFACING LABORATORY OR 08. PROGRAMMING LABORATORY 50 20 39 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 17 P TW 09. PROGRAMMING LABORATORY PR 50 20 30 P 19. DATA STRUCTURES AND FILES LAB PR 50 20 12 F 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 44 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 39 P 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 15 F GRAND TOTAL = 810/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS: S8058538 GAWANDE ANAGHA ARVIND MAYA , 71100800M , , PICT , s8058538 01. DISCRETE STRUCTURES PP 100 40 47 P 11. ENG MATHS III PP 100 40 23 F PP 100 40 53 P 02. COMPUTER ORGANIZATION PP 100 40 42 P C 12. COMPUTER GRAPHICS 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 40 58 P C 52 P 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 53 P 100 40 43 P C PP 40 40 47 P C PP 100 05. HUMANITIES AND SOCIAL SCIENCES PP 100 15. DATA COMMUNICATION 40 51 P 50 20 33 P C 18 P 06. DIGITAL LABORATORY 25 10 TW 16. PROCESSOR INTERFACING LABORATORY TW 50 20 32 P 50 20 28 P 07. DIGITAL LABORATORY PR 17. PROCESSOR INTERFACING LABORATORY OR 50 20 38 P C 08. PROGRAMMING LABORATORY TW 18. DATA STRUCTURES AND FILES LAB TW 25 10 16 P 09. PROGRAMMING LABORATORY PR 50 20 23 P 19. DATA STRUCTURES AND FILES LAB PR 50 20 38 P 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 39 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 36 P 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 37 P GRAND TOTAL = 807/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS: , 71100802H , , PICT S8058539 GHODAKE PRAJAKTA DHANANJAY SWATI , s8058539 01. DISCRETE STRUCTURES PP 100 40 49 P 11. ENG MATHS III 100 40 24 F 02. COMPUTER ORGANIZATION PP 100 40 47 P C 12. COMPUTER GRAPHICS PP 100 40 45 P 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 40 43 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 40 P 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 40 P C 14. DATA STRUCTURES AND FILES PP 100 40 43 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 45 P C PP 100 40 26 F 15. DATA COMMUNICATION 06. DIGITAL LABORATORY 50 20 30 P C 25 10 10 P TW 16. PROCESSOR INTERFACING LABORATORY TW 50 20 25 P 17. PROCESSOR INTERFACING LABORATORY OR 50 20 06 F 07. DIGITAL LABORATORY PR 18. DATA STRUCTURES AND FILES LAB TW 25 10 13 P 08. PROGRAMMING LABORATORY TW 50 20 24 P C 19. DATA STRUCTURES AND FILES LAB PR 50 20 P 50 20 30 P 09. PROGRAMMING LABORATORY PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 31 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 30 P 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 10 F GRAND TOTAL = 631/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS:

UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(INFORMATION TECHNOLOGY)

DATE : 14 AUG. 2012 CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 14 (494)

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S80	58540 GHODESWAR MAYUR DEEPAK				RA	TNAPRABHA	, 71241759в , , , ріст , s80	058540
01.	DISCRETE STRUCTURES	PP	100	40	55	Р	11. ENG MATHS III PP 100 40 4	45 P
02.	COMPUTER ORGANIZATION	PP	100	40	53	P C	12. COMPUTER GRAPHICS PP 100 40	53 P
03.	DIGITAL ELECTRONICS & LOGIC DESI	GPP	100	40	49	P C	13. PROCESSOR ARCHITECTURE & INTER. PP 100 40	53 P
04.	FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	61	P C	14. DATA STRUCTURES AND FILES PP 100 40	54 P
05.	HUMANITIES AND SOCIAL SCIENCES	PP	100	40	53	P C	15. DATA COMMUNICATION PP 100 40	52 P
06.	DIGITAL LABORATORY	TW	50	20	36	P C	16. PROCESSOR INTERFACING LABORATORY TW 25 10	L4 P
07.	DIGITAL LABORATORY	PR	50	20	30	P C	17. PROCESSOR INTERFACING LABORATORY OR 50 20 2	26 P
08.	PROGRAMMING LABORATORY	TW	50	20	38	P C	18. DATA STRUCTURES AND FILES LAB TW 25 10	L8 P
09.	PROGRAMMING LABORATORY	PR	50	20	22	P C	19. DATA STRUCTURES AND FILES LAB PR 50 20	39 P
10.	COMMUNICATION AND LANGUAGE LAB.	TW	50	20	38	P C	20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20	39 P
							21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20	38 P

GRAND TOTAL = 866/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS:

							•
S8058541 GHODKE AMRUTA PRAKASH				JY	OTI	, 71241760F , , PICT , S8058541	
01. DISCRETE STRUCTURES	PP	100	40	57	РС	11. ENG MATHS III PP 100 40 78 P	
02. COMPUTER ORGANIZATION	PP	100	40	70	РС	12. COMPUTER GRAPHICS PP 100 40 65 P	
03. DIGITAL ELECTRONICS & LOGIC DESI	GPP	100	40	60	РС	13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 58 P	
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	70	РС	14. DATA STRUCTURES AND FILES PP 100 40 59 P	
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	59	РС	15. DATA COMMUNICATION PP 100 40 60 P	
06. DIGITAL LABORATORY	TW	50	20	37	РС	16. PROCESSOR INTERFACING LABORATORY TW 25 10 24 P	
07. DIGITAL LABORATORY	PR	50	20	38	РС	17. PROCESSOR INTERFACING LABORATORY OR 50 20 42 P	
08. PROGRAMMING LABORATORY	TW	50	20	41	РС	18. DATA STRUCTURES AND FILES LAB TW 25 10 22 P	
09. PROGRAMMING LABORATORY	PR	50	20	25	РС	19. DATA STRUCTURES AND FILES LAB PR 50 20 40 P	
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	41	РС	20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 45 P	
						21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 34 P	

GRAND TOTAL = 1025/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS :

S8058542 GOENKA PALAK DILIP				MA	 NJU	, 71100807J , PICT , S8058542	
01. DISCRETE STRUCTURES	PP	100	40	59	РС	11. ENG MATHS III PP 100 40 68 P	
02. COMPUTER ORGANIZATION	PP	100	40	49	РС	12. COMPUTER GRAPHICS PP 100 40 40 P	
03. DIGITAL ELECTRONICS & LOGIC DESI	GPP	100	40	57	РС	13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 51 P	
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	54	РС	14. DATA STRUCTURES AND FILES PP 100 40 54 P	
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	44	РС	15. DATA COMMUNICATION PP 100 40 46 P	
06. DIGITAL LABORATORY	TW	50	20	33	P C	16. PROCESSOR INTERFACING LABORATORY TW 25 10 17 P	
07. DIGITAL LABORATORY	PR	50	20	25	РС	17. PROCESSOR INTERFACING LABORATORY OR 50 20 25 P	
08. PROGRAMMING LABORATORY	TW	50	20	39	РС	18. DATA STRUCTURES AND FILES LAB TW 25 10 17 P	
09. PROGRAMMING LABORATORY	PR	50	20	38	Р	19. DATA STRUCTURES AND FILES LAB PR 50 20 05 F	
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	39	P C	20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 34 P	
						21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 31 P	

GRAND TOTAL = 825/1500, RESULT: FAILS A.T.K.T.

UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(INFORMATION TECHNOLOGY) DATE: 14 AUG. 2012 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 15 (495) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71100810J , , PICT , s8058543 S8058543 GORADE TUSHAR BHAUSAHEB ALKA 01. DISCRETE STRUCTURES PP 100 40 40 P 11. ENG MATHS III PP 100 40 10 F 02. COMPUTER ORGANIZATION PP 100 40 23 F 12. COMPUTER GRAPHICS 100 40 40 P 100 40 42 P 100 40 31 F 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 13. PROCESSOR ARCHITECTURE & INTER. PP 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 46 P PP 100 40 40 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 40 P 15. DATA COMMUNICATION PP 100 40 23 F 50 20 32 P C 25 10 10 P 06. DIGITAL LABORATORY 16. PROCESSOR INTERFACING LABORATORY TW TW 50 20 24 P 07. DIGITAL LABORATORY 50 20 05 F PR 17. PROCESSOR INTERFACING LABORATORY OR 08. PROGRAMMING LABORATORY 50 20 22 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 13 P TW 09. PROGRAMMING LABORATORY PR 50 20 28 P 19. DATA STRUCTURES AND FILES LAB PR 50 20 34 P 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 29 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 35 P 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 35 P GRAND TOTAL = 602/1500, RESULT: FAILS RESULT RESERVED FOR BKLG ORDN. 1 MARKS : S8058544 GUJRATHI DHANESH NARESH JYOTI , s8058544 01. DISCRETE STRUCTURES PP 100 40 52 P C 11. ENG MATHS III PP 100 40 69 P PP 100 40 02. COMPUTER ORGANIZATION PP 100 40 51 P C 12. COMPUTER GRAPHICS 65 P 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 40 68 P C 58 P 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 70 P C 100 100 40 PP 40 46 P PP 100 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 53 P C 15. DATA COMMUNICATION 40 61 P 50 20 39 P C 23 P 06. DIGITAL LABORATORY 25 10 TW 16. PROCESSOR INTERFACING LABORATORY TW 50 20 38 P C 50 20 07. DIGITAL LABORATORY PR 17. PROCESSOR INTERFACING LABORATORY OR 33 P 50 20 42 P C 08. PROGRAMMING LABORATORY TW 18. DATA STRUCTURES AND FILES LAB TW 25 10 20 P 09. PROGRAMMING LABORATORY PR 50 20 40 P C 19. DATA STRUCTURES AND FILES LAB PR 50 20 37 P 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 44 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 44 P 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 38 P GRAND TOTAL = 991/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS: , 71100815K , , PICT , S8058545 S8058545 GUTTE ASHWINI ASHOKRAO SUNANDA 01. DISCRETE STRUCTURES PP 100 40 40 P C 11. ENG MATHS III 100 40 60 P 02. COMPUTER ORGANIZATION PP 100 40 51 P C 12. COMPUTER GRAPHICS PP 100 40 44 P 100 40 66 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 51 P 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 60 P C 14. DATA STRUCTURES AND FILES PP 100 40 56 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 52 P C 100 40 48 P 15. DATA COMMUNICATION PP 06. DIGITAL LABORATORY 50 20 39 P C 25 10 14 P TW 16. PROCESSOR INTERFACING LABORATORY TW 50 20 35 P C 17. PROCESSOR INTERFACING LABORATORY OR 50 20 07 F 07. DIGITAL LABORATORY PR 18. DATA STRUCTURES AND FILES LAB TW 25 10 08. PROGRAMMING LABORATORY TW 50 20 33 P C 17 P 50 20 30 P C 19. DATA STRUCTURES AND FILES LAB PR 50 20 30 P 09. PROGRAMMING LABORATORY PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 40 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 41 P 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 40 P GRAND TOTAL = 854/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER MANISHA , 71100826E , , PICT , s8058549 S8058549 JAIN BHUSHAN SUMATILAL 01. DISCRETE STRUCTURES PP 100 40 65 P C 11. ENG MATHS III PP 100 40 56 P PP 100 40 60 P C 12. COMPUTER GRAPHICS 100 40 02. COMPUTER ORGANIZATION 68 P 100 40 68 P C 100 40 58 P 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 13. PROCESSOR ARCHITECTURE & INTER. PP 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 66 P C 14. DATA STRUCTURES AND FILES PP 100 40 60 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 60 P C PP 100 40 15. DATA COMMUNICATION 50 20 33 P C 25 10 19 P 06. DIGITAL LABORATORY 16. PROCESSOR INTERFACING LABORATORY TW TW 07. DIGITAL LABORATORY 50 20 38 P C 50 20 39 P PR 17. PROCESSOR INTERFACING LABORATORY OR 08. PROGRAMMING LABORATORY 50 20 37 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 17 P TW 09. PROGRAMMING LABORATORY PR 50 20 25 P C 19. DATA STRUCTURES AND FILES LAB PR 50 20 38 P 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 39 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 39 P 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 37 P GRAND TOTAL = 977+05/1500, RESULT: FIRST CLASS[0.163] ORDN. 1 MARKS: S8058550 JAIPURIA ROHIT SHARADKUMAR AMITA , s8058550 01. DISCRETE STRUCTURES 47 P 11. ENG MATHS III PP 100 40 PP 100 40 40 P PP 100 02. COMPUTER ORGANIZATION PP 100 40 40 P 12. COMPUTER GRAPHICS 40 40 P 40 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 40 40 P 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 42 P 100 100 40 PP 40 43 P 40 100 40 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 43 P C 15. DATA COMMUNICATION 40 50 20 33 P C 10 19 P 06. DIGITAL LABORATORY 25 TW 16. PROCESSOR INTERFACING LABORATORY TW 50 20 35 P C 20 29 P 07. DIGITAL LABORATORY PR 17. PROCESSOR INTERFACING LABORATORY OR 50 50 20 25 P C 08. PROGRAMMING LABORATORY TW 18. DATA STRUCTURES AND FILES LAB TW 25 10 15 P 09. PROGRAMMING LABORATORY PR 50 20 22 P C 19. DATA STRUCTURES AND FILES LAB PR 50 20 07 F 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 33 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 39 P 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 28 P GRAND TOTAL = 700/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS: , 71100829K , , PICT S8058551 JAMBHULKAR SAYALI SUNIL SHUBHAANGI , S8058551 01. DISCRETE STRUCTURES PP 100 40 71 P C 11. ENG MATHS III 100 40 76 P 02. COMPUTER ORGANIZATION PP 100 40 68 P C 12. COMPUTER GRAPHICS PP 100 40 63 P 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 40 78 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 45 P 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 77 P C 14. DATA STRUCTURES AND FILES PP 100 40 59 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 66 P C 100 40 62 P 15. DATA COMMUNICATION PP 50 20 37 P C 23 P 06. DIGITAL LABORATORY 25 10 TW 16. PROCESSOR INTERFACING LABORATORY TW 50 20 40 P C 17. PROCESSOR INTERFACING LABORATORY OR 50 20 42 P 07. DIGITAL LABORATORY PR 18. DATA STRUCTURES AND FILES LAB TW 25 10 19 P 08. PROGRAMMING LABORATORY TW 50 20 41 P C 50 20 20 P C 19. DATA STRUCTURES AND FILES LAB PR 50 20 40 P 09. PROGRAMMING LABORATORY PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 42 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 39 P 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 41 P GRAND TOTAL = 1049/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS:

UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(INFORMATION TECHNOLOGY) DATE: 14 AUG. 2012 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 18 (498) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , s8058552 S8058552 JOSHI AKHIL NIRANJAN NEHA 01. DISCRETE STRUCTURES PP 100 40 49 P PP 100 40 40 P C 11. ENG MATHS III 02. COMPUTER ORGANIZATION PP 100 40 48 P C 12. COMPUTER GRAPHICS PP 100 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 40 66 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 49 P 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 53 P C 14. DATA STRUCTURES AND FILES PP 100 40 56 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 49 PC 15. DATA COMMUNICATION PP 100 40 66 P

08. PROGRAMMING LABORATORY TW 50 20 25 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 17 P 09. PROGRAMMING LABORATORY PR 50 20 37 P 19. DATA STRUCTURES AND FILES LAB PR 50 20 35 P 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 36 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 41 P 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 27 P GRAND TOTAL = 873/1500, RESULT: HIGHER SECOND CLASS

16. PROCESSOR INTERFACING LABORATORY TW

17. PROCESSOR INTERFACING LABORATORY OR

25 10 14 P

50 20 32 P

50 20 33 P C

50 20 40 P C

TW

PR

ORDN. 1 MARKS:

06. DIGITAL LABORATORY

07. DIGITAL LABORATORY

ORDN. 1 MARKS:

S8058553 KABRE AMEYA ATUL MANGALA , 71100838J , PICT , S8058553
01. DISCRETE STRUCTURES PP 100 40 61 P C 11. ENG MATHS III PP 100 40 56 P

PP 100 40 61 P C PP 100 40 02. COMPUTER ORGANIZATION 12. COMPUTER GRAPHICS 61 P 13. PROCESSOR ARCHITECTURE & INTER. PP 100 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 40 65 P C 40 55 P 100 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 40 70 P C 100 40 58 P 100 PP 100 40 58 P C PP 100 40 52 P 05. HUMANITIES AND SOCIAL SCIENCES PP 15. DATA COMMUNICATION 50 20 40 P C 25 10 23 P 06. DIGITAL LABORATORY TW 16. PROCESSOR INTERFACING LABORATORY TW 07. DIGITAL LABORATORY 50 20 35 P C 50 20 41 P PR 17. PROCESSOR INTERFACING LABORATORY OR 50 20 43 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 21 P 08. PROGRAMMING LABORATORY TW

09. PROGRAMMING LABORATORY PR 50 20 44 P C 19. DATA STRUCTURES AND FILES LAB PR 50 20 45 P 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 43 P C 20. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 45 P 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 45 P

GRAND TOTAL = 1021/1500, RESULT: FIRST CLASS WITH DISTINCTION

S8058554 KADAM CHIRAG RAM				APS	SARA	, 71100839G , , PICT , S8058554	
01. DISCRETE STRUCTURES	PP	100	40	68	РС	11. ENG MATHS III PP 100 40 80 P	
02. COMPUTER ORGANIZATION	PP	100	40	75	РС	12. COMPUTER GRAPHICS PP 100 40 62 P	
03. DIGITAL ELECTRONICS & LOGIC DESI	GPP	100	40	77	РС	13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 61 P	
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	71	РС	14. DATA STRUCTURES AND FILES PP 100 40 60 P	
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	55	РС	15. DATA COMMUNICATION PP 100 40 62 P	
06. DIGITAL LABORATORY	TW	50	20	41	РС	16. PROCESSOR INTERFACING LABORATORY TW 25 10 22 P	
07. DIGITAL LABORATORY	PR	50	20	42	РС	17. PROCESSOR INTERFACING LABORATORY OR 50 20 44 P	
08. PROGRAMMING LABORATORY	TW	50	20	42	РС	18. DATA STRUCTURES AND FILES LAB TW 25 10 20 P	
09. PROGRAMMING LABORATORY	PR	50	20	42	РС	19. DATA STRUCTURES AND FILES LAB PR 50 20 43 P	
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	43	РС	20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 45 P	
						21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 44 P	

GRAND TOTAL = 1099/1500, RESULT: FIRST CLASS WITH DISTINCTION

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OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S8058555 KAHATE SAUMITRA SHYAMKAN	IT			JY	OTI			, 71100841J	,	, P	ICT	,	s8058	555
01. DISCRETE STRUCTURES	PP	100	40	62	P C	11	1.	ENG MATHS III		PP	100	40	54	Р
02. COMPUTER ORGANIZATION	PP	100	40	63	P C	12	2.	COMPUTER GRAPHICS		PP	100	40	59	Р
03. DIGITAL ELECTRONICS & LOGIC DESI	GPP	100	40	64	P C	13	3.	PROCESSOR ARCHITE	CTURE & INTER.	PP	100	40	55	Р
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	65	P C	14	4.	DATA STRUCTURES A	ND FILES	PP	100	40	60	Р
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	62	P C	15	5.	DATA COMMUNICATIO	N	PP	100	40	60	Р
06. DIGITAL LABORATORY	TW	50	20	40	P C	16	6.	PROCESSOR INTERFA	CING LABORATORY	Y TW	25	10	21	Р
07. DIGITAL LABORATORY	PR	50	20	38	P C	17	7.	PROCESSOR INTERFA	CING LABORATORY	Y OR	50	20	40	Р
08. PROGRAMMING LABORATORY	TW	50	20	38	P C	18	8.	DATA STRUCTURES A	ND FILES LAB	TW	25	10	20	Р
09. PROGRAMMING LABORATORY	PR	50	20	22	P C	19	9.	DATA STRUCTURES A	ND FILES LAB	PR	50	20	44	Р
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	43	РС	20	0.	OBJECT ORIENTED P	ROGRAMMING LAB	TW	50	20	41	Р
						21	1.	OBJECT ORIENTED P	ROGRAMMING LAB	PR	50	20	38	Р

GRAND TOTAL = 989/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS:

S8058557 KAUTHALE TRUPTI SAMPAT JAYASHRI , 71100853B , , PICT , s8058557 01. DISCRETE STRUCTURES PP 100 40 48 P C 11. ENG MATHS III 100 40 40 P 02. COMPUTER ORGANIZATION PP 100 40 59 P C 100 40 47 P 12. COMPUTER GRAPHICS 100 40 52 P C 100 40 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 13. PROCESSOR ARCHITECTURE & INTER. PP 56 P 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 61 P C 100 40 47 P 14. DATA STRUCTURES AND FILES 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 55 P C 15. DATA COMMUNICATION 100 06. DIGITAL LABORATORY 50 20 36 P C 16. PROCESSOR INTERFACING LABORATORY TW 25 10 18 P TW 50 20 40 P C 07. DIGITAL LABORATORY 50 20 PR 17. PROCESSOR INTERFACING LABORATORY OR 31 P 08. PROGRAMMING LABORATORY 50 20 35 P C 25 10 17 P TW 18. DATA STRUCTURES AND FILES LAB TW 19. DATA STRUCTURES AND FILES LAB PR 50 20 32 P 09. PROGRAMMING LABORATORY PR 50 20 32 P C 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 40 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 38 P 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 40 P

GRAND TOTAL = 868/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS:

S8058558 KHADKIWALA HATIM JAINUDI	 OTN			 ΜΔ	 SUMA	
01. DISCRETE STRUCTURES	PP	100	40		P C	11. ENG MATHS III PP 100 40 86 P
02. COMPUTER ORGANIZATION	PP	100	40		P C	12. COMPUTER GRAPHICS PP 100 40 60 P
03. DIGITAL ELECTRONICS & LOGIC DES	IGPP	100	40	77	РС	13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 46 P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	72	РС	14. DATA STRUCTURES AND FILES PP 100 40 59 P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	55	РС	15. DATA COMMUNICATION PP 100 40 52 P
06. DIGITAL LABORATORY	TW	50	20	42	P C	16. PROCESSOR INTERFACING LABORATORY TW 25 10 23 P
07. DIGITAL LABORATORY	PR	50	20	42	РС	17. PROCESSOR INTERFACING LABORATORY OR 50 20 41 P
08. PROGRAMMING LABORATORY	TW	50	20	47	РС	18. DATA STRUCTURES AND FILES LAB TW 25 10 20 P
09. PROGRAMMING LABORATORY	PR	50	20	42	P C	19. DATA STRUCTURES AND FILES LAB PR 50 20 46 P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	47	РС	20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 43 P
						21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 42 P

GRAND TOTAL = 1082/1500, RESULT: FIRST CLASS WITH DISTINCTION

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER PRAMILA , 71100857E , , PICT , s8058559 S8058559 KHADSE MAYUR DILIP 01. DISCRETE STRUCTURES 40 20 F PP 100 40 40 P C 11. ENG MATHS III PP 100 02. COMPUTER ORGANIZATION PP 100 40 40 P C 12. COMPUTER GRAPHICS PP 100 40 41 P 100 40 45 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 41 P 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 51 P C 14. DATA STRUCTURES AND FILES PP 100 40 42 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 51 P C PP 100 40 40 P 15. DATA COMMUNICATION 50 20 23 P C 25 10 12 P 06. DIGITAL LABORATORY 16. PROCESSOR INTERFACING LABORATORY TW TW 07. DIGITAL LABORATORY 50 20 38 P C 50 20 26 P PR 17. PROCESSOR INTERFACING LABORATORY OR 08. PROGRAMMING LABORATORY 50 20 28 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 11 P TW 09. PROGRAMMING LABORATORY PR 50 20 29 P C 19. DATA STRUCTURES AND FILES LAB PR 50 20 27 P 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 32 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 29 P 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 25 P GRAND TOTAL = 691/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS: S8058560 KHAN RUQAIYA IQBAL , 71100860E , , PICT SHAHEEN , s8058560 PP 100 40 73 P C 11. ENG MATHS III PP 100 40 83 P 01. DISCRETE STRUCTURES 40 58 P C PP 100 40 02. COMPUTER ORGANIZATION PP 100 12. COMPUTER GRAPHICS 55 P 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 40 76 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 100 70 P 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 40 77 P C 100 51 P 100 PP 40 40 55 P C PP 100 59 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 15. DATA COMMUNICATION 40 50 20 45 P C 23 P 06. DIGITAL LABORATORY 25 10 TW 16. PROCESSOR INTERFACING LABORATORY TW 50 20 45 P C 50 20 07. DIGITAL LABORATORY PR 17. PROCESSOR INTERFACING LABORATORY OR 42 P 50 20 47 P C 08. PROGRAMMING LABORATORY TW 18. DATA STRUCTURES AND FILES LAB TW 25 10 22 P 09. PROGRAMMING LABORATORY PR 50 20 44 P C 19. DATA STRUCTURES AND FILES LAB PR 50 20 46 P 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 46 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 46 P 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 40 P GRAND TOTAL = 1103/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS: , 71241761D , , PICT , S8058561 S8058561 KODITKAR POOJA NAMDEO JYOTI 01. DISCRETE STRUCTURES PP 100 40 46 P 11. ENG MATHS III PP 100 40 41 P 02. COMPUTER ORGANIZATION PP 100 40 55 P C 12. COMPUTER GRAPHICS PP 100 40 59 P 100 40 62 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 60 P 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 63 P C 14. DATA STRUCTURES AND FILES PP 100 40 52 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 59 P C PP 100 40 57 P 15. DATA COMMUNICATION 06. DIGITAL LABORATORY 50 20 39 P C 25 10 21 P TW 16. PROCESSOR INTERFACING LABORATORY TW 50 20 40 P C 17. PROCESSOR INTERFACING LABORATORY OR 50 20 25 P 07. DIGITAL LABORATORY PR 18. DATA STRUCTURES AND FILES LAB TW 25 10 21 P 08. PROGRAMMING LABORATORY TW 50 20 41 P C 50 20 20 P C 19. DATA STRUCTURES AND FILES LAB PR 50 20 P 09. PROGRAMMING LABORATORY PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 40 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 43 P 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 21 P GRAND TOTAL = 885/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS:

UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(INFORMATION TECHNOLOGY) DATE: 14 AUG. 2012 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 21 (501)

DATE . 14 AUG. 2012	CENT	KE . PU	JINE I	NOIT	TOTE OF	- COMPUTER	R TECHNOLOGY, PUNE.	PAG	E NO.	21	()()1)
NOTE: FIRST LINE : SEAT NO., NAME O			-		-				•			
OTHER LINES: HEAD OF PASSING,	MAX.	MARKS	, MI	N. P.	ASS MAF	RKS, MAR	(S OBTAINED, P/F:PASS/FAIL, C:P	REVIO	US CAF	RRY C	VER	
S8058562 KOSHTI ANUJA RAMESH					NGAL		, 71045485G , ,	ΡI	CT	,	S80585	
01. DISCRETE STRUCTURES	PP	100	40	58	Р		ENG MATHS III		100	40	16	F
02. COMPUTER ORGANIZATION	PP	100	40	41	PC	12.	COMPUTER GRAPHICS	PP	100	40	27	F
03. DIGITAL ELECTRONICS & LOGIC DESI	GPP	100	40	53	PC	13.	PROCESSOR ARCHITECTURE & INTER.	PP	100	40	44	Р
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	50	P C	14.	DATA STRUCTURES AND FILES	PP	100	40	49	Р
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	54	P C	15.	DATA COMMUNICATION	PP	100	40	40	Р
06. DIGITAL LABORATORY	TW	50	20	34	P C	16.	PROCESSOR INTERFACING LABORATORY	TW	25	10	13	Р
07. DIGITAL LABORATORY	PR	50	20	22	Р	17.	PROCESSOR INTERFACING LABORATORY	OR	50	20	23	Р
08. PROGRAMMING LABORATORY	TW	50	20	37	PC	18.	DATA STRUCTURES AND FILES LAB	TW	25	10	17	Р
09. PROGRAMMING LABORATORY	PR	50	20	35	Р	19.	DATA STRUCTURES AND FILES LAB	PR	50	20	10	F
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	37	P C	20.	OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	36	Р
						21.	OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	20	Р
GRAND TOTAL = 716/1500, RESULT: FAILS	A.T.	K.T.										
ORDN. 1 MARKS :												
S8058563 KULKARNI MITALI SUHAS				RU	PA		, 71100872」 , , ,	ΡI	CT	,	S80585	563
01. DISCRETE STRUCTURES	PP	100	40	47	РС	11.	ENG MATHS III	PP	100	40	62	Р
02. COMPUTER ORGANIZATION	PP	100	40	51	РС	12.	COMPUTER GRAPHICS	PP	100	40	50	Р
03. DIGITAL ELECTRONICS & LOGIC DESI	GPP	100	40	54	РС	13.	PROCESSOR ARCHITECTURE & INTER.	PP	100	40	60	Р
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	56	РС	14.	DATA STRUCTURES AND FILES	PP	100	40	56	Р
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	47	РС	15.	DATA COMMUNICATION	PP	100	40	55	Р
06. DIGITAL LABORATORY	TW	50	20	40	РС	16.	PROCESSOR INTERFACING LABORATORY	TW	25	10	19	Р
07. DIGITAL LABORATORY	PR	50	20	40	РС	17.	PROCESSOR INTERFACING LABORATORY	OR	50	20	37	Р
08. PROGRAMMING LABORATORY	TW	50	20	37	РС	18.	DATA STRUCTURES AND FILES LAB	TW	25	10	18	Р
09. PROGRAMMING LABORATORY	PR	50	20	32	РС	19.	DATA STRUCTURES AND FILES LAB	PR	50	20	23	Р
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	43	РС	20.	OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	38	Р
						21.	OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	32	Р
GRAND TOTAL = $897+03/1500$, RESULT: FI	RST C	LASS	Го.21									
ORDN. 1 MARKS :		·										
S8058564 LAGWANKAR NIKHIL KISHOR					SHMI		, 71100878н , , ,		СТ		S80585	
01. DISCRETE STRUCTURES		100	40		РС	11.	ENG MATHS III	PP	100	40	68	
02. COMPUTER ORGANIZATION	PP	100	40		PC		COMPUTER GRAPHICS	PP	100	40	59	
03. DIGITAL ELECTRONICS & LOGIC DESI		100	40		PC		PROCESSOR ARCHITECTURE & INTER.		100	40	49	
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40		P C		DATA STRUCTURES AND FILES	PP	100	40	62	
05. HUMANITIES AND SOCIAL SCIENCES		100	40		P C		DATA COMMUNICATION	PP	100	40	54	
06. DIGITAL LABORATORY	TW	50	20		P C		PROCESSOR INTERFACING LABORATORY		25	10	23	
07. DIGITAL LABORATORY	PR	50	20		PC		PROCESSOR INTERFACING LABORATORY		50	20	35	
08. PROGRAMMING LABORATORY	TW		20		PC		DATA STRUCTURES AND FILES LAB	TW	25	10	19	
09. PROGRAMMING LABORATORY	PR		20		PC	_	DATA STRUCTURES AND FILES LAB	PR	50	20	40	
10. COMMUNICATION AND LANGUAGE LAB.			20		PC		OBJECT ORIENTED PROGRAMMING LAB		50	20	45	
TO. COMMUNICATION AND LANGUAGE LAD.	I VV	30	20	7/	r C		OBJECT ORIENTED PROGRAMMING LAB		50	20	43	
GRAND TOTAL = 1054/1500, RESULT: FIRST	CLAS	C WTTU	חדפד	TNCT	TON	۷1.	OBJECT ONTENTED FROMMINITING LAD	ΓK	30	20	-1 3	Г
GIVAIND IOIME - TOTAL TOOP KEOUFI FIROI	CLAS	שודאא כ	וכדח	TINCI	TON							

GRAND TOTAL = 1054/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS:

UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(INFORMATION TECHNOLOGY) DATE: 14 AUG. 2012 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 22 (502)

•				-	-		INT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
					'ASS MA		MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
S8058565 LUV VARMA					 .RVEEN		, 71100884B , , , PICT , S8058565
01. DISCRETE STRUCTURES	PP	100	40		P C	11	1. ENG MATHS III PP 100 40 82 P
02. COMPUTER ORGANIZATION	PP	100	40	75			.2. COMPUTER GRAPHICS PP 100 40 69 P
03. DIGITAL ELECTRONICS & LOGIC DESI	GPP	100	40	79			.3. PROCESSOR ARCHITECTURE & INTER. PP 100 40 75 P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	56	РC		4. DATA STRUCTURES AND FILES PP 100 40 58 P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	67	РС	15	.5. DATA COMMUNICATION PP 100 40 68 P
06. DIGITAL LABORATORY	TW	50	20	32	РС	16	.6. PROCESSOR INTERFACING LABORATORY TW 25 10 19 P
07. DIGITAL LABORATORY	PR	50	20	40	РС	17	.7. PROCESSOR INTERFACING LABORATORY OR 50 20 42 P
08. PROGRAMMING LABORATORY	TW	50	20	33	РС	18	.8. DATA STRUCTURES AND FILES LAB TW 25 10 20 P
09. PROGRAMMING LABORATORY	PR	50	20	25	РС	19	9. DATA STRUCTURES AND FILES LAB PR 50 20 38 P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	38	РС	20	0. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 42 P
						21	1. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 34 P
GRAND TOTAL = 1050/1500, RESULT: FIRST	CLAS	S WITH	DIST	TINCT	ION		
ORDN. 1 MARKS :							
S8058566 MADGULWAR AMIT ASHOK					JAYA		, 71100886J , PICT , S8058566
01. DISCRETE STRUCTURES	PP	100	40	55		11	, 71100886J , , , PICT , S8058566 .1. ENG MATHS III PP 100 40 70 P
02. COMPUTER ORGANIZATION	PP PP	100	40	60			
		100	40	77	PC		
03. DIGITAL ELECTRONICS & LOGIC DESI				77 78	_		
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40		PC		4. DATA STRUCTURES AND FILES PP 100 40 72 P
05. HUMANITIES AND SOCIAL SCIENCES	PP Tw	100	40	57	P C		.5. DATA COMMUNICATION PP 100 40 60 P .6. PROCESSOR INTERFACING LABORATORY TW 25 10 21 P
06. DIGITAL LABORATORY	TW	50	20	40	PC		
07. DIGITAL LABORATORY	PR	50	20	32	PC		7. PROCESSOR INTERFACING LABORATORY OR 50 20 35 P
08. PROGRAMMING LABORATORY	TW	50	20	42	PC		8. DATA STRUCTURES AND FILES LAB TW 25 10 21 P
09. PROGRAMMING LABORATORY	PR	50	20	38	P C		9. DATA STRUCTURES AND FILES LAB PR 50 20 43 P
10. COMMUNICATION AND LANGUAGE LAB.	I W	50	20	41	РС		O. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 44 P
CRAND TOTAL 1055/1500 RECULT: STRCT	CI 4 C	C 147711	DIC	NG-	TON	23	1. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 36 P
GRAND TOTAL = 1055/1500, RESULT: FIRST ORDN. 1 MARKS:	CLAS	S WITH	DIS	IINCI	ION		
S8058567 MAHAJAN DEEPIKA UDDHAV				MA	LATI		, 71241762в , , , ріст , s8058567
01. DISCRETE STRUCTURES	PP	100	40	40	РС	11	1. ENG MATHS III PP 100 40 40 P
02. COMPUTER ORGANIZATION	PP	100	40	50	РС	12	2. COMPUTER GRAPHICS PP 100 40 61 P
03. DIGITAL ELECTRONICS & LOGIC DESI	GPP	100	40	66	РС	13	3. PROCESSOR ARCHITECTURE & INTER. PP 100 40 63 P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	77	РС	14	4. DATA STRUCTURES AND FILES PP 100 40 56 P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	57	РС	15	5. DATA COMMUNICATION PP 100 40 49 P
06. DIGITAL LABORATORY	TW	50	20	40	РС	16	.6. PROCESSOR INTERFACING LABORATORY TW 25 10 23 P
07. DIGITAL LABORATORY	PR	50	20	38	РС	17	7. PROCESSOR INTERFACING LABORATORY OR 50 20 39 P
08. PROGRAMMING LABORATORY	TW	50	20	42	РС	18	8. DATA STRUCTURES AND FILES LAB TW 25 10 21 P
09. PROGRAMMING LABORATORY	PR	50	20	30	РС	19	9. DATA STRUCTURES AND FILES LAB PR 50 20 41 P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	41	РС	20	0. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 45 P
						21	1. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 41 P
GRAND TOTAL = 960/1500, RESULT: FIRST	CLAS	S					
ORDN. 1 MARKS :							

DATE: 14 AUG. 2012 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 23 (503)

NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , s8058568 S8058568 MAHARSHI PAYAL VIJAY NIRMALA 01. DISCRETE STRUCTURES PP 100 40 48 P C 11. ENG MATHS III PP 100 40 41 P 02. COMPUTER ORGANIZATION PP 100 40 61 P C 12. COMPUTER GRAPHICS PP 100 40 56 P 100 40 61 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 49 P 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 71 P C 14. DATA STRUCTURES AND FILES PP 100 40 59 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 57 P C PP 100 40 53 P 15. DATA COMMUNICATION 50 20 33 P C 25 10 20 P 06. DIGITAL LABORATORY 16. PROCESSOR INTERFACING LABORATORY TW TW 07. DIGITAL LABORATORY 50 20 32 P C 50 20 41 P PR 17. PROCESSOR INTERFACING LABORATORY OR 08. PROGRAMMING LABORATORY 50 20 34 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 21 P TW 09. PROGRAMMING LABORATORY PR 50 20 32 P C 19. DATA STRUCTURES AND FILES LAB PR 50 20 38 P 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 37 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 43 P 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 40 P GRAND TOTAL = 927/1500, RESULT: FIRST CLASS ORDN. 1 MARKS : S8058569 MANSI MATHUR PREETI , s8058569 40 P C 11. ENG MATHS III PP 100 40 26 F 01. DISCRETE STRUCTURES PP 100 40 48 P C PP 100 40 02. COMPUTER ORGANIZATION PP 100 40 12. COMPUTER GRAPHICS 46 P 40 54 P 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 42 P 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 40 100 100 46 P C PP 40 51 P 40 53 P C PP 100 05. HUMANITIES AND SOCIAL SCIENCES PP 100 15. DATA COMMUNICATION 40 46 P 50 20 31 P C 06. DIGITAL LABORATORY 25 10 14 P TW 16. PROCESSOR INTERFACING LABORATORY TW 50 20 30 P 50 20 07. DIGITAL LABORATORY PR 17. PROCESSOR INTERFACING LABORATORY OR 28 P 50 20 30 P C 08. PROGRAMMING LABORATORY TW 18. DATA STRUCTURES AND FILES LAB TW 25 10 13 P 09. PROGRAMMING LABORATORY 19. DATA STRUCTURES AND FILES LAB PR 50 20 04 F PR 50 20 20 P C 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 31 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 35 P 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 16 F GRAND TOTAL = 704/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS: , 71100891E , , PICT S8058570 MANTRI ANKIT JAGDISH SANGITA , s8058570 01. DISCRETE STRUCTURES PP 100 40 50 P C 11. ENG MATHS III PP 100 40 77 P 02. COMPUTER ORGANIZATION PP 100 40 51 P C 12. COMPUTER GRAPHICS PP 100 40 53 P 100 40 67 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 48 P 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 65 P C 14. DATA STRUCTURES AND FILES PP 100 40 57 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 49 P C PP 100 40 50 P 15. DATA COMMUNICATION 50 20 43 P C 06. DIGITAL LABORATORY 25 10 21 P TW 16. PROCESSOR INTERFACING LABORATORY TW 50 20 40 P C 17. PROCESSOR INTERFACING LABORATORY OR 50 20 36 P 07. DIGITAL LABORATORY PR 18. DATA STRUCTURES AND FILES LAB TW 25 10 23 P 08. PROGRAMMING LABORATORY TW 50 20 43 P C PR 50 20 44 P C 19. DATA STRUCTURES AND FILES LAB PR 50 20 42 P 09. PROGRAMMING LABORATORY 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 45 P.C. 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 46 P 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 38 P GRAND TOTAL = 988/1500, RESULT: FIRST CLASS ORDN. 1 MARKS:

UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(INFORMATION TECHNOLOGY) DATE: 14 AUG. 2012 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 24 (504) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71100892C , , PICT , S8058571 S8058571 MATHANGI KRISHNAMURTHI K JEYANTHY 01. DISCRETE STRUCTURES PP 100 40 74 P C PP 100 40 97 P 11. ENG MATHS III 02. COMPUTER ORGANIZATION PP 100 40 77 P C 12. COMPUTER GRAPHICS PP 100 40 71 P 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 40 75 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 71 P 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 79 P C 14. DATA STRUCTURES AND FILES PP 100 40 61 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 65 P C PP 100 40 58 P 15. DATA COMMUNICATION

25 10

50 20 42 P

16. PROCESSOR INTERFACING LABORATORY TW

17. PROCESSOR INTERFACING LABORATORY OR

18. DATA STRUCTURES AND FILES LAB TW 25 10 21 P

19. DATA STRUCTURES AND FILES LAB PR 50 20 42 P

20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 47 P 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 42 P

24 P

50 20 44 P C

50 20 46 P C

50 20 48 P C

PR 50 20 38 P C

GRAND TOTAL = 1167/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS:

10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 45 P C

TW

PR

TW

06. DIGITAL LABORATORY

07. DIGITAL LABORATORY

ORDN. 1 MARKS:

08. PROGRAMMING LABORATORY

09. PROGRAMMING LABORATORY

S8058572 MATNANI PRIYA SHIVADAS UPASANA , s8058572 01. DISCRETE STRUCTURES PP 100 40 57 P C 11. ENG MATHS III PP 100 40 87 P PP 100 40 02. COMPUTER ORGANIZATION PP 100 40 66 P C 12. COMPUTER GRAPHICS 70 P 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 40 67 P C 70 P 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 40 80 P C 100 61 P 100 PP 40 40 64 P C PP 100 05. HUMANITIES AND SOCIAL SCIENCES PP 100 15. DATA COMMUNICATION 40 68 P 50 20 36 P C 23 P 06. DIGITAL LABORATORY 25 10 TW 16. PROCESSOR INTERFACING LABORATORY TW 50 20 43 P C 50 20 07. DIGITAL LABORATORY PR 17. PROCESSOR INTERFACING LABORATORY OR 40 P 50 20 36 P C 08. PROGRAMMING LABORATORY TW 18. DATA STRUCTURES AND FILES LAB TW 25 10 21 P 09. PROGRAMMING LABORATORY PR 50 20 28 P C 19. DATA STRUCTURES AND FILES LAB PR 50 20 44 P 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 38 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 43 P 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 44 P

GRAND TOTAL = 1086/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS:

, 71100898B , , PICT , S8058573 GURPREET KAUR S8058573 MONGA MANPREET KAUR DARSHAN SINGH 01. DISCRETE STRUCTURES 100 40 40 P C 11. ENG MATHS III 100 40 41 P 02. COMPUTER ORGANIZATION PP 100 40 56 P C 12. COMPUTER GRAPHICS PP 100 40 54 P 100 40 44 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 42 P 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 45 P C 14. DATA STRUCTURES AND FILES PP 100 40 46 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 48 P C 100 40 56 P 15. DATA COMMUNICATION PP 22 P 06. DIGITAL LABORATORY 50 20 35 P C 25 10 TW 16. PROCESSOR INTERFACING LABORATORY TW 50 20 35 P C 17. PROCESSOR INTERFACING LABORATORY OR 50 20 30 P 07. DIGITAL LABORATORY PR 18. DATA STRUCTURES AND FILES LAB TW 25 10 17 P 08. PROGRAMMING LABORATORY TW 50 20 34 P C 50 20 20 P C 19. DATA STRUCTURES AND FILES LAB PR 50 20 36 P 09. PROGRAMMING LABORATORY PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 38 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 40 P 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 39 P

GRAND TOTAL = 818+07/1500, RESULT: HIGHER SECOND CLASS [0.2]

UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(INFORMATION TECHNOLOGY) DATE: 14 AUG. 2012 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 25 (505) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71241764J , , PICT , s8058574 S8058574 MORE SWATI ANNARAO ANITA 01. DISCRETE STRUCTURES PP 100 40 64 P C 11. ENG MATHS III PP 100 40 81 P 02. COMPUTER ORGANIZATION PP 100 40 63 P C 12. COMPUTER GRAPHICS 100 40 68 P 100 40 68 P C 100 40 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 13. PROCESSOR ARCHITECTURE & INTER. PP 61 P 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 71 P C PP 100 40 69 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 63 P C PP 100 40 72 P 15. DATA COMMUNICATION 50 20 40 P C 25 10 24 P 06. DIGITAL LABORATORY 16. PROCESSOR INTERFACING LABORATORY TW TW 50 20 46 P C 07. DIGITAL LABORATORY 50 20 42 P PR 17. PROCESSOR INTERFACING LABORATORY OR 08. PROGRAMMING LABORATORY 50 20 41 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 21 P TW 09. PROGRAMMING LABORATORY PR 50 20 32 P C 19. DATA STRUCTURES AND FILES LAB PR 50 20 42 P 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 43 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 47 P 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 41 P GRAND TOTAL = 1099/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS: S8058575 MULCHANDANI POOJA OM PRITI , s8058575 01. DISCRETE STRUCTURES 56 P C 11. ENG MATHS III 40 48 P PP 100 40 PP 100 40 P C PP 100 40 53 P 02. COMPUTER ORGANIZATION PP 100 40 12. COMPUTER GRAPHICS 40 40 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 46 P 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 40 57 P C 100 57 P 100 PP 40 40 40 P C PP 100 05. HUMANITIES AND SOCIAL SCIENCES PP 100 15. DATA COMMUNICATION 40 40 P 50 20 38 P C 10 19 P 06. DIGITAL LABORATORY 25 TW 16. PROCESSOR INTERFACING LABORATORY TW 50 20 36 P C 50 20 35 P 07. DIGITAL LABORATORY PR 17. PROCESSOR INTERFACING LABORATORY OR 50 20 40 P C 08. PROGRAMMING LABORATORY TW 18. DATA STRUCTURES AND FILES LAB TW 25 10 21 P 09. PROGRAMMING LABORATORY PR 50 20 42 P C 19. DATA STRUCTURES AND FILES LAB PR 50 20 42 P

10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 44 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 47 P 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 38 P

GRAND TOTAL = 879/1500, RESULT: HIGHER SECOND CLASS ORDN. 1 MARKS:

, 71100903B , , PICT S8058576 MUNOT SHRENIK SUMTILAL SADHANA , s8058576 01. DISCRETE STRUCTURES PP 100 40 40 P 11. ENG MATHS III 100 40 40 P 02. COMPUTER ORGANIZATION PP 100 40 31 F 12. COMPUTER GRAPHICS PP 100 40 40 P 100 40 41 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 13 F 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 41 P 14. DATA STRUCTURES AND FILES PP 100 40 40 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 32 F 100 40 30 F 15. DATA COMMUNICATION PP 06. DIGITAL LABORATORY 50 20 32 P C 25 10 14 P TW 16. PROCESSOR INTERFACING LABORATORY TW 50 20 28 P 17. PROCESSOR INTERFACING LABORATORY OR 50 20 07. DIGITAL LABORATORY PR 06 F 18. DATA STRUCTURES AND FILES LAB TW 25 10 12 P 08. PROGRAMMING LABORATORY TW 50 20 28 P C 50 20 22 P C 19. DATA STRUCTURES AND FILES LAB PR 50 20 28 P 09. PROGRAMMING LABORATORY PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 37 P.C. 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 33 P 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 21 P

GRAND TOTAL = 609/1500, RESULT: FAILS

DATE: 14 AUG. 2012 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 26 (506)

NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER KANTABAI , s8058577 S8058577 MUPADE PRIYA SHIVAJI PP 100 40 40 P 01. DISCRETE STRUCTURES 11. ENG MATHS III PP 100 40 40 P 02. COMPUTER ORGANIZATION PP 100 40 56 P C 12. COMPUTER GRAPHICS PP 100 40 47 P 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 40 40 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 47 P 14. DATA STRUCTURES AND FILES PP 100 40 42 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 40 P C 15. DATA COMMUNICATION PP 100 40 45 P 50 20 37 P.C 25 10 16 P 06. DIGITAL LABORATORY 16. PROCESSOR INTERFACING LABORATORY TW TW 07. DIGITAL LABORATORY 50 20 05 F 50 20 05 F PR 17. PROCESSOR INTERFACING LABORATORY OR 08. PROGRAMMING LABORATORY 50 20 36 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 21 P TW 09. PROGRAMMING LABORATORY PR 50 20 30 P 19. DATA STRUCTURES AND FILES LAB PR 50 20 34 P 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 43 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 36 P 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 32 P GRAND TOTAL = 736/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS: S8058578 MUTHA VINITA VINOD SUREKHA , s8058578 58 P 11. ENG MATHS III PP 100 40 22 F 01. DISCRETE STRUCTURES PP 100 40 40 P C 40 02. COMPUTER ORGANIZATION PP 100 40 12. COMPUTER GRAPHICS PP 100 51 P 40 13. PROCESSOR ARCHITECTURE & INTER. PP 100 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 42 P C 40 48 P 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 40 49 P C 100 100 PP 40 50 P 40 40 P C PP 100 05. HUMANITIES AND SOCIAL SCIENCES PP 100 15. DATA COMMUNICATION 40 51 P 50 20 32 P C 25 10 13 P 06. DIGITAL LABORATORY TW 16. PROCESSOR INTERFACING LABORATORY TW 50 20 32 P C 50 20 14 F 07. DIGITAL LABORATORY PR 17. PROCESSOR INTERFACING LABORATORY OR 50 20 35 P C 08. PROGRAMMING LABORATORY TW 18. DATA STRUCTURES AND FILES LAB TW 25 10 13 P 09. PROGRAMMING LABORATORY PR 50 20 38 P 19. DATA STRUCTURES AND FILES LAB PR 50 20 31 P 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 34 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 35 P 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 44 P GRAND TOTAL = 772/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS: , 71100907E , , PICT S8058579 NAIK RASHMI BHARAT MEENA , s8058579 01. DISCRETE STRUCTURES PP 100 40 46 P 11. ENG MATHS III PP 100 40 17 F 02. COMPUTER ORGANIZATION PP 100 40 30 F 12. COMPUTER GRAPHICS PP 100 40 43 P 100 40 40 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 40 P 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 49 P C 14. DATA STRUCTURES AND FILES PP 100 40 46 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 48 P C PP 100 40 48 P 15. DATA COMMUNICATION 50 20 38 P C 06. DIGITAL LABORATORY 25 10 18 P TW 16. PROCESSOR INTERFACING LABORATORY TW 50 20 22 P C 17. PROCESSOR INTERFACING LABORATORY OR 50 20 31 P 07. DIGITAL LABORATORY PR 18. DATA STRUCTURES AND FILES LAB TW 25 10 18 P 08. PROGRAMMING LABORATORY TW 50 20 34 P C 50 20 20 P C 19. DATA STRUCTURES AND FILES LAB PR 50 20 38 P 09. PROGRAMMING LABORATORY PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 41 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 40 P 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 10 F GRAND TOTAL = 717/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS:

UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(INFORMATION TECHNOLOGY)

DATE : 14 AUG. 2012 CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 27 (507)

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

						• •						• •
S8058580 NARAD POONAM DEEPAK				VII	DYA		, 71100911C , ,	PIC	CT	, :	S8058	580
01. DISCRETE STRUCTURES	PP	100	40	63	Р	11.	ENG MATHS III	PP	100	40	40	Р
02. COMPUTER ORGANIZATION	PP	100	40	40	P C	12.	COMPUTER GRAPHICS	PP	100	40	64	Р
03. DIGITAL ELECTRONICS & LOGIC DESIGNATION OF THE PROPERTY OF	GPP	100	40	49	P C	13.	PROCESSOR ARCHITECTURE & INTER.	PP	100	40	56	Р
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	49	P C	14.	DATA STRUCTURES AND FILES	PP	100	40	48	Р
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	63	P C	15.	DATA COMMUNICATION	PP	100	40	52	Р
06. DIGITAL LABORATORY	TW	50	20	37	P C	16.	PROCESSOR INTERFACING LABORATORY	TW	25	10	17	Р
07. DIGITAL LABORATORY	PR	50	20	25	P C	17.	PROCESSOR INTERFACING LABORATORY	OR	50	20	37	Р
08. PROGRAMMING LABORATORY	TW	50	20	38	P C	18.	DATA STRUCTURES AND FILES LAB	TW	25	10	20	Р
09. PROGRAMMING LABORATORY	PR	50	20	23	P C	19.	DATA STRUCTURES AND FILES LAB	PR	50	20	35	Р
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	39	P C	20.	OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	43	Р
						21.	OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	80	F

GRAND TOTAL = 846/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

S8058581 NARANG KARISHMA KISHOR SUNITA , 71100912M , , PICT , s8058581 01. DISCRETE STRUCTURES PP 100 40 49 P C 11. ENG MATHS III 100 40 41 P 100 02. COMPUTER ORGANIZATION PP 100 40 63 P C 12. COMPUTER GRAPHICS 40 54 P 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 40 100 40 62 P C 50 P 13. PROCESSOR ARCHITECTURE & INTER. PP 40 58 P C 100 63 P 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 14. DATA STRUCTURES AND FILES PP 40 40 57 P C 100 40 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 15. DATA COMMUNICATION 40 50 20 39 P C 06. DIGITAL LABORATORY 25 10 21 P TW 16. PROCESSOR INTERFACING LABORATORY TW 50 20 40 P C 50 20 07. DIGITAL LABORATORY PR 17. PROCESSOR INTERFACING LABORATORY OR 44 P 50 20 36 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 17 P 08. PROGRAMMING LABORATORY TW PR 19. DATA STRUCTURES AND FILES LAB PR 50 20 34 P 09. PROGRAMMING LABORATORY 50 20 41 P C 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 39 P C 50 20 40 P 20. OBJECT ORIENTED PROGRAMMING LAB TW 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 42 P

GRAND TOTAL = 930/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS:

, 71100916D , , PICT , S8058582 S8058582 NEVATIA SAKSHI SUNIL ANJU 01. DISCRETE STRUCTURES PP 100 40 40 P C 11. ENG MATHS III 100 40 66 P 02. COMPUTER ORGANIZATION PP 100 40 65 P C 12. COMPUTER GRAPHICS PP 100 40 69 P 100 40 51 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 56 P 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 62 P C 14. DATA STRUCTURES AND FILES PP 100 40 44 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 60 P C 100 40 72 P 15. DATA COMMUNICATION PP 50 20 40 P C 06. DIGITAL LABORATORY 25 10 18 P TW 16. PROCESSOR INTERFACING LABORATORY TW 07. DIGITAL LABORATORY 50 20 30 P C 17. PROCESSOR INTERFACING LABORATORY OR 50 20 43 P PR 18. DATA STRUCTURES AND FILES LAB TW 25 10 19 P 08. PROGRAMMING LABORATORY TW 50 20 40 P C 09. PROGRAMMING LABORATORY 50 20 22 P C 50 20 38 P PR 19. DATA STRUCTURES AND FILES LAB PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 41 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 41 P 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 33 P

GRAND TOTAL = 950/1500, RESULT: FIRST CLASS

UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(INFORMATION TECHNOLOGY) DATE : 14 AUG. 2012 CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 28 (508) NOTE: FIRST LINE: SEAT NO. NAME OF THE CANDIDATE MOTHER PERMANENT REG. NO. PREVIOUS SEAT NO. COLLEGE. SEAT NO.

	8058583 OKA TANVI VISHWAS					 DHA			 100918L				 ICT		 S8058	
	1. DISCRETE STRUCTURES	PP	100	40		P C	11	ENG MATH		,		, r	100	, 40	86	
_	2. COMPUTER ORGANIZATION	PP	100	40		P C		COMPUTER		rcs		PP	100	40	79	
	3. DIGITAL ELECTRONICS & LOGIC DESI		100	40		P C					& INTER.		100	40	67	
0	4. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40		РС	14.	DATA STE	RUCTURES	S AND FI	LES	PP	100	40	64	
0	5. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	58	РС	15.	DATA COM	MMUNICAT	ΓΙΟΝ		PP	100	40	70	Р
0	6. DIGITAL LABORATORY	TW	50	20	27	РС	16.	PROCESSO	OR INTER	RFACING	LABORATOR	Y TW	25	10	18	Р
0	7. DIGITAL LABORATORY	PR	50	20	44	РС	17.	PROCESSO	OR INTER	RFACING	LABORATOR	Y OR	50	20	42	Р
0	8. PROGRAMMING LABORATORY	TW	50	20	27	РС	18.	DATA ST	RUCTURES	S AND FI	LES LAB	TW	25	10	16	Р
0	9. PROGRAMMING LABORATORY	PR	50	20	36	P C	19.	DATA ST	RUCTURES	S AND FI	LES LAB	PR	50	20	38	Р
1	0. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	32	P C	20.	OBJECT (ORIENTE	PROGRA	MMING LAB	TW	50	20	33	Р
							21.	OBJECT (ORIENTE	PROGRA	MMING LAB	PR	50	20	45	Р
GRA	ND TOTAL = $1030/1500$, RESULT: FIRST	CLAS	S WITH	DIST	INCT:	ION										
ORD	N. 1 MARKS :															
S	8058584 OSWAL DARSHAN MANISH				NE	ETA		, 711	100919յ	,		, P.	ICT	,	S8058	584
0	1 DISCRETE STRUCTURES	PP	100	40	66	PC	11	FNG MATE	ITT 2F			PP	100	40	71	P

S8058584 OSWAL DARSHAN MANISH				NEI	ETA		, 71100919」 , , ,	PI	СТ	, 9	8058	584
01. DISCRETE STRUCTURES	PP	100	40	66	РС	11.	ENG MATHS III	PP	100	40	71	Р
02. COMPUTER ORGANIZATION	PP	100	40	57	P C	12.	COMPUTER GRAPHICS	PP	100	40	63	Р
03. DIGITAL ELECTRONICS & LOGIC DESI	GPP	100	40	66	P C	13.	PROCESSOR ARCHITECTURE & INTER.	PP	100	40	56	Р
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	52	P C	14.	DATA STRUCTURES AND FILES	PP	100	40	69	Р
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	45	РС	15.	DATA COMMUNICATION	PP	100	40	60	Р
06. DIGITAL LABORATORY	TW	50	20	43	РС	16.	PROCESSOR INTERFACING LABORATORY	TW	25	10	24	Р
07. DIGITAL LABORATORY	PR	50	20	40	P C	17.	PROCESSOR INTERFACING LABORATORY	OR	50	20	46	Р
08. PROGRAMMING LABORATORY	TW	50	20	44	РС	18.	DATA STRUCTURES AND FILES LAB	TW	25	10	24	Р
09. PROGRAMMING LABORATORY	PR	50	20	43	РС	19.	DATA STRUCTURES AND FILES LAB	PR	50	20	44	Р
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	44	РС	20.	OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	47	Р
						21.	OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	41	Р

GRAND TOTAL = 1045/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS:

S8058585 PAGAR NILESH DADAJI				YO	GITA		, 71100922〕	,	ΡI	CT	,	s8058	3585
01. DISCRETE STRUCTURES	PP	100	40	46	P C	11.	ENG MATHS III		PP	100	40	69	Р
02. COMPUTER ORGANIZATION	PP	100	40	66	РС	12	COMPUTER GRAPHICS		PP	100	40	57	Р
03. DIGITAL ELECTRONICS & LOGIC DES	IGPP	100	40	60	РС	13	PROCESSOR ARCHITE	CTURE & INTER.	PP	100	40	53	Р
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	61	РС	14	DATA STRUCTURES A	ND FILES	PP	100	40	55	Р
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	55	РС	15	DATA COMMUNICATIO	N	PP	100	40	58	Р
06. DIGITAL LABORATORY	TW	50	20	41	РС	16	PROCESSOR INTERFA	CING LABORATORY	TW	25	10	19	Р
07. DIGITAL LABORATORY	PR	50	20	32	PC	17	PROCESSOR INTERFA	CING LABORATORY	OR	50	20	42	Р
08. PROGRAMMING LABORATORY	TW	50	20	42	PC	18	DATA STRUCTURES A	ND FILES LAB	TW	25	10	20	Р
09. PROGRAMMING LABORATORY	PR	50	20	20	РС	19	DATA STRUCTURES A	ND FILES LAB	PR	50	20	40	Р
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	44	РС	20	OBJECT ORIENTED P	ROGRAMMING LAB	TW	50	20	44	Р
						21.	OBJECT ORIENTED P	ROGRAMMING LAB	PR	50	20	29	Р

GRAND TOTAL = 953/1500, RESULT: FIRST CLASS

UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(INFORMATION TECHNOLOGY)

DATE : 14 AUG. 2012 CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 29 (509)

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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9	58058586 PANDAY PRASHAI	NT RAMDHARASH			AS	HA		, 7110	0925C	,	, F	ICT	,	S8058	586
(01. DISCRETE STRUCTURES	PF	100	40	49	РС	11	ENG MATHS	SIII		PP	100	40	87	Р
(02. COMPUTER ORGANIZATION	PF	100	40	61	РС	12	COMPUTER	GRAPHICS		PP	100	40	58	Р
(03. DIGITAL ELECTRONICS &	LOGIC DESIGPE	100	40	56	РС	13	PROCESSOR	RARCHITEC	CTURE & INTER	. PP	100	40	60	Р
(04. FUNDAMENTAL OF DATA S	TRUCTURES P	100	40	58	РС	14	DATA STRU	JCTURES AN	ND FILES	PP	100	40	54	Р
(05. HUMANITIES AND SOCIAL	SCIENCES PR	100	40	61	РС	15	DATA COMM	MUNICATION	N .	PP	100	40	64	Р
(06. DIGITAL LABORATORY	ΤV	<i>y</i> 50	20	32	РС	16	PROCESSOR	R INTERFAC	CING LABORATOR	RY TW	25	10	19	Р
(07. DIGITAL LABORATORY	PF	s 50	20	42	РС	17	PROCESSOR	R INTERFAC	CING LABORATOR	RY OR	50	20	45	Р
(08. PROGRAMMING LABORATOR	Y TV	<i>y</i> 50	20	29	РС	18	DATA STRU	JCTURES AN	ND FILES LAB	TW	25	10	19	Р
(9. PROGRAMMING LABORATOR	Y PF	s 50	20	26	РС	19	DATA STRU	JCTURES AN	ND FILES LAB	PR	50	20	38	Р
-	10. COMMUNICATION AND LANG	GUAGE LAB. TV	<i>y</i> 50	20	33	РС	20	OBJECT OF	RIENTED PR	ROGRAMMING LA	B TW	50	20	33	Р
							21	OBJECT OF	RIENTED PR	ROGRAMMING LA	B PR	50	20	28	Р

GRAND TOTAL = 952/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

S8058587 PANDIT SNEHALATA GANAPATI SHOBHA , S8058587 01. DISCRETE STRUCTURES PP 100 40 58 P C 11. ENG MATHS III 100 40 64 P PP 100 40 61 P C 100 40 02. COMPUTER ORGANIZATION 12. COMPUTER GRAPHICS 62 P 100 40 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 40 63 P C 66 P 13. PROCESSOR ARCHITECTURE & INTER. PP 14. DATA STRUCTURES AND FILES 40 56 P C 100 72 P 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 PP 40 40 52 P C 100 05. HUMANITIES AND SOCIAL SCIENCES PP 100 15. DATA COMMUNICATION 40 65 P 50 20 37 P C 22 P 06. DIGITAL LABORATORY 25 10 TW 16. PROCESSOR INTERFACING LABORATORY TW 07. DIGITAL LABORATORY 50 20 42 P C 50 20 39 P PR 17. PROCESSOR INTERFACING LABORATORY OR 50 20 41 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 22 P 08. PROGRAMMING LABORATORY TW 09. PROGRAMMING LABORATORY 50 20 36 P C 19. DATA STRUCTURES AND FILES LAB PR 50 20 42 P PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 41 P C 50 20 44 P 20. OBJECT ORIENTED PROGRAMMING LAB TW 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 41 P

GRAND TOTAL = 1026/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS:

, 71241767C , , PICT , S8058588 S8058588 PARSE JYOTI SHRIKRISHNA **AMBIKA** 01. DISCRETE STRUCTURES PP 100 40 40 P C 11. ENG MATHS III 100 40 40 P 02. COMPUTER ORGANIZATION PP 100 40 66 P C 12. COMPUTER GRAPHICS PP 100 40 68 P 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 40 63 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 58 P C 14. DATA STRUCTURES AND FILES PP 100 40 60 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 47 P C PP 100 40 74 P 15. DATA COMMUNICATION 50 20 37 P C 25 10 22 P 06. DIGITAL LABORATORY TW 16. PROCESSOR INTERFACING LABORATORY TW 07. DIGITAL LABORATORY 50 20 22 P C 17. PROCESSOR INTERFACING LABORATORY OR 50 20 41 P PR 18. DATA STRUCTURES AND FILES LAB TW 25 10 18 P 08. PROGRAMMING LABORATORY TW 50 20 40 P C 09. PROGRAMMING LABORATORY 50 20 27 P C 19. DATA STRUCTURES AND FILES LAB PR 50 20 37 P PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 40 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 42 P 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 39 P

GRAND TOTAL = 943/1500, RESULT: FIRST CLASS

UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(INFORMATION TECHNOLOGY)

DATE: 14 AUG. 2012 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 30 (510)

NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8058589 PATEL MANISH UTTAM KAILAS , 71100933D , PICT , S8058589

01. DISCRETE STRUCTURES PP 100 40 63 P C 11. ENG MATHS III PP 100 40 90 P

02. COMPUTER ORGANIZATION PP 100 40 73 P C 12. COMPUTER GRAPHICS PP 100 40 74 P 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 40 68 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 61 P 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 65 P C 14. DATA STRUCTURES AND FILES PP 100 40 59 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 64 P C 15. DATA COMMUNICATION PP 100 40 72 P 06. DIGITAL LABORATORY 50 20 42 P C 25 10 19 P TW 16. PROCESSOR INTERFACING LABORATORY TW 50 20 33 P C 07. DIGITAL LABORATORY 50 20 34 P PR 17. PROCESSOR INTERFACING LABORATORY OR 08. PROGRAMMING LABORATORY 50 20 43 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 19 P TW 09. PROGRAMMING LABORATORY PR 50 20 36 P C 19. DATA STRUCTURES AND FILES LAB PR 50 20 36 P 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 45 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 41 P 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 41 P

GRAND TOTAL = 1078/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS:

S8058590 PATHAK ANUJA PRAVIN				UJJ	WALA		, 71100934в , ,	Р	ICT	,	s8058	590
01. DISCRETE STRUCTURES	PP	100	40	40	PС	11.	ENG MATHS III	PP	100	40	23	F
02. COMPUTER ORGANIZATION	PP	100	40	40	Р	12.	COMPUTER GRAPHICS	PP	100	40	40	Р
03. DIGITAL ELECTRONICS & LOGIC DESI	GPP	100	40	43	РС	13.	PROCESSOR ARCHITECTURE & INTER.	PP	100	40	40	Р
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	48	РС	14.	DATA STRUCTURES AND FILES	PP	100	40	47	Р
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	55	РС	15.	DATA COMMUNICATION	PP	100	40	48	Р
06. DIGITAL LABORATORY	TW	50	20	34	РС	16.	PROCESSOR INTERFACING LABORATORY	/ TW	25	10	16	Р
07. DIGITAL LABORATORY	PR	50	20	30	РС	17.	PROCESSOR INTERFACING LABORATORY	OR	50	20	38	Р
08. PROGRAMMING LABORATORY	TW	50	20	29	РС	18.	DATA STRUCTURES AND FILES LAB	TW	25	10	16	Р
09. PROGRAMMING LABORATORY	PR	50	20	26	РС	19.	DATA STRUCTURES AND FILES LAB	PR	50	20	20	Р
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	36	РС	20.	OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	36	Р
						21.	OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	27	Р

GRAND TOTAL = 732/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS:

S8058591	PATIL AMOL MADHUKAR				JY(ITC		, 71100938E	, ,	ΡI	CT	, :	8058	591
01. DISCRET	E STRUCTURES	PP	100	40	50	P C	11.	ENG MATHS III		PP	100	40	40	Р
02. COMPUTE	R ORGANIZATION	PP	100	40	40	P C	12.	COMPUTER GRAPHICS	5	PP	100	40	48	Р
03. DIGITAL	ELECTRONICS & LOGIC DESIGNATION	GPP	100	40	55	РС	13.	PROCESSOR ARCHITE	CTURE & INTER.	PP	100	40	26	F
04. FUNDAME	NTAL OF DATA STRUCTURES	PP	100	40	44	РС	14.	DATA STRUCTURES A	AND FILES	PP	100	40	55	Р
05. HUMANIT	IES AND SOCIAL SCIENCES	PP	100	40	47	РС	15.	DATA COMMUNICATIO)N	PP	100	40	50	Р
06. DIGITAL	LABORATORY	TW	50	20	33	РС	16.	PROCESSOR INTERFA	CING LABORATORY	′ TW	25	10	14	Р
07. DIGITAL	LABORATORY	PR	50	20	28	РС	17.	PROCESSOR INTERFA	CING LABORATORY	OR	50	20	20	Р
08. PROGRAMI	MING LABORATORY	TW	50	20	33	PС	18.	DATA STRUCTURES A	ND FILES LAB	TW	25	10	14	Р
09. PROGRAMI	MING LABORATORY	PR	50	20	25	PС	19.	DATA STRUCTURES A	ND FILES LAB	PR	50	20	42	Р
10. COMMUNIO	CATION AND LANGUAGE LAB.	TW	50	20	37	PС	20.	OBJECT ORIENTED F	ROGRAMMING LAB	TW	50	20	35	Р
							21.	OBJECT ORIENTED F	ROGRAMMING LAB	PR	50	20	32	Р

GRAND TOTAL = 768/1500, RESULT: FAILS A.T.K.T.

UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(INFORMATION TECHNOLOGY) DATE : 14 AUG. 2012 CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 31 (511)

NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER S8058592 PATIL PRIYA KISHOR PUSHPA . 71100941E PICT . S8058592

30030332	PAILL PRITA KISHOK				FU.	эпга		, /11003416	,	, ГТ	C I	, -	30030.	J J Z
01. DISCRE	TE STRUCTURES	PP	100	40	41	P C	11.	ENG MATHS III		PP	100	40	85	Р
02. COMPUTI	ER ORGANIZATION	PP	100	40	66	P C	12.	COMPUTER GRAPHICS		PP	100	40	69	Р
03. DIGITAL	L ELECTRONICS & LOGIC DESI	GPP	100	40	66	P C	13.	PROCESSOR ARCHITEC	TURE & INTER.	PP	100	40	59	Р
04. FUNDAMI	ENTAL OF DATA STRUCTURES	PP	100	40	52	P C	14.	DATA STRUCTURES AN	ND FILES	PP	100	40	60	Р
05. HUMANI	TIES AND SOCIAL SCIENCES	PP	100	40	57	P C	15.	DATA COMMUNICATION	I	PP	100	40	54	Р
06. DIGITAL	L LABORATORY	TW	50	20	38	P C	16.	PROCESSOR INTERFAC	CING LABORATORY	Y TW	25	10	20	Р
07. DIGITAL	L LABORATORY	PR	50	20	30	РС	17.	PROCESSOR INTERFAC	CING LABORATORY	Y OR	50	20	36	Р
08. PROGRAM	MMING LABORATORY	TW	50	20	36	РС	18.	DATA STRUCTURES AN	ND FILES LAB	TW	25	10	21	Р
09. PROGRAM	MMING LABORATORY	PR	50	20	26	P C	19.	DATA STRUCTURES AN	ND FILES LAB	PR	50	20	38	Р
10. COMMUN	ICATION AND LANGUAGE LAB.	TW	50	20	38	РС	20.	OBJECT ORIENTED PR	ROGRAMMING LAB	TW	50	20	43	Р
							21.	OBJECT ORIENTED PR	ROGRAMMING LAB	PR	50	20	43	Р

GRAND TOTAL = 978/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS:

S8058593	PATIL RUPESHSING RAMESH				KAN	NCHAN		, 71100943м	, ,	PI	CT	, 9	8058	593
01. DISCRET	E STRUCTURES	PP	100	40	41	PC	11.	ENG MATHS III		PP	100	40	24	F
02. COMPUTE	R ORGANIZATION	PP	100	40	49	P C	12.	COMPUTER GRAPHICS		PP	100	40	60	Р
03. DIGITAL	ELECTRONICS & LOGIC DESI	GPP	100	40	63	P C	13.	PROCESSOR ARCHITEC	TURE & INTER.	PP	100	40	52	Р
04. FUNDAME	ENTAL OF DATA STRUCTURES	PP	100	40	53	P C	14.	DATA STRUCTURES AN	D FILES	PP	100	40	43	Р
05. HUMANIT	TIES AND SOCIAL SCIENCES	PP	100	40	58	P C	15.	DATA COMMUNICATION		PP	100	40	61	Р
06. DIGITAL	LABORATORY	TW	50	20	30	PC	16.	PROCESSOR INTERFAC	ING LABORATORY	TW	25	10	11	Р
07. DIGITAL	LABORATORY	PR	50	20	40	P C	17.	PROCESSOR INTERFAC	ING LABORATORY	OR	50	20	16	F
08. PROGRAM	MING LABORATORY	TW	50	20	34	P C	18.	DATA STRUCTURES AN	D FILES LAB	TW	25	10	13	Р
09. PROGRAM	MING LABORATORY	PR	50	20	22	P C	19.	DATA STRUCTURES AN	D FILES LAB	PR	50	20	16	F
10. COMMUNI	CATION AND LANGUAGE LAB.	TW	50	20	34	P C	20.	OBJECT ORIENTED PR	OGRAMMING LAB	TW	50	20	33	Р
							21.	OBJECT ORIENTED PR	OGRAMMING LAB	PR	50	20	21	Р

GRAND TOTAL = 774/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS:

S8058594 PATIL RUSHIKESH VASANTRA	0			SHC	BHANA		, 71100944К	, ,	PI	СТ	, 9	80585	594
01. DISCRETE STRUCTURES	PP	100	40	47	P C	11.	ENG MATHS III		PP	100	40	66	Р
02. COMPUTER ORGANIZATION	PP	100	40	58	P C	12.	COMPUTER GRAPHIC	S	PP	100	40	58	Р
03. DIGITAL ELECTRONICS & LOGIC DESIGNATION OF THE PROPERTY OF	GPP	100	40	63	P C	13.	PROCESSOR ARCHIT	ECTURE & INTER.	PP	100	40	53	Р
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	60	P C	14.	DATA STRUCTURES	AND FILES	PP	100	40	58	Р
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	45	P C	15.	DATA COMMUNICATI	ON	PP	100	40	51	Р
06. DIGITAL LABORATORY	TW	50	20	32	P C	16.	PROCESSOR INTERF	ACING LABORATORY	TW	25	10	18	Р
07. DIGITAL LABORATORY	PR	50	20	42	P C	17.	PROCESSOR INTERF	ACING LABORATORY	OR	50	20	38	Р
08. PROGRAMMING LABORATORY	TW	50	20	31	P C	18.	DATA STRUCTURES	AND FILES LAB	TW	25	10	15	Р
09. PROGRAMMING LABORATORY	PR	50	20	35	P C	19.	DATA STRUCTURES	AND FILES LAB	PR	50	20	39	Р
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	35	P C	20.	OBJECT ORIENTED	PROGRAMMING LAB	TW	50	20	32	Р
						21.	OBJECT ORIENTED	PROGRAMMING LAB	PR	50	20	28	Р

GRAND TOTAL = 904/1500, RESULT: FIRST CLASS

UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(INFORMATION TECHNOLOGY) DATE : 14 AUG. 2012 CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 32 (512) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71100949L , , , PICT , s8058595 S8058595 PAWAR NEHA ANIL JAYA 40 15 F 01. DISCRETE STRUCTURES PP 100 40 31 F 11. ENG MATHS III PP 100 02. COMPUTER ORGANIZATION 100 40 45 P C 12. COMPUTER GRAPHICS 100 40 55 P 100 40 40 P C 100 40 27 F 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 13. PROCESSOR ARCHITECTURE & INTER. PP 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 50 P C 100 40 44 P PP 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 51 P C PP 100 40 42 P 15. DATA COMMUNICATION 20 35 P C 25 10 12 P 06. DIGITAL LABORATORY 50 16. PROCESSOR INTERFACING LABORATORY TW TW 07. DIGITAL LABORATORY 50 20 34 P C 50 20 24 P PR 17. PROCESSOR INTERFACING LABORATORY OR 08. PROGRAMMING LABORATORY 50 20 34 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 15 P TW 09. PROGRAMMING LABORATORY PR 50 20 10 F 19. DATA STRUCTURES AND FILES LAB PR 50 20 02 F 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 38 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 33 P 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 12 F GRAND TOTAL = 649/1500, RESULT: FAILS RESULT RESERVED FOR BKLG ORDN. 1 MARKS: S8058596 PAWAR PRASAD GORAKSH MEENAKSHI , s8058596 01. DISCRETE STRUCTURES PP 100 40 43 P 40 19 F 11. ENG MATHS III PP 100 PP 100 02. COMPUTER ORGANIZATION PP 100 40 41 P 12. COMPUTER GRAPHICS 40 41 P 41 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 40 40 P 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 43 P C 100 100 40 PP 40 45 P 100 40 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 53 P C 15. DATA COMMUNICATION 40 20 24 P C 10 10 P 06. DIGITAL LABORATORY 50 25 TW 16. PROCESSOR INTERFACING LABORATORY TW 50 20 30 P 20 28 P 07. DIGITAL LABORATORY PR 17. PROCESSOR INTERFACING LABORATORY OR 50 50 20 28 P C 08. PROGRAMMING LABORATORY TW 18. DATA STRUCTURES AND FILES LAB TW 25 10 10 P 09. PROGRAMMING LABORATORY 19. DATA STRUCTURES AND FILES LAB PR 50 20 AA F PR 50 20 22 P C 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 34 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 24 P 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 08 F GRAND TOTAL = 624/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS : , 71241768M , , PICT , S8058597 S8058597 PAWAR SNEHA SADASHIV JYOTI 01. DISCRETE STRUCTURES PP 100 40 43 P C 11. ENG MATHS III 100 40 63 P 02. COMPUTER ORGANIZATION PP 100 40 75 P C 12. COMPUTER GRAPHICS PP 100 40 74 P 100 40 56 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 59 P 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 79 P C 14. DATA STRUCTURES AND FILES PP 100 40 58 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 56 P C 100 40 71 P 15. DATA COMMUNICATION PP 24 P 06. DIGITAL LABORATORY 50 20 39 P C 25 10 TW 16. PROCESSOR INTERFACING LABORATORY TW 50 20 32 P C 17. PROCESSOR INTERFACING LABORATORY OR 50 20 25 P 07. DIGITAL LABORATORY PR 18. DATA STRUCTURES AND FILES LAB TW 25 10 08. PROGRAMMING LABORATORY TW 50 20 44 P C 17 P 50 20 36 P C 19. DATA STRUCTURES AND FILES LAB PR 50 20 38 P 09. PROGRAMMING LABORATORY PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 43 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 40 P 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 45 P GRAND TOTAL = 1017/1500, RESULT: FIRST CLASS WITH DISTINCTION

DATE: 14 AUG. 2012 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 33 (513)

NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71241769K , , PICT , s8058598 S8058598 PAYGUDE GAUTAMI SANJAY SUMAN 01. DISCRETE STRUCTURES PP 100 40 49 P C 11. ENG MATHS III PP 100 40 46 P 02. COMPUTER ORGANIZATION PP 100 40 56 P C 12. COMPUTER GRAPHICS PP 100 40 58 P 100 40 61 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 59 P 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 40 P C PP 100 40 52 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 50 P C PP 100 40 62 P 15. DATA COMMUNICATION 50 20 41 P C 25 10 20 P 06. DIGITAL LABORATORY 16. PROCESSOR INTERFACING LABORATORY TW TW 07. DIGITAL LABORATORY 50 20 25 P C 50 20 41 P PR 17. PROCESSOR INTERFACING LABORATORY OR 08. PROGRAMMING LABORATORY 50 20 43 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 21 P TW 09. PROGRAMMING LABORATORY PR 50 20 22 P C 19. DATA STRUCTURES AND FILES LAB PR 50 20 41 P 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 45 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 47 P 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 40 P GRAND TOTAL = 919/1500, RESULT: FIRST CLASS ORDN. 1 MARKS: S8058599 PHADKE MUGDHA RAVINDRA PRADNYA , s8058599 01. DISCRETE STRUCTURES 40 56 P PP 100 40 66 P C 11. ENG MATHS III PP 100 50 P C 40 02. COMPUTER ORGANIZATION PP 100 40 12. COMPUTER GRAPHICS PP 100 59 P 40 49 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 43 P 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 100 40 67 P C PP 40 56 P 40 54 P C PP 100 05. HUMANITIES AND SOCIAL SCIENCES PP 100 15. DATA COMMUNICATION 40 62 P 50 20 40 P C 19 P 06. DIGITAL LABORATORY 25 10 TW 16. PROCESSOR INTERFACING LABORATORY TW 50 20 37 P C 50 20 07. DIGITAL LABORATORY PR 17. PROCESSOR INTERFACING LABORATORY OR 36 P 50 20 41 P C 08. PROGRAMMING LABORATORY TW 18. DATA STRUCTURES AND FILES LAB TW 25 10 20 P 09. PROGRAMMING LABORATORY 19. DATA STRUCTURES AND FILES LAB PR 50 20 38 P PR 50 20 39 P C 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 42 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 43 P 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 P GRAND TOTAL = 937/1500, RESULT: FIRST CLASS ORDN. 1 MARKS: , 71100957M , , PICT , S8058600 S8058600 POTE ISHWAR VITTHAL SUNITA 01. DISCRETE STRUCTURES PP 100 40 58 P 11. ENG MATHS III 100 40 53 P 02. COMPUTER ORGANIZATION PP 100 40 53 P 12. COMPUTER GRAPHICS PP 100 40 53 P 100 40 42 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 40 P 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 46 P C 14. DATA STRUCTURES AND FILES PP 100 40 47 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 44 P C PP 100 40 53 P 15. DATA COMMUNICATION 10 20 P 06. DIGITAL LABORATORY 50 20 33 P C 25 TW 16. PROCESSOR INTERFACING LABORATORY TW 50 20 22 P C 17. PROCESSOR INTERFACING LABORATORY OR 50 20 38 P 07. DIGITAL LABORATORY PR 18. DATA STRUCTURES AND FILES LAB TW 25 10 16 P 08. PROGRAMMING LABORATORY TW 50 20 36 P C 19. DATA STRUCTURES AND FILES LAB PR 50 20 34 P 50 20 37 P C 09. PROGRAMMING LABORATORY PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 39 P.C. 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 40 P 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 21 P GRAND TOTAL = 825/1500, RESULT: HIGHER SECOND CLASS ORDN. 1 MARKS:

DATE: 14 AUG. 2012 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 34 (514)

NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

:	S8058601 RAHUL BAIJAL				SAI	DHNA		, 71	L100962н	1	,	,	PICT		, S	8058	601
(01. DISCRETE STRUCTURES	PP	100	40	48	PС	11	. ENG MAT	THS III			PI	1 0	0 4	10	50	Р
(02. COMPUTER ORGANIZATION	PP	100	40	69	РС	12	. COMPUTE	R GRAPH	HICS		PI	1 0	0 4	10	73	Р
(03. DIGITAL ELECTRONICS & LOGIC DESI	[GPP	100	40	68	РС	13	. PROCESS	OR ARCH	HITEC	TURE & INTE	ER. PI	1 0	0 4	10	59	Р
(04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	54	РС	14	. DATA ST	RUCTURE	ES ANI	D FILES	PI	1 0	0 4	10	61	Р
(05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	55	РС	15	. DATA CO	OMMUNICA	ATION		PI	1 0	0 4	10	62	Р
(06. DIGITAL LABORATORY	TW	50	20	40	РС	16	. PROCESS	OR INTE	ERFAC	ING LABORAT	TORY T	N 2	5 3	LO	17	Р
(07. DIGITAL LABORATORY	PR	50	20	45	РС	17	. PROCESS	OR INTE	ERFAC	ING LABORAT	TORY O	٦ 5	0 2	20	41	Р
(08. PROGRAMMING LABORATORY	TW	50	20	42	РС	18	. DATA ST	RUCTURE	ES ANI	D FILES LAB	3 T\	N 2	5 3	LO	19	Р
(09. PROGRAMMING LABORATORY	PR	50	20	25	РС	19	. DATA ST	RUCTURE	ES ANI	D FILES LAB	3 PI	٦ 5	0 2	20	35	Р
	10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	42	РС	20	. OBJECT	ORIENTE	ED PR	OGRAMMING L	LAB T\	N 5	0 2	20	45	Р
							21	. OBJECT	ORIENTE	ED PR	OGRAMMING L	LAB PI	R 5	0 2	20	39	Р

GRAND TOTAL = 989/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS:

S8058602 RAHUL SHARMA PROMILA , 71100963F , , PICT , S8058602 50 P 01. DISCRETE STRUCTURES 100 40 11. ENG MATHS III 100 40 44 P 100 02. COMPUTER ORGANIZATION PP 100 40 44 P C 12. COMPUTER GRAPHICS 40 40 P 40 P C 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 40 100 40 100 40 P 13. PROCESSOR ARCHITECTURE & INTER. PP 40 52 P C 100 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 14. DATA STRUCTURES AND FILES PP 40 40 P 40 49 P C 100 05. HUMANITIES AND SOCIAL SCIENCES PP 100 15. DATA COMMUNICATION 40 66 P 50 20 21 P C 10 P 06. DIGITAL LABORATORY 25 10 TW 16. PROCESSOR INTERFACING LABORATORY TW 50 20 23 P 20 20 P 07. DIGITAL LABORATORY PR 17. PROCESSOR INTERFACING LABORATORY OR 50 50 20 20 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 10 P 08. PROGRAMMING LABORATORY TW 19. DATA STRUCTURES AND FILES LAB PR 50 20 AA F 09. PROGRAMMING LABORATORY PR 50 20 23 P C 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 31 P C 50 20 22 P 20. OBJECT ORIENTED PROGRAMMING LAB TW 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 AA F

GRAND TOTAL = 645/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

, 71100964D , , PICT , S8058603 S8058603 RAMPURAWALA MARIYA FIROZ FARIDA 01. DISCRETE STRUCTURES PP 100 40 62 P C 11. ENG MATHS III 100 40 59 P 02. COMPUTER ORGANIZATION PP 100 40 45 P C 12. COMPUTER GRAPHICS PP 100 40 48 P 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 40 64 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 52 P 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 59 P C 14. DATA STRUCTURES AND FILES PP 100 40 52 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 45 P C 100 40 59 P 15. DATA COMMUNICATION PP 50 20 38 P C 06. DIGITAL LABORATORY 25 10 18 P TW 16. PROCESSOR INTERFACING LABORATORY TW 07. DIGITAL LABORATORY 50 20 39 P C 17. PROCESSOR INTERFACING LABORATORY OR 50 20 42 P PR 18. DATA STRUCTURES AND FILES LAB TW 25 10 19 P 08. PROGRAMMING LABORATORY TW 50 20 41 P C 09. PROGRAMMING LABORATORY 50 20 34 P C 50 20 40 P PR 19. DATA STRUCTURES AND FILES LAB PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 41 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 41 P 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 46 P

GRAND TOTAL = 944/1500, RESULT: FIRST CLASS

40 59 P C

DATE: 14 AUG. 2012 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 35 (515)

NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8058604	RANADIVE ABHIJEET	JANARDAN			KANCHAN		, 71100966L	,	, P	ICT	, :	s8058	8604
01. DISCRET	E STRUCTURES	PP	100	40	41 P C	11. E	NG MATHS III		PP	100	40	62	Р

12. COMPUTER GRAPHICS

100

21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 46 P

40

60 P

03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 40 53 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 55 P 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 64 P C 14. DATA STRUCTURES AND FILES PP 100 40 61 P

05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 51 P C 15. DATA COMMUNICATION PP 100 40 50 P 06. DIGITAL LABORATORY TW 50 20 32 P C 16. PROCESSOR INTERFACING LABORATORY TW 25 10 19 P

07. DIGITAL LABORATORY PR 50 20 42 P C 17. PROCESSOR INTERFACING LABORATORY OR 50 20 43 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 18 P

09. PROGRAMMING LABORATORY PR 50 20 29 P C 19. DATA STRUCTURES AND FILES LAB PR 50 20 41 P 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 40 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 33 P

21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 35 P
GRAND TOTAL = 920/1500, RESULT: FIRST CLASS

PP 100

ORDN. 1 MARKS:

02. COMPUTER ORGANIZATION

S805	8606 RATHOD ASHWINKUMAR AMARS	ING			RAN	NJANA		, 71241770C	,	Р	ICT	,	S8058	606
01.	DISCRETE STRUCTURES	PP	100	40	40	P C	11	. ENG MATHS III		PP	100	40	61	Р
02.	COMPUTER ORGANIZATION	PP	100	40	70	P C	12	COMPUTER GRAPHICS		PP	100	40	64	Р
03.	DIGITAL ELECTRONICS & LOGIC DESI	GPP	100	40	70	P C	13	PROCESSOR ARCHITEC	CTURE & INTER.	PP	100	40	59	Р
04.	FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	58	P C	14	. DATA STRUCTURES AN	ND FILES	PP	100	40	57	Р
05.	HUMANITIES AND SOCIAL SCIENCES	PP	100	40	41	P C	15	DATA COMMUNICATION	N	PP	100	40	76	Р
06.	DIGITAL LABORATORY	TW	50	20	38	P C	16	PROCESSOR INTERFAC	CING LABORATORY	TW	25	10	17	Р
07.	DIGITAL LABORATORY	PR	50	20	35	P C	17	PROCESSOR INTERFAC	CING LABORATORY	OR	50	20	41	Р
08.	PROGRAMMING LABORATORY	TW	50	20	38	P C	18	. DATA STRUCTURES AN	ND FILES LAB	TW	25	10	19	Р
09.	PROGRAMMING LABORATORY	PR	50	20	32	P C	19	. DATA STRUCTURES AN	ND FILES LAB	PR	50	20	33	Р
10.	COMMUNICATION AND LANGUAGE LAB.	TW	50	20	43	P C	20	OBJECT ORIENTED PR	ROGRAMMING LAB	TW	50	20	38	Р

GRAND TOTAL = 976/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS:

S8058607 RATTAN NAKUL VISHWAS				BI	MLA	, 71100971G , , PICT , S80	58607
01. DISCRETE STRUCTURES	PP	100	40	47	PС	11. ENG MATHS III PP 100 40 2	0 F
02. COMPUTER ORGANIZATION	PP	100	40	41	PC	12. COMPUTER GRAPHICS PP 100 40 4	4 P
03. DIGITAL ELECTRONICS & LOGIC DESI	GPP	100	40	53	PC	13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 4	8 P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	52	P C	14. DATA STRUCTURES AND FILES PP 100 40 4	0 P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	40	PC	15. DATA COMMUNICATION PP 100 40 4	9 P
06. DIGITAL LABORATORY	TW	50	20	33	PC	16. PROCESSOR INTERFACING LABORATORY TW 25 10 1	.6 P
07. DIGITAL LABORATORY	PR	50	20	36	PC	17. PROCESSOR INTERFACING LABORATORY OR 50 20 3	8 P
08. PROGRAMMING LABORATORY	TW	50	20	37	PC	18. DATA STRUCTURES AND FILES LAB TW 25 10 1	.7 P
09. PROGRAMMING LABORATORY	PR	50	20	38	РС	19. DATA STRUCTURES AND FILES LAB PR 50 20 3	9 P

10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 38 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 40 P 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 39 P

GRAND TOTAL = 805/1500, RESULT: FAILS A.T.K.T.

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER MANJU , 71100975K , , PICT , s8058608 S8058608 RIA NARAYAN 01. DISCRETE STRUCTURES PP 100 40 56 P C 11. ENG MATHS III PP 100 40 40 P 02. COMPUTER ORGANIZATION 100 40 63 P C 12. COMPUTER GRAPHICS 100 40 59 P PP 100 40 67 P C 100 40 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 13. PROCESSOR ARCHITECTURE & INTER. PP 62 P 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 66 P C 100 40 41 P PP 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 50 P C PP 100 40 61 P 15. DATA COMMUNICATION 20 31 P C 25 10 15 P 06. DIGITAL LABORATORY 50 16. PROCESSOR INTERFACING LABORATORY TW TW 07. DIGITAL LABORATORY 50 20 37 P C 50 20 31 P PR 17. PROCESSOR INTERFACING LABORATORY OR 08. PROGRAMMING LABORATORY 50 20 28 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 TW 15 P 09. PROGRAMMING LABORATORY PR 50 20 40 P C 19. DATA STRUCTURES AND FILES LAB PR 50 20 38 P 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 38 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 31 P 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 25 P GRAND TOTAL = 894+06/1500, RESULT: FIRST CLASS [0.2] ORDN. 1 MARKS: S8058609 ROMIL MEHTA SHEELA , 71100978D , , PICT , s8058609 01. DISCRETE STRUCTURES PP 100 40 66 P C 11. ENG MATHS III PP 100 40 67 P 60 P C 40 02. COMPUTER ORGANIZATION PP 100 40 12. COMPUTER GRAPHICS PP 100 57 P 40 13. PROCESSOR ARCHITECTURE & INTER. PP 40 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 67 P C 100 59 P 14. DATA STRUCTURES AND FILES 55 P C 100 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 PP 40 69 P 40 100 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 P C 15. DATA COMMUNICATION 40 68 P 50 20 39 P C 06. DIGITAL LABORATORY 25 10 18 P TW 16. PROCESSOR INTERFACING LABORATORY TW 50 20 35 P C 20 07. DIGITAL LABORATORY PR 17. PROCESSOR INTERFACING LABORATORY OR 50 36 P 50 20 43 P C 08. PROGRAMMING LABORATORY TW 18. DATA STRUCTURES AND FILES LAB TW 25 10 19 P 09. PROGRAMMING LABORATORY PR 50 20 40 P C 19. DATA STRUCTURES AND FILES LAB PR 50 20 38 P 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 42 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 41 P 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 42 P GRAND TOTAL = 1001/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS: , 71100980F , , PICT , S8058610 S8058610 SABALE BHUSHAN BALASAHEB SUMAN 01. DISCRETE STRUCTURES PP 100 40 49 P C 11. ENG MATHS III 100 40 73 P 02. COMPUTER ORGANIZATION PP 100 40 58 P C 12. COMPUTER GRAPHICS PP 100 40 63 P 100 40 64 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 54 P 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 60 P C 14. DATA STRUCTURES AND FILES PP 100 40 44 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 45 P C 100 40 59 P 15. DATA COMMUNICATION PP 10 19 P 06. DIGITAL LABORATORY 50 20 36 P C 25 TW 16. PROCESSOR INTERFACING LABORATORY TW 50 20 40 P C 17. PROCESSOR INTERFACING LABORATORY OR 50 20 32 P 07. DIGITAL LABORATORY PR 18. DATA STRUCTURES AND FILES LAB TW 25 10 17 P 08. PROGRAMMING LABORATORY TW 50 20 37 P C 50 20 30 P C 19. DATA STRUCTURES AND FILES LAB PR 50 20 30 P 09. PROGRAMMING LABORATORY PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 41 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 36 P 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 32 P GRAND TOTAL = 919/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS:

UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(INFORMATION TECHNOLOGY) DATE: 14 AUG. 2012 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 37 (517) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER SHAHIN S8058611 SAGAR SABIR DAMANI , 71100981D , , PICT , S8058611 PP 100 40 90 P 01. DISCRETE STRUCTURES PP 100 40 79 P C 11. ENG MATHS III 02. COMPUTER ORGANIZATION PP 100 40 80 P C 12. COMPUTER GRAPHICS PP 100 65 P 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 40 66 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 66 P

06. DIGITAL LABORATORY 50 20 44 P C 25 10 24 P 16. PROCESSOR INTERFACING LABORATORY TW TW 50 20 45 P C 07. DIGITAL LABORATORY 50 20 44 P PR 17. PROCESSOR INTERFACING LABORATORY OR 08. PROGRAMMING LABORATORY 50 20 44 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 22 P TW 09. PROGRAMMING LABORATORY PR 50 20 44 P C 19. DATA STRUCTURES AND FILES LAB PR 50 20 40 P 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 44 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 47 P 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 44 P

14. DATA STRUCTURES AND FILES

15. DATA COMMUNICATION

PP 100 40 69 P

PP 100 40 68 P

GRAND TOTAL = 1152/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS:

04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 87 P C

05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 P C

S8058612 SAHARE NUPUR ARVIND MAMTA 71100982R PTCT S8058612

5805	SAHARE NUPUR ARVIND				MAI	ИΙА		, /1100982B	, ,	,	PICI	,	58058	612
01.	DISCRETE STRUCTURES	PP	100	40	40	P C	11.	ENG MATHS III		PP	100	40	58	Р
02.	COMPUTER ORGANIZATION	PP	100	40	54	P C	12.	COMPUTER GRAPHICS		PP	100	40	53	Р
03.	DIGITAL ELECTRONICS & LOGIC DESI	GPP	100	40	46	Р	13.	PROCESSOR ARCHITE	CTURE & INTER.	PP	100	40	58	Р
04.	FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	52	P C	14.	DATA STRUCTURES A	ND FILES	PP	100	40	46	Р
05.	HUMANITIES AND SOCIAL SCIENCES	PP	100	40	43	P C	15.	DATA COMMUNICATIO	N	PP	100	40	56	Р
06.	DIGITAL LABORATORY	TW	50	20	34	P C	16.	PROCESSOR INTERFA	CING LABORATORY	/ TW	25	10	17	Р
07.	DIGITAL LABORATORY	PR	50	20	22	P C	17.	PROCESSOR INTERFA	CING LABORATORY	OR	50	20	30	Р
08.	PROGRAMMING LABORATORY	TW	50	20	38	P C	18.	DATA STRUCTURES A	ND FILES LAB	TW	25	10	15	Р
09.	PROGRAMMING LABORATORY	PR	50	20	30	Р	19.	DATA STRUCTURES A	ND FILES LAB	PR	50	20	16	F
10.	COMMUNICATION AND LANGUAGE LAB.	TW	50	20	39	P C	20.	OBJECT ORIENTED P	ROGRAMMING LAB	TW	50	20	38	Р
							21.	OBJECT ORIENTED P	ROGRAMMING LAB	PR	50	20	07	F

GRAND TOTAL = 792/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS:

S8058613 SARAF DARPAN RAJENDRA				AM]	ITA	, 71100986E , , PICT , S80586	513
01. DISCRETE STRUCTURES	PP	100	40	40	РС	11. ENG MATHS III PP 100 40 65	Р
02. COMPUTER ORGANIZATION	PP	100	40	57	РС	12. COMPUTER GRAPHICS PP 100 40 46	Р
03. DIGITAL ELECTRONICS & LOGIC DES	[GPP	100	40	57	РС	13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 48	Р
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	63	РС	14. DATA STRUCTURES AND FILES PP 100 40 47	Р
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	40	РС	15. DATA COMMUNICATION PP 100 40 63	Р
06. DIGITAL LABORATORY	TW	50	20	34	РС	16. PROCESSOR INTERFACING LABORATORY TW 25 10 19	Р
07. DIGITAL LABORATORY	PR	50	20	42	РС	17. PROCESSOR INTERFACING LABORATORY OR 50 20 35	Р
08. PROGRAMMING LABORATORY	TW	50	20	32	РС	18. DATA STRUCTURES AND FILES LAB TW 25 10 20	Р
09. PROGRAMMING LABORATORY	PR	50	20	40	РС	19. DATA STRUCTURES AND FILES LAB PR 50 20 16#	Р
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	41	РС	20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 39	Р
						21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 34	Р

GRAND TOTAL = 878/1500, RESULT: HIGHER SECOND CLASS # [0.4]

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8058614 SASANE PRANITA RAMESH				ALI	KA			, 71100	989к	,		,	PICT	-	,	S8058	614
01. DISCRETE STRUCTURES	PP	100	40	49	P C	11	l. E	NG MATHS	III			F	PP 1	.00	40	40	Р
02. COMPUTER ORGANIZATION	PP	100	40	30	F	12	2. c	OMPUTER G	RAPHICS	5		F	P 1	.00	40	49	Р
03. DIGITAL ELECTRONICS & LOGIC DESIG	GPP	100	40	51	РС	13	3. P	ROCESSOR	ARCHITE	ECTUR	E & INTER	. F	P 1	.00	40	31	F
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	40	P C	14	4. D	ATA STRUC	TURES A	AND F	ILES	F	P 1	.00	40	65	Р
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	40	P C	15	5. D	ATA COMMU	NICATIO	ON		F	P 1	.00	40	40	Р
06. DIGITAL LABORATORY	TW	50	20	34	P C	16	δ. P	ROCESSOR	INTERFA	ACING	LABORATO	RY 7	ΓW	25	10	14	Р
07. DIGITAL LABORATORY	PR	50	20	22	P C	17	7. P	ROCESSOR	INTERFA	ACING	LABORATO	RY (OR	50	20	38	Р
08. PROGRAMMING LABORATORY	TW	50	20	35	P C	18	3. D	ATA STRUC	TURES A	AND F	ILES LAB	٦	ΓW	25	10	17	Р
09. PROGRAMMING LABORATORY	PR	50	20	20	P C	19). D	ATA STRUC	TURES A	AND F	ILES LAB	F	PR	50	20	28	Р
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	39	P C	20). 0	BJECT ORI	ENTED F	PROGR	AMMING LA	\B 7	ΓW	50	20	38	Р
						21	L. o	BJECT ORI	ENTED F	PROGR	AMMING LA	AB F	PR	50	20	25	Р

GRAND TOTAL = 745/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

S8058615 SAYYED FAKHRUDDIN KAMALUDDIN RAZIYA KHANAM , 71241771M , PICT , s8058615 PP 100 40 62 P C 01. DISCRETE STRUCTURES 11. ENG MATHS III 100 40 55 P PP 100 100 02. COMPUTER ORGANIZATION 40 63 P C 12. COMPUTER GRAPHICS 40 68 P 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 40 66 P C 100 40 68 P 13. PROCESSOR ARCHITECTURE & INTER. PP 40 70 P C 100 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 14. DATA STRUCTURES AND FILES PP 40 61 P 40 40 P C 100 05. HUMANITIES AND SOCIAL SCIENCES PP 100 15. DATA COMMUNICATION 40 66 P 50 20 38 P C 06. DIGITAL LABORATORY 25 10 24 P TW 16. PROCESSOR INTERFACING LABORATORY TW 50 20 36 P C 20 07. DIGITAL LABORATORY PR 17. PROCESSOR INTERFACING LABORATORY OR 50 46 P 50 20 38 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 20 P 08. PROGRAMMING LABORATORY TW 19. DATA STRUCTURES AND FILES LAB PR 50 20 41 P 09. PROGRAMMING LABORATORY PR 50 20 30 P C 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 44 P C 50 20 47 P 20. OBJECT ORIENTED PROGRAMMING LAB TW 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 47 P

GRAND TOTAL = 1030/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS:

, 71100999G , , PICT , s8058616 S8058616 SHARMA LAKSHMI KANAK 01. DISCRETE STRUCTURES PP 100 40 58 P C 11. ENG MATHS III 100 40 52 P 02. COMPUTER ORGANIZATION PP 100 40 58 P C 12. COMPUTER GRAPHICS PP 100 40 57 P 100 40 56 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 46 P 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 69 P C 14. DATA STRUCTURES AND FILES PP 100 40 59 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 52 P C 100 40 67 P 15. DATA COMMUNICATION PP 50 20 39 P C 06. DIGITAL LABORATORY 25 10 21 P TW 16. PROCESSOR INTERFACING LABORATORY TW 07. DIGITAL LABORATORY 50 20 40 P C 17. PROCESSOR INTERFACING LABORATORY OR 50 20 34 P PR 18. DATA STRUCTURES AND FILES LAB TW 25 10 21 P 08. PROGRAMMING LABORATORY TW 50 20 44 P C 09. PROGRAMMING LABORATORY 50 20 35 P C 19. DATA STRUCTURES AND FILES LAB PR 50 20 40 P PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 44 P.C. 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 46 P 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 47 P

GRAND TOTAL = 985/1500, RESULT: FIRST CLASS

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14. DATA STRUCTURES AND FILES PP

16. PROCESSOR INTERFACING LABORATORY TW

17. PROCESSOR INTERFACING LABORATORY OR

18. DATA STRUCTURES AND FILES LAB TW 25 10 20 P

19. DATA STRUCTURES AND FILES LAB PR 50 20 28 P

20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 43 P

15. DATA COMMUNICATION

100

25

PP 100

40

40

10

50 20

69 P

66 P

22 P

40 P

21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 46 PGRAND TOTAL = 1046/1500, RESULT: FIRST CLASS WITH DISTINCTION

PR 50 20 28 P C

100

100

TW

PR

TW

10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 44 P C

40 77 P C

40 52 P C

50 20 42 P C

50 20 43 P C

50 20 44 P C

S8058619 SHINDE SUSHANT GAUTAM , s8058619 SANGITA 01. DISCRETE STRUCTURES 11. ENG MATHS III PP 100 40 44 P C PP 100 40 40 P 40 51 P C 02. COMPUTER ORGANIZATION PP 100 12. COMPUTER GRAPHICS PP 100 40 58 P 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 40 43 P C 40 P 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 100 40 80 P C PP 40 51 P 40 42 P C 100 05. HUMANITIES AND SOCIAL SCIENCES PP 100 15. DATA COMMUNICATION 40 69 P 50 20 41 P C 22 P 06. DIGITAL LABORATORY 25 10 TW 16. PROCESSOR INTERFACING LABORATORY TW 50 20 25 P C 20 07. DIGITAL LABORATORY PR 17. PROCESSOR INTERFACING LABORATORY OR 50 28 P 50 20 42 P C 08. PROGRAMMING LABORATORY TW 18. DATA STRUCTURES AND FILES LAB TW 25 10 18 P 09. PROGRAMMING LABORATORY PR 50 20 28 P C 19. DATA STRUCTURES AND FILES LAB PR 50 20 34 P 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 42 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 43 P 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 42 P

GRAND TOTAL = 883/1500, RESULT: HIGHER SECOND CLASS ORDN. 1 MARKS:

04. FUNDAMENTAL OF DATA STRUCTURES PP

05. HUMANITIES AND SOCIAL SCIENCES PP

06. DIGITAL LABORATORY

07. DIGITAL LABORATORY

ORDN. 1 MARKS:

ORDN. 1 MARKS:

08. PROGRAMMING LABORATORY

09. PROGRAMMING LABORATORY

, 71101009K , , PICT S8058620 SHIRA VISHAL BHAGWANSING MALTA , s8058620 01. DISCRETE STRUCTURES PP 100 40 42 P C 11. ENG MATHS III 100 40 49 P 02. COMPUTER ORGANIZATION PP 100 40 54 P C 12. COMPUTER GRAPHICS PP 100 40 48 P 100 40 62 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 41 P 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 49 P C 14. DATA STRUCTURES AND FILES PP 100 40 58 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 40 P C 100 40 59 P 15. DATA COMMUNICATION PP 06. DIGITAL LABORATORY 50 20 35 P C 25 10 17 P TW 16. PROCESSOR INTERFACING LABORATORY TW 50 20 28 P C 17. PROCESSOR INTERFACING LABORATORY OR 50 20 27 P 07. DIGITAL LABORATORY PR 18. DATA STRUCTURES AND FILES LAB TW 25 10 17 P 08. PROGRAMMING LABORATORY TW 50 20 34 P C 50 20 28 P C 19. DATA STRUCTURES AND FILES LAB PR 50 20 37 P 09. PROGRAMMING LABORATORY PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 41 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 39 P 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 29 P

GRAND TOTAL = 834/1500, RESULT: HIGHER SECOND CLASS

DATE: 14 AUG. 2012 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 40 (520)

NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71101010C , , PICT , s8058621 S8058621 SHIRBHATE ABHILASH DNYANESHWAR RAJSHRI 01. DISCRETE STRUCTURES PP 100 40 46 P C 11. ENG MATHS III PP 100 40 18 F 02. COMPUTER ORGANIZATION PP 100 40 40 P C 12. COMPUTER GRAPHICS 100 40 43 P 100 40 63 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 26 F 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 48 P C 14. DATA STRUCTURES AND FILES PP 100 40 40 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 40 P C PP 100 40 15. DATA COMMUNICATION 50 20 33 P C 25 10 10 P 06. DIGITAL LABORATORY 16. PROCESSOR INTERFACING LABORATORY TW TW 50 20 22 P C 07. DIGITAL LABORATORY 50 20 36 P PR 17. PROCESSOR INTERFACING LABORATORY OR 08. PROGRAMMING LABORATORY 50 20 36 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 13 P TW 09. PROGRAMMING LABORATORY PR 50 20 27 P C 19. DATA STRUCTURES AND FILES LAB PR 50 20 32 P 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 35 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 29 P 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 08 F GRAND TOTAL = 688/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS: S8058622 SHUKLA MANISH NITYANAND MADHURI , 71045622M , , PICT , S8058622 01. DISCRETE STRUCTURES PP 100 40 57 P C 11. ENG MATHS III PP 100 40 23 F PP 100 40 02. COMPUTER ORGANIZATION PP 100 40 44 P C 12. COMPUTER GRAPHICS 40 P 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 40 57 P C 10 F 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 40 100 40 P 100 49 P C PP 40 40 45 P C PP 100 05. HUMANITIES AND SOCIAL SCIENCES PP 100 15. DATA COMMUNICATION 40 45 P 50 20 28 P C 12 P 06. DIGITAL LABORATORY 25 10 TW 16. PROCESSOR INTERFACING LABORATORY TW 50 20 30 P C 50 20 07. DIGITAL LABORATORY PR 17. PROCESSOR INTERFACING LABORATORY OR 21 P 50 20 28 P C 08. PROGRAMMING LABORATORY TW 18. DATA STRUCTURES AND FILES LAB TW 25 10 11 P 09. PROGRAMMING LABORATORY 19. DATA STRUCTURES AND FILES LAB PR 50 20 28 P PR 50 20 32 P C 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 31 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 27 P 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 06 F GRAND TOTAL = 664/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS : , 71101015D , , PICT S8058623 SIDDHANT BANSAL , s8058623 SUJATA 01. DISCRETE STRUCTURES PP 100 40 64 P C 11. ENG MATHS III 100 40 57 P 02. COMPUTER ORGANIZATION PP 100 40 52 P C 12. COMPUTER GRAPHICS PP 100 40 50 P 100 40 63 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 44 P 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 62 P C 14. DATA STRUCTURES AND FILES PP 100 40 62 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 45 P C PP 100 40 56 P 15. DATA COMMUNICATION 15 P 06. DIGITAL LABORATORY 50 20 27 P C 25 10 TW 16. PROCESSOR INTERFACING LABORATORY TW 50 20 32 P C 17. PROCESSOR INTERFACING LABORATORY OR 50 20 39 P 07. DIGITAL LABORATORY PR 18. DATA STRUCTURES AND FILES LAB TW 25 10 16 P 08. PROGRAMMING LABORATORY TW 50 20 27 P C 50 20 34 P C 19. DATA STRUCTURES AND FILES LAB PR 50 20 37 P 09. PROGRAMMING LABORATORY PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 36 P.C. 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 37 P 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 21 P GRAND TOTAL = 876/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS:

DATE: 14 AUG. 2012 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 41 (521)

NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S	8058624	SOLANKE BHUSHAN NARENDRA				REI	KHA		, 71101017L	,	, P]	CT	,	s8058	624
0	1. DISCRETI	E STRUCTURES	PP	100	40	44	P C	11.	ENG MATHS III		PP	100	40	06	F
0	2. COMPUTE	R ORGANIZATION	PP	100	40	43	РС	12.	COMPUTER GRAPHICS	5	PP	100	40	43	Р
0	3. DIGITAL	ELECTRONICS & LOGIC DESIGNATION	GPP	100	40	40	РС	13.	PROCESSOR ARCHITE	CTURE & INTER.	PP	100	40	19	F
0	4. FUNDAME	NTAL OF DATA STRUCTURES	PP	100	40	48	РС	14.	DATA STRUCTURES A	ND FILES	PP	100	40	57	Р
0	5. HUMANIT	IES AND SOCIAL SCIENCES	PP	100	40	40	РС	15.	DATA COMMUNICATIO	N	PP	100	40	46	Р
0	6. DIGITAL	LABORATORY	TW	50	20	33	РС	16.	PROCESSOR INTERFA	CING LABORATOR	Y TW	25	10	10	Р
0	7. DIGITAL	LABORATORY	PR	50	20	30	РС	17.	PROCESSOR INTERFA	CING LABORATOR	Y OR	50	20	14	F
0	8. PROGRAM	MING LABORATORY	TW	50	20	31	РС	18.	DATA STRUCTURES A	ND FILES LAB	TW	25	10	12	Р
0	9. PROGRAM	MING LABORATORY	PR	50	20	24	РС	19.	DATA STRUCTURES A	ND FILES LAB	PR	50	20	15	F
1	O. COMMUNIO	CATION AND LANGUAGE LAB.	TW	50	20	35	РС	20.	OBJECT ORIENTED P	ROGRAMMING LAB	TW	50	20	33	Р
								21.	OBJECT ORIENTED P	ROGRAMMING LAB	PR	50	20	10	F

GRAND TOTAL = 633/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS:

S8058625 AVINASH SONAWANE SOJARBAI , S8058625 01. DISCRETE STRUCTURES PP 100 40 65 P C 11. ENG MATHS III PP 100 40 07 F PP 100 40 02. COMPUTER ORGANIZATION PP 100 40 48 P C 12. COMPUTER GRAPHICS 40 P

40 40 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 27 F 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 14. DATA STRUCTURES AND FILES 40 57 P C PP 100 40 42 P

40 47 P.C PP 100 05. HUMANITIES AND SOCIAL SCIENCES PP 100 15. DATA COMMUNICATION 57 P 50 20 20 P C 25 10 10 P 06. DIGITAL LABORATORY TW 16. PROCESSOR INTERFACING LABORATORY TW 07. DIGITAL LABORATORY 50 20 22 P 50 20 12 F PR 17. PROCESSOR INTERFACING LABORATORY OR

50 20 34 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 12 P 08. PROGRAMMING LABORATORY TW 09. PROGRAMMING LABORATORY PR 50 20 27 P C 19. DATA STRUCTURES AND FILES LAB PR 50 20 36 P

10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 31 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 35 P 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 34 P

GRAND TOTAL = 703/1500, RESULT: FAILS A.T.K.T.

S8058626 SONAWANE JAGDISH RAJENDRA

ORDN. 1 MARKS:

HIRABAI

, s8058626

01. DISCRETE STRUCTURES PP 100 40 43 P C 11. ENG MATHS III PP 100 40 53 P 02. COMPUTER ORGANIZATION PP 100 40 68 P C 12. COMPUTER GRAPHICS PP 100 40 58 P 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 40 55 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 59 P 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 71 PC 14. DATA STRUCTURES AND FILES PP 100 40 50 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 46 P C PP 100 40 64 P 15. DATA COMMUNICATION TW 50 20 36 P C 25 10 19 P 06. DIGITAL LABORATORY 16. PROCESSOR INTERFACING LABORATORY TW

07. DIGITAL LABORATORY 50 20 42 P C 17. PROCESSOR INTERFACING LABORATORY OR 50 20 39 P PR 18. DATA STRUCTURES AND FILES LAB TW 25 10 20 P 08. PROGRAMMING LABORATORY TW 50 20 36 P C 09. PROGRAMMING LABORATORY PR 50 20 27 P C

19. DATA STRUCTURES AND FILES LAB PR 50 20 36 P 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 41 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 41 P 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 29 P

GRAND TOTAL = 933/1500, RESULT: FIRST CLASS

DATE: 14 AUG. 2012 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 42 (522)

NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

							 	-				 						
S8058627 S	TUTI MITTAL					AVNA			, 71101022	G	,	,	ΡI	CT	,	S805	8627	
01. DISCRETE	STRUCTURES	PP	100	40	48	Р	1:		ENG MATHS III				PP	100	40	44	Р	

02. COMPUTER ORGANIZATION PP	100	40	51 P C	12. COMPUTER GRAPHICS	PP	100	40	46	Р
03. DIGITAL ELECTRONICS & LOGIC DESIGPP	100	40	44 P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	40	Р
04. FUNDAMENTAL OF DATA STRUCTURES PP	100	40	43 P C	14. DATA STRUCTURES AND FILES	PP	100	40	53	Р

05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 40 P C 15. DATA COMMUNICATION PP 100 40 47 P 06. DIGITAL LABORATORY TW 50 20 33 P C 16. PROCESSOR INTERFACING LABORATORY TW 25 10 18 P

07. DIGITAL LABORATORY PR 50 20 32 P C 17. PROCESSOR INTERFACING LABORATORY OR 50 20 41 P 08. PROGRAMMING LABORATORY TW 50 20 30 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 14 P

09. PROGRAMMING LABORATORY PR 50 20 22 P 19. DATA STRUCTURES AND FILES LAB PR 50 20 36 P 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 37 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 38 P

21. OBJECT ORIENTED PROGRAMMING LAB PR

21. OBJECT ORIENTED PROGRAMMING LAB PR

50 20

50 20 04 F

06 F

GRAND TOTAL = 763/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS:

S8058628 SWETAMBARI				AN.	JU	, 71101024C , , PICT , S805862	28
01. DISCRETE STRUCTURES	PP	100	40	67	P C	11. ENG MATHS III PP 100 40 89	Р
02. COMPUTER ORGANIZATION	PP	100	40	70	P C	12. COMPUTER GRAPHICS PP 100 40 78	Р
03. DIGITAL ELECTRONICS & LOGIC DESI	GPP	100	40	57	P C	13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 53	Р
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	71	P C	14. DATA STRUCTURES AND FILES PP 100 40 70	Р
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	49	P C	15. DATA COMMUNICATION PP 100 40 73	Р
06. DIGITAL LABORATORY	TW	50	20	43	P C	16. PROCESSOR INTERFACING LABORATORY TW 25 10 22	Р
07. DIGITAL LABORATORY	PR	50	20	38	P C	17. PROCESSOR INTERFACING LABORATORY OR 50 20 44	Р
08. PROGRAMMING LABORATORY	TW	50	20	45	P C	18. DATA STRUCTURES AND FILES LAB TW 25 10 18	Р
09. PROGRAMMING LABORATORY	PR	50	20	38	РС	19. DATA STRUCTURES AND FILES LAB PR 50 20 35	Р
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	45	РС	20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 40	Р
						21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 47	Р

GRAND TOTAL = 1092/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

02. COMPUTER ORGANIZATION PP 100 40 44 P C 12. COMPUTER GRAPHICS PP 100 40 22 F

03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 40 40 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 24 F 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 P C 14. DATA STRUCTURES AND FILES PP 100 40 28 F

05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 40 P C 15. DATA COMMUNICATION PP 100 40 23 F 06. DIGITAL LABORATORY TW 50 20 24 P C 16. PROCESSOR INTERFACING LABORATORY TW 25 10 10 P

07. DIGITAL LABORATORY PR 50 20 06 F 17. PROCESSOR INTERFACING LABORATORY OR 50 20 11 F 08. PROGRAMMING LABORATORY TW 50 20 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 10 P

09. PROGRAMMING LABORATORY PR 50 20 20 P C 19. DATA STRUCTURES AND FILES LAB PR 50 20 05 F 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 33 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 27 P

GRAND TOTAL = 476/1500, RESULT: FAILS

DATE: 14 AUG. 2012 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 43 (523)

NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71101028F , , PICT , s8058630 S8058630 TEKE AMRUTA PRAKASH SHALINEE 01. DISCRETE STRUCTURES PP 100 40 47 P C 40 25 F 11. ENG MATHS III PP 100 PP 100 40 47 P C 12. COMPUTER GRAPHICS PP 100 40 02. COMPUTER ORGANIZATION 100 40 42 P C 100 40 44 P 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 13. PROCESSOR ARCHITECTURE & INTER. PP 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 41 P C 14. DATA STRUCTURES AND FILES PP 100 40 53 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 40 P C PP 100 40 15. DATA COMMUNICATION 50 20 34 P C 25 10 17 P 06. DIGITAL LABORATORY 16. PROCESSOR INTERFACING LABORATORY TW TW 50 20 22 P C 07. DIGITAL LABORATORY 50 20 28 P PR 17. PROCESSOR INTERFACING LABORATORY OR 08. PROGRAMMING LABORATORY 50 20 31 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 14 P TW 09. PROGRAMMING LABORATORY PR 50 20 20 P C 19. DATA STRUCTURES AND FILES LAB PR 50 20 26 P 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 40 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 39 P 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 30 P GRAND TOTAL = 748/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS: S8058631 THORAT AKSHAY RAMAKANT SUNANDA , s8058631 01. DISCRETE STRUCTURES PP 100 40 40 P C 11. ENG MATHS III PP 100 40 51 P PP 100 02. COMPUTER ORGANIZATION PP 100 40 50 P C 12. COMPUTER GRAPHICS 40 45 P 40 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 67 P C 40 P 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 65 P C 100 100 40 PP 40 60 P PP 100 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 41 P C 15. DATA COMMUNICATION 40 58 P 50 20 35 P C 10 19 P 06. DIGITAL LABORATORY 25 TW 16. PROCESSOR INTERFACING LABORATORY TW 50 20 40 P C 50 20 38 P 07. DIGITAL LABORATORY PR 17. PROCESSOR INTERFACING LABORATORY OR 50 20 34 P C 08. PROGRAMMING LABORATORY TW 18. DATA STRUCTURES AND FILES LAB TW 25 10 21 P 09. PROGRAMMING LABORATORY 19. DATA STRUCTURES AND FILES LAB PR 50 20 30 P PR 50 20 27 P C 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 39 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 42 P 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 40 P GRAND TOTAL = 882/1500, RESULT: HIGHER SECOND CLASS ORDN. 1 MARKS: , 71101035J , , PICT S8058632 THOSARE YOGESH YASHWANT USHADEVI , s8058632 01. DISCRETE STRUCTURES PP 100 40 54 P C 11. ENG MATHS III PP 100 40 41 P 02. COMPUTER ORGANIZATION PP 100 40 54 P C 12. COMPUTER GRAPHICS PP 100 40 63 P 100 40 46 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 45 P 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 54 P C 14. DATA STRUCTURES AND FILES PP 100 40 68 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 44 P C PP 100 40 60 P 15. DATA COMMUNICATION 06. DIGITAL LABORATORY 50 20 39 P C 25 10 21 P TW 16. PROCESSOR INTERFACING LABORATORY TW 50 20 38 P C 17. PROCESSOR INTERFACING LABORATORY OR 50 20 36 P 07. DIGITAL LABORATORY PR 18. DATA STRUCTURES AND FILES LAB TW 25 10 08. PROGRAMMING LABORATORY TW 50 20 41 P C 16 P 50 20 22 P C 19. DATA STRUCTURES AND FILES LAB PR 50 20 38 P 09. PROGRAMMING LABORATORY PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 41 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 39 P 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 42 P GRAND TOTAL = 902/1500, RESULT: FIRST CLASS ORDN. 1 MARKS:

UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(INFORMATION TECHNOLOGY)

DATE : 14 AUG. 2012 CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 44 (524)

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S	8058633	TOSHNIWAL VIVEK ANILKUMA	.R			SAN	AYAU		, 71101039м	,	, PI	CT	,	s80586	533
0	1. DISCR	ETE STRUCTURES	PP	100	40	64	РС	11.	ENG MATHS III		PP	100	40	61	Р
0	2. COMPU	TER ORGANIZATION	PP	100	40	56	РС	12.	COMPUTER GRAPHICS	5	PP	100	40	59	Р
0	3. DIGIT	AL ELECTRONICS & LOGIC DESI	GPP	100	40	63	РС	13.	PROCESSOR ARCHITE	ECTURE & INTER.	PP	100	40	31#	Р
0	4. FUNDA	MENTAL OF DATA STRUCTURES	PP	100	40	72	РС	14.	DATA STRUCTURES A	AND FILES	PP	100	40	66	Р
0	5. HUMAN	ITIES AND SOCIAL SCIENCES	PP	100	40	40	РС	15.	DATA COMMUNICATIO	ON	PP	100	40	55	Р
0	6. DIGIT	AL LABORATORY	TW	50	20	38	РС	16.	PROCESSOR INTERFA	ACING LABORATORY	Y TW	25	10	21	Р
0	7. DIGIT	AL LABORATORY	PR	50	20	38	РС	17.	PROCESSOR INTERFA	ACING LABORATORY	Y OR	50	20	35	Р
0	8. PROGR	AMMING LABORATORY	TW	50	20	38	РС	18.	DATA STRUCTURES A	AND FILES LAB	TW	25	10	18	Р
0	9. PROGRA	AMMING LABORATORY	PR	50	20	28	РС	19.	DATA STRUCTURES A	AND FILES LAB	PR	50	20	38	Р
1	.0. COMMUI	NICATION AND LANGUAGE LAB.	TW	50	20	38	РС	20.	OBJECT ORIENTED F	PROGRAMMING LAB	TW	50	20	36	Р
								21.	OBJECT ORIENTED F	PROGRAMMING LAB	PR	50	20	43	Р

GRAND TOTAL = 938/1500, RESULT: FIRST CLASS # [0.4]

ORDN. 1 MARKS:

S8058634 TRIPATHI SHREYA BHARAT REENA , 71101040E , , PICT , s8058634 01. DISCRETE STRUCTURES PP 100 40 47 P C 11. ENG MATHS III 100 40 46 P PP 100 40 44 P C PP 100 40 02. COMPUTER ORGANIZATION 12. COMPUTER GRAPHICS 43 P 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 53 P C 40 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 51 P 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 40 54 P C 100 41 P 100 PP 40 40 42 P C PP 100 05. HUMANITIES AND SOCIAL SCIENCES PP 100 15. DATA COMMUNICATION 40 63 P 50 20 27 P C 25 10 19 P 06. DIGITAL LABORATORY TW 16. PROCESSOR INTERFACING LABORATORY TW 07. DIGITAL LABORATORY 50 20 39 P C 50 20 42 P PR 17. PROCESSOR INTERFACING LABORATORY OR 50 20 27 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 12 P 08. PROGRAMMING LABORATORY TW 09. PROGRAMMING LABORATORY 50 20 34 P C 19. DATA STRUCTURES AND FILES LAB PR 50 20 38 P PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 37 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 33 P 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 30 P

GRAND TOTAL = 822+03/1500, RESULT: HIGHER SECOND CLASS [0.2] ORDN. 1 MARKS:

, 71101042M , , PICT , S8058635 S8058635 UTSAV DUSAD LAXMI 01. DISCRETE STRUCTURES PP 100 40 77 P C 11. ENG MATHS III 100 40 91 P 02. COMPUTER ORGANIZATION PP 100 40 62 P C 12. COMPUTER GRAPHICS PP 100 40 66 P 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 40 68 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 56 P 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 73 P C 14. DATA STRUCTURES AND FILES PP 100 40 66 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 44 P C PP 100 40 60 P 15. DATA COMMUNICATION 50 20 41 P C 25 10 22 P 06. DIGITAL LABORATORY TW 16. PROCESSOR INTERFACING LABORATORY TW 07. DIGITAL LABORATORY 50 20 36 P C 17. PROCESSOR INTERFACING LABORATORY OR 50 20 40 P PR 18. DATA STRUCTURES AND FILES LAB TW 25 10 22 P 08. PROGRAMMING LABORATORY TW 50 20 45 P C PR 50 20 34 P C 09. PROGRAMMING LABORATORY 19. DATA STRUCTURES AND FILES LAB PR 50 20 43 P 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 46 P.C. 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 45 P 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 46 P

GRAND TOTAL = 1083/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS:

DATE: 14 AUG. 2012 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 45 (525)

NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71101046D , , PICT , s8058636 S8058636 VARAT KAUSTUBH SOMKANT TILOTTAMA 01. DISCRETE STRUCTURES PP 100 40 58 P C 11. ENG MATHS III PP 100 40 55 P PP 100 40 45 P C 12. COMPUTER GRAPHICS PP 100 40 02. COMPUTER ORGANIZATION 45 P 100 40 47 P C 100 40 25 F 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 13. PROCESSOR ARCHITECTURE & INTER. PP 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 51 P C 100 40 45 P PP 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 41 P C PP 100 40 40 P 15. DATA COMMUNICATION 50 20 32 P C 25 10 10 P 06. DIGITAL LABORATORY 16. PROCESSOR INTERFACING LABORATORY TW TW 07. DIGITAL LABORATORY 50 20 33 P C 50 20 20 P PR 17. PROCESSOR INTERFACING LABORATORY OR 08. PROGRAMMING LABORATORY 50 20 29 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 11 P TW 09. PROGRAMMING LABORATORY PR 50 20 23 P C 19. DATA STRUCTURES AND FILES LAB PR 50 20 37 P 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 33 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 30 P 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 21 P GRAND TOTAL = 731/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS: S8058637 VICHARE GAURAV GIRISH VINA , s8058637 01. DISCRETE STRUCTURES 100 40 59 P PP 100 40 64 P C 11. ENG MATHS III PP 40 02. COMPUTER ORGANIZATION PP 100 40 42 P C 12. COMPUTER GRAPHICS PP 100 AA F 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 40 46 P.C AA F 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 51 P C 100 100 40 PP 40 46 P 40 40 P C PP 100 47 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 15. DATA COMMUNICATION 40 50 20 30 P C 10 20 P 06. DIGITAL LABORATORY 25 TW 16. PROCESSOR INTERFACING LABORATORY TW 50 20 30 P C 20 36 P 07. DIGITAL LABORATORY PR 17. PROCESSOR INTERFACING LABORATORY OR 50 50 20 28 P C 08. PROGRAMMING LABORATORY TW 18. DATA STRUCTURES AND FILES LAB TW 25 10 15 P 09. PROGRAMMING LABORATORY 19. DATA STRUCTURES AND FILES LAB PR 50 20 30 P PR 50 20 25 P C 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 38 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 41 P 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 24 P GRAND TOTAL = 712/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS: , 71101050B , , PICT , S8058638 S8058638 VIMAL BHAT KIRAN 01. DISCRETE STRUCTURES PP 100 40 58 P C 11. ENG MATHS III 100 40 69 P 02. COMPUTER ORGANIZATION PP 100 40 56 P C 12. COMPUTER GRAPHICS PP 100 40 65 P 100 40 67 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 58 P 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 69 P C 14. DATA STRUCTURES AND FILES PP 100 40 60 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 47 P C 100 40 69 P 15. DATA COMMUNICATION PP 06. DIGITAL LABORATORY 50 20 33 P C 25 10 18 P TW 16. PROCESSOR INTERFACING LABORATORY TW 50 20 28 P C 17. PROCESSOR INTERFACING LABORATORY OR 50 20 43 P 07. DIGITAL LABORATORY PR 18. DATA STRUCTURES AND FILES LAB TW 25 10 18 P 08. PROGRAMMING LABORATORY TW 50 20 37 P C 50 20 22 P C 19. DATA STRUCTURES AND FILES LAB PR 50 20 37 P 09. PROGRAMMING LABORATORY PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 36 P.C. 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 37 P 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 44 P GRAND TOTAL = 971/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS:

UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(INFORMATION TECHNOLOGY) DATE: 14 AUG. 2012 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 46 (526)

NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER KIRTI , 71101051L , , PICT , s8058639 S8058639 VIRWANI SUNNY JAMANLAL 01. DISCRETE STRUCTURES PP 100 40 40 P C 11. ENG MATHS III PP 100 40 23 F PP 100 40 23 F 12. COMPUTER GRAPHICS 100 40 40 P 02. COMPUTER ORGANIZATION 100 40 31 F 100 40 32 F 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 13. PROCESSOR ARCHITECTURE & INTER. PP 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 45 P C 100 40 41 P PP 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 42 P C 100 40 31 F 15. DATA COMMUNICATION 20 28 P C 25 10 10 P 06. DIGITAL LABORATORY 50 16. PROCESSOR INTERFACING LABORATORY TW TW 07. DIGITAL LABORATORY 50 20 34 P C 50 20 30 P PR 17. PROCESSOR INTERFACING LABORATORY OR 08. PROGRAMMING LABORATORY 50 20 33 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 11 P TW 09. PROGRAMMING LABORATORY 50 20 27 P C 19. DATA STRUCTURES AND FILES LAB PR 50 20 02 F PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 34 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 30 P 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 06 F GRAND TOTAL = 593/1500, RESULT: FAILS ORDN. 1 MARKS : S8058640 WADILE NITIN ADHAR SUMAN , s8058640 PP 100 40 100 40 40 P 01. DISCRETE STRUCTURES 51 P C 11. ENG MATHS III PP PP 100 40 29 F 02. COMPUTER ORGANIZATION PP 100 40 46 P C 12. COMPUTER GRAPHICS 40 40 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 17 F 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 40 100 51 P 100 48 P C PP 40 40 40 P C 100 05. HUMANITIES AND SOCIAL SCIENCES PP 100 15. DATA COMMUNICATION 40 40 P 50 20 35 P C 10 10 P 06. DIGITAL LABORATORY 25 TW 16. PROCESSOR INTERFACING LABORATORY TW 50 20 20 P C 20 34 P 07. DIGITAL LABORATORY PR 17. PROCESSOR INTERFACING LABORATORY OR 50 50 20 37 P C 08. PROGRAMMING LABORATORY TW 18. DATA STRUCTURES AND FILES LAB TW 25 10 12 P 09. PROGRAMMING LABORATORY 19. DATA STRUCTURES AND FILES LAB PR 50 20 05 F PR 50 20 20 P C 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 37 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 32 P 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 10 F GRAND TOTAL = 654/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS : , 71101054E , , PICT , S8058642 S8058642 WALKE AMRUTA GHANSHYAM ASHA 01. DISCRETE STRUCTURES PP 100 40 41 P C 11. ENG MATHS III 100 40 63 P

02. COMPUTER ORGANIZATION PP 100 40 46 P C 12. COMPUTER GRAPHICS PP 100 40 59 P 100 40 50 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 59 P 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 56 P C 14. DATA STRUCTURES AND FILES PP 100 40 53 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 53 P C 100 40 53 P 15. DATA COMMUNICATION PP 10 19 P 06. DIGITAL LABORATORY 50 20 38 P C 25 TW 16. PROCESSOR INTERFACING LABORATORY TW 50 20 21 P C 17. PROCESSOR INTERFACING LABORATORY OR 50 20 34 P 07. DIGITAL LABORATORY PR 18. DATA STRUCTURES AND FILES LAB TW 25 10 15 P 08. PROGRAMMING LABORATORY TW 50 20 38 P C 50 20 32 P C 19. DATA STRUCTURES AND FILES LAB PR 50 20 28 P 09. PROGRAMMING LABORATORY PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 40 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 35 P 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 28 P

GRAND TOTAL = 861/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS:

UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(INFORMATION TECHNOLOGY) DATE: 14 AUG. 2012 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 47 (527) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER VIDYA , 71101056M , , PICT , S8058643 S8058643 WANKHADE POOJA SUNIL 01. DISCRETE STRUCTURES PP 100 40 57 P C 40 72 P 11. ENG MATHS III PP 100 02. COMPUTER ORGANIZATION PP 100 40 69 P C 12. COMPUTER GRAPHICS 100 40 65 P 100 40 76 P C 100 40 64 P 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 13. PROCESSOR ARCHITECTURE & INTER. PP 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 58 P C 100 40 58 P PP

15. DATA COMMUNICATION

16. PROCESSOR INTERFACING LABORATORY TW

17. PROCESSOR INTERFACING LABORATORY OR

18. DATA STRUCTURES AND FILES LAB TW 25 10

19. DATA STRUCTURES AND FILES LAB PR 50 20 41 P

20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 44 P 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 46 P

PP 100

25

40

10

50 20

66 P

21 P

41 P

20 P

GRAND TOTAL = 1043/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS:

GRAND TOTAL = 994/1500, RESULT: FIRST CLASS WITH DISTINCTION

10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 38 P C

100

TW

PR

TW

PR

40

50 20 45 P C

50 20 34 P C

50 20 27 P C

50 20

66 P C

35 P C

05. HUMANITIES AND SOCIAL SCIENCES PP

06. DIGITAL LABORATORY

07. DIGITAL LABORATORY

ORDN. 1 MARKS:

ORDN. 1 MARKS:

08. PROGRAMMING LABORATORY

09. PROGRAMMING LABORATORY

S8058644 WAVAL BHAGYASHREE RAMESH MEGHA , S8058644 01. DISCRETE STRUCTURES PP 100 40 55 P C 11. ENG MATHS III PP 100 40 42 P PP 100 02. COMPUTER ORGANIZATION PP 100 40 67 P C 12. COMPUTER GRAPHICS 40 64 P 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 40 65 P C 54 P 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 40 52 P C 100 100 PP 40 57 P 100 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 58 P C 15. DATA COMMUNICATION 40 50 20 43 P C 06. DIGITAL LABORATORY 25 10 22 P TW 16. PROCESSOR INTERFACING LABORATORY TW 50 20 45 P C 20 07. DIGITAL LABORATORY PR 17. PROCESSOR INTERFACING LABORATORY OR 50 40 P 50 20 45 P C 08. PROGRAMMING LABORATORY TW 18. DATA STRUCTURES AND FILES LAB TW 25 10 21 P 09. PROGRAMMING LABORATORY PR 50 20 38 P C 19. DATA STRUCTURES AND FILES LAB PR 50 20 34 P 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 44 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 39 P 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 45 P

, S8058645 S8058645 WHABI JAIDEEP MAHESH CHANDRIKA 01. DISCRETE STRUCTURES PP 100 40 48 P C 11. ENG MATHS III 100 40 40 P 02. COMPUTER ORGANIZATION PP 100 40 62 P C 12. COMPUTER GRAPHICS PP 100 40 54 P 100 40 43 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 47 P 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 44 P C 14. DATA STRUCTURES AND FILES PP 100 40 73 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 41 P C 100 40 47 P 15. DATA COMMUNICATION PP 06. DIGITAL LABORATORY 50 20 35 P C 25 10 17 P TW 16. PROCESSOR INTERFACING LABORATORY TW

50 20 37 P C 17. PROCESSOR INTERFACING LABORATORY OR 50 20 40 P 07. DIGITAL LABORATORY PR 18. DATA STRUCTURES AND FILES LAB TW 25 10 16 P 08. PROGRAMMING LABORATORY TW 50 20 35 P C 50 20 20 P C 19. DATA STRUCTURES AND FILES LAB PR 50 20 40 P 09. PROGRAMMING LABORATORY PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 36 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 37 P 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 41 P

GRAND TOTAL = 853/1500, RESULT: HIGHER SECOND CLASS

DATE: 14 AUG. 2012 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 48 (528)

NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S80	058646 YEOLE SHRADDHA CHANDRASH	IEKHAR			HEM	ILATA		, 71101063D , ,	ΡI	СТ	,	S8058	646
01	. DISCRETE STRUCTURES	PP	100	40	43	РС	11.	ENG MATHS III	PP	100	40	57	Р
02	. COMPUTER ORGANIZATION	PP	100	40	53	РС	12.	COMPUTER GRAPHICS	PP	100	40	51	Р
03	. DIGITAL ELECTRONICS & LOGIC DESI	GPP	100	40	71	РС	13.	PROCESSOR ARCHITECTURE & INTER.	PP	100	40	60	Р
04	. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	62	РС	14.	DATA STRUCTURES AND FILES	PP	100	40	62	Р
05	. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	56	РС	15.	DATA COMMUNICATION	PP	100	40	49	Р
06	. DIGITAL LABORATORY	TW	50	20	37	РС	16.	PROCESSOR INTERFACING LABORATORY	TW	25	10	17	Р
07	. DIGITAL LABORATORY	PR	50	20	45	РС	17.	PROCESSOR INTERFACING LABORATORY	OR	50	20	28	Р
08	. PROGRAMMING LABORATORY	TW	50	20	34	PС	18.	DATA STRUCTURES AND FILES LAB	TW	25	10	14	Р
09	. PROGRAMMING LABORATORY	PR	50	20	30	РС	19.	DATA STRUCTURES AND FILES LAB	PR	50	20	35	Р
10	. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	40	РС	20.	OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	40	Р
							21.	OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	42	Р

GRAND TOTAL = 926/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS:

S8058647 YUSRA AIJAZ SAHAF SAMINA FARHAT , 71101066J , PICT , s8058647 01. DISCRETE STRUCTURES PP 100 40 52 P C 11. ENG MATHS III PP 100 40 26 F PP 100 40 02. COMPUTER ORGANIZATION PP 100 40 51 P C 12. COMPUTER GRAPHICS 47 P 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 49 P C 40 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 40 P 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 40 40 P C PP 100 100 40 45 P 40 51 P C PP 100 05. HUMANITIES AND SOCIAL SCIENCES PP 100 15. DATA COMMUNICATION 40 51 P 50 20 34 P C 10 17 P 06. DIGITAL LABORATORY 25 TW 16. PROCESSOR INTERFACING LABORATORY TW 50 20 34 P C 50 20 30 P 07. DIGITAL LABORATORY PR 17. PROCESSOR INTERFACING LABORATORY OR 50 20 32 P C 08. PROGRAMMING LABORATORY TW 18. DATA STRUCTURES AND FILES LAB TW 25 10 16 P 09. PROGRAMMING LABORATORY PR 50 20 23 P C 19. DATA STRUCTURES AND FILES LAB PR 50 20 27 P 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 39 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 35 P 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 25 P

GRAND TOTAL = 764/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

, s8058648 S8058648 ANUJA WANGNOO AMBICA 01. DISCRETE STRUCTURES PP 100 40 60 P C 11. ENG MATHS III PP 100 40 44 P C 02. COMPUTER ORGANIZATION PP 100 40 48 P C 12. COMPUTER GRAPHICS PP 100 40 49 P C 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 40 55 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 48 P 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 59 P C 14. DATA STRUCTURES AND FILES PP 100 40 45 P.C 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 41 P C PP 100 40 47 P C 15. DATA COMMUNICATION 50 20 32 P C 06. DIGITAL LABORATORY 25 10 12 P C TW 16. PROCESSOR INTERFACING LABORATORY TW 07. DIGITAL LABORATORY 50 20 42 P C 17. PROCESSOR INTERFACING LABORATORY OR 50 20 32 P C PR 18. DATA STRUCTURES AND FILES LAB TW 25 10 15 P C 08. PROGRAMMING LABORATORY TW 50 20 33 P C 09. PROGRAMMING LABORATORY 50 20 44 P C 19. DATA STRUCTURES AND FILES LAB PR 50 20 25 P C PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 34 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 36 P C 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 30 P C

GRAND TOTAL = 831/1500, RESULT: HIGHER SECOND CLASS

UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(INFORMATION TECHNOLOGY) DATE: 14 AUG. 2012 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.

PAGE NO. 49 (529) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71073822G , , PICT , S8058653 S8058653 BERDE SIDDESH VISHWANATH VAISHALI 01. DISCRETE STRUCTURES PP 100 40 40 P C 11. ENG MATHS III PP 100 40 AA F 02. COMPUTER ORGANIZATION PP 100 40 51 P C 12. COMPUTER GRAPHICS 100 40 56 P C 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 40 59 P C 100 40 40 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 50 P C 100 40 55 P C PP 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 51 P C 100 40 52 P.C 15. DATA COMMUNICATION 50 20 34 P C 25 10 18 P C 06. DIGITAL LABORATORY 16. PROCESSOR INTERFACING LABORATORY TW TW 07. DIGITAL LABORATORY 50 20 30 P C 50 20 31 P C PR 17. PROCESSOR INTERFACING LABORATORY OR 08. PROGRAMMING LABORATORY 50 20 39 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 15 P C TW 09. PROGRAMMING LABORATORY PR 50 20 30 P C 19. DATA STRUCTURES AND FILES LAB PR 50 20 36 P C 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 38 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 36 P C 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 32 P C GRAND TOTAL = 793/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS: S8058654 BHALERAO MANGESH GANESH NANDA , S8058654 01. DISCRETE STRUCTURES PP 100 40 52 P C PP 100 40 44 P C 11. ENG MATHS III 51 P C 02. COMPUTER ORGANIZATION PP 100 40 40 P C 12. COMPUTER GRAPHICS PP 100 40 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 40 45 P C 40 P 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 100 40 40 P C PP 40 63 P C 40 100 05. HUMANITIES AND SOCIAL SCIENCES PP 100 44 P C 15. DATA COMMUNICATION 40 40 P C 50 20 33 P C 10 14 P C 06. DIGITAL LABORATORY 25 TW 16. PROCESSOR INTERFACING LABORATORY TW 50 20 22 P C 20 22 P C 07. DIGITAL LABORATORY PR 17. PROCESSOR INTERFACING LABORATORY OR 50 50 20 32 P C 08. PROGRAMMING LABORATORY TW 18. DATA STRUCTURES AND FILES LAB TW 25 10 13 P C 09. PROGRAMMING LABORATORY 50 20 30 P C 19. DATA STRUCTURES AND FILES LAB PR 50 20 26 P C PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 36 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 25 P C 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 32 P C GRAND TOTAL = 744+06/1500, RESULT: SECOND CLASS [0.2] ORDN. 1 MARKS: , 70801397L , , PICT S8058658 DEORE NIKHIL BAPUSAHEB MANDAKINI , S8058658 01. DISCRETE STRUCTURES PP 100 40 53 P C 11. ENG MATHS III 100 40 40 P C PP 02. COMPUTER ORGANIZATION PP 100 40 41 P C 12. COMPUTER GRAPHICS 100 40 49 P.C 100 40 40 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 40 P C 03. DIGITAL ELECTRONICS & LOGIC DESIGPP

04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 40 P C 14. DATA STRUCTURES AND FILES PP 100 40 58 P C 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 40 P C 100 40 48 P C 15. DATA COMMUNICATION PP 06. DIGITAL LABORATORY 50 20 25 P C 25 10 10 P C TW 16. PROCESSOR INTERFACING LABORATORY TW 50 20 21 P C 17. PROCESSOR INTERFACING LABORATORY OR 50 20 25 P 07. DIGITAL LABORATORY PR 18. DATA STRUCTURES AND FILES LAB TW 25 10 10 P C 08. PROGRAMMING LABORATORY TW 50 20 33 P C 50 20 28 P C 19. DATA STRUCTURES AND FILES LAB PR 50 20 25 P C 09. PROGRAMMING LABORATORY PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 32 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 20 P C 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 25 P C GRAND TOTAL = 703/1500, RESULT: PASS CLASS

DATE: 14 AUG. 2012 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 50 (530)

NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S8058660 DESHPANDE KIRTI VINOD				SH	ALINI		, 71134950к , ,		PICT	,	S8058	660
01. DISCRETE STRUCTURES	PP	100	40	44	РС	11	. ENG MATHS III	PP	100	40	40	Р
02. COMPUTER ORGANIZATION	PP	100	40	49	РС	12	. COMPUTER GRAPHICS	PP	100	40	58	РС
03. DIGITAL ELECTRONICS & LOGIC DESI	GPP	100	40	60	РС	13	. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	53	РС
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	55	РС	14	. DATA STRUCTURES AND FILES	PP	100	40	54	РС
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	51	РС	15	. DATA COMMUNICATION	PP	100	40	69	РС
06. DIGITAL LABORATORY	TW	50	20	29	РС	16	. PROCESSOR INTERFACING LABORATORY	/ TW	25	10	19	РС
07. DIGITAL LABORATORY	PR	50	20	22	РС	17	. PROCESSOR INTERFACING LABORATORY	OR	50	20	27	РС
08. PROGRAMMING LABORATORY	TW	50	20	37	РС	18	. DATA STRUCTURES AND FILES LAB	TW	25	10	16	РС
09. PROGRAMMING LABORATORY	PR	50	20	27	РС	19	. DATA STRUCTURES AND FILES LAB	PR	50	20	29	Р
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	30	РС	20	. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	41	РС
						21	. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	31	РС

GRAND TOTAL = 841/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS:

S8058663 DIKE ASHISH VIJAY				RA	JANI	, 71045418L , , PICT , S8058663
01. DISCRETE STRUCTURES	PP	100	40	51	РС	11. ENG MATHS III PP 100 40 40 P
02. COMPUTER ORGANIZATION	PP	100	40	40	PC	12. COMPUTER GRAPHICS PP 100 40 41 P C
03. DIGITAL ELECTRONICS & LOGIC DESI	GPP	100	40	43	РС	13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 53 P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	40	РС	14. DATA STRUCTURES AND FILES PP 100 40 45 P C
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	50	РС	15. DATA COMMUNICATION PP 100 40 50 P
06. DIGITAL LABORATORY	TW	50	20	38	РС	16. PROCESSOR INTERFACING LABORATORY TW 25 10 16 P C
07. DIGITAL LABORATORY	PR	50	20	36	РС	17. PROCESSOR INTERFACING LABORATORY OR 50 20 24 P C
08. PROGRAMMING LABORATORY	TW	50	20	35	РС	18. DATA STRUCTURES AND FILES LAB TW 25 10 12 P C
09. PROGRAMMING LABORATORY	PR	50	20	27	РС	19. DATA STRUCTURES AND FILES LAB PR 50 20 38 P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	38	РС	20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 34 P C
						21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 30 P C

GRAND TOTAL = 781/1500, RESULT: SECOND CLASS

ORDN. 1 MARKS:

, 71045436J , , PICT , S8058665 S8058665 GHODE AMAR PRADEEPKUMAR SINDHU 01. DISCRETE STRUCTURES PP 100 40 52 P C 11. ENG MATHS III 100 40 16 F 02. COMPUTER ORGANIZATION PP 100 40 42 P C 12. COMPUTER GRAPHICS PP 100 40 42 P C 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 40 44 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 40 P C 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 40 P C 14. DATA STRUCTURES AND FILES 100 40 49 P C 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 41 P C 15. DATA COMMUNICATION 100 40 45 P C 06. DIGITAL LABORATORY 50 20 20 P C 25 16. PROCESSOR INTERFACING LABORATORY TW 10 11 P C TW 07. DIGITAL LABORATORY 50 20 33 P C 17. PROCESSOR INTERFACING LABORATORY OR 50 20 22 P C PR 08. PROGRAMMING LABORATORY TW 50 20 30 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 12 P C 09. PROGRAMMING LABORATORY PR 50 20 33 P C 19. DATA STRUCTURES AND FILES LAB PR 50 20 30 P 50 20 25 P C 10. COMMUNICATION AND LANGUAGE LAB. TW 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 26 P C 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 20 P C

GRAND TOTAL = 673/1500, RESULT: FAILS A.T.K.T.

DATE: 14 AUG. 2012 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 51 (531)

NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71134959C , , PICT , s8058666 S8058666 GUNDECHA ANIKET PRAKASH UJWALA 01. DISCRETE STRUCTURES PP 100 40 45 P C 11. ENG MATHS III PP 100 40 AA F 02. COMPUTER ORGANIZATION PP 100 40 52 P C 12. COMPUTER GRAPHICS 100 40 100 40 40 P C 100 40 40 P C 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 13. PROCESSOR ARCHITECTURE & INTER. PP 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 53 P C 100 40 41 P C PP 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 57 P C 100 40 69 P.C 15. DATA COMMUNICATION 20 35 P C 25 10 10 P C 06. DIGITAL LABORATORY 50 16. PROCESSOR INTERFACING LABORATORY TW TW 07. DIGITAL LABORATORY 50 20 21 P C 50 20 24 P C PR 17. PROCESSOR INTERFACING LABORATORY OR 08. PROGRAMMING LABORATORY 50 20 33 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 11 P C TW 09. PROGRAMMING LABORATORY 50 20 37 P C 19. DATA STRUCTURES AND FILES LAB PR 50 20 35 P C PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 36 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 27 P C 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 P C GRAND TOTAL = 735/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS: S8058668 JADHAV DEEPANJAN MADHAV ARUNA , S8058668 01. DISCRETE STRUCTURES PP 100 40 40 P C 40 51 P C 11. ENG MATHS III PP 100 40 55 P C 02. COMPUTER ORGANIZATION PP 100 12. COMPUTER GRAPHICS PP 100 40 53 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 40 51 P C 100 40 43 P C 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 100 40 46 P C PP 40 61 P C 100 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 48 P C 15. DATA COMMUNICATION 40 47 P C 50 20 10 11 P C 06. DIGITAL LABORATORY 34 P C 25 TW 16. PROCESSOR INTERFACING LABORATORY TW 50 20 28 P C 20 21 P C 07. DIGITAL LABORATORY PR 17. PROCESSOR INTERFACING LABORATORY OR 50 50 20 41 P C 08. PROGRAMMING LABORATORY TW 18. DATA STRUCTURES AND FILES LAB TW 25 10 12 P C 09. PROGRAMMING LABORATORY 19. DATA STRUCTURES AND FILES LAB PR 50 20 27 P PR 50 20 20 P C 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 41 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 34 P C 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 21 P GRAND TOTAL = 785/1500, RESULT: SECOND CLASS ORDN. 1 MARKS: , 71134960G , , PICT , s8058669 S8058669 JADHAV DHANRAJ KHANDERAO CHHAYA 01. DISCRETE STRUCTURES PP 100 40 49 P C 11. ENG MATHS III 100 40 49 P 02. COMPUTER ORGANIZATION PP 100 40 65 P C 12. COMPUTER GRAPHICS PP 100 40 53 P.C 100 40 59 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 53 P C 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 75 P C 14. DATA STRUCTURES AND FILES PP 100 40 71 P C 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 53 P C PP 100 40 68 P C 15. DATA COMMUNICATION 06. DIGITAL LABORATORY 50 20 39 P C 25 10 20 P C TW 16. PROCESSOR INTERFACING LABORATORY TW 50 20 30 P C 17. PROCESSOR INTERFACING LABORATORY OR 50 20 43 P C 07. DIGITAL LABORATORY PR 18. DATA STRUCTURES AND FILES LAB TW 25 10 23 P C 08. PROGRAMMING LABORATORY TW 50 20 42 P C 50 20 46 P C 19. DATA STRUCTURES AND FILES LAB PR 50 20 35 P C 09. PROGRAMMING LABORATORY PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 38 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 46 P C 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 32 P C GRAND TOTAL = 989/1500, RESULT: FIRST CLASS ORDN. 1 MARKS:

UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(INFORMATION TECHNOLOGY) DATE: 14 AUG. 2012 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 52 (532) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER GEETA , 71045491M , , , PICT , s8058673 S8058673 KULKARNI ALOK DIPAK 01. DISCRETE STRUCTURES PP 100 40 AA F 11. ENG MATHS III PP 100 40 AA F 02. COMPUTER ORGANIZATION PP 100 40 12. COMPUTER GRAPHICS 100 40 AA F AA F 100 40 AA F 100 40 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 13. PROCESSOR ARCHITECTURE & INTER. PP 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 45 P C 14. DATA STRUCTURES AND FILES 100 40 25 F PP 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 40 P C 100 40 AA F 15. DATA COMMUNICATION 50 20 22 P C 25 10 15 P C 06. DIGITAL LABORATORY 16. PROCESSOR INTERFACING LABORATORY TW TW 07. DIGITAL LABORATORY 50 20 33 P C 50 20 AA F PR 17. PROCESSOR INTERFACING LABORATORY OR 08. PROGRAMMING LABORATORY 50 20 35 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 15 P C TW 09. PROGRAMMING LABORATORY 50 20 35 P C 19. DATA STRUCTURES AND FILES LAB PR 50 20 29 P C PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 37 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 35 P C 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 40 P C GRAND TOTAL = 406/1500, RESULT: FAILS RESULT RESERVED FOR BKLG ORDN. 1 MARKS : , 71045672H , , PICT S8058674 KULKARNI PRAMOD SHIVAJIRAO SUMATI , S8058674 01. DISCRETE STRUCTURES PP 100 40 40 P C 11. ENG MATHS III PP 100 40 40 P C 100 02. COMPUTER ORGANIZATION PP 100 40 52 P C 12. COMPUTER GRAPHICS 40 43 P C 40 40 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 40 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 100 50 P C 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 42 P C 100 50 P C 100 40 PP 40 40 100 05. HUMANITIES AND SOCIAL SCIENCES PP 100 43 P C 15. DATA COMMUNICATION 40 40 P C 20 P C 50 20 06. DIGITAL LABORATORY 25 10 19 P C TW 16. PROCESSOR INTERFACING LABORATORY TW

50 20 28 P C 20 07. DIGITAL LABORATORY PR 17. PROCESSOR INTERFACING LABORATORY OR 50 20 P 50 20 27 P C 08. PROGRAMMING LABORATORY TW 18. DATA STRUCTURES AND FILES LAB TW 25 10 12 P C 09. PROGRAMMING LABORATORY 19. DATA STRUCTURES AND FILES LAB PR 50 20 38 P PR 50 20 27 P C 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 23 P C 50 20 32 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 P C

GRAND TOTAL = 706/1500, RESULT: PASS CLASS

ORDN. 1 MARKS:

, 71045526н , , ріст S8058676 MOHAMMED AAQUIB ANSARI MOHAMMED YUSUF SUFIYA , S8058676 01. DISCRETE STRUCTURES PP 100 40 43 P C 11. ENG MATHS III 100 40 40 P C 02. COMPUTER ORGANIZATION PP 100 40 41 P C 12. COMPUTER GRAPHICS PP 100 40 50 P C 100 40 45 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 40 P C 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 40 P C 14. DATA STRUCTURES AND FILES PP 100 40 73 P.C. 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 40 P C 100 40 45 P C 15. DATA COMMUNICATION PP 06. DIGITAL LABORATORY 50 20 20 P C 25 10 10 P C TW 16. PROCESSOR INTERFACING LABORATORY TW 50 20 17. PROCESSOR INTERFACING LABORATORY OR 50 20 07. DIGITAL LABORATORY PR AA F AA F 18. DATA STRUCTURES AND FILES LAB TW 25 10 13 P C 08. PROGRAMMING LABORATORY TW 50 20 20 P C 39 P C 50 20 35 P C 19. DATA STRUCTURES AND FILES LAB PR 50 20 09. PROGRAMMING LABORATORY PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 20 P C 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 P C

GRAND TOTAL = 654/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS:

DATE: 14 AUG. 2012 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 53 (533)

NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER NEETA , 70925508L , , PICT , S8058677 S8058677 MONISH PATEL 01. DISCRETE STRUCTURES PP 100 40 40 P C 11. ENG MATHS III PP 100 40 40 P C 02. COMPUTER ORGANIZATION PP 100 40 40 P C 12. COMPUTER GRAPHICS 100 40 100 40 62 P 100 40 40 P C 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 13. PROCESSOR ARCHITECTURE & INTER. PP 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 49 P C 100 40 40 P C PP 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 42 P C 100 40 40 P.C 15. DATA COMMUNICATION 50 20 32 P C 25 10 10 P C 06. DIGITAL LABORATORY 16. PROCESSOR INTERFACING LABORATORY TW TW 07. DIGITAL LABORATORY 50 20 20 P C 50 20 25 P C PR 17. PROCESSOR INTERFACING LABORATORY OR 08. PROGRAMMING LABORATORY 50 20 38 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 10 P C TW 09. PROGRAMMING LABORATORY PR 50 20 30 P C 19. DATA STRUCTURES AND FILES LAB PR 50 20 P C 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 35 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 20 P C 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 32 P C GRAND TOTAL = 705/1500, RESULT: PASS CLASS ORDN. 1 MARKS: S8058678 MULAY AMIT YESHWANT SNEHAL , s8058678 01. DISCRETE STRUCTURES 61 P C 100 40 12 F PP 100 40 11. ENG MATHS III PP 02. COMPUTER ORGANIZATION PP 100 40 44 P C 12. COMPUTER GRAPHICS PP 100 40 56 P C 40 13. PROCESSOR ARCHITECTURE & INTER. PP 40 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 44 P C 100 40 P C 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 100 40 41 P C PP 40 51 P C 53 P C 100 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 15. DATA COMMUNICATION 40 40 P C 20 P C 50 20 06. DIGITAL LABORATORY 25 10 10 P C TW 16. PROCESSOR INTERFACING LABORATORY TW 50 20 38 P C 20 20 P 07. DIGITAL LABORATORY PR 17. PROCESSOR INTERFACING LABORATORY OR 50 50 20 25 P C 08. PROGRAMMING LABORATORY TW 18. DATA STRUCTURES AND FILES LAB TW 25 10 12 P C 09. PROGRAMMING LABORATORY 19. DATA STRUCTURES AND FILES LAB PR 50 20 21 P PR 50 20 29 P C 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 25 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 23 P C 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 25 P C GRAND TOTAL = 690/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS: , 71134970D , , PICT S8058680 MULLA ASMA MUSA RABIYA , s8058680 01. DISCRETE STRUCTURES PP 100 40 49 P C 11. ENG MATHS III 100 40 18 F PP 02. COMPUTER ORGANIZATION PP 100 40 40 P C 12. COMPUTER GRAPHICS 100 40 40 P C 100 40 40 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 40 P C 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 48 P C 14. DATA STRUCTURES AND FILES PP 100 40 54 P C 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 44 P C 100 40 43 P C 15. DATA COMMUNICATION PP 06. DIGITAL LABORATORY 50 20 38 P C 25 10 21 P C TW 16. PROCESSOR INTERFACING LABORATORY TW 50 20 22 P C 17. PROCESSOR INTERFACING LABORATORY OR 50 20 29 P C 07. DIGITAL LABORATORY PR 18. DATA STRUCTURES AND FILES LAB TW 25 10 22 P C 08. PROGRAMMING LABORATORY TW 50 20 42 P C 50 20 33 P C 19. DATA STRUCTURES AND FILES LAB PR 50 20 22 P C 09. PROGRAMMING LABORATORY PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 37 P.C. 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 43 P C 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 P C GRAND TOTAL = 745/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS:

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER MEENAKSHI , s8058681 S8058681 MUNOT PAYAL KAMLESHKUMAR 01. DISCRETE STRUCTURES PP 100 40 51 P C 40 26 F 11. ENG MATHS III PP 100 PP 100 40 40 P C 12. COMPUTER GRAPHICS 100 40 31 F 02. COMPUTER ORGANIZATION

100 40 50 P C 100 40 29 F 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 13. PROCESSOR ARCHITECTURE & INTER. PP 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 45 P C 100 40 40 P C PP 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 40 P C 100 40 AA F 15. DATA COMMUNICATION 50 20 35 P C 25 10 11 PC 06. DIGITAL LABORATORY 16. PROCESSOR INTERFACING LABORATORY TW TW

07. DIGITAL LABORATORY PR 50 20 P C 17. PROCESSOR INTERFACING LABORATORY OR 50 20 AA F 08. PROGRAMMING LABORATORY TW 50 20 33 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 15 P C

09. PROGRAMMING LABORATORY PR 50 20 20 P C 19. DATA STRUCTURES AND FILES LAB PR 50 20 27 P C 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 40 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 41 P C 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 AA F

GRAND TOTAL = 594/1500, RESULT: FAILS

ORDN. 1 MARKS :

S8058682 N Y GARIN YI MALIVATTEY , 70925519F , PICT , S8058682 45 P C 11. ENG MATHS III PP 100 40 40 P C 01. DISCRETE STRUCTURES 100 40 02. COMPUTER ORGANIZATION PP 100 40 40 P C 12. COMPUTER GRAPHICS PP 100 40 42 P C 40 13. PROCESSOR ARCHITECTURE & INTER. PP 40 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 41 P C 100 48 P 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 40 40 P C 100 100 PP 40 40 P C 40 40 P C 100 05. HUMANITIES AND SOCIAL SCIENCES PP 100 15. DATA COMMUNICATION 40 40 P C 50 20 33 P C 10 16 P C 06. DIGITAL LABORATORY 25 TW 16. PROCESSOR INTERFACING LABORATORY TW 50 20 30 P C 20 07. DIGITAL LABORATORY PR 17. PROCESSOR INTERFACING LABORATORY OR 50 35 P C 50 20 35 P C 08. PROGRAMMING LABORATORY TW 18. DATA STRUCTURES AND FILES LAB TW 25 10 18 P C 09. PROGRAMMING LABORATORY 19. DATA STRUCTURES AND FILES LAB PR 50 20 40 P C PR 50 20 30 P C 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 38 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 44 P C 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 29 P C

GRAND TOTAL = 764/1500, RESULT: SECOND CLASS

ORDN. 1 MARKS :

, 70925526J , , PICT , S8058684 S8058684 NILESH D PHADTARE KUNDA 01. DISCRETE STRUCTURES PP 100 40 43 P C 11. ENG MATHS III 100 40 40 P C 02. COMPUTER ORGANIZATION PP 100 40 50 P C 12. COMPUTER GRAPHICS PP 100 40 43 P.C 100 40 50 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 25 F 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 40 P C 14. DATA STRUCTURES AND FILES PP 100 40 72 P.C 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 40 P C PP 100 40 43 P C 15. DATA COMMUNICATION 06. DIGITAL LABORATORY 50 20 35 P C 25 10 20 P C TW 16. PROCESSOR INTERFACING LABORATORY TW 50 20 31 P C 17. PROCESSOR INTERFACING LABORATORY OR 50 20 20 P C 07. DIGITAL LABORATORY PR 18. DATA STRUCTURES AND FILES LAB TW 25 10 23 P C 08. PROGRAMMING LABORATORY TW 50 20 43 P C

09. PROGRAMMING LABORATORY PR 50 20 32 P C 19. DATA STRUCTURES AND FILES LAB PR 50 20 25 P C 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 36 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 44 P C

21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 41 P C

GRAND TOTAL = 796/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS:

DATE: 14 AUG. 2012 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 55 (535)

NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71045560н , , , ріст , S8058685 S8058685 PARTH SARTHI PIPLANI SUREKHA 01. DISCRETE STRUCTURES PP 100 40 56 P C 11. ENG MATHS III PP 100 40 40 P C 02. COMPUTER ORGANIZATION PP 100 40 43 P C 12. COMPUTER GRAPHICS 100 40 100 40 40 P C 100 40 44 P 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 13. PROCESSOR ARCHITECTURE & INTER. PP 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 45 P C 100 40 72 P C PP 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 44 P C 100 40 40 P.C 15. DATA COMMUNICATION 50 20 22 P C 25 10 10 P C 06. DIGITAL LABORATORY 16. PROCESSOR INTERFACING LABORATORY TW TW 50 20 23 P C 07. DIGITAL LABORATORY 50 20 28 P PR 17. PROCESSOR INTERFACING LABORATORY OR 08. PROGRAMMING LABORATORY 50 20 23 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 19 P C TW 09. PROGRAMMING LABORATORY 50 20 25 P C 19. DATA STRUCTURES AND FILES LAB PR 50 20 41 P C PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 35 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 37 P C 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 22 P C GRAND TOTAL = 763/1500, RESULT: SECOND CLASS ORDN. 1 MARKS: S8058686 PATEL NAZNIN JAVED MAHERUNISSA , S8058686 01. DISCRETE STRUCTURES 100 40 22 F PP 100 40 40 P C 11. ENG MATHS III PP 40 P C 40 02. COMPUTER ORGANIZATION PP 100 40 12. COMPUTER GRAPHICS PP 100 48 P C 40 13. PROCESSOR ARCHITECTURE & INTER. PP 40 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 45 P C 100 40 P C 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 40 100 100 44 P C PP 40 64 P C 40 100 53 P C 05. HUMANITIES AND SOCIAL SCIENCES PP 100 46 P C 15. DATA COMMUNICATION 40 50 20 35 P C 06. DIGITAL LABORATORY 25 10 14 P C TW 16. PROCESSOR INTERFACING LABORATORY TW 50 20 40 P C 20 07. DIGITAL LABORATORY PR 17. PROCESSOR INTERFACING LABORATORY OR 50 22 P C 50 20 42 P C 08. PROGRAMMING LABORATORY TW 18. DATA STRUCTURES AND FILES LAB TW 25 10 19 P C 09. PROGRAMMING LABORATORY 50 20 22 P C 19. DATA STRUCTURES AND FILES LAB PR 50 20 35 P C PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 43 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 41 P C 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 35 P C GRAND TOTAL = 790/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS: , 71045576D , , PICT S8058687 POLE AKSHAYKUMAR RAHUL SHEELA , s8058687 01. DISCRETE STRUCTURES PP 100 40 43 P C 11. ENG MATHS III 100 40 40 P C PP 02. COMPUTER ORGANIZATION PP 100 40 41 P C 12. COMPUTER GRAPHICS 100 40 44 P.C 100 40 40 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 40 P C 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 49 P C 14. DATA STRUCTURES AND FILES PP 100 40 46 P.C 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 47 P C 100 40 49 P C 15. DATA COMMUNICATION PP 50 20 06. DIGITAL LABORATORY 30 P C 25 10 12 P C TW 16. PROCESSOR INTERFACING LABORATORY TW 50 20 20 P C 17. PROCESSOR INTERFACING LABORATORY OR 50 20 20 P 07. DIGITAL LABORATORY PR 18. DATA STRUCTURES AND FILES LAB TW 25 10 14 P C 08. PROGRAMMING LABORATORY TW 50 20 35 P C 50 20 29 P C 19. DATA STRUCTURES AND FILES LAB PR 50 20 P C 09. PROGRAMMING LABORATORY PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 38 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 27 P C 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 32 P C GRAND TOTAL = 716/1500, RESULT: PASS CLASS ORDN. 1 MARKS:

UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(INFORMATION TECHNOLOGY) DATE: 14 AUG. 2012 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 56 (536)

NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER SEEMA , 71045584E , , PICT , s8058690 S8058690 PUNTAMBEKAR SHREYA SHAILESH 01. DISCRETE STRUCTURES PP 100 40 40 P C 11. ENG MATHS III PP 100 40 40 P 02. COMPUTER ORGANIZATION PP 100 40 44 P C 12. COMPUTER GRAPHICS 100 40 100 40 40 P C 100 40 40 P C 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 13. PROCESSOR ARCHITECTURE & INTER. PP 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 40 P C 100 40 40 P C PP 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 48 P C 100 40 40 P.C 15. DATA COMMUNICATION 50 20 27 P C 25 10 19 P C 06. DIGITAL LABORATORY 16. PROCESSOR INTERFACING LABORATORY TW TW 50 20 22 P C 07. DIGITAL LABORATORY 50 20 20 P C PR 17. PROCESSOR INTERFACING LABORATORY OR 08. PROGRAMMING LABORATORY 50 20 27 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 13 P C TW 09. PROGRAMMING LABORATORY PR 50 20 33 P C 19. DATA STRUCTURES AND FILES LAB PR 50 20 25 P 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 23 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 35 P C 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 P C GRAND TOTAL = 676/1500, RESULT: PASS CLASS

ORDN. 1 MARKS:

S8058693 SARTHAK MAJITHIA POOJA , S8058693 63 P C 100 40 19 F 01. DISCRETE STRUCTURES PP 100 40 11. ENG MATHS III PP 52 P C 02. COMPUTER ORGANIZATION PP 100 40 12. COMPUTER GRAPHICS PP 100 40 57 P C 62 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 40 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 40 100 41 P 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 63 P C 100 42 P C 100 40 PP 40 40 100 05. HUMANITIES AND SOCIAL SCIENCES PP 100 48 P C 15. DATA COMMUNICATION 40 48 P C 50 20 20 P C 06. DIGITAL LABORATORY 25 10 10 P C TW 16. PROCESSOR INTERFACING LABORATORY TW 50 20 30 P C 20 07. DIGITAL LABORATORY PR 17. PROCESSOR INTERFACING LABORATORY OR 50 20 P C 50 20 20 P C 08. PROGRAMMING LABORATORY TW 18. DATA STRUCTURES AND FILES LAB TW 25 10 15 P C 09. PROGRAMMING LABORATORY 19. DATA STRUCTURES AND FILES LAB PR 50 20 43 P C PR 50 20 48 P C 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 30 P C 50 20 20 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 39 P C

GRAND TOTAL = 790/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS:

, 71045599C , , PICT , S8058694 S8058694 SARWAR PRIYANKA ASHOK MEERA 01. DISCRETE STRUCTURES PP 100 40 59 P C 11. ENG MATHS III 100 40 40 P C 02. COMPUTER ORGANIZATION PP 100 40 44 P C 12. COMPUTER GRAPHICS PP 100 40 50 P C 100 40 52 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 40 P C 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 49 P C 14. DATA STRUCTURES AND FILES PP 100 40 55 P C 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 55 P C 100 40 46 P C 15. DATA COMMUNICATION PP 06. DIGITAL LABORATORY 50 20 31 P C 25 10 19 P C TW 16. PROCESSOR INTERFACING LABORATORY TW 50 20 22 P C 17. PROCESSOR INTERFACING LABORATORY OR 50 20 20 P C 07. DIGITAL LABORATORY PR 18. DATA STRUCTURES AND FILES LAB TW 25 10 14 P C 08. PROGRAMMING LABORATORY TW 50 20 38 P C 50 20 30 P C 19. DATA STRUCTURES AND FILES LAB PR 50 20 25 P 09. PROGRAMMING LABORATORY PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 31 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 38 P C 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 P C

GRAND TOTAL = 778/1500, RESULT: SECOND CLASS

ORDN. 1 MARKS:

UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(INFORMATION TECHNOLOGY)

DATE: 14 AUG. 2012 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 57 (537)

NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

s8	058695 SETHI SUPREET SATISHKUMA	.R			PR	EETI	, 71045605M , , PICT , S80	58695
01	. DISCRETE STRUCTURES	PP	100	40	50	РС	11. ENG MATHS III PP 100 40 4	0 P C
02	. COMPUTER ORGANIZATION	PP	100	40	49	РС	12. COMPUTER GRAPHICS PP 100 40 4	0 P C
03	. DIGITAL ELECTRONICS & LOGIC DESI	GPP	100	40	53	РС	13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 4	3 P
04	. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	47	РС	14. DATA STRUCTURES AND FILES PP 100 40 4	8 P C
05	. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	40	РС	15. DATA COMMUNICATION PP 100 40 6	61 P C
06	. DIGITAL LABORATORY	TW	50	20	27	РС	16. PROCESSOR INTERFACING LABORATORY TW 25 10 1	.3 P C
07	. DIGITAL LABORATORY	PR	50	20	25	РС	17. PROCESSOR INTERFACING LABORATORY OR 50 20 3	32 P C
08	. PROGRAMMING LABORATORY	TW	50	20	30	РС	18. DATA STRUCTURES AND FILES LAB TW 25 10 1	.3 P C
09	. PROGRAMMING LABORATORY	PR	50	20	40	РС	19. DATA STRUCTURES AND FILES LAB PR 50 20 3	80 P C
10	. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	26	РС	20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 3	86 P C
							21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 4	2 P C

GRAND TOTAL = 785/1500, RESULT: SECOND CLASS

ORDN. 1 MARKS:

S8058696 SHEWALE ANULA TUSHAR NEETA , S8058696 01. DISCRETE STRUCTURES PP 100 40 49 P C 40 57 P C 11. ENG MATHS III 100 02. COMPUTER ORGANIZATION PP 100 40 50 P C 100 40 40 P C 12. COMPUTER GRAPHICS 100 40 40 P C 100 40 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 13. PROCESSOR ARCHITECTURE & INTER. PP 40 P C 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 42 P C 100 55 P C 14. DATA STRUCTURES AND FILES 40 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 40 P C 15. DATA COMMUNICATION 100 40 40 P C 06. DIGITAL LABORATORY 50 20 27 P C 25 16. PROCESSOR INTERFACING LABORATORY TW 10 16 P C TW 50 20 30 P C 07. DIGITAL LABORATORY 50 20 24 P C PR 17. PROCESSOR INTERFACING LABORATORY OR 08. PROGRAMMING LABORATORY 50 20 29 P C 25 10 14 P C TW 18. DATA STRUCTURES AND FILES LAB TW 50 20 38 P C 19. DATA STRUCTURES AND FILES LAB PR 50 20 26 P 09. PROGRAMMING LABORATORY PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 31 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 37 P C 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 25 P C

GRAND TOTAL = 750/1500, RESULT: SECOND CLASS

ORDN. 1 MARKS :

S8058700 SOMANI NEHA NANDKISHOR					ATI		, 71045628L ,			, s8058700		
01. DISCRETE STRUCTURES	PP	100	40	52	РС	11	. ENG MATHS III	PP	100	40	56	РС
02. COMPUTER ORGANIZATION	PP	100	40	51	РС	12	. COMPUTER GRAPHICS	PP	100	40	49	РС
03. DIGITAL ELECTRONICS & LOGIC DESI	GPP	100	40	52	РС	13	. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	44	Р
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	40	РС	14	. DATA STRUCTURES AND FILES	PP	100	40	67	РС
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	51	РС	15	. DATA COMMUNICATION	PP	100	40	69	РС
06. DIGITAL LABORATORY	TW	50	20	26	РС	16	. PROCESSOR INTERFACING LABORATORY	TW	25	10	17	РС
07. DIGITAL LABORATORY	PR	50	20	37	РС	17	. PROCESSOR INTERFACING LABORATORY	OR	50	20	23	РС
08. PROGRAMMING LABORATORY	TW	50	20	28	РС	18	. DATA STRUCTURES AND FILES LAB	TW	25	10	15	РС
09. PROGRAMMING LABORATORY	PR	50	20	20	РС	19	. DATA STRUCTURES AND FILES LAB	PR	50	20	37	РС
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	36	РС	20	. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	41	РС
						21	. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	35	РС

GRAND TOTAL = 846/1500, RESULT: HIGHER SECOND CLASS

DATE: 14 AUG. 2012 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 58 (538)

NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 70925625G , , PICT , s8058703 S8058703 SUYASH PANDEY SHAILA 01. DISCRETE STRUCTURES PP 100 40 58 P C 11. ENG MATHS III PP 100 40 40 P C 02. COMPUTER ORGANIZATION PP 100 40 48 P C 12. COMPUTER GRAPHICS PP 100 40 100 40 46 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 26 F 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 50 P C 14. DATA STRUCTURES AND FILES PP 100 40 40 P C 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 40 P C PP 100 40 40 P.C 15. DATA COMMUNICATION 50 20 21 P C 25 10 10 P C 06. DIGITAL LABORATORY 16. PROCESSOR INTERFACING LABORATORY TW TW 50 20 27 P C 07. DIGITAL LABORATORY 50 20 AA F PR 17. PROCESSOR INTERFACING LABORATORY OR 08. PROGRAMMING LABORATORY 50 20 23 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 10 P C TW 09. PROGRAMMING LABORATORY PR 50 20 30 P C 19. DATA STRUCTURES AND FILES LAB PR 50 20 P C 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 25 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 20 P C 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 32 P C GRAND TOTAL = 646/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS: S8058705 THOMBRE PRIYANKA MILIND JAYMALA , S8058705 01. DISCRETE STRUCTURES 11. ENG MATHS III PP 100 40 53 P C PP 100 40 58 P C 51 P 02. COMPUTER ORGANIZATION PP 100 40 12. COMPUTER GRAPHICS PP 100 40 53 P C 50 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 40 40 40 P 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 55 P C 100 100 40 PP 40 60 P C 100 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 47 P C 15. DATA COMMUNICATION 40 40 P C 50 20 36 P C 06. DIGITAL LABORATORY 25 10 20 P C TW 16. PROCESSOR INTERFACING LABORATORY TW 50 20 20 P C 20 37 P C 07. DIGITAL LABORATORY PR 17. PROCESSOR INTERFACING LABORATORY OR 50 50 20 37 P C 08. PROGRAMMING LABORATORY TW 18. DATA STRUCTURES AND FILES LAB TW 25 10 17 P C 09. PROGRAMMING LABORATORY 19. DATA STRUCTURES AND FILES LAB PR 50 20 28 P C PR 50 20 42 P C 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 36 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 42 P C 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 30 P C GRAND TOTAL = 852/1500, RESULT: HIGHER SECOND CLASS ORDN. 1 MARKS: , 71045652C , , PICT S8058706 TIWARI ROHIT VINOD SHAKUNTALA , s8058706 01. DISCRETE STRUCTURES PP 100 40 47 P C 11. ENG MATHS III 100 40 40 P C PP 02. COMPUTER ORGANIZATION PP 100 40 40 P C 12. COMPUTER GRAPHICS 100 40 40 P C 100 40 43 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 40 P C 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 46 P C 14. DATA STRUCTURES AND FILES PP 100 40 47 P.C 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 46 P C 100 40 53 P C 15. DATA COMMUNICATION PP 50 20 20 P C 06. DIGITAL LABORATORY 25 10 13 P C TW 16. PROCESSOR INTERFACING LABORATORY TW 50 20 35 P C 17. PROCESSOR INTERFACING LABORATORY OR 50 20 30 P C 07. DIGITAL LABORATORY PR 18. DATA STRUCTURES AND FILES LAB TW 25 10 11 P C 08. PROGRAMMING LABORATORY TW 50 20 28 P C 50 20 32 P C 19. DATA STRUCTURES AND FILES LAB PR 50 20 29 P 09. PROGRAMMING LABORATORY PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 27 P.C. 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 30 P C 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 35 P C GRAND TOTAL = 732/1500, RESULT: PASS CLASS ORDN. 1 MARKS:

UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(INFORMATION TECHNOLOGY) DATE: 14 AUG. 2012 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 59 (539) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71045653M , , , PICT , s8058707 S8058707 UPPOD BALAJI SAMBHAJI SHOBHA 01. DISCRETE STRUCTURES PP 100 40 40 P C 11. ENG MATHS III PP 100 40 46 P 02. COMPUTER ORGANIZATION PP 100 40 51 P C 12. COMPUTER GRAPHICS 100 40 100 40 40 P C 100 40 40 P C 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 13. PROCESSOR ARCHITECTURE & INTER. PP 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 40 P C 100 40 43 P C PP 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 45 P C 100 40 47 P.C 15. DATA COMMUNICATION 50 20 35 P C 25 10 13 P C 06. DIGITAL LABORATORY 16. PROCESSOR INTERFACING LABORATORY TW TW 07. DIGITAL LABORATORY 50 20 23 P C 50 20 28 P C PR 17. PROCESSOR INTERFACING LABORATORY OR 08. PROGRAMMING LABORATORY 50 20 34 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 13 P C TW 09. PROGRAMMING LABORATORY PR 50 20 20 P C 19. DATA STRUCTURES AND FILES LAB PR 50 20 33 P 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 38 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 37 P C 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 P C GRAND TOTAL = 726/1500, RESULT: PASS CLASS ORDN. 1 MARKS: S8058710 WAGH ABHISHEK KARBHARI KRANTI , s8058710 01. DISCRETE STRUCTURES PP 100 40 46 P C 11. ENG MATHS III PP 100 40 AA F PP 100 40 02. COMPUTER ORGANIZATION PP 100 40 40 P C 12. COMPUTER GRAPHICS 40 P C 40 40 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 AA F 14. DATA STRUCTURES AND FILES 43 P C 04. FUNDAMENTAL OF DATA STRUCTURES PP 40 100 100 40 P C PP 40 40 40 P C 100 05. HUMANITIES AND SOCIAL SCIENCES PP 100 15. DATA COMMUNICATION 40 50 20 25 P C 06. DIGITAL LABORATORY 25 10 10 P C TW 16. PROCESSOR INTERFACING LABORATORY TW 50 20 30 P C 20 20 P C 07. DIGITAL LABORATORY PR 17. PROCESSOR INTERFACING LABORATORY OR 50 50 20 25 P C 08. PROGRAMMING LABORATORY TW 18. DATA STRUCTURES AND FILES LAB TW 25 10 10 P C 09. PROGRAMMING LABORATORY 19. DATA STRUCTURES AND FILES LAB PR 50 20 PC PR 50 20 28 P C 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 21 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 P C 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 22 P C

GRAND TOTAL = 520/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

, 70925555B , , PICT , S8058711 S8058711 PAWAR CHANDAN DILIP REKHA 01. DISCRETE STRUCTURES PP 100 40 40 P C 11. ENG MATHS III 100 40 40 P C PP 02. COMPUTER ORGANIZATION PP 100 40 40 P C 12. COMPUTER GRAPHICS 100 40 54 P.C 100 40 40 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 40 P C 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 50 P C 14. DATA STRUCTURES AND FILES PP 100 40 49 P.C 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 53 P C PP 100 40 42 P C 15. DATA COMMUNICATION 06. DIGITAL LABORATORY 50 20 29 P C 25 10 13 P C TW 16. PROCESSOR INTERFACING LABORATORY TW 50 20 25 P C 17. PROCESSOR INTERFACING LABORATORY OR 50 20 36 P C 07. DIGITAL LABORATORY PR 18. DATA STRUCTURES AND FILES LAB TW 25 10 13 P C 08. PROGRAMMING LABORATORY TW 50 20 41 P C 50 20 30 P C 19. DATA STRUCTURES AND FILES LAB PR 50 20 33 P 09. PROGRAMMING LABORATORY PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 40 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 32 P C 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 32 P

GRAND TOTAL = 772/1500, RESULT: SECOND CLASS

ORDN. 1 MARKS:

UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(INFORMATION TECHNOLOGY)

DATE : 14 AUG. 2012	CENT	RE : P	UNE I	NSTI	TUTE OF	COMPUTER TECHNOLOGY, PUNE.	P	AGE NO.	60	(5	40)
NOTE: FIRST LINE : SEAT NO., NAME O	F THE	CANDI	DATE,	MO	THER, P	ERMANENT REG. NO., PREVIOUS SEAT NO.,	COLLI	EGE, S	SEAT I	ΝΟ.	
OTHER LINES: HEAD OF PASSING,	MAX.	MARKS	, MI	N. PA	ASS MAR	KS, MARKS OBTAINED, P/F:PASS/FAIL, C	: PREV	OUS CAI	RRY O	√ER	
S8058712 MAHAJAN SACHIN RAVINDRA				SAF	RALA	, 70701521к ,	, I	PICT	, :	s8058	712
01. DISCRETE STRUCTURES	PP	100	40	AA	F	11. ENG MATHS III	PP	100	40	40	Р
02. COMPUTER ORGANIZATION	PP	100	40	AA	F	12. COMPUTER GRAPHICS	PP	100	40	48	РС
03. DIGITAL ELECTRONICS & LOGIC DESI	GPP	100	40	AA	F	13. PROCESSOR ARCHITECTURE & INTER	. PP	100	40	40	РС
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	AA	F	14. DATA STRUCTURES AND FILES	PP	100	40	45	РС
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	AA	F	15. DATA COMMUNICATION	PP	100	40	57	РС
06. DIGITAL LABORATORY	TW	50	20	AA	F	16. PROCESSOR INTERFACING LABORATO	RY TW	25	10	11	РС
07. DIGITAL LABORATORY	PR	50	20	AA	F	17. PROCESSOR INTERFACING LABORATO	RY OR	50	20	20	РС
08. PROGRAMMING LABORATORY	TW	50	20	AA	F	18. DATA STRUCTURES AND FILES LAB	TW	25	10	12	РС
09. PROGRAMMING LABORATORY	PR	50	20	AA	F	19. DATA STRUCTURES AND FILES LAB	PR	50	20	25	РС
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	AA	F	20. OBJECT ORIENTED PROGRAMMING LA	B TW	50	20	24	РС
						21. OBJECT ORIENTED PROGRAMMING LA	B PR	50	20	26	РС
GRAND TOTAL = 348/1500, RESULT: FAILS	;										
ORDN. 1 MARKS :											