NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8053001 ABDULE MONALI MUKUND SANDHYA , 71045354L , , PICT , T8053001

01. CONTROL SYSTEMS	PP	100	40	48	P	
02. DIGITAL COMMUNICATION	PP	100	40	49	P	
03. DIGITAL COMMUNICATION	PR	50	20	28	P	
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	53	P	
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	41	P	
06. MICROCONTROLLERS & APPLICATION	PP	100	40	47	P	
07. MICROCONTROLLERS & APPLICATION	PR	50	20	32	P	
08. DIGITAL SIGNAL PROCESSING	PP	100	40	42	P	
09. DIGITAL SIGNAL PROCESSING	OR	50	20	30	P	
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	25	P	

FIRST TERM TOTAL = 395/750.
ORDN. 1 MARKS :

T8053002 ABHINIT KUMAR ASHA DEVI , 71050778L , , PICT , T8053002

01. CONTROL SYSTEMS	PP	100	40	75	P	
02. DIGITAL COMMUNICATION	PP	100	40	47	P	
03. DIGITAL COMMUNICATION	PR	50	20	33	P	
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	55	P	
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	37	P	
06. MICROCONTROLLERS & APPLICATION	PP	100	40	46	P	
07. MICROCONTROLLERS & APPLICATION	PR	50	20	33	P	
08. DIGITAL SIGNAL PROCESSING	PP	100	40	63	P	
09. DIGITAL SIGNAL PROCESSING	OR	50	20	39	P	
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	20	P	

FIRST TERM TOTAL = 448/750.
ORDN. 1 MARKS :

T8053003 ABHISHEK ANAND INDRANI , 71045356G , , PICT , T8053003

01. CONTROL SYSTEMS	PP	100	40	63	P	
02. DIGITAL COMMUNICATION	PP	100	40	40	P	
03. DIGITAL COMMUNICATION	PR	50	20	33	P	
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	51	P	
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	36	P	
06. MICROCONTROLLERS & APPLICATION	PP	100	40	40	P	
07. MICROCONTROLLERS & APPLICATION	PR	50	20	25	P	
08. DIGITAL SIGNAL PROCESSING	PP	100	40	52	P	
09. DIGITAL SIGNAL PROCESSING	OR	50	20	37	P	
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	25	P	

FIRST TERM TOTAL = 402/750.
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8053004 AGARWAL VINIT SHARAD SANTOSH , 70925321E , , PICT , T8053004

01. CONTROL SYSTEMS	PP	100	40	65	P
02. DIGITAL COMMUNICATION	PP	100	40	55	P
03. DIGITAL COMMUNICATION	PR	50	20	39	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	66	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	38	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	58	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	40	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	63	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	38	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	28	P

FIRST TERM TOTAL = 490/750.
ORDN. 1 MARKS :

T8053005 AMIT KUMAR RITA , 71045367B , , PICT , T8053005

01. CONTROL SYSTEMS	PP	100	40	67	P
02. DIGITAL COMMUNICATION	PP	100	40	49	P
03. DIGITAL COMMUNICATION	PR	50	20	39	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	61	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	31	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	54	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	32	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	72	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	34	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	32	P

FIRST TERM TOTAL = 471/750.
ORDN. 1 MARKS :

T8053006 ANAGH PAL MANDIRA , 71045368L , , PICT , T8053006

01. CONTROL SYSTEMS	PP	100	40	58	P
02. DIGITAL COMMUNICATION	PP	100	40	50	P
03. DIGITAL COMMUNICATION	PR	50	20	38	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	55	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	36	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	54	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	35	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	57	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	34	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	07	F

FIRST TERM TOTAL = 424/750.
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8053007 ANANNYA MAZUMDER NEELA , 71045369J , , PICT , T8053007

01.	CONTROL SYSTEMS	PP	100	40	61	P
02.	DIGITAL COMMUNICATION	PP	100	40	52	P
03.	DIGITAL COMMUNICATION	PR	50	20	40	P
04.	NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	75	P
05.	NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	41	P
06.	MICROCONTROLLERS & APPLICATION	PP	100	40	52	P
07.	MICROCONTROLLERS & APPLICATION	PR	50	20	25	P
08.	DIGITAL SIGNAL PROCESSING	PP	100	40	60	P
09.	DIGITAL SIGNAL PROCESSING	OR	50	20	20	P
10.	ELECTRONIC DESIGN PRACTICE	OR	50	20	37	P

FIRST TERM TOTAL = 463/750.
ORDN. 1 MARKS :

T8053008 ATTAR BENAZIR MEHBOOB HASINA , 71129924C , , PICT , T8053008

01.	CONTROL SYSTEMS	PP	100	40	62	P
02.	DIGITAL COMMUNICATION	PP	100	40	47	P
03.	DIGITAL COMMUNICATION	PR	50	20	43	P
04.	NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	62	P
05.	NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	42	P
06.	MICROCONTROLLERS & APPLICATION	PP	100	40	55	P
07.	MICROCONTROLLERS & APPLICATION	PR	50	20	42	P
08.	DIGITAL SIGNAL PROCESSING	PP	100	40	63	P
09.	DIGITAL SIGNAL PROCESSING	OR	50	20	39	P
10.	ELECTRONIC DESIGN PRACTICE	OR	50	20	39	P

FIRST TERM TOTAL = 494/750.
ORDN. 1 MARKS :

T8053009 AWASARE VRUSHALI VALMIK URMILA , 71045379F , , PICT , T8053009

01.	CONTROL SYSTEMS	PP	100	40	55	P
02.	DIGITAL COMMUNICATION	PP	100	40	43	P
03.	DIGITAL COMMUNICATION	PR	50	20	29	P
04.	NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	54	P
05.	NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	42	P
06.	MICROCONTROLLERS & APPLICATION	PP	100	40	40	P
07.	MICROCONTROLLERS & APPLICATION	PR	50	20	20	P
08.	DIGITAL SIGNAL PROCESSING	PP	100	40	40	P
09.	DIGITAL SIGNAL PROCESSING	OR	50	20	30	P
10.	ELECTRONIC DESIGN PRACTICE	OR	50	20	36	P

FIRST TERM TOTAL = 389/750.
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8053010 BAGMAR RITESH SURESH ANITA , 71045381H , , PICT , T8053010

01.	CONTROL SYSTEMS	PP	100	40	62	P
02.	DIGITAL COMMUNICATION	PP	100	40	54	P
03.	DIGITAL COMMUNICATION	PR	50	20	30	P
04.	NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	71	P
05.	NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	39	P
06.	MICROCONTROLLERS & APPLICATION	PP	100	40	56	P
07.	MICROCONTROLLERS & APPLICATION	PR	50	20	30	P
08.	DIGITAL SIGNAL PROCESSING	PP	100	40	68	P
09.	DIGITAL SIGNAL PROCESSING	OR	50	20	44	P
10.	ELECTRONIC DESIGN PRACTICE	OR	50	20	33	P

FIRST TERM TOTAL = 487/750.
ORDN. 1 MARKS :

T8053011 BAHETI BHAKTI VILAS PADMA , 71045382F , , PICT , T8053011

01.	CONTROL SYSTEMS	PP	100	40	65	P
02.	DIGITAL COMMUNICATION	PP	100	40	47	P
03.	DIGITAL COMMUNICATION	PR	50	20	45	P
04.	NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	75	P
05.	NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	42	P
06.	MICROCONTROLLERS & APPLICATION	PP	100	40	51	P
07.	MICROCONTROLLERS & APPLICATION	PR	50	20	42	P
08.	DIGITAL SIGNAL PROCESSING	PP	100	40	61	P
09.	DIGITAL SIGNAL PROCESSING	OR	50	20	35	P
10.	ELECTRONIC DESIGN PRACTICE	OR	50	20	32	P

FIRST TERM TOTAL = 495/750.
ORDN. 1 MARKS :

T8053012 BANSAL SHRUTI SANJAY MEENA , 71129925M , , PICT , T8053012

01.	CONTROL SYSTEMS	PP	100	40	63	P
02.	DIGITAL COMMUNICATION	PP	100	40	53	P
03.	DIGITAL COMMUNICATION	PR	50	20	37	P
04.	NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	53	P
05.	NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	44	P
06.	MICROCONTROLLERS & APPLICATION	PP	100	40	55	P
07.	MICROCONTROLLERS & APPLICATION	PR	50	20	42	P
08.	DIGITAL SIGNAL PROCESSING	PP	100	40	73	P
09.	DIGITAL SIGNAL PROCESSING	OR	50	20	36	P
10.	ELECTRONIC DESIGN PRACTICE	OR	50	20	28	P

FIRST TERM TOTAL = 484/750.
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8053013 BHAGAT ABHILASHA RAMDAS MADHURI , 71045386J , , PICT , T8053013

01.	CONTROL SYSTEMS	PP	100	40	21	F
02.	DIGITAL COMMUNICATION	PP	100	40	50	P
03.	DIGITAL COMMUNICATION	PR	50	20	20	P
04.	NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	48	P
05.	NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	39	P
06.	MICROCONTROLLERS & APPLICATION	PP	100	40	41	P
07.	MICROCONTROLLERS & APPLICATION	PR	50	20	10	F
08.	DIGITAL SIGNAL PROCESSING	PP	100	40	56	P
09.	DIGITAL SIGNAL PROCESSING	OR	50	20	32	P
10.	ELECTRONIC DESIGN PRACTICE	OR	50	20	32	P

FIRST TERM TOTAL = 349/750.
ORDN. 1 MARKS :

T8053014 BHAT TUSHAR GOVIND VEENA , 71045388E , , PICT , T8053014

01.	CONTROL SYSTEMS	PP	100	40	57	P
02.	DIGITAL COMMUNICATION	PP	100	40	50	P
03.	DIGITAL COMMUNICATION	PR	50	20	35	P
04.	NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	75	P
05.	NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	34	P
06.	MICROCONTROLLERS & APPLICATION	PP	100	40	55	P
07.	MICROCONTROLLERS & APPLICATION	PR	50	20	32	P
08.	DIGITAL SIGNAL PROCESSING	PP	100	40	72	P
09.	DIGITAL SIGNAL PROCESSING	OR	50	20	30	P
10.	ELECTRONIC DESIGN PRACTICE	OR	50	20	32	P

FIRST TERM TOTAL = 472/750.
ORDN. 1 MARKS :

T8053015 BHOITE AJINKYA CHANDRAKANT MADHAVI , 71045392C , , PICT , T8053015

01.	CONTROL SYSTEMS	PP	100	40	63	P
02.	DIGITAL COMMUNICATION	PP	100	40	40	P
03.	DIGITAL COMMUNICATION	PR	50	20	26	P
04.	NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	61	P
05.	NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	40	P
06.	MICROCONTROLLERS & APPLICATION	PP	100	40	59	P
07.	MICROCONTROLLERS & APPLICATION	PR	50	20	31	P
08.	DIGITAL SIGNAL PROCESSING	PP	100	40	71	P
09.	DIGITAL SIGNAL PROCESSING	OR	50	20	30	P
10.	ELECTRONIC DESIGN PRACTICE	OR	50	20	30	P

FIRST TERM TOTAL = 451/750.
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8053016 BHOSALE PRASHANT DNYANADEO RANJANA , 71045393M , , PICT , T8053016

01.	CONTROL SYSTEMS	PP	100	40	59	P
02.	DIGITAL COMMUNICATION	PP	100	40	45	P
03.	DIGITAL COMMUNICATION	PR	50	20	32	P
04.	NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	61	P
05.	NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	32	P
06.	MICROCONTROLLERS & APPLICATION	PP	100	40	46	P
07.	MICROCONTROLLERS & APPLICATION	PR	50	20	36	P
08.	DIGITAL SIGNAL PROCESSING	PP	100	40	60	P
09.	DIGITAL SIGNAL PROCESSING	OR	50	20	22	P
10.	ELECTRONIC DESIGN PRACTICE	OR	50	20	06	F

FIRST TERM TOTAL = 399/750.
ORDN. 1 MARKS :

T8053017 BICHU TANMAY NITIN DHANASHREE , 71045396F , , PICT , T8053017

01.	CONTROL SYSTEMS	PP	100	40	75	P
02.	DIGITAL COMMUNICATION	PP	100	40	58	P
03.	DIGITAL COMMUNICATION	PR	50	20	45	P
04.	NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	71	P
05.	NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	41	P
06.	MICROCONTROLLERS & APPLICATION	PP	100	40	68	P
07.	MICROCONTROLLERS & APPLICATION	PR	50	20	41	P
08.	DIGITAL SIGNAL PROCESSING	PP	100	40	78	P
09.	DIGITAL SIGNAL PROCESSING	OR	50	20	44	P
10.	ELECTRONIC DESIGN PRACTICE	OR	50	20	45	P

FIRST TERM TOTAL = 566/750.
ORDN. 1 MARKS :

T8053018 CHAVAN TRUPTI GANESH LATA , 71045406G , , PICT , T8053018

01.	CONTROL SYSTEMS	PP	100	40	46	P
02.	DIGITAL COMMUNICATION	PP	100	40	51	P
03.	DIGITAL COMMUNICATION	PR	50	20	39	P
04.	NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	67	P
05.	NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	46	P
06.	MICROCONTROLLERS & APPLICATION	PP	100	40	45	P
07.	MICROCONTROLLERS & APPLICATION	PR	50	20	40	P
08.	DIGITAL SIGNAL PROCESSING	PP	100	40	65	P
09.	DIGITAL SIGNAL PROCESSING	OR	50	20	35	P
10.	ELECTRONIC DESIGN PRACTICE	OR	50	20	39	P

FIRST TERM TOTAL = 473/750.
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8053019 DABHADE ROHIDAS RAGHUNATH BEBEE , 71045409M , , PICT , T8053019

01.	CONTROL SYSTEMS	PP	100	40	57	P
02.	DIGITAL COMMUNICATION	PP	100	40	40	P
03.	DIGITAL COMMUNICATION	PR	50	20	37	P
04.	NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	62	P
05.	NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	34	P
06.	MICROCONTROLLERS & APPLICATION	PP	100	40	50	P
07.	MICROCONTROLLERS & APPLICATION	PR	50	20	35	P
08.	DIGITAL SIGNAL PROCESSING	PP	100	40	61	P
09.	DIGITAL SIGNAL PROCESSING	OR	50	20	33	P
10.	ELECTRONIC DESIGN PRACTICE	OR	50	20	20	P

FIRST TERM TOTAL = 429/750.
ORDN. 1 MARKS :

T8053020 DESAI SHWETA ARUN ANJANI , 71129926K , , PICT , T8053020

01.	CONTROL SYSTEMS	PP	100	40	65	P
02.	DIGITAL COMMUNICATION	PP	100	40	46	P
03.	DIGITAL COMMUNICATION	PR	50	20	35	P
04.	NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	61	P
05.	NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	44	P
06.	MICROCONTROLLERS & APPLICATION	PP	100	40	45	P
07.	MICROCONTROLLERS & APPLICATION	PR	50	20	34	P
08.	DIGITAL SIGNAL PROCESSING	PP	100	40	63	P
09.	DIGITAL SIGNAL PROCESSING	OR	50	20	30	P
10.	ELECTRONIC DESIGN PRACTICE	OR	50	20	22	P

FIRST TERM TOTAL = 445/750.
ORDN. 1 MARKS :

T8053021 DHANNE ANJALI BASAVRAJ MADHURI , 71129927H , , PICT , T8053021

01.	CONTROL SYSTEMS	PP	100	40	70	P
02.	DIGITAL COMMUNICATION	PP	100	40	43	P
03.	DIGITAL COMMUNICATION	PR	50	20	37	P
04.	NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	70	P
05.	NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	41	P
06.	MICROCONTROLLERS & APPLICATION	PP	100	40	60	P
07.	MICROCONTROLLERS & APPLICATION	PR	50	20	42	P
08.	DIGITAL SIGNAL PROCESSING	PP	100	40	63	P
09.	DIGITAL SIGNAL PROCESSING	OR	50	20	08	F
10.	ELECTRONIC DESIGN PRACTICE	OR	50	20	22	P

FIRST TERM TOTAL = 456/750.
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8053022 DHIKALE JITENDRA BABAN USHA , 71045414H , , PICT , T8053022

01.	CONTROL SYSTEMS	PP	100	40	59	P
02.	DIGITAL COMMUNICATION	PP	100	40	58	P
03.	DIGITAL COMMUNICATION	PR	50	20	39	P
04.	NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	62	P
05.	NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	39	P
06.	MICROCONTROLLERS & APPLICATION	PP	100	40	53	P
07.	MICROCONTROLLERS & APPLICATION	PR	50	20	37	P
08.	DIGITAL SIGNAL PROCESSING	PP	100	40	79	P
09.	DIGITAL SIGNAL PROCESSING	OR	50	20	39	P
10.	ELECTRONIC DESIGN PRACTICE	OR	50	20	20	P

FIRST TERM TOTAL = 485/750.
ORDN. 1 MARKS :

T8053023 DIGHE GANESH DATTATRAY SHOBHA , 71045417B , , PICT , T8053023

01.	CONTROL SYSTEMS	PP	100	40	66	P
02.	DIGITAL COMMUNICATION	PP	100	40	54	P
03.	DIGITAL COMMUNICATION	PR	50	20	22	P
04.	NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	59	P
05.	NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	36	P
06.	MICROCONTROLLERS & APPLICATION	PP	100	40	54	P
07.	MICROCONTROLLERS & APPLICATION	PR	50	20	34	P
08.	DIGITAL SIGNAL PROCESSING	PP	100	40	58	P
09.	DIGITAL SIGNAL PROCESSING	OR	50	20	34	P
10.	ELECTRONIC DESIGN PRACTICE	OR	50	20	22	P

FIRST TERM TOTAL = 439/750.
ORDN. 1 MARKS :

T8053024 FIRAKE PARESH RAVINDRA LAXMI , 71045425C , , PICT , T8053024

01.	CONTROL SYSTEMS	PP	100	40	69	P
02.	DIGITAL COMMUNICATION	PP	100	40	59	P
03.	DIGITAL COMMUNICATION	PR	50	20	45	P
04.	NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	67	P
05.	NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	47	P
06.	MICROCONTROLLERS & APPLICATION	PP	100	40	49	P
07.	MICROCONTROLLERS & APPLICATION	PR	50	20	35	P
08.	DIGITAL SIGNAL PROCESSING	PP	100	40	73	P
09.	DIGITAL SIGNAL PROCESSING	OR	50	20	40	P
10.	ELECTRONIC DESIGN PRACTICE	OR	50	20	24	P

FIRST TERM TOTAL = 508/750.
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8053025 GADHE GANESH VITTHALRAO KEVALBAI , 71045427K , , PICT , T8053025

01.	CONTROL SYSTEMS	PP	100	40	42	P
02.	DIGITAL COMMUNICATION	PP	100	40	40	P
03.	DIGITAL COMMUNICATION	PR	50	20	36	P
04.	NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	47	P
05.	NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	35	P
06.	MICROCONTROLLERS & APPLICATION	PP	100	40	45	P
07.	MICROCONTROLLERS & APPLICATION	PR	50	20	38	P
08.	DIGITAL SIGNAL PROCESSING	PP	100	40	57	P
09.	DIGITAL SIGNAL PROCESSING	OR	50	20	35	P
10.	ELECTRONIC DESIGN PRACTICE	OR	50	20	08	F

FIRST TERM TOTAL = 383/750.
ORDN. 1 MARKS :

T8053026 GAVNEKAR SHREYAS SANJIV NILIMA , 71045435L , , PICT , T8053026

01.	CONTROL SYSTEMS	PP	100	40	44	P
02.	DIGITAL COMMUNICATION	PP	100	40	43	P
03.	DIGITAL COMMUNICATION	PR	50	20	31	P
04.	NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	56	P
05.	NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	31	P
06.	MICROCONTROLLERS & APPLICATION	PP	100	40	47	P
07.	MICROCONTROLLERS & APPLICATION	PR	50	20	12	F
08.	DIGITAL SIGNAL PROCESSING	PP	100	40	57	P
09.	DIGITAL SIGNAL PROCESSING	OR	50	20	30	P
10.	ELECTRONIC DESIGN PRACTICE	OR	50	20	27	P

FIRST TERM TOTAL = 378/750.
ORDN. 1 MARKS :

T8053027 GHOGARE SAYALI RAJESH SUJATA , 71129928F , , PICT , T8053027

01.	CONTROL SYSTEMS	PP	100	40	57	P
02.	DIGITAL COMMUNICATION	PP	100	40	46	P
03.	DIGITAL COMMUNICATION	PR	50	20	30	P
04.	NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	56	P
05.	NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	40	P
06.	MICROCONTROLLERS & APPLICATION	PP	100	40	40	P
07.	MICROCONTROLLERS & APPLICATION	PR	50	20	43	P
08.	DIGITAL SIGNAL PROCESSING	PP	100	40	52	P
09.	DIGITAL SIGNAL PROCESSING	OR	50	20	20	P
10.	ELECTRONIC DESIGN PRACTICE	OR	50	20	07	F

FIRST TERM TOTAL = 391/750.
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8053028 GHONE PRASAD SHARAD HEMLATA , 71045437G , , PICT , T8053028

01.	CONTROL SYSTEMS	PP	100	40	64	P
02.	DIGITAL COMMUNICATION	PP	100	40	40	P
03.	DIGITAL COMMUNICATION	PR	50	20	29	P
04.	NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	52	P
05.	NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	37	P
06.	MICROCONTROLLERS & APPLICATION	PP	100	40	40	P
07.	MICROCONTROLLERS & APPLICATION	PR	50	20	35	P
08.	DIGITAL SIGNAL PROCESSING	PP	100	40	52	P
09.	DIGITAL SIGNAL PROCESSING	OR	50	20	27	P
10.	ELECTRONIC DESIGN PRACTICE	OR	50	20	23	P

FIRST TERM TOTAL = 399/750.
ORDN. 1 MARKS :

T8053029 GUJAR AKASH SANJAY ROHINI , 71045438E , , PICT , T8053029

01.	CONTROL SYSTEMS	PP	100	40	45	P
02.	DIGITAL COMMUNICATION	PP	100	40	41	P
03.	DIGITAL COMMUNICATION	PR	50	20	27	P
04.	NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	50	P
05.	NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	32	P
06.	MICROCONTROLLERS & APPLICATION	PP	100	40	48	P
07.	MICROCONTROLLERS & APPLICATION	PR	50	20	36	P
08.	DIGITAL SIGNAL PROCESSING	PP	100	40	52	P
09.	DIGITAL SIGNAL PROCESSING	OR	50	20	20	P
10.	ELECTRONIC DESIGN PRACTICE	OR	50	20	07	F

FIRST TERM TOTAL = 358/750.
ORDN. 1 MARKS :

T8053030 GULAVANI ADITYA VISHRAM MEGHA , 71045439C , , PICT , T8053030

01.	CONTROL SYSTEMS	PP	100	40	46	P
02.	DIGITAL COMMUNICATION	PP	100	40	42	P
03.	DIGITAL COMMUNICATION	PR	50	20	34	P
04.	NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	65	P
05.	NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	39	P
06.	MICROCONTROLLERS & APPLICATION	PP	100	40	49	P
07.	MICROCONTROLLERS & APPLICATION	PR	50	20	39	P
08.	DIGITAL SIGNAL PROCESSING	PP	100	40	59	P
09.	DIGITAL SIGNAL PROCESSING	OR	50	20	34	P
10.	ELECTRONIC DESIGN PRACTICE	OR	50	20	20	P

FIRST TERM TOTAL = 427/750.
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8053031 GULSHEEN KAUR AHUJA BHUP INDER , 71045440G , , PICT , T8053031

01.	CONTROL SYSTEMS	PP	100	40	57	P
02.	DIGITAL COMMUNICATION	PP	100	40	49	P
03.	DIGITAL COMMUNICATION	PR	50	20	44	P
04.	NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	56	P
05.	NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	42	P
06.	MICROCONTROLLERS & APPLICATION	PP	100	40	46	P
07.	MICROCONTROLLERS & APPLICATION	PR	50	20	38	P
08.	DIGITAL SIGNAL PROCESSING	PP	100	40	69	P
09.	DIGITAL SIGNAL PROCESSING	OR	50	20	39	P
10.	ELECTRONIC DESIGN PRACTICE	OR	50	20	37	P

FIRST TERM TOTAL = 477/750.
ORDN. 1 MARKS :

T8053032 GUNDI NACHIKET DINESH VINEETA , 71045441E , , PICT , T8053032

01.	CONTROL SYSTEMS	PP	100	40	46	P
02.	DIGITAL COMMUNICATION	PP	100	40	46	P
03.	DIGITAL COMMUNICATION	PR	50	20	30	P
04.	NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	51	P
05.	NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	43	P
06.	MICROCONTROLLERS & APPLICATION	PP	100	40	49	P
07.	MICROCONTROLLERS & APPLICATION	PR	50	20	41	P
08.	DIGITAL SIGNAL PROCESSING	PP	100	40	56	P
09.	DIGITAL SIGNAL PROCESSING	OR	50	20	30	P
10.	ELECTRONIC DESIGN PRACTICE	OR	50	20	29	P

FIRST TERM TOTAL = 421/750.
ORDN. 1 MARKS :

T8053033 HIMANSHU KAMAT ROOPASHRI , 71045446F , , PICT , T8053033

01.	CONTROL SYSTEMS	PP	100	40	44	P
02.	DIGITAL COMMUNICATION	PP	100	40	45	P
03.	DIGITAL COMMUNICATION	PR	50	20	32	P
04.	NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	52	P
05.	NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	28	P
06.	MICROCONTROLLERS & APPLICATION	PP	100	40	40	P
07.	MICROCONTROLLERS & APPLICATION	PR	50	20	34	P
08.	DIGITAL SIGNAL PROCESSING	PP	100	40	54	P
09.	DIGITAL SIGNAL PROCESSING	OR	50	20	35	P
10.	ELECTRONIC DESIGN PRACTICE	OR	50	20	20	P

FIRST TERM TOTAL = 384/750.
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8053034 HISARIYA RISHI RAMMOHAN SANDHYA , 71129929D , , PICT , T8053034

01.	CONTROL SYSTEMS	PP	100	40	59	P
02.	DIGITAL COMMUNICATION	PP	100	40	46	P
03.	DIGITAL COMMUNICATION	PR	50	20	35	P
04.	NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	47	P
05.	NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	39	P
06.	MICROCONTROLLERS & APPLICATION	PP	100	40	57	P
07.	MICROCONTROLLERS & APPLICATION	PR	50	20	38	P
08.	DIGITAL SIGNAL PROCESSING	PP	100	40	63	P
09.	DIGITAL SIGNAL PROCESSING	OR	50	20	35	P
10.	ELECTRONIC DESIGN PRACTICE	OR	50	20	20	P

FIRST TERM TOTAL = 439/750.
ORDN. 1 MARKS :

T8053035 INGLE CHETAN VASUDEO SHALINI , 71129930H , , PICT , T8053035

01.	CONTROL SYSTEMS	PP	100	40	64	P
02.	DIGITAL COMMUNICATION	PP	100	40	48	P
03.	DIGITAL COMMUNICATION	PR	50	20	37	P
04.	NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	53	P
05.	NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	42	P
06.	MICROCONTROLLERS & APPLICATION	PP	100	40	56	P
07.	MICROCONTROLLERS & APPLICATION	PR	50	20	34	P
08.	DIGITAL SIGNAL PROCESSING	PP	100	40	52	P
09.	DIGITAL SIGNAL PROCESSING	OR	50	20	37	P
10.	ELECTRONIC DESIGN PRACTICE	OR	50	20	24	P

FIRST TERM TOTAL = 447/750.
ORDN. 1 MARKS :

T8053036 JADHAV ARTI GULAB VIMAL , 71129931F , , PICT , T8053036

01.	CONTROL SYSTEMS	PP	100	40	64	P
02.	DIGITAL COMMUNICATION	PP	100	40	56	P
03.	DIGITAL COMMUNICATION	PR	50	20	33	P
04.	NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	59	P
05.	NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	42	P
06.	MICROCONTROLLERS & APPLICATION	PP	100	40	78	P
07.	MICROCONTROLLERS & APPLICATION	PR	50	20	40	P
08.	DIGITAL SIGNAL PROCESSING	PP	100	40	64	P
09.	DIGITAL SIGNAL PROCESSING	OR	50	20	37	P
10.	ELECTRONIC DESIGN PRACTICE	OR	50	20	26	P

FIRST TERM TOTAL = 499/750.
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8053037 JADHAV OMKAR FULCHAND JYOTSNA , 70701463J , , PICT , T8053037

01.	CONTROL SYSTEMS	PP	100	40	45	P
02.	DIGITAL COMMUNICATION	PP	100	40	41	P
03.	DIGITAL COMMUNICATION	PR	50	20	29	P
04.	NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	28	F
05.	NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	27	P
06.	MICROCONTROLLERS & APPLICATION	PP	100	40	48	P
07.	MICROCONTROLLERS & APPLICATION	PR	50	20	08	F
08.	DIGITAL SIGNAL PROCESSING	PP	100	40	40	P
09.	DIGITAL SIGNAL PROCESSING	OR	50	20	36	P
10.	ELECTRONIC DESIGN PRACTICE	OR	50	20	07	F

FIRST TERM TOTAL = 309/750.
ORDN. 1 MARKS :

T8053038 JAIN AMEY MANOJ NILIMA , 71045455E , , PICT , T8053038

01.	CONTROL SYSTEMS	PP	100	40	40	P
02.	DIGITAL COMMUNICATION	PP	100	40	43	P
03.	DIGITAL COMMUNICATION	PR	50	20	33	P
04.	NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	40	P
05.	NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	34	P
06.	MICROCONTROLLERS & APPLICATION	PP	100	40	45	P
07.	MICROCONTROLLERS & APPLICATION	PR	50	20	31	P
08.	DIGITAL SIGNAL PROCESSING	PP	100	40	57	P
09.	DIGITAL SIGNAL PROCESSING	OR	50	20	28	P
10.	ELECTRONIC DESIGN PRACTICE	OR	50	20	20	P

FIRST TERM TOTAL = 371/750.
ORDN. 1 MARKS :

T8053039 JAJOO POONAM ASHOK JAYWANTI , 71045459H , , PICT , T8053039

01.	CONTROL SYSTEMS	PP	100	40	54	P
02.	DIGITAL COMMUNICATION	PP	100	40	47	P
03.	DIGITAL COMMUNICATION	PR	50	20	37	P
04.	NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	66	P
05.	NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	41	P
06.	MICROCONTROLLERS & APPLICATION	PP	100	40	68	P
07.	MICROCONTROLLERS & APPLICATION	PR	50	20	25	P
08.	DIGITAL SIGNAL PROCESSING	PP	100	40	52	P
09.	DIGITAL SIGNAL PROCESSING	OR	50	20	38	P
10.	ELECTRONIC DESIGN PRACTICE	OR	50	20	28	P

FIRST TERM TOTAL = 456/750.
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8053040 JOSHI PURVA AVINASH AARTI , 71045463F , , PICT , T8053040

01.	CONTROL SYSTEMS	PP	100	40	62	P
02.	DIGITAL COMMUNICATION	PP	100	40	59	P
03.	DIGITAL COMMUNICATION	PR	50	20	29	P
04.	NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	69	P
05.	NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	45	P
06.	MICROCONTROLLERS & APPLICATION	PP	100	40	64	P
07.	MICROCONTROLLERS & APPLICATION	PR	50	20	30	P
08.	DIGITAL SIGNAL PROCESSING	PP	100	40	75	P
09.	DIGITAL SIGNAL PROCESSING	OR	50	20	35	P
10.	ELECTRONIC DESIGN PRACTICE	OR	50	20	36	P

FIRST TERM TOTAL = 504/750.
ORDN. 1 MARKS :

T8053041 KAINGADE SARTHAK RANJEET RINISHA , 71045464D , , PICT , T8053041

01.	CONTROL SYSTEMS	PP	100	40	72	P
02.	DIGITAL COMMUNICATION	PP	100	40	63	P
03.	DIGITAL COMMUNICATION	PR	50	20	45	P
04.	NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	67	P
05.	NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	39	P
06.	MICROCONTROLLERS & APPLICATION	PP	100	40	74	P
07.	MICROCONTROLLERS & APPLICATION	PR	50	20	40	P
08.	DIGITAL SIGNAL PROCESSING	PP	100	40	69	P
09.	DIGITAL SIGNAL PROCESSING	OR	50	20	40	P
10.	ELECTRONIC DESIGN PRACTICE	OR	50	20	32	P

FIRST TERM TOTAL = 541/750.
ORDN. 1 MARKS :

T8053042 KAKADE ABHIJIT SURESH VANDANA , 71045465B , , PICT , T8053042

01.	CONTROL SYSTEMS	PP	100	40	46	P
02.	DIGITAL COMMUNICATION	PP	100	40	55	P
03.	DIGITAL COMMUNICATION	PR	50	20	33	P
04.	NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	51	P
05.	NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	39	P
06.	MICROCONTROLLERS & APPLICATION	PP	100	40	54	P
07.	MICROCONTROLLERS & APPLICATION	PR	50	20	38	P
08.	DIGITAL SIGNAL PROCESSING	PP	100	40	71	P
09.	DIGITAL SIGNAL PROCESSING	OR	50	20	37	P
10.	ELECTRONIC DESIGN PRACTICE	OR	50	20	27	P

FIRST TERM TOTAL = 451/750.
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8053043 KAMPU DHANANJAY ANANTRAO SHUBHANGI , 71045467J , , PICT , T8053043

01.	CONTROL SYSTEMS	PP	100	40	52	P
02.	DIGITAL COMMUNICATION	PP	100	40	49	P
03.	DIGITAL COMMUNICATION	PR	50	20	44	P
04.	NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	55	P
05.	NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	32	P
06.	MICROCONTROLLERS & APPLICATION	PP	100	40	63	P
07.	MICROCONTROLLERS & APPLICATION	PR	50	20	25	P
08.	DIGITAL SIGNAL PROCESSING	PP	100	40	71	P
09.	DIGITAL SIGNAL PROCESSING	OR	50	20	22	P
10.	ELECTRONIC DESIGN PRACTICE	OR	50	20	33	P

FIRST TERM TOTAL = 446/750.
ORDN. 1 MARKS :

T8053044 KARE NACHIKET CHANDRASHEKHAR NANDINI , 71045473C , , PICT , T8053044

01.	CONTROL SYSTEMS	PP	100	40	53	P
02.	DIGITAL COMMUNICATION	PP	100	40	55	P
03.	DIGITAL COMMUNICATION	PR	50	20	38	P
04.	NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	62	P
05.	NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	45	P
06.	MICROCONTROLLERS & APPLICATION	PP	100	40	71	P
07.	MICROCONTROLLERS & APPLICATION	PR	50	20	38	P
08.	DIGITAL SIGNAL PROCESSING	PP	100	40	63	P
09.	DIGITAL SIGNAL PROCESSING	OR	50	20	37	P
10.	ELECTRONIC DESIGN PRACTICE	OR	50	20	35	P

FIRST TERM TOTAL = 497/750.
ORDN. 1 MARKS :

T8053045 KULKARNI AKSHADA VIJAY VIDYA , 71045490C , , PICT , T8053045

01.	CONTROL SYSTEMS	PP	100	40	68	P
02.	DIGITAL COMMUNICATION	PP	100	40	48	P
03.	DIGITAL COMMUNICATION	PR	50	20	35	P
04.	NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	69	P
05.	NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	42	P
06.	MICROCONTROLLERS & APPLICATION	PP	100	40	72	P
07.	MICROCONTROLLERS & APPLICATION	PR	50	20	25	P
08.	DIGITAL SIGNAL PROCESSING	PP	100	40	74	P
09.	DIGITAL SIGNAL PROCESSING	OR	50	20	38	P
10.	ELECTRONIC DESIGN PRACTICE	OR	50	20	30	P

FIRST TERM TOTAL = 501/750.
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8053046 KULKARNI GAURAV PRADEEP NISHA , 71059376H , , PICT , T8053046

01.	CONTROL SYSTEMS	PP	100	40	50	P
02.	DIGITAL COMMUNICATION	PP	100	40	50	P
03.	DIGITAL COMMUNICATION	PR	50	20	34	P
04.	NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	66	P
05.	NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	45	P
06.	MICROCONTROLLERS & APPLICATION	PP	100	40	62	P
07.	MICROCONTROLLERS & APPLICATION	PR	50	20	33	P
08.	DIGITAL SIGNAL PROCESSING	PP	100	40	71	P
09.	DIGITAL SIGNAL PROCESSING	OR	50	20	35	P
10.	ELECTRONIC DESIGN PRACTICE	OR	50	20	43	P

FIRST TERM TOTAL = 489/750.
ORDN. 1 MARKS :

T8053047 KULKARNI HARSHAD RAVINDRA SADHANA , 71045495D , , PICT , T8053047

01.	CONTROL SYSTEMS	PP	100	40	47	P
02.	DIGITAL COMMUNICATION	PP	100	40	47	P
03.	DIGITAL COMMUNICATION	PR	50	20	34	P
04.	NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	54	P
05.	NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	33	P
06.	MICROCONTROLLERS & APPLICATION	PP	100	40	59	P
07.	MICROCONTROLLERS & APPLICATION	PR	50	20	30	P
08.	DIGITAL SIGNAL PROCESSING	PP	100	40	70	P
09.	DIGITAL SIGNAL PROCESSING	OR	50	20	35	P
10.	ELECTRONIC DESIGN PRACTICE	OR	50	20	32	P

FIRST TERM TOTAL = 441/750.
ORDN. 1 MARKS :

T8053048 KULKARNI SAURABH CHANDRAKANT SWARALI , 71045497L , , PICT , T8053048

01.	CONTROL SYSTEMS	PP	100	40	57	P
02.	DIGITAL COMMUNICATION	PP	100	40	48	P
03.	DIGITAL COMMUNICATION	PR	50	20	07	F
04.	NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	65	P
05.	NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	39	P
06.	MICROCONTROLLERS & APPLICATION	PP	100	40	56	P
07.	MICROCONTROLLERS & APPLICATION	PR	50	20	32	P
08.	DIGITAL SIGNAL PROCESSING	PP	100	40	65	P
09.	DIGITAL SIGNAL PROCESSING	OR	50	20	35	P
10.	ELECTRONIC DESIGN PRACTICE	OR	50	20	20	P

FIRST TERM TOTAL = 424/750.
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8053049 KULKARNI YASH VIJAY VINITA , 71045499G , , PICT , T8053049

01.	CONTROL SYSTEMS	PP	100	40	59	P
02.	DIGITAL COMMUNICATION	PP	100	40	47	P
03.	DIGITAL COMMUNICATION	PR	50	20	37	P
04.	NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	48	P
05.	NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	38	P
06.	MICROCONTROLLERS & APPLICATION	PP	100	40	59	P
07.	MICROCONTROLLERS & APPLICATION	PR	50	20	25	P
08.	DIGITAL SIGNAL PROCESSING	PP	100	40	60	P
09.	DIGITAL SIGNAL PROCESSING	OR	50	20	28	P
10.	ELECTRONIC DESIGN PRACTICE	OR	50	20	25	P

FIRST TERM TOTAL = 426/750.
ORDN. 1 MARKS :

T8053050 KUMBHAKARN MANSI ARUN SULBHA , 71045500D , , PICT , T8053050

01.	CONTROL SYSTEMS	PP	100	40	51	P
02.	DIGITAL COMMUNICATION	PP	100	40	48	P
03.	DIGITAL COMMUNICATION	PR	50	20	41	P
04.	NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	60	P
05.	NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	47	P
06.	MICROCONTROLLERS & APPLICATION	PP	100	40	65	P
07.	MICROCONTROLLERS & APPLICATION	PR	50	20	40	P
08.	DIGITAL SIGNAL PROCESSING	PP	100	40	71	P
09.	DIGITAL SIGNAL PROCESSING	OR	50	20	37	P
10.	ELECTRONIC DESIGN PRACTICE	OR	50	20	32	P

FIRST TERM TOTAL = 492/750.
ORDN. 1 MARKS :

T8053051 LIMBHORE NITIN SHIVAJI SANGITA , 71129932D , , PICT , T8053051

01.	CONTROL SYSTEMS	PP	100	40	60	P
02.	DIGITAL COMMUNICATION	PP	100	40	48	P
03.	DIGITAL COMMUNICATION	PR	50	20	21	P
04.	NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	62	P
05.	NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	35	P
06.	MICROCONTROLLERS & APPLICATION	PP	100	40	73	P
07.	MICROCONTROLLERS & APPLICATION	PR	50	20	41	P
08.	DIGITAL SIGNAL PROCESSING	PP	100	40	73	P
09.	DIGITAL SIGNAL PROCESSING	OR	50	20	38	P
10.	ELECTRONIC DESIGN PRACTICE	OR	50	20	30	P

FIRST TERM TOTAL = 481/750.
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8053052 LONDHE AARTI SURESH NANDA , 71045505E , , PICT , T8053052

01.	CONTROL SYSTEMS	PP	100	40	49	P
02.	DIGITAL COMMUNICATION	PP	100	40	43	P
03.	DIGITAL COMMUNICATION	PR	50	20	39	P
04.	NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	54	P
05.	NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	40	P
06.	MICROCONTROLLERS & APPLICATION	PP	100	40	65	P
07.	MICROCONTROLLERS & APPLICATION	PR	50	20	31	P
08.	DIGITAL SIGNAL PROCESSING	PP	100	40	64	P
09.	DIGITAL SIGNAL PROCESSING	OR	50	20	32	P
10.	ELECTRONIC DESIGN PRACTICE	OR	50	20	27	P

FIRST TERM TOTAL = 444/750.
ORDN. 1 MARKS :

T8053053 LUCKY KUMAR SARITA DEVI , 71051136B , , PICT , T8053053

01.	CONTROL SYSTEMS	PP	100	40	61	P
02.	DIGITAL COMMUNICATION	PP	100	40	58	P
03.	DIGITAL COMMUNICATION	PR	50	20	24	P
04.	NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	74	P
05.	NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	28	P
06.	MICROCONTROLLERS & APPLICATION	PP	100	40	60	P
07.	MICROCONTROLLERS & APPLICATION	PR	50	20	34	P
08.	DIGITAL SIGNAL PROCESSING	PP	100	40	64	P
09.	DIGITAL SIGNAL PROCESSING	OR	50	20	39	P
10.	ELECTRONIC DESIGN PRACTICE	OR	50	20	05	F

FIRST TERM TOTAL = 447/750.
ORDN. 1 MARKS :

T8053054 MAGDUM FAREEN JABBAR YASMIN , 71045506C , , PICT , T8053054

01.	CONTROL SYSTEMS	PP	100	40	42	P
02.	DIGITAL COMMUNICATION	PP	100	40	46	P
03.	DIGITAL COMMUNICATION	PR	50	20	38	P
04.	NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	52	P
05.	NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	42	P
06.	MICROCONTROLLERS & APPLICATION	PP	100	40	58	P
07.	MICROCONTROLLERS & APPLICATION	PR	50	20	36	P
08.	DIGITAL SIGNAL PROCESSING	PP	100	40	60	P
09.	DIGITAL SIGNAL PROCESSING	OR	50	20	30	P
10.	ELECTRONIC DESIGN PRACTICE	OR	50	20	26	P

FIRST TERM TOTAL = 430/750.
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8053055 MAHAJAN TUSHAR GOPAL JANABAI , 71045508K , , PICT , T8053055

01.	CONTROL SYSTEMS	PP	100	40	52	P
02.	DIGITAL COMMUNICATION	PP	100	40	40	P
03.	DIGITAL COMMUNICATION	PR	50	20	32	P
04.	NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	54	P
05.	NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	29	P
06.	MICROCONTROLLERS & APPLICATION	PP	100	40	48	P
07.	MICROCONTROLLERS & APPLICATION	PR	50	20	32	P
08.	DIGITAL SIGNAL PROCESSING	PP	100	40	62	P
09.	DIGITAL SIGNAL PROCESSING	OR	50	20	36	P
10.	ELECTRONIC DESIGN PRACTICE	OR	50	20	29	P

FIRST TERM TOTAL = 414/750.
ORDN. 1 MARKS :

T8053056 MAHENDRA CHOUDHARY HEERA , 71045511K , , PICT , T8053056

01.	CONTROL SYSTEMS	PP	100	40	49	P
02.	DIGITAL COMMUNICATION	PP	100	40	62	P
03.	DIGITAL COMMUNICATION	PR	50	20	40	P
04.	NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	63	P
05.	NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	37	P
06.	MICROCONTROLLERS & APPLICATION	PP	100	40	62	P
07.	MICROCONTROLLERS & APPLICATION	PR	50	20	30	P
08.	DIGITAL SIGNAL PROCESSING	PP	100	40	57	P
09.	DIGITAL SIGNAL PROCESSING	OR	50	20	36	P
10.	ELECTRONIC DESIGN PRACTICE	OR	50	20	07	F

FIRST TERM TOTAL = 443/750.
ORDN. 1 MARKS :

T8053057 MAHESHWARI SNEHAL RAVINDRA AARTI , 71045512H , , PICT , T8053057

01.	CONTROL SYSTEMS	PP	100	40	40	P
02.	DIGITAL COMMUNICATION	PP	100	40	31	F
03.	DIGITAL COMMUNICATION	PR	50	20	38	P
04.	NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	45	P
05.	NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	38	P
06.	MICROCONTROLLERS & APPLICATION	PP	100	40	49	P
07.	MICROCONTROLLERS & APPLICATION	PR	50	20	34	P
08.	DIGITAL SIGNAL PROCESSING	PP	100	40	41	P
09.	DIGITAL SIGNAL PROCESSING	OR	50	20	32	P
10.	ELECTRONIC DESIGN PRACTICE	OR	50	20	06	F

FIRST TERM TOTAL = 354/750.
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8053058 MALDODE RAHUL SUDHAKAR SANGEETA , 71045514D , , PICT , T8053058

01.	CONTROL SYSTEMS	PP	100	40	45	P
02.	DIGITAL COMMUNICATION	PP	100	40	52	P
03.	DIGITAL COMMUNICATION	PR	50	20	39	P
04.	NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	56	P
05.	NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	44	P
06.	MICROCONTROLLERS & APPLICATION	PP	100	40	64	P
07.	MICROCONTROLLERS & APPLICATION	PR	50	20	35	P
08.	DIGITAL SIGNAL PROCESSING	PP	100	40	74	P
09.	DIGITAL SIGNAL PROCESSING	OR	50	20	37	P
10.	ELECTRONIC DESIGN PRACTICE	OR	50	20	20	P

FIRST TERM TOTAL = 466/750.
ORDN. 1 MARKS :

T8053059 MANE DEEPALI LIMBAJI REKHA , 71045517J , , PICT , T8053059

01.	CONTROL SYSTEMS	PP	100	40	49	P
02.	DIGITAL COMMUNICATION	PP	100	40	43	P
03.	DIGITAL COMMUNICATION	PR	50	20	35	P
04.	NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	66	P
05.	NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	38	P
06.	MICROCONTROLLERS & APPLICATION	PP	100	40	60	P
07.	MICROCONTROLLERS & APPLICATION	PR	50	20	32	P
08.	DIGITAL SIGNAL PROCESSING	PP	100	40	60	P
09.	DIGITAL SIGNAL PROCESSING	OR	50	20	07	F
10.	ELECTRONIC DESIGN PRACTICE	OR	50	20	29	P

FIRST TERM TOTAL = 419/750.
ORDN. 1 MARKS :

T8053060 MANE GAURANGI LALASAHEB KALPANA , 71045518G , , PICT , T8053060

01.	CONTROL SYSTEMS	PP	100	40	40	P
02.	DIGITAL COMMUNICATION	PP	100	40	54	P
03.	DIGITAL COMMUNICATION	PR	50	20	30	P
04.	NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	44	P
05.	NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	42	P
06.	MICROCONTROLLERS & APPLICATION	PP	100	40	57	P
07.	MICROCONTROLLERS & APPLICATION	PR	50	20	39	P
08.	DIGITAL SIGNAL PROCESSING	PP	100	40	52	P
09.	DIGITAL SIGNAL PROCESSING	OR	50	20	32	P
10.	ELECTRONIC DESIGN PRACTICE	OR	50	20	30	P

FIRST TERM TOTAL = 420/750.
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8053061 MANTRI SHRUTI BAJRANG SUREKHA , 71045519E , , PICT , T8053061

01.	CONTROL SYSTEMS	PP	100	40	47	P
02.	DIGITAL COMMUNICATION	PP	100	40	46	P
03.	DIGITAL COMMUNICATION	PR	50	20	31	P
04.	NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	55	P
05.	NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	37	P
06.	MICROCONTROLLERS & APPLICATION	PP	100	40	59	P
07.	MICROCONTROLLERS & APPLICATION	PR	50	20	30	P
08.	DIGITAL SIGNAL PROCESSING	PP	100	40	59	P
09.	DIGITAL SIGNAL PROCESSING	OR	50	20	27	P
10.	ELECTRONIC DESIGN PRACTICE	OR	50	20	20	P

FIRST TERM TOTAL = 411/750.
ORDN. 1 MARKS :

T8053062 MERCHANT ALIAKBAR YAHAYA MEELA , 71129933B , , PICT , T8053062

01.	CONTROL SYSTEMS	PP	100	40	40	P
02.	DIGITAL COMMUNICATION	PP	100	40	50	P
03.	DIGITAL COMMUNICATION	PR	50	20	30	P
04.	NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	65	P
05.	NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	37	P
06.	MICROCONTROLLERS & APPLICATION	PP	100	40	66	P
07.	MICROCONTROLLERS & APPLICATION	PR	50	20	32	P
08.	DIGITAL SIGNAL PROCESSING	PP	100	40	63	P
09.	DIGITAL SIGNAL PROCESSING	OR	50	20	20	P
10.	ELECTRONIC DESIGN PRACTICE	OR	50	20	07	F

FIRST TERM TOTAL = 410/750.
ORDN. 1 MARKS :

T8053063 MISHRA ABHISHEK ASHUTOSH MALA , 71045525K , , PICT , T8053063

01.	CONTROL SYSTEMS	PP	100	40	43	P
02.	DIGITAL COMMUNICATION	PP	100	40	41	P
03.	DIGITAL COMMUNICATION	PR	50	20	33	P
04.	NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	51	P
05.	NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	32	P
06.	MICROCONTROLLERS & APPLICATION	PP	100	40	55	P
07.	MICROCONTROLLERS & APPLICATION	PR	50	20	11	F
08.	DIGITAL SIGNAL PROCESSING	PP	100	40	50	P
09.	DIGITAL SIGNAL PROCESSING	OR	50	20	36	P
10.	ELECTRONIC DESIGN PRACTICE	OR	50	20	26	P

FIRST TERM TOTAL = 378/750.
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8053064 MOHAMMED HABIBULLAH BAIG NASRULLAH BAIG SADIYA BEGUM , 71045673F , , PICT , T8053064

01. CONTROL SYSTEMS	PP	100	40	63	P
02. DIGITAL COMMUNICATION	PP	100	40	61	P
03. DIGITAL COMMUNICATION	PR	50	20	44	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	71	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	32	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	71	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	35	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	73	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	39	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	20	P

FIRST TERM TOTAL = 509/750.
ORDN. 1 MARKS :

T8053065 MORE NISHANT PRAVIN SHARADA , 70925509J , , PICT , T8053065

01. CONTROL SYSTEMS	PP	100	40	25	F
02. DIGITAL COMMUNICATION	PP	100	40	25	F
03. DIGITAL COMMUNICATION	PR	50	20	30	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	43	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	37	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	43	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	37	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	50	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	20	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	06	F

FIRST TERM TOTAL = 316/750.
ORDN. 1 MARKS :

T8053066 MRIDUL NAGAR MANJU , 71045528D , , PICT , T8053066

01. CONTROL SYSTEMS	PP	100	40	40	P
02. DIGITAL COMMUNICATION	PP	100	40	51	P
03. DIGITAL COMMUNICATION	PR	50	20	38	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	54	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	32	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	63	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	30	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	59	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	36	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	05	F

FIRST TERM TOTAL = 408/750.
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8053067 MUJAWAR SAMIR HANIF RUBAB , 71045529B , , PICT , T8053067

01.	CONTROL SYSTEMS	PP	100	40	49	P
02.	DIGITAL COMMUNICATION	PP	100	40	43	P
03.	DIGITAL COMMUNICATION	PR	50	20	37	P
04.	NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	58	P
05.	NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	31	P
06.	MICROCONTROLLERS & APPLICATION	PP	100	40	57	P
07.	MICROCONTROLLERS & APPLICATION	PR	50	20	32	P
08.	DIGITAL SIGNAL PROCESSING	PP	100	40	63	P
09.	DIGITAL SIGNAL PROCESSING	OR	50	20	20	P
10.	ELECTRONIC DESIGN PRACTICE	OR	50	20	05	F

FIRST TERM TOTAL = 395/750.
ORDN. 1 MARKS :

T8053068 MULLA SHARIQA AZAMALI ARIFA , 71045531D , , PICT , T8053068

01.	CONTROL SYSTEMS	PP	100	40	58	P
02.	DIGITAL COMMUNICATION	PP	100	40	65	P
03.	DIGITAL COMMUNICATION	PR	50	20	40	P
04.	NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	58	P
05.	NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	37	P
06.	MICROCONTROLLERS & APPLICATION	PP	100	40	76	P
07.	MICROCONTROLLERS & APPLICATION	PR	50	20	34	P
08.	DIGITAL SIGNAL PROCESSING	PP	100	40	58	P
09.	DIGITAL SIGNAL PROCESSING	OR	50	20	27	P
10.	ELECTRONIC DESIGN PRACTICE	OR	50	20	25	P

FIRST TERM TOTAL = 478/750.
ORDN. 1 MARKS :

T8053069 MUNDANKAR SUSHANT SANJAY SHAILA , 71045532B , , PICT , T8053069

01.	CONTROL SYSTEMS	PP	100	40	52	P
02.	DIGITAL COMMUNICATION	PP	100	40	51	P
03.	DIGITAL COMMUNICATION	PR	50	20	32	P
04.	NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	57	P
05.	NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	35	P
06.	MICROCONTROLLERS & APPLICATION	PP	100	40	63	P
07.	MICROCONTROLLERS & APPLICATION	PR	50	20	35	P
08.	DIGITAL SIGNAL PROCESSING	PP	100	40	75	P
09.	DIGITAL SIGNAL PROCESSING	OR	50	20	32	P
10.	ELECTRONIC DESIGN PRACTICE	OR	50	20	20	P

FIRST TERM TOTAL = 452/750.
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8053070 NAGHATE ANKIT VIJAY MADHURI , 71045537C , , PICT , T8053070

01.	CONTROL SYSTEMS	PP	100	40	42	P
02.	DIGITAL COMMUNICATION	PP	100	40	40	P
03.	DIGITAL COMMUNICATION	PR	50	20	35	P
04.	NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	53	P
05.	NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	38	P
06.	MICROCONTROLLERS & APPLICATION	PP	100	40	61	P
07.	MICROCONTROLLERS & APPLICATION	PR	50	20	35	P
08.	DIGITAL SIGNAL PROCESSING	PP	100	40	50	P
09.	DIGITAL SIGNAL PROCESSING	OR	50	20	36	P
10.	ELECTRONIC DESIGN PRACTICE	OR	50	20	20	P

FIRST TERM TOTAL = 410/750.
ORDN. 1 MARKS :

T8053072 NEVASE PRAJAKTA MARUTI NANDA , 71129934L , , PICT , T8053072

01.	CONTROL SYSTEMS	PP	100	40	61	P
02.	DIGITAL COMMUNICATION	PP	100	40	53	P
03.	DIGITAL COMMUNICATION	PR	50	20	26	P
04.	NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	40	P
05.	NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	41	P
06.	MICROCONTROLLERS & APPLICATION	PP	100	40	50	P
07.	MICROCONTROLLERS & APPLICATION	PR	50	20	32	P
08.	DIGITAL SIGNAL PROCESSING	PP	100	40	60	P
09.	DIGITAL SIGNAL PROCESSING	OR	50	20	30	P
10.	ELECTRONIC DESIGN PRACTICE	OR	50	20	05	F

FIRST TERM TOTAL = 398/750.
ORDN. 1 MARKS :

T8053073 NIRMAL MAYUR VINAYAKRAO SUNITA , 71045548J , , PICT , T8053073

01.	CONTROL SYSTEMS	PP	100	40	67	P
02.	DIGITAL COMMUNICATION	PP	100	40	59	P
03.	DIGITAL COMMUNICATION	PR	50	20	36	P
04.	NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	52	P
05.	NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	42	P
06.	MICROCONTROLLERS & APPLICATION	PP	100	40	50	P
07.	MICROCONTROLLERS & APPLICATION	PR	50	20	34	P
08.	DIGITAL SIGNAL PROCESSING	PP	100	40	66	P
09.	DIGITAL SIGNAL PROCESSING	OR	50	20	38	P
10.	ELECTRONIC DESIGN PRACTICE	OR	50	20	35	P

FIRST TERM TOTAL = 479/750.
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8053074 PACHANGE ASHISH NARAYAN KALPANA , 71045551J , , PICT , T8053074

01.	CONTROL SYSTEMS	PP	100	40	55	P
02.	DIGITAL COMMUNICATION	PP	100	40	50	P
03.	DIGITAL COMMUNICATION	PR	50	20	36	P
04.	NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	63	P
05.	NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	35	P
06.	MICROCONTROLLERS & APPLICATION	PP	100	40	40	P
07.	MICROCONTROLLERS & APPLICATION	PR	50	20	31	P
08.	DIGITAL SIGNAL PROCESSING	PP	100	40	59	P
09.	DIGITAL SIGNAL PROCESSING	OR	50	20	33	P
10.	ELECTRONIC DESIGN PRACTICE	OR	50	20	32	P

FIRST TERM TOTAL = 434/750.
ORDN. 1 MARKS :

T8053075 PARODE ASHWINI SANJAY JAISHREE , 71045559D , , PICT , T8053075

01.	CONTROL SYSTEMS	PP	100	40	50	P
02.	DIGITAL COMMUNICATION	PP	100	40	50	P
03.	DIGITAL COMMUNICATION	PR	50	20	34	P
04.	NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	49	P
05.	NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	43	P
06.	MICROCONTROLLERS & APPLICATION	PP	100	40	40	P
07.	MICROCONTROLLERS & APPLICATION	PR	50	20	36	P
08.	DIGITAL SIGNAL PROCESSING	PP	100	40	57	P
09.	DIGITAL SIGNAL PROCESSING	OR	50	20	35	P
10.	ELECTRONIC DESIGN PRACTICE	OR	50	20	35	P

FIRST TERM TOTAL = 429/750.
ORDN. 1 MARKS :

T8053076 PATIL GAURAV VISHRAM PRAMILA , 71045564L , , PICT , T8053076

01.	CONTROL SYSTEMS	PP	100	40	49	P
02.	DIGITAL COMMUNICATION	PP	100	40	49	P
03.	DIGITAL COMMUNICATION	PR	50	20	08	F
04.	NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	40	P
05.	NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	33	P
06.	MICROCONTROLLERS & APPLICATION	PP	100	40	43	P
07.	MICROCONTROLLERS & APPLICATION	PR	50	20	26	P
08.	DIGITAL SIGNAL PROCESSING	PP	100	40	42	P
09.	DIGITAL SIGNAL PROCESSING	OR	50	20	32	P
10.	ELECTRONIC DESIGN PRACTICE	OR	50	20	07	F

FIRST TERM TOTAL = 329/750.
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8053077 PATIL OMKAR VISHWAS VANITA , 71045566G , , PICT , T8053077

01. CONTROL SYSTEMS	PP	100	40	60	P
02. DIGITAL COMMUNICATION	PP	100	40	49	P
03. DIGITAL COMMUNICATION	PR	50	20	07	F
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	51	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	38	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	40	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	31	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	55	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	38	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	25	P

FIRST TERM TOTAL = 394/750.
ORDN. 1 MARKS :

T8053078 PATIL SONAM SHASHIKANT SHUBHANGI , 71129935J , , PICT , T8053078

01. CONTROL SYSTEMS	PP	100	40	62	P
02. DIGITAL COMMUNICATION	PP	100	40	50	P
03. DIGITAL COMMUNICATION	PR	50	20	20	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	55	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	40	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	54	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	25	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	66	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	28	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	06	F

FIRST TERM TOTAL = 406/750.
ORDN. 1 MARKS :

T8053079 PATIL TEJAS LAXMAN SUREKHA , 71045568C , , PICT , T8053079

01. CONTROL SYSTEMS	PP	100	40	59	P
02. DIGITAL COMMUNICATION	PP	100	40	51	P
03. DIGITAL COMMUNICATION	PR	50	20	27	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	58	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	47	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	40	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	34	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	58	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	39	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	28	P

FIRST TERM TOTAL = 441/750.
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8053080 PAWADE NITIN NARAYAN RENUKA , 71045569M , , PICT , T8053080

01.	CONTROL SYSTEMS	PP	100	40	56	P
02.	DIGITAL COMMUNICATION	PP	100	40	49	P
03.	DIGITAL COMMUNICATION	PR	50	20	33	P
04.	NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	56	P
05.	NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	41	P
06.	MICROCONTROLLERS & APPLICATION	PP	100	40	40	P
07.	MICROCONTROLLERS & APPLICATION	PR	50	20	36	P
08.	DIGITAL SIGNAL PROCESSING	PP	100	40	43	P
09.	DIGITAL SIGNAL PROCESSING	OR	50	20	34	P
10.	ELECTRONIC DESIGN PRACTICE	OR	50	20	21	P

FIRST TERM TOTAL = 409/750.
ORDN. 1 MARKS :

T8053081 PAWAR PRADIPKUMAR RAOSAHEB SANGITA , 71045572M , , PICT , T8053081

01.	CONTROL SYSTEMS	PP	100	40	59	P
02.	DIGITAL COMMUNICATION	PP	100	40	58	P
03.	DIGITAL COMMUNICATION	PR	50	20	32	P
04.	NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	65	P
05.	NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	36	P
06.	MICROCONTROLLERS & APPLICATION	PP	100	40	40	P
07.	MICROCONTROLLERS & APPLICATION	PR	50	20	28	P
08.	DIGITAL SIGNAL PROCESSING	PP	100	40	55	P
09.	DIGITAL SIGNAL PROCESSING	OR	50	20	38	P
10.	ELECTRONIC DESIGN PRACTICE	OR	50	20	07	F

FIRST TERM TOTAL = 418/750.
ORDN. 1 MARKS :

T8053082 PAWAR SURAJ SUBHASH VIDYA , 71045573K , , PICT , T8053082

01.	CONTROL SYSTEMS	PP	100	40	46	P
02.	DIGITAL COMMUNICATION	PP	100	40	40	P
03.	DIGITAL COMMUNICATION	PR	50	20	06	F
04.	NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	26	F
05.	NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	31	P
06.	MICROCONTROLLERS & APPLICATION	PP	100	40	25	F
07.	MICROCONTROLLERS & APPLICATION	PR	50	20	10	F
08.	DIGITAL SIGNAL PROCESSING	PP	100	40	46	P
09.	DIGITAL SIGNAL PROCESSING	OR	50	20	20	P
10.	ELECTRONIC DESIGN PRACTICE	OR	50	20	06	F

FIRST TERM TOTAL = 256/750.
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8053083 PAWAR VISHAL ALIAS DIGVIJAY BALASAHEB VIJAYANTI , 71070203F , , PICT , T8053083

01.	CONTROL SYSTEMS	PP	100	40	55	P
02.	DIGITAL COMMUNICATION	PP	100	40	26	F
03.	DIGITAL COMMUNICATION	PR	50	20	44	P
04.	NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	AA	F
05.	NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	27	P
06.	MICROCONTROLLERS & APPLICATION	PP	100	40	40	P
07.	MICROCONTROLLERS & APPLICATION	PR	50	20	30	P
08.	DIGITAL SIGNAL PROCESSING	PP	100	40	AA	F
09.	DIGITAL SIGNAL PROCESSING	OR	50	20	20	P
10.	ELECTRONIC DESIGN PRACTICE	OR	50	20	21	P

FIRST TERM TOTAL = 263/750.
ORDN. 1 MARKS :

T8053084 PITRODA UTSAV RAJENDRA KAILASH , 71129936G , , PICT , T8053084

01.	CONTROL SYSTEMS	PP	100	40	57	P
02.	DIGITAL COMMUNICATION	PP	100	40	40	P
03.	DIGITAL COMMUNICATION	PR	50	20	30	P
04.	NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	40	P
05.	NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	44	P
06.	MICROCONTROLLERS & APPLICATION	PP	100	40	46	P
07.	MICROCONTROLLERS & APPLICATION	PR	50	20	44	P
08.	DIGITAL SIGNAL PROCESSING	PP	100	40	57	P
09.	DIGITAL SIGNAL PROCESSING	OR	50	20	30	P
10.	ELECTRONIC DESIGN PRACTICE	OR	50	20	27	P

FIRST TERM TOTAL = 415/750.
ORDN. 1 MARKS :

T8053085 POL AMIT KISHOR DAYA , 71045575F , , PICT , T8053085

01.	CONTROL SYSTEMS	PP	100	40	49	P
02.	DIGITAL COMMUNICATION	PP	100	40	52	P
03.	DIGITAL COMMUNICATION	PR	50	20	34	P
04.	NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	55	P
05.	NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	42	P
06.	MICROCONTROLLERS & APPLICATION	PP	100	40	48	P
07.	MICROCONTROLLERS & APPLICATION	PR	50	20	32	P
08.	DIGITAL SIGNAL PROCESSING	PP	100	40	56	P
09.	DIGITAL SIGNAL PROCESSING	OR	50	20	32	P
10.	ELECTRONIC DESIGN PRACTICE	OR	50	20	22	P

FIRST TERM TOTAL = 422/750.
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8053086 PRABHUDESAI PRATHAMESH PRAKASH ADITI , 71045579J , , PICT , T8053086

01.	CONTROL SYSTEMS	PP	100	40	60	P
02.	DIGITAL COMMUNICATION	PP	100	40	49	P
03.	DIGITAL COMMUNICATION	PR	50	20	40	P
04.	NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	53	P
05.	NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	37	P
06.	MICROCONTROLLERS & APPLICATION	PP	100	40	40	P
07.	MICROCONTROLLERS & APPLICATION	PR	50	20	44	P
08.	DIGITAL SIGNAL PROCESSING	PP	100	40	64	P
09.	DIGITAL SIGNAL PROCESSING	OR	50	20	38	P
10.	ELECTRONIC DESIGN PRACTICE	OR	50	20	30	P

FIRST TERM TOTAL = 455/750.
ORDN. 1 MARKS :

T8053087 PRIYANKA TOMAR SHASHI , 71045583G , , PICT , T8053087

01.	CONTROL SYSTEMS	PP	100	40	53	P
02.	DIGITAL COMMUNICATION	PP	100	40	56	P
03.	DIGITAL COMMUNICATION	PR	50	20	07	F
04.	NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	55	P
05.	NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	31	P
06.	MICROCONTROLLERS & APPLICATION	PP	100	40	49	P
07.	MICROCONTROLLERS & APPLICATION	PR	50	20	34	P
08.	DIGITAL SIGNAL PROCESSING	PP	100	40	57	P
09.	DIGITAL SIGNAL PROCESSING	OR	50	20	28	P
10.	ELECTRONIC DESIGN PRACTICE	OR	50	20	22	P

FIRST TERM TOTAL = 392/750.
ORDN. 1 MARKS :

T8053088 ROOPAL OBEROI AMARJEET , 71045591H , , PICT , T8053088

01.	CONTROL SYSTEMS	PP	100	40	48	P
02.	DIGITAL COMMUNICATION	PP	100	40	41	P
03.	DIGITAL COMMUNICATION	PR	50	20	34	P
04.	NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	49	P
05.	NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	35	P
06.	MICROCONTROLLERS & APPLICATION	PP	100	40	40	P
07.	MICROCONTROLLERS & APPLICATION	PR	50	20	27	P
08.	DIGITAL SIGNAL PROCESSING	PP	100	40	50	P
09.	DIGITAL SIGNAL PROCESSING	OR	50	20	35	P
10.	ELECTRONIC DESIGN PRACTICE	OR	50	20	30	P

FIRST TERM TOTAL = 389/750.
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8053089 SAURABH SANJAY MANE PALLAVI , 71045601J , , PICT , T8053089

01.	CONTROL SYSTEMS	PP	100	40	54	P
02.	DIGITAL COMMUNICATION	PP	100	40	56	P
03.	DIGITAL COMMUNICATION	PR	50	20	32	P
04.	NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	53	P
05.	NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	33	P
06.	MICROCONTROLLERS & APPLICATION	PP	100	40	40	P
07.	MICROCONTROLLERS & APPLICATION	PR	50	20	30	P
08.	DIGITAL SIGNAL PROCESSING	PP	100	40	57	P
09.	DIGITAL SIGNAL PROCESSING	OR	50	20	38	P
10.	ELECTRONIC DESIGN PRACTICE	OR	50	20	27	P

FIRST TERM TOTAL = 420/750.
ORDN. 1 MARKS :

T8053090 SAYED SAIF IFTEKAR SHAKILA , 71045604C , , PICT , T8053090

01.	CONTROL SYSTEMS	PP	100	40	20	F
02.	DIGITAL COMMUNICATION	PP	100	40	31	F
03.	DIGITAL COMMUNICATION	PR	50	20	07	F
04.	NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	40	P
05.	NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	27	P
06.	MICROCONTROLLERS & APPLICATION	PP	100	40	40	P
07.	MICROCONTROLLERS & APPLICATION	PR	50	20	30	P
08.	DIGITAL SIGNAL PROCESSING	PP	100	40	48	P
09.	DIGITAL SIGNAL PROCESSING	OR	50	20	38	P
10.	ELECTRONIC DESIGN PRACTICE	OR	50	20	39	P

FIRST TERM TOTAL = 320/750.
ORDN. 1 MARKS :

T8053091 SHAIKH SHOAIB IQBAL REHANA , 71129937E , , PICT , T8053091

01.	CONTROL SYSTEMS	PP	100	40	52	P
02.	DIGITAL COMMUNICATION	PP	100	40	46	P
03.	DIGITAL COMMUNICATION	PR	50	20	35	P
04.	NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	46	P
05.	NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	41	P
06.	MICROCONTROLLERS & APPLICATION	PP	100	40	40	P
07.	MICROCONTROLLERS & APPLICATION	PR	50	20	42	P
08.	DIGITAL SIGNAL PROCESSING	PP	100	40	64	P
09.	DIGITAL SIGNAL PROCESSING	OR	50	20	37	P
10.	ELECTRONIC DESIGN PRACTICE	OR	50	20	40	P

FIRST TERM TOTAL = 443/750.
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8053092 SHELKE MINAXI BHARAT PUTULA , 71129938C , , PICT , T8053092

01.	CONTROL SYSTEMS	PP	100	40	54	P
02.	DIGITAL COMMUNICATION	PP	100	40	44	P
03.	DIGITAL COMMUNICATION	PR	50	20	20	P
04.	NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	51	P
05.	NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	44	P
06.	MICROCONTROLLERS & APPLICATION	PP	100	40	46	P
07.	MICROCONTROLLERS & APPLICATION	PR	50	20	36	P
08.	DIGITAL SIGNAL PROCESSING	PP	100	40	51	P
09.	DIGITAL SIGNAL PROCESSING	OR	50	20	26	P
10.	ELECTRONIC DESIGN PRACTICE	OR	50	20	33	P

FIRST TERM TOTAL = 405/750.
ORDN. 1 MARKS :

T8053093 SHENDKAR SUSHMA HANUMANT REKHA , 71129939M , , PICT , T8053093

01.	CONTROL SYSTEMS	PP	100	40	47	P
02.	DIGITAL COMMUNICATION	PP	100	40	31	F
03.	DIGITAL COMMUNICATION	PR	50	20	24	P
04.	NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	40	P
05.	NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	45	P
06.	MICROCONTROLLERS & APPLICATION	PP	100	40	43	P
07.	MICROCONTROLLERS & APPLICATION	PR	50	20	30	P
08.	DIGITAL SIGNAL PROCESSING	PP	100	40	57	P
09.	DIGITAL SIGNAL PROCESSING	OR	50	20	30	P
10.	ELECTRONIC DESIGN PRACTICE	OR	50	20	27	P

FIRST TERM TOTAL = 374/750.
ORDN. 1 MARKS :

T8053094 SHINDE MANOJ SURESH MANJUSHRI , 71045615J , , PICT , T8053094

01.	CONTROL SYSTEMS	PP	100	40	59	P
02.	DIGITAL COMMUNICATION	PP	100	40	43	P
03.	DIGITAL COMMUNICATION	PR	50	20	39	P
04.	NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	50	P
05.	NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	35	P
06.	MICROCONTROLLERS & APPLICATION	PP	100	40	42	P
07.	MICROCONTROLLERS & APPLICATION	PR	50	20	35	P
08.	DIGITAL SIGNAL PROCESSING	PP	100	40	75	P
09.	DIGITAL SIGNAL PROCESSING	OR	50	20	39	P
10.	ELECTRONIC DESIGN PRACTICE	OR	50	20	30	P

FIRST TERM TOTAL = 447/750.
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8053095 SHITOLE JAYESH DASHRATH SULOCHANA , 71129940E , , PICT , T8053095

01.	CONTROL SYSTEMS	PP	100	40	46	P
02.	DIGITAL COMMUNICATION	PP	100	40	40	P
03.	DIGITAL COMMUNICATION	PR	50	20	26	P
04.	NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	42	P
05.	NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	41	P
06.	MICROCONTROLLERS & APPLICATION	PP	100	40	40	P
07.	MICROCONTROLLERS & APPLICATION	PR	50	20	29	P
08.	DIGITAL SIGNAL PROCESSING	PP	100	40	43	P
09.	DIGITAL SIGNAL PROCESSING	OR	50	20	25	P
10.	ELECTRONIC DESIGN PRACTICE	OR	50	20	37	P

FIRST TERM TOTAL = 369/750.
ORDN. 1 MARKS :

T8053096 SIDDHARTHA BANGA SUMEDHA RANI , 71045625F , , PICT , T8053096

01.	CONTROL SYSTEMS	PP	100	40	40	P
02.	DIGITAL COMMUNICATION	PP	100	40	31	F
03.	DIGITAL COMMUNICATION	PR	50	20	26	P
04.	NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	28	F
05.	NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	30	P
06.	MICROCONTROLLERS & APPLICATION	PP	100	40	28	F
07.	MICROCONTROLLERS & APPLICATION	PR	50	20	35	P
08.	DIGITAL SIGNAL PROCESSING	PP	100	40	40	P
09.	DIGITAL SIGNAL PROCESSING	OR	50	20	06	F
10.	ELECTRONIC DESIGN PRACTICE	OR	50	20	21	P

FIRST TERM TOTAL = 285/750.
ORDN. 1 MARKS :

T8053098 SUBAGHYA MAHAJAN VIJAY , 71045635C , , PICT , T8053098

01.	CONTROL SYSTEMS	PP	100	40	45	P
02.	DIGITAL COMMUNICATION	PP	100	40	20	F
03.	DIGITAL COMMUNICATION	PR	50	20	30	P
04.	NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	40	P
05.	NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	26	P
06.	MICROCONTROLLERS & APPLICATION	PP	100	40	31	F
07.	MICROCONTROLLERS & APPLICATION	PR	50	20	32	P
08.	DIGITAL SIGNAL PROCESSING	PP	100	40	49	P
09.	DIGITAL SIGNAL PROCESSING	OR	50	20	33	P
10.	ELECTRONIC DESIGN PRACTICE	OR	50	20	22	P

FIRST TERM TOTAL = 328/750.
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8053099 SURVE SMIT BALASAHEB PUSHPA , 71045637K , , PICT , T8053099

01. CONTROL SYSTEMS	PP	100	40	50	P
02. DIGITAL COMMUNICATION	PP	100	40	47	P
03. DIGITAL COMMUNICATION	PR	50	20	38	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	47	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	35	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	40	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	38	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	47	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	33	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	21	P

FIRST TERM TOTAL = 396/750.
ORDN. 1 MARKS :

T8053100 TELI REKHA MALLAPPA KASTURI , 71045643D , , PICT , T8053100

01. CONTROL SYSTEMS	PP	100	40	46	P
02. DIGITAL COMMUNICATION	PP	100	40	54	P
03. DIGITAL COMMUNICATION	PR	50	20	34	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	48	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	39	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	40	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	35	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	53	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	27	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	37	P

FIRST TERM TOTAL = 413/750.
ORDN. 1 MARKS :

T8053101 TEMGIRE PRASAD SANTU TAI , 71045644B , , PICT , T8053101

01. CONTROL SYSTEMS	PP	100	40	56	P
02. DIGITAL COMMUNICATION	PP	100	40	42	P
03. DIGITAL COMMUNICATION	PR	50	20	43	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	60	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	46	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	48	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	38	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	60	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	44	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	34	P

FIRST TERM TOTAL = 471/750.
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8053102 THORAT TANVI TATYASAHEB SULOCHANA , 71045650G , , PICT , T8053102

01.	CONTROL SYSTEMS	PP	100	40	40	P
02.	DIGITAL COMMUNICATION	PP	100	40	45	P
03.	DIGITAL COMMUNICATION	PR	50	20	39	P
04.	NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	45	P
05.	NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	37	P
06.	MICROCONTROLLERS & APPLICATION	PP	100	40	40	P
07.	MICROCONTROLLERS & APPLICATION	PR	50	20	23	P
08.	DIGITAL SIGNAL PROCESSING	PP	100	40	45	P
09.	DIGITAL SIGNAL PROCESSING	OR	50	20	38	P
10.	ELECTRONIC DESIGN PRACTICE	OR	50	20	32	P

FIRST TERM TOTAL = 384/750.
ORDN. 1 MARKS :

T8053103 THOSAR TRUSHA MILIND SUNANDA , 70801652K , , PICT , T8053103

01.	CONTROL SYSTEMS	PP	100	40	29	F
02.	DIGITAL COMMUNICATION	PP	100	40	28	F
03.	DIGITAL COMMUNICATION	PR	50	20	06	F
04.	NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	25	F
05.	NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	26	P
06.	MICROCONTROLLERS & APPLICATION	PP	100	40	31	F
07.	MICROCONTROLLERS & APPLICATION	PR	50	20	32	P
08.	DIGITAL SIGNAL PROCESSING	PP	100	40	22	F
09.	DIGITAL SIGNAL PROCESSING	OR	50	20	27	P
10.	ELECTRONIC DESIGN PRACTICE	OR	50	20	06	F

FIRST TERM TOTAL = 232/750.
ORDN. 1 MARKS :

T8053104 VYAS HARIKESH PRADEEP BHAVANA , 71045661B , , PICT , T8053104

01.	CONTROL SYSTEMS	PP	100	40	52	P
02.	DIGITAL COMMUNICATION	PP	100	40	44	P
03.	DIGITAL COMMUNICATION	PR	50	20	34	P
04.	NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	47	P
05.	NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	34	P
06.	MICROCONTROLLERS & APPLICATION	PP	100	40	43	P
07.	MICROCONTROLLERS & APPLICATION	PR	50	20	28	P
08.	DIGITAL SIGNAL PROCESSING	PP	100	40	46	P
09.	DIGITAL SIGNAL PROCESSING	OR	50	20	34	P
10.	ELECTRONIC DESIGN PRACTICE	OR	50	20	25	P

FIRST TERM TOTAL = 387/750.
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8053105 WAGHMARE ABHISHEK SUBHASH VIDYA , 71045662L , , PICT , T8053105

01.	CONTROL SYSTEMS	PP	100	40	41	P
02.	DIGITAL COMMUNICATION	PP	100	40	40	P
03.	DIGITAL COMMUNICATION	PR	50	20	36	P
04.	NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	40	P
05.	NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	42	P
06.	MICROCONTROLLERS & APPLICATION	PP	100	40	30	F
07.	MICROCONTROLLERS & APPLICATION	PR	50	20	32	P
08.	DIGITAL SIGNAL PROCESSING	PP	100	40	47	P
09.	DIGITAL SIGNAL PROCESSING	OR	50	20	35	P
10.	ELECTRONIC DESIGN PRACTICE	OR	50	20	20	P

FIRST TERM TOTAL = 363/750.
ORDN. 1 MARKS :

T8053106 WATHORE SANKET UTTAMRAO KAVITA , 71045665E , , PICT , T8053106

01.	CONTROL SYSTEMS	PP	100	40	30	F
02.	DIGITAL COMMUNICATION	PP	100	40	23	F
03.	DIGITAL COMMUNICATION	PR	50	20	05	F
04.	NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	47	P
05.	NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	32	P
06.	MICROCONTROLLERS & APPLICATION	PP	100	40	41	P
07.	MICROCONTROLLERS & APPLICATION	PR	50	20	10	F
08.	DIGITAL SIGNAL PROCESSING	PP	100	40	30	F
09.	DIGITAL SIGNAL PROCESSING	OR	50	20	10	F
10.	ELECTRONIC DESIGN PRACTICE	OR	50	20	25	P

FIRST TERM TOTAL = 253/750.
ORDN. 1 MARKS :

T8053107 YADAV VIKAS PRABHURAO SUREKHA , 71045667M , , PICT , T8053107

01.	CONTROL SYSTEMS	PP	100	40	56	P
02.	DIGITAL COMMUNICATION	PP	100	40	41	P
03.	DIGITAL COMMUNICATION	PR	50	20	42	P
04.	NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	63	P
05.	NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	28	P
06.	MICROCONTROLLERS & APPLICATION	PP	100	40	47	P
07.	MICROCONTROLLERS & APPLICATION	PR	50	20	33	P
08.	DIGITAL SIGNAL PROCESSING	PP	100	40	40	P
09.	DIGITAL SIGNAL PROCESSING	OR	50	20	38	P
10.	ELECTRONIC DESIGN PRACTICE	OR	50	20	22	P

FIRST TERM TOTAL = 410/750.
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8053108 YARDI NIRANJAN DHANANJAY KRANTI , 71045668K , , PICT , T8053108

01. CONTROL SYSTEMS	PP	100	40	52	P	
02. DIGITAL COMMUNICATION	PP	100	40	40	P	
03. DIGITAL COMMUNICATION	PR	50	20	34	P	
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	67	P	
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	32	P	
06. MICROCONTROLLERS & APPLICATION	PP	100	40	58	P	
07. MICROCONTROLLERS & APPLICATION	PR	50	20	32	P	
08. DIGITAL SIGNAL PROCESSING	PP	100	40	62	P	
09. DIGITAL SIGNAL PROCESSING	OR	50	20	37	P	
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	38	P	

FIRST TERM TOTAL = 452/750.
ORDN. 1 MARKS :

T8053110 AMIT KUMAR BHASKAR VEENA KUMARI , 70801330K , T8053003 , PICT , T8053110

01. CONTROL SYSTEMS	PP	100	40	47	P	C	11. SIGNAL CODING & ESTIMATION THEORY	PP	100	40	46	P C
02. DIGITAL COMMUNICATION	PP	100	40	20	F		12. SIGNAL CODING & ESTIMATION THEORY	PR	50	20	25	P C
03. DIGITAL COMMUNICATION	PR	50	20	21	P	C	13. SYSTEM PROGRA.& OPERATING SYS.	PP	100	40	41	P C
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	40	P	C	14. SYSTEM PROGRA.& OPERATING SYS.	TW	50	20	39	P C
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	35	P	C	15. COMPUTER ORGANIZATION & ARCHITEC	PP	100	40	49	P C
06. MICROCONTROLLERS & APPLICATION	PP	100	40	40	P	C	16. INDUSTRIAL MANAGEMENT	PP	100	40	52	P C
07. MICROCONTROLLERS & APPLICATION	PR	50	20	29	P	C	17. WAVE THEORY & ANTENNA	PP	100	40	53	P C
08. DIGITAL SIGNAL PROCESSING	PP	100	40	41	P	C	18. WAVE THEORY & ANTENNA	PR	50	20	37	P C
09. DIGITAL SIGNAL PROCESSING	OR	50	20	24	P	C	19. MINI PROJECT & SEMINAR	OR	50	20	40	P C
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	24	P	C	20. TEST & MEASUREMENT TECHNIQUES	OR	50	20	35	P C

GRAND TOTAL = 738/1500, RESULT: FAILS A.T.K.T.
ORDN. 1 MARKS :

T8053111 ARPITA CHAKRABORTY RITA , 70925339H , T8053006 , PICT , T8053111

01. CONTROL SYSTEMS	PP	100	40	68	P	C	11. SIGNAL CODING & ESTIMATION THEORY	PP	100	40	52	P C
02. DIGITAL COMMUNICATION	PP	100	40	59	P	C	12. SIGNAL CODING & ESTIMATION THEORY	PR	50	20	24	P C
03. DIGITAL COMMUNICATION	PR	50	20	37	P	C	13. SYSTEM PROGRA.& OPERATING SYS.	PP	100	40	72	P C
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	54	P	C	14. SYSTEM PROGRA.& OPERATING SYS.	TW	50	20	46	P C
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	41	P	C	15. COMPUTER ORGANIZATION & ARCHITEC	PP	100	40	56	P C
06. MICROCONTROLLERS & APPLICATION	PP	100	40	59	P	C	16. INDUSTRIAL MANAGEMENT	PP	100	40	54	P C
07. MICROCONTROLLERS & APPLICATION	PR	50	20	38	P	C	17. WAVE THEORY & ANTENNA	PP	100	40	55	P C
08. DIGITAL SIGNAL PROCESSING	PP	100	40	48	P	C	18. WAVE THEORY & ANTENNA	PR	50	20	39	P C
09. DIGITAL SIGNAL PROCESSING	OR	50	20	33	P	C	19. MINI PROJECT & SEMINAR	OR	50	20	32	P C
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	29	P	C	20. TEST & MEASUREMENT TECHNIQUES	OR	50	20	39	P

GRAND TOTAL = 935/1500, RESULT: FIRST CLASS
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8053112 AVIJEET DUBEY MEERA , 70801347D , T8053009 , PICT , T8053112

01.	CONTROL SYSTEMS	PP	100	40	49	P C	11.	SIGNAL CODING & ESTIMATION THEORY	PP	100	40	40	P C
02.	DIGITAL COMMUNICATION	PP	100	40	23	F	12.	SIGNAL CODING & ESTIMATION THEORY	PR	50	20	20	P
03.	DIGITAL COMMUNICATION	PR	50	20	21	P C	13.	SYSTEM PROGRA.& OPERATING SYS.	PP	100	40	43	P C
04.	NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	48	P C	14.	SYSTEM PROGRA.& OPERATING SYS.	TW	50	20	36	P C
05.	NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	20	P C	15.	COMPUTER ORGANIZATION & ARCHITEC	PP	100	40	02	F
06.	MICROCONTROLLERS & APPLICATION	PP	100	40	AA	F	16.	INDUSTRIAL MANAGEMENT	PP	100	40	41	P C
07.	MICROCONTROLLERS & APPLICATION	PR	50	20	22	P C	17.	WAVE THEORY & ANTENNA	PP	100	40	40	P C
08.	DIGITAL SIGNAL PROCESSING	PP	100	40	40	P	18.	WAVE THEORY & ANTENNA	PR	50	20	34	P C
09.	DIGITAL SIGNAL PROCESSING	OR	50	20	24	P C	19.	MINI PROJECT & SEMINAR	OR	50	20	29	P C
10.	ELECTRONIC DESIGN PRACTICE	OR	50	20	31	P C	20.	TEST & MEASUREMENT TECHNIQUES	OR	50	20	30	P C

GRAND TOTAL = 593/1500, RESULT: FAILS A.T.K.T.
ORDN. 1 MARKS :

T8053113 BANKAR DHANESH BAPURAO SINDHU , 71070194C , T8053010 , PICT , T8053113

01.	CONTROL SYSTEMS	PP	100	40	75	P C	11.	SIGNAL CODING & ESTIMATION THEORY	PP	100	40	46	P C
02.	DIGITAL COMMUNICATION	PP	100	40	63	P C	12.	SIGNAL CODING & ESTIMATION THEORY	PR	50	20	22	P C
03.	DIGITAL COMMUNICATION	PR	50	20	38	P C	13.	SYSTEM PROGRA.& OPERATING SYS.	PP	100	40	74	P C
04.	NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	78	P C	14.	SYSTEM PROGRA.& OPERATING SYS.	TW	50	20	43	P C
05.	NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	45	P C	15.	COMPUTER ORGANIZATION & ARCHITEC	PP	100	40	62	P C
06.	MICROCONTROLLERS & APPLICATION	PP	100	40	88	P C	16.	INDUSTRIAL MANAGEMENT	PP	100	40	64	P C
07.	MICROCONTROLLERS & APPLICATION	PR	50	20	35	P C	17.	WAVE THEORY & ANTENNA	PP	100	40	61	P C
08.	DIGITAL SIGNAL PROCESSING	PP	100	40	47	P C	18.	WAVE THEORY & ANTENNA	PR	50	20	36	P C
09.	DIGITAL SIGNAL PROCESSING	OR	50	20	35	P C	19.	MINI PROJECT & SEMINAR	OR	50	20	32	P C
10.	ELECTRONIC DESIGN PRACTICE	OR	50	20	29	P C	20.	TEST & MEASUREMENT TECHNIQUES	OR	50	20	38	P

GRAND TOTAL = 1011/1500, RESULT: FIRST CLASS WITH DISTINCTION
ORDN. 1 MARKS :

T8053114 BHATIA DHAWAL RAMESHCHAND REENA , 70801366L , T8053014 , PICT , T8053114

01.	CONTROL SYSTEMS	PP	100	40	49	P	11.	SIGNAL CODING & ESTIMATION THEORY	PP	100	40	27	F
02.	DIGITAL COMMUNICATION	PP	100	40	18	F	12.	SIGNAL CODING & ESTIMATION THEORY	PR	50	20	22	P
03.	DIGITAL COMMUNICATION	PR	50	20	40	P C	13.	SYSTEM PROGRA.& OPERATING SYS.	PP	100	40	49	P C
04.	NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	40	P C	14.	SYSTEM PROGRA.& OPERATING SYS.	TW	50	20	38	P C
05.	NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	39	P C	15.	COMPUTER ORGANIZATION & ARCHITEC	PP	100	40	40	P C
06.	MICROCONTROLLERS & APPLICATION	PP	100	40	40	P C	16.	INDUSTRIAL MANAGEMENT	PP	100	40	44	P C
07.	MICROCONTROLLERS & APPLICATION	PR	50	20	35	P C	17.	WAVE THEORY & ANTENNA	PP	100	40	10	F
08.	DIGITAL SIGNAL PROCESSING	PP	100	40	44	P C	18.	WAVE THEORY & ANTENNA	PR	50	20	38	P C
09.	DIGITAL SIGNAL PROCESSING	OR	50	20	22	P C	19.	MINI PROJECT & SEMINAR	OR	50	20	39	P C
10.	ELECTRONIC DESIGN PRACTICE	OR	50	20	25	P C	20.	TEST & MEASUREMENT TECHNIQUES	OR	50	20	39	P C

GRAND TOTAL = 698/1500, RESULT: FAILS A.T.K.T.
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8053115 BHOIR PRASAD SHIVAJI USHA , 70925368M , T8053016 , PICT , T8053115

01.	CONTROL SYSTEMS	PP	100	40	40	P	11.	SIGNAL CODING & ESTIMATION THEORY	PP	100	40	40	P
02.	DIGITAL COMMUNICATION	PP	100	40	40	P	12.	SIGNAL CODING & ESTIMATION THEORY	PR	50	20	30	P
03.	DIGITAL COMMUNICATION	PR	50	20	23	P C	13.	SYSTEM PROGRA.& OPERATING SYS.	PP	100	40	40	P C
04.	NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	40	P C	14.	SYSTEM PROGRA.& OPERATING SYS.	TW	50	20	34	P C
05.	NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	38	P C	15.	COMPUTER ORGANIZATION & ARCHITEC	PP	100	40	09	F
06.	MICROCONTROLLERS & APPLICATION	PP	100	40	43	P C	16.	INDUSTRIAL MANAGEMENT	PP	100	40	40	P C
07.	MICROCONTROLLERS & APPLICATION	PR	50	20	20	P C	17.	WAVE THEORY & ANTENNA	PP	100	40	10	F
08.	DIGITAL SIGNAL PROCESSING	PP	100	40	40	P C	18.	WAVE THEORY & ANTENNA	PR	50	20	35	P C
09.	DIGITAL SIGNAL PROCESSING	OR	50	20	26	P C	19.	MINI PROJECT & SEMINAR	OR	50	20	32	P C
10.	ELECTRONIC DESIGN PRACTICE	OR	50	20	21	P C	20.	TEST & MEASUREMENT TECHNIQUES	OR	50	20	28	P

GRAND TOTAL = 629/1500, RESULT: FAILS A.T.K.T.
ORDN. 1 MARKS :

T8053116 BORAWAKE MANOJ VILAS NIRMALA , 70925372K , T8053019 , PICT , T8053116

01.	CONTROL SYSTEMS	PP	100	40	75	P C	11.	SIGNAL CODING & ESTIMATION THEORY	PP	100	40	44	P C
02.	DIGITAL COMMUNICATION	PP	100	40	42	P C	12.	SIGNAL CODING & ESTIMATION THEORY	PR	50	20	42	P
03.	DIGITAL COMMUNICATION	PR	50	20	35	P C	13.	SYSTEM PROGRA.& OPERATING SYS.	PP	100	40	62	P C
04.	NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	46	P C	14.	SYSTEM PROGRA.& OPERATING SYS.	TW	50	20	40	P C
05.	NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	42	P C	15.	COMPUTER ORGANIZATION & ARCHITEC	PP	100	40	40	P C
06.	MICROCONTROLLERS & APPLICATION	PP	100	40	45	P C	16.	INDUSTRIAL MANAGEMENT	PP	100	40	45	P C
07.	MICROCONTROLLERS & APPLICATION	PR	50	20	30	P C	17.	WAVE THEORY & ANTENNA	PP	100	40	54	P C
08.	DIGITAL SIGNAL PROCESSING	PP	100	40	57	P C	18.	WAVE THEORY & ANTENNA	PR	50	20	42	P C
09.	DIGITAL SIGNAL PROCESSING	OR	50	20	34	P C	19.	MINI PROJECT & SEMINAR	OR	50	20	33	P C
10.	ELECTRONIC DESIGN PRACTICE	OR	50	20	26	P C	20.	TEST & MEASUREMENT TECHNIQUES	OR	50	20	40	P C

GRAND TOTAL = 874/1500, RESULT: HIGHER SECOND CLASS
ORDN. 1 MARKS :

T8053117 GANGARDE PALLAVI POPATRAO MANDAKINI , 70925423H , T8053037 , PICT , T8053117

01.	CONTROL SYSTEMS	PP	100	40	42	P C	11.	SIGNAL CODING & ESTIMATION THEORY	PP	100	40	40	P
02.	DIGITAL COMMUNICATION	PP	100	40	26	F	12.	SIGNAL CODING & ESTIMATION THEORY	PR	50	20	22	P C
03.	DIGITAL COMMUNICATION	PR	50	20	22	P C	13.	SYSTEM PROGRA.& OPERATING SYS.	PP	100	40	40	P C
04.	NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	45	P C	14.	SYSTEM PROGRA.& OPERATING SYS.	TW	50	20	42	P C
05.	NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	40	P C	15.	COMPUTER ORGANIZATION & ARCHITEC	PP	100	40	43	P C
06.	MICROCONTROLLERS & APPLICATION	PP	100	40	40	P C	16.	INDUSTRIAL MANAGEMENT	PP	100	40	41	P C
07.	MICROCONTROLLERS & APPLICATION	PR	50	20	30	P C	17.	WAVE THEORY & ANTENNA	PP	100	40	40	P C
08.	DIGITAL SIGNAL PROCESSING	PP	100	40	45	P C	18.	WAVE THEORY & ANTENNA	PR	50	20	39	P C
09.	DIGITAL SIGNAL PROCESSING	OR	50	20	32	P C	19.	MINI PROJECT & SEMINAR	OR	50	20	42	P C
10.	ELECTRONIC DESIGN PRACTICE	OR	50	20	22	P C	20.	TEST & MEASUREMENT TECHNIQUES	OR	50	20	32	P C

GRAND TOTAL = 725/1500, RESULT: FAILS A.T.K.T.
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8053118 JAGTAP SAUDAMINI DHANANJAY PRERANA , 71070199D , T8053045 , PICT , T8053118

01.	CONTROL SYSTEMS	PP	100	40	43	P C	11.	SIGNAL CODING & ESTIMATION THEORY	PP	100	40	40	P C
02.	DIGITAL COMMUNICATION	PP	100	40	52	P C	12.	SIGNAL CODING & ESTIMATION THEORY	PR	50	20	38	P
03.	DIGITAL COMMUNICATION	PR	50	20	38	P C	13.	SYSTEM PROGRA.& OPERATING SYS.	PP	100	40	40	P C
04.	NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	41	P C	14.	SYSTEM PROGRA.& OPERATING SYS.	TW	50	20	40	P C
05.	NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	41	P C	15.	COMPUTER ORGANIZATION & ARCHITEC	PP	100	40	51	P C
06.	MICROCONTROLLERS & APPLICATION	PP	100	40	48	P C	16.	INDUSTRIAL MANAGEMENT	PP	100	40	49	P C
07.	MICROCONTROLLERS & APPLICATION	PR	50	20	37	P C	17.	WAVE THEORY & ANTENNA	PP	100	40	53	P C
08.	DIGITAL SIGNAL PROCESSING	PP	100	40	48	P C	18.	WAVE THEORY & ANTENNA	PR	50	20	40	P C
09.	DIGITAL SIGNAL PROCESSING	OR	50	20	34	P C	19.	MINI PROJECT & SEMINAR	OR	50	20	42	P C
10.	ELECTRONIC DESIGN PRACTICE	OR	50	20	35	P C	20.	TEST & MEASUREMENT TECHNIQUES	OR	50	20	40	P C

GRAND TOTAL = 850/1500, RESULT: HIGHER SECOND CLASS
ORDN. 1 MARKS :

T8053119 JITESH MAHESH KUMAR ADNANI BHAVNA , 70925453K , T8053047 , PICT , T8053119

01.	CONTROL SYSTEMS	PP	100	40	53	P C	11.	SIGNAL CODING & ESTIMATION THEORY	PP	100	40	45	P C
02.	DIGITAL COMMUNICATION	PP	100	40	40	P C	12.	SIGNAL CODING & ESTIMATION THEORY	PR	50	20	20	P C
03.	DIGITAL COMMUNICATION	PR	50	20	37	P C	13.	SYSTEM PROGRA.& OPERATING SYS.	PP	100	40	43	P C
04.	NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	40	P C	14.	SYSTEM PROGRA.& OPERATING SYS.	TW	50	20	43	P C
05.	NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	38	P C	15.	COMPUTER ORGANIZATION & ARCHITEC	PP	100	40	45	P C
06.	MICROCONTROLLERS & APPLICATION	PP	100	40	47	P C	16.	INDUSTRIAL MANAGEMENT	PP	100	40	40	P C
07.	MICROCONTROLLERS & APPLICATION	PR	50	20	30	P C	17.	WAVE THEORY & ANTENNA	PP	100	40	40	P C
08.	DIGITAL SIGNAL PROCESSING	PP	100	40	50	P C	18.	WAVE THEORY & ANTENNA	PR	50	20	36	P C
09.	DIGITAL SIGNAL PROCESSING	OR	50	20	36	P C	19.	MINI PROJECT & SEMINAR	OR	50	20	43	P C
10.	ELECTRONIC DESIGN PRACTICE	OR	50	20	32	P C	20.	TEST & MEASUREMENT TECHNIQUES	OR	50	20	38	P

GRAND TOTAL = 796/1500, RESULT: SECOND CLASS
ORDN. 1 MARKS :

T8053121 MAUSKAR CHAITANYA UMESH SUNITA , 70925501C , T8053058 , PICT , T8053121

01.	CONTROL SYSTEMS	PP	100	40	55	P	11.	SIGNAL CODING & ESTIMATION THEORY	PP	100	40	68	P C
02.	DIGITAL COMMUNICATION	PP	100	40	48	P C	12.	SIGNAL CODING & ESTIMATION THEORY	PR	50	20	33	P C
03.	DIGITAL COMMUNICATION	PR	50	20	26	P C	13.	SYSTEM PROGRA.& OPERATING SYS.	PP	100	40	54	P C
04.	NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	46	P	14.	SYSTEM PROGRA.& OPERATING SYS.	TW	50	20	25	P C
05.	NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	20	P C	15.	COMPUTER ORGANIZATION & ARCHITEC	PP	100	40	55	P C
06.	MICROCONTROLLERS & APPLICATION	PP	100	40	61	P C	16.	INDUSTRIAL MANAGEMENT	PP	100	40	59	P C
07.	MICROCONTROLLERS & APPLICATION	PR	50	20	25	P C	17.	WAVE THEORY & ANTENNA	PP	100	40	58	P C
08.	DIGITAL SIGNAL PROCESSING	PP	100	40	46	P C	18.	WAVE THEORY & ANTENNA	PR	50	20	39	P C
09.	DIGITAL SIGNAL PROCESSING	OR	50	20	25	P C	19.	MINI PROJECT & SEMINAR	OR	50	20	33	P C
10.	ELECTRONIC DESIGN PRACTICE	OR	50	20	20	P C	20.	TEST & MEASUREMENT TECHNIQUES	OR	50	20	38	P C

GRAND TOTAL = 834/1500, RESULT: HIGHER SECOND CLASS
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8053122 MOHIT M. HARDIKAR ANJALI , 70925507B , T8053060 , PICT , T8053122

01.	CONTROL SYSTEMS	PP	100	40	50	P C	11.	SIGNAL CODING & ESTIMATION THEORY	PP	100	40	53	P C
02.	DIGITAL COMMUNICATION	PP	100	40	40	P C	12.	SIGNAL CODING & ESTIMATION THEORY	PR	50	20	34	P
03.	DIGITAL COMMUNICATION	PR	50	20	34	P C	13.	SYSTEM PROGRA.& OPERATING SYS.	PP	100	40	61	P C
04.	NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	40	P C	14.	SYSTEM PROGRA.& OPERATING SYS.	TW	50	20	20	P C
05.	NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	20	P C	15.	COMPUTER ORGANIZATION & ARCHITEC	PP	100	40	50	P C
06.	MICROCONTROLLERS & APPLICATION	PP	100	40	44	P C	16.	INDUSTRIAL MANAGEMENT	PP	100	40	71	P C
07.	MICROCONTROLLERS & APPLICATION	PR	50	20	22	P C	17.	WAVE THEORY & ANTENNA	PP	100	40	60	P C
08.	DIGITAL SIGNAL PROCESSING	PP	100	40	40	P C	18.	WAVE THEORY & ANTENNA	PR	50	20	25	P C
09.	DIGITAL SIGNAL PROCESSING	OR	50	20	25	P C	19.	MINI PROJECT & SEMINAR	OR	50	20	35	P C
10.	ELECTRONIC DESIGN PRACTICE	OR	50	20	24	P C	20.	TEST & MEASUREMENT TECHNIQUES	OR	50	20	32	P C

GRAND TOTAL = 780/1500, RESULT: SECOND CLASS
ORDN. 1 MARKS :

T8053123 MOTWANI NIKITA SUDAMA KRISHNA , 71070201K , T8053061 , PICT , T8053123

01.	CONTROL SYSTEMS	PP	100	40	63	P	11.	SIGNAL CODING & ESTIMATION THEORY	PP	100	40	46	P C
02.	DIGITAL COMMUNICATION	PP	100	40	40	P C	12.	SIGNAL CODING & ESTIMATION THEORY	PR	50	20	30	P C
03.	DIGITAL COMMUNICATION	PR	50	20	30	P C	13.	SYSTEM PROGRA.& OPERATING SYS.	PP	100	40	43	P C
04.	NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	AA	F	14.	SYSTEM PROGRA.& OPERATING SYS.	TW	50	20	40	P C
05.	NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	40	P C	15.	COMPUTER ORGANIZATION & ARCHITEC	PP	100	40	47	P C
06.	MICROCONTROLLERS & APPLICATION	PP	100	40	42	P C	16.	INDUSTRIAL MANAGEMENT	PP	100	40	62	P C
07.	MICROCONTROLLERS & APPLICATION	PR	50	20	26	P C	17.	WAVE THEORY & ANTENNA	PP	100	40	40	P C
08.	DIGITAL SIGNAL PROCESSING	PP	100	40	40	P C	18.	WAVE THEORY & ANTENNA	PR	50	20	32	P
09.	DIGITAL SIGNAL PROCESSING	OR	50	20	30	P C	19.	MINI PROJECT & SEMINAR	OR	50	20	36	P C
10.	ELECTRONIC DESIGN PRACTICE	OR	50	20	27	P C	20.	TEST & MEASUREMENT TECHNIQUES	OR	50	20	35	P C

GRAND TOTAL = 749/1500, RESULT: FAILS A.T.K.T.
ORDN. 1 MARKS :

T8053124 MULGE VAIJNATH SIDDANNA INDU , 70925514E , T8053063 , PICT , T8053124

01.	CONTROL SYSTEMS	PP	100	40	59	P C	11.	SIGNAL CODING & ESTIMATION THEORY	PP	100	40	60	P C
02.	DIGITAL COMMUNICATION	PP	100	40	52	P C	12.	SIGNAL CODING & ESTIMATION THEORY	PR	50	20	32	P C
03.	DIGITAL COMMUNICATION	PR	50	20	38	P C	13.	SYSTEM PROGRA.& OPERATING SYS.	PP	100	40	51	P C
04.	NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	70	P C	14.	SYSTEM PROGRA.& OPERATING SYS.	TW	50	20	41	P C
05.	NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	40	P C	15.	COMPUTER ORGANIZATION & ARCHITEC	PP	100	40	62	P C
06.	MICROCONTROLLERS & APPLICATION	PP	100	40	66	P C	16.	INDUSTRIAL MANAGEMENT	PP	100	40	67	P C
07.	MICROCONTROLLERS & APPLICATION	PR	50	20	30	P C	17.	WAVE THEORY & ANTENNA	PP	100	40	54	P C
08.	DIGITAL SIGNAL PROCESSING	PP	100	40	54	P C	18.	WAVE THEORY & ANTENNA	PR	50	20	35	P
09.	DIGITAL SIGNAL PROCESSING	OR	50	20	35	P C	19.	MINI PROJECT & SEMINAR	OR	50	20	41	P C
10.	ELECTRONIC DESIGN PRACTICE	OR	50	20	30	P C	20.	TEST & MEASUREMENT TECHNIQUES	OR	50	20	45	P C

GRAND TOTAL = 962/1500, RESULT: FIRST CLASS
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8053125 NITESH KR SINGH RITA , 70801532J , T8053065 , PICT , T8053125

01.	CONTROL SYSTEMS	PP	100	40	50	P	11.	SIGNAL CODING & ESTIMATION THEORY	PP	100	40	AA	F
02.	DIGITAL COMMUNICATION	PP	100	40	AA	F	12.	SIGNAL CODING & ESTIMATION THEORY	PR	50	20	AA	F
03.	DIGITAL COMMUNICATION	PR	50	20	23	P C	13.	SYSTEM PROGRA.& OPERATING SYS.	PP	100	40	AA	F
04.	NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	44	P C	14.	SYSTEM PROGRA.& OPERATING SYS.	TW	50	20	26	P C
05.	NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	22	P C	15.	COMPUTER ORGANIZATION & ARCHITEC	PP	100	40	46	P C
06.	MICROCONTROLLERS & APPLICATION	PP	100	40	44	P C	16.	INDUSTRIAL MANAGEMENT	PP	100	40	49	P C
07.	MICROCONTROLLERS & APPLICATION	PR	50	20	28	P C	17.	WAVE THEORY & ANTENNA	PP	100	40	AA	F
08.	DIGITAL SIGNAL PROCESSING	PP	100	40	AA	F	18.	WAVE THEORY & ANTENNA	PR	50	20	25	P C
09.	DIGITAL SIGNAL PROCESSING	OR	50	20	26	P C	19.	MINI PROJECT & SEMINAR	OR	50	20	AA	F
10.	ELECTRONIC DESIGN PRACTICE	OR	50	20	28	P C	20.	TEST & MEASUREMENT TECHNIQUES	OR	50	20	30	P C

GRAND TOTAL = 441/1500, RESULT: FAILS
ORDN. 1 MARKS :

T8053126 PATIL JAYA PANDHARINATH KAMAL , 70925548K , T8053071 , PICT , T8053126

01.	CONTROL SYSTEMS	PP	100	40	60	P C	11.	SIGNAL CODING & ESTIMATION THEORY	PP	100	40	49	P C
02.	DIGITAL COMMUNICATION	PP	100	40	40	P C	12.	SIGNAL CODING & ESTIMATION THEORY	PR	50	20	33	P C
03.	DIGITAL COMMUNICATION	PR	50	20	25	P C	13.	SYSTEM PROGRA.& OPERATING SYS.	PP	100	40	41	P C
04.	NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	63	P C	14.	SYSTEM PROGRA.& OPERATING SYS.	TW	50	20	41	P C
05.	NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	38	P C	15.	COMPUTER ORGANIZATION & ARCHITEC	PP	100	40	45	P C
06.	MICROCONTROLLERS & APPLICATION	PP	100	40	66	P C	16.	INDUSTRIAL MANAGEMENT	PP	100	40	50	P C
07.	MICROCONTROLLERS & APPLICATION	PR	50	20	27	P C	17.	WAVE THEORY & ANTENNA	PP	100	40	40	P C
08.	DIGITAL SIGNAL PROCESSING	PP	100	40	40	P C	18.	WAVE THEORY & ANTENNA	PR	50	20	36	P
09.	DIGITAL SIGNAL PROCESSING	OR	50	20	34	P C	19.	MINI PROJECT & SEMINAR	OR	50	20	41	P C
10.	ELECTRONIC DESIGN PRACTICE	OR	50	20	30	P C	20.	TEST & MEASUREMENT TECHNIQUES	OR	50	20	38	P C

GRAND TOTAL = 837/1500, RESULT: HIGHER SECOND CLASS
ORDN. 1 MARKS :

T8053127 PATIL SAYALI ANIL SUMATI , 70925552H , T8053073 , PICT , T8053127

01.	CONTROL SYSTEMS	PP	100	40	56	P C	11.	SIGNAL CODING & ESTIMATION THEORY	PP	100	40	70	P C
02.	DIGITAL COMMUNICATION	PP	100	40	58	P C	12.	SIGNAL CODING & ESTIMATION THEORY	PR	50	20	26	P C
03.	DIGITAL COMMUNICATION	PR	50	20	41	P C	13.	SYSTEM PROGRA.& OPERATING SYS.	PP	100	40	44	P C
04.	NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	80	P C	14.	SYSTEM PROGRA.& OPERATING SYS.	TW	50	20	40	P C
05.	NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	38	P C	15.	COMPUTER ORGANIZATION & ARCHITEC	PP	100	40	55	P C
06.	MICROCONTROLLERS & APPLICATION	PP	100	40	65	P C	16.	INDUSTRIAL MANAGEMENT	PP	100	40	59	P C
07.	MICROCONTROLLERS & APPLICATION	PR	50	20	27	P C	17.	WAVE THEORY & ANTENNA	PP	100	40	55	P C
08.	DIGITAL SIGNAL PROCESSING	PP	100	40	51	P C	18.	WAVE THEORY & ANTENNA	PR	50	20	35	P
09.	DIGITAL SIGNAL PROCESSING	OR	50	20	33	P C	19.	MINI PROJECT & SEMINAR	OR	50	20	41	P C
10.	ELECTRONIC DESIGN PRACTICE	OR	50	20	40	P C	20.	TEST & MEASUREMENT TECHNIQUES	OR	50	20	34	P C

GRAND TOTAL = 948/1500, RESULT: FIRST CLASS
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8053128 PRASHANT POPAT JAGTAP MUKTA , 70801568K , T8053078 , PICT , T8053128

01.	CONTROL SYSTEMS	PP	100	40	46	P C	11.	SIGNAL CODING & ESTIMATION THEORY	PP	100	40	40	P
02.	DIGITAL COMMUNICATION	PP	100	40	19	F	12.	SIGNAL CODING & ESTIMATION THEORY	PR	50	20	24	P
03.	DIGITAL COMMUNICATION	PR	50	20	22	P C	13.	SYSTEM PROGRA.& OPERATING SYS.	PP	100	40	40	P
04.	NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	47	P C	14.	SYSTEM PROGRA.& OPERATING SYS.	TW	50	20	26	P C
05.	NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	20	P C	15.	COMPUTER ORGANIZATION & ARCHITEC	PP	100	40	40	P C
06.	MICROCONTROLLERS & APPLICATION	PP	100	40	41	P C	16.	INDUSTRIAL MANAGEMENT	PP	100	40	51	P C
07.	MICROCONTROLLERS & APPLICATION	PR	50	20	25	P C	17.	WAVE THEORY & ANTENNA	PP	100	40	40	P
08.	DIGITAL SIGNAL PROCESSING	PP	100	40	25	F	18.	WAVE THEORY & ANTENNA	PR	50	20	23	P
09.	DIGITAL SIGNAL PROCESSING	OR	50	20	21	P C	19.	MINI PROJECT & SEMINAR	OR	50	20	AA	F
10.	ELECTRONIC DESIGN PRACTICE	OR	50	20	21	P	20.	TEST & MEASUREMENT TECHNIQUES	OR	50	20	30	P C

GRAND TOTAL = 601/1500, RESULT: FAILS A.T.K.T.
ORDN. 1 MARKS :

T8053129 SAKHALE ANJALI ANAND MADHURI , 71070206L , T8053084 , PICT , T8053129

01.	CONTROL SYSTEMS	PP	100	40	46	P C	11.	SIGNAL CODING & ESTIMATION THEORY	PP	100	40	40	P C
02.	DIGITAL COMMUNICATION	PP	100	40	40	P C	12.	SIGNAL CODING & ESTIMATION THEORY	PR	50	20	37	P
03.	DIGITAL COMMUNICATION	PR	50	20	34	P C	13.	SYSTEM PROGRA.& OPERATING SYS.	PP	100	40	43	P C
04.	NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	62	P C	14.	SYSTEM PROGRA.& OPERATING SYS.	TW	50	20	42	P C
05.	NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	42	P C	15.	COMPUTER ORGANIZATION & ARCHITEC	PP	100	40	54	P C
06.	MICROCONTROLLERS & APPLICATION	PP	100	40	50	P C	16.	INDUSTRIAL MANAGEMENT	PP	100	40	49	P C
07.	MICROCONTROLLERS & APPLICATION	PR	50	20	33	P C	17.	WAVE THEORY & ANTENNA	PP	100	40	40	P C
08.	DIGITAL SIGNAL PROCESSING	PP	100	40	43	P C	18.	WAVE THEORY & ANTENNA	PR	50	20	28	P C
09.	DIGITAL SIGNAL PROCESSING	OR	50	20	34	P C	19.	MINI PROJECT & SEMINAR	OR	50	20	32	P C
10.	ELECTRONIC DESIGN PRACTICE	OR	50	20	27	P C	20.	TEST & MEASUREMENT TECHNIQUES	OR	50	20	28	P C

GRAND TOTAL = 804/1500, RESULT: SECOND CLASS
ORDN. 1 MARKS :

T8053130 SHELAR SONALI SUBHASH SUSHMA , 70925599D , T8053088 , PICT , T8053130

01.	CONTROL SYSTEMS	PP	100	40	58	P C	11.	SIGNAL CODING & ESTIMATION THEORY	PP	100	40	42	P C
02.	DIGITAL COMMUNICATION	PP	100	40	40	P C	12.	SIGNAL CODING & ESTIMATION THEORY	PR	50	20	39	P
03.	DIGITAL COMMUNICATION	PR	50	20	36	P C	13.	SYSTEM PROGRA.& OPERATING SYS.	PP	100	40	44	P C
04.	NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	62	P C	14.	SYSTEM PROGRA.& OPERATING SYS.	TW	50	20	41	P C
05.	NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	39	P C	15.	COMPUTER ORGANIZATION & ARCHITEC	PP	100	40	44	P C
06.	MICROCONTROLLERS & APPLICATION	PP	100	40	53	P C	16.	INDUSTRIAL MANAGEMENT	PP	100	40	46	P C
07.	MICROCONTROLLERS & APPLICATION	PR	50	20	34	P C	17.	WAVE THEORY & ANTENNA	PP	100	40	40	P C
08.	DIGITAL SIGNAL PROCESSING	PP	100	40	40	P C	18.	WAVE THEORY & ANTENNA	PR	50	20	37	P C
09.	DIGITAL SIGNAL PROCESSING	OR	50	20	37	P C	19.	MINI PROJECT & SEMINAR	OR	50	20	32	P C
10.	ELECTRONIC DESIGN PRACTICE	OR	50	20	29	P C	20.	TEST & MEASUREMENT TECHNIQUES	OR	50	20	35	P C

GRAND TOTAL = 828/1500, RESULT: HIGHER SECOND CLASS
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8053131 SNEHAL SOHANLAL CHHAJED UJWALA , 70925612E , T8053092 , PICT , T8053131

01.	CONTROL SYSTEMS	PP	100	40	68	P C	11.	SIGNAL CODING & ESTIMATION THEORY	PP	100	40	59	P C
02.	DIGITAL COMMUNICATION	PP	100	40	59	P C	12.	SIGNAL CODING & ESTIMATION THEORY	PR	50	20	40	P C
03.	DIGITAL COMMUNICATION	PR	50	20	36	P C	13.	SYSTEM PROGRA.& OPERATING SYS.	PP	100	40	61	P C
04.	NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	86	P C	14.	SYSTEM PROGRA.& OPERATING SYS.	TW	50	20	43	P C
05.	NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	43	P C	15.	COMPUTER ORGANIZATION & ARCHITEC	PP	100	40	56	P C
06.	MICROCONTROLLERS & APPLICATION	PP	100	40	66	P C	16.	INDUSTRIAL MANAGEMENT	PP	100	40	58	P C
07.	MICROCONTROLLERS & APPLICATION	PR	50	20	32	P C	17.	WAVE THEORY & ANTENNA	PP	100	40	52	P C
08.	DIGITAL SIGNAL PROCESSING	PP	100	40	47	P C	18.	WAVE THEORY & ANTENNA	PR	50	20	30	P
09.	DIGITAL SIGNAL PROCESSING	OR	50	20	36	P C	19.	MINI PROJECT & SEMINAR	OR	50	20	35	P C
10.	ELECTRONIC DESIGN PRACTICE	OR	50	20	31	P C	20.	TEST & MEASUREMENT TECHNIQUES	OR	50	20	38	P C

GRAND TOTAL = 976/1500, RESULT: FIRST CLASS
ORDN. 1 MARKS :

T8053132 VATSLA KANIKA , 70801664C , T8053101 , PICT , T8053132

01.	CONTROL SYSTEMS	PP	100	40	40	P C	11.	SIGNAL CODING & ESTIMATION THEORY	PP	100	40	50	P C
02.	DIGITAL COMMUNICATION	PP	100	40	40	P C	12.	SIGNAL CODING & ESTIMATION THEORY	PR	50	20	34	P C
03.	DIGITAL COMMUNICATION	PR	50	20	25	P C	13.	SYSTEM PROGRA.& OPERATING SYS.	PP	100	40	41	P C
04.	NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	43	P C	14.	SYSTEM PROGRA.& OPERATING SYS.	TW	50	20	38	P C
05.	NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	37	P C	15.	COMPUTER ORGANIZATION & ARCHITEC	PP	100	40	43	P C
06.	MICROCONTROLLERS & APPLICATION	PP	100	40	55	P C	16.	INDUSTRIAL MANAGEMENT	PP	100	40	52	P C
07.	MICROCONTROLLERS & APPLICATION	PR	50	20	26	P C	17.	WAVE THEORY & ANTENNA	PP	100	40	46	P C
08.	DIGITAL SIGNAL PROCESSING	PP	100	40	31*	P	18.	WAVE THEORY & ANTENNA	PR	50	20	23	P C
09.	DIGITAL SIGNAL PROCESSING	OR	50	20	25	P C	19.	MINI PROJECT & SEMINAR	OR	50	20	38	P C
10.	ELECTRONIC DESIGN PRACTICE	OR	50	20	21	P C	20.	TEST & MEASUREMENT TECHNIQUES	OR	50	20	30	P

GRAND TOTAL = 738/1500, RESULT: PASS CLASS * [0.4]
ORDN. 1 MARKS :

T8053133 VILHEKAR PRANIT GANESHRAO JYOTI , 70701689E , T8053102 , PICT , T8053133

01.	CONTROL SYSTEMS	PP	100	40	59	P C	11.	SIGNAL CODING & ESTIMATION THEORY	PP	100	40	40	P C
02.	DIGITAL COMMUNICATION	PP	100	40	42	P C	12.	SIGNAL CODING & ESTIMATION THEORY	PR	50	20	24	P
03.	DIGITAL COMMUNICATION	PR	50	20	23	P C	13.	SYSTEM PROGRA.& OPERATING SYS.	PP	100	40	55	P C
04.	NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	60	P C	14.	SYSTEM PROGRA.& OPERATING SYS.	TW	50	20	34	P C
05.	NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	36	P C	15.	COMPUTER ORGANIZATION & ARCHITEC	PP	100	40	59	P C
06.	MICROCONTROLLERS & APPLICATION	PP	100	40	56	P C	16.	INDUSTRIAL MANAGEMENT	PP	100	40	58	P C
07.	MICROCONTROLLERS & APPLICATION	PR	50	20	25	P C	17.	WAVE THEORY & ANTENNA	PP	100	40	47	P C
08.	DIGITAL SIGNAL PROCESSING	PP	100	40	40	P C	18.	WAVE THEORY & ANTENNA	PR	50	20	20	P C
09.	DIGITAL SIGNAL PROCESSING	OR	50	20	21	P C	19.	MINI PROJECT & SEMINAR	OR	50	20	32	P C
10.	ELECTRONIC DESIGN PRACTICE	OR	50	20	25	P C	20.	TEST & MEASUREMENT TECHNIQUES	OR	50	20	42	P C

GRAND TOTAL = 798/1500, RESULT: SECOND CLASS
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8053134 WALKE RAHUL VASANTRAO MANDAKINI , 70701698D , T8053104 , PICT , T8053134

01.	CONTROL SYSTEMS	PP	100	40	42	P	C	11.	SIGNAL CODING & ESTIMATION THEORY	PP	100	40	40	P	C
02.	DIGITAL COMMUNICATION	PP	100	40	52	P	C	12.	SIGNAL CODING & ESTIMATION THEORY	PR	50	20	34	P	C
03.	DIGITAL COMMUNICATION	PR	50	20	24	P	C	13.	SYSTEM PROGRA.& OPERATING SYS.	PP	100	40	40	P	C
04.	NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	59	P	C	14.	SYSTEM PROGRA.& OPERATING SYS.	TW	50	20	40	P	C
05.	NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	39	P	C	15.	COMPUTER ORGANIZATION & ARCHITEC	PP	100	40	52	P	C
06.	MICROCONTROLLERS & APPLICATION	PP	100	40	49	P	C	16.	INDUSTRIAL MANAGEMENT	PP	100	40	44	P	C
07.	MICROCONTROLLERS & APPLICATION	PR	50	20	28	P	C	17.	WAVE THEORY & ANTENNA	PP	100	40	47	P	C
08.	DIGITAL SIGNAL PROCESSING	PP	100	40	50	P	C	18.	WAVE THEORY & ANTENNA	PR	50	20	37	P	C
09.	DIGITAL SIGNAL PROCESSING	OR	50	20	24	P	C	19.	MINI PROJECT & SEMINAR	OR	50	20	35	P	C
10.	ELECTRONIC DESIGN PRACTICE	OR	50	20	23	P	C	20.	TEST & MEASUREMENT TECHNIQUES	OR	50	20	35	P	

GRAND TOTAL = 794/1500, RESULT: SECOND CLASS
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8054201 ABHIMANYU BHOSALE SUJATA , 71045355J , , PICT , T8054201

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	50	P
02.	DATA COMMUNICATION	PP	100	40	40	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	56	P
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	45	P
05.	THEORY OF COMPUTATION	PP	100	40	22	F
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	32	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	36	P
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	15	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	10	F
10.	HARDWARE LABORATORY	TW	25	10	15	P
11.	HARDWARE LABORATORY	PR	50	20	32	P

FIRST TERM TOTAL = 353/750.
ORDN. 1 MARKS :

T8054202 ADHAV ASHISH DNYANDEO SUJATA , 71045359M , , PICT , T8054202

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	60	P
02.	DATA COMMUNICATION	PP	100	40	52	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	57	P
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	48	P
05.	THEORY OF COMPUTATION	PP	100	40	40	P
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	45	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	41	P
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	21	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	34	P
10.	HARDWARE LABORATORY	TW	25	10	23	P
11.	HARDWARE LABORATORY	PR	50	20	42	P

FIRST TERM TOTAL = 463/750.
ORDN. 1 MARKS :

T8054203 ADHIKARI DHANASHREE HEMCHANDRA YAYUTA , 71045360E , , PICT , T8054203

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	60	P
02.	DATA COMMUNICATION	PP	100	40	62	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	67	P
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	58	P
05.	THEORY OF COMPUTATION	PP	100	40	40	P
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	45	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	46	P
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	23	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	38	P
10.	HARDWARE LABORATORY	TW	25	10	22	P
11.	HARDWARE LABORATORY	PR	50	20	43	P

FIRST TERM TOTAL = 504/750.
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8054204 ADITI JOSHI SURUCHI , 71045361C , , PICT , T8054204

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	60	P
02.	DATA COMMUNICATION	PP	100	40	52	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	61	P
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	55	P
05.	THEORY OF COMPUTATION	PP	100	40	40	P
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	35	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	38	P
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	15	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	34	P
10.	HARDWARE LABORATORY	TW	25	10	20	P
11.	HARDWARE LABORATORY	PR	50	20	20	P

FIRST TERM TOTAL = 430/750.
ORDN. 1 MARKS :

T8054205 ADTANI VASHISHTHA MOHAN BHAWANA , 71045362M , , PICT , T8054205

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	59	P
02.	DATA COMMUNICATION	PP	100	40	63	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	53	P
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	48	P
05.	THEORY OF COMPUTATION	PP	100	40	41	P
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	40	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	42	P
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	20	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	35	P
10.	HARDWARE LABORATORY	TW	25	10	19	P
11.	HARDWARE LABORATORY	PR	50	20	33	P

FIRST TERM TOTAL = 453/750.
ORDN. 1 MARKS :

T8054206 AGRAWAL ANKIT UMESHKUMAR SHAMADEVI , 71045364H , , PICT , T8054206

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	64	P
02.	DATA COMMUNICATION	PP	100	40	55	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	66	P
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	48	P
05.	THEORY OF COMPUTATION	PP	100	40	55	P
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	38	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	36	P
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	16	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	33	P
10.	HARDWARE LABORATORY	TW	25	10	19	P
11.	HARDWARE LABORATORY	PR	50	20	34	P

FIRST TERM TOTAL = 464/750.
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8054207 AGRAWAL ANNU ARUN MEENA , 71045365F , , PICT , T8054207

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	59	P
02.	DATA COMMUNICATION	PP	100	40	57	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	62	P
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	56	P
05.	THEORY OF COMPUTATION	PP	100	40	43	P
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	42	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	45	P
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	17	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	35	P
10.	HARDWARE LABORATORY	TW	25	10	21	P
11.	HARDWARE LABORATORY	PR	50	20	20	P

FIRST TERM TOTAL = 457/750.
ORDN. 1 MARKS :

T8054208 AKASH DEEP AGRAWAL KAUSALYA , 71045366D , , PICT , T8054208

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	62	P
02.	DATA COMMUNICATION	PP	100	40	67	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	74	P
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	59	P
05.	THEORY OF COMPUTATION	PP	100	40	51	P
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	40	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	38	P
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	21	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	33	P
10.	HARDWARE LABORATORY	TW	25	10	22	P
11.	HARDWARE LABORATORY	PR	50	20	38	P

FIRST TERM TOTAL = 505/750.
ORDN. 1 MARKS :

T8054209 ANIRUDH JODHA ANJU , 71045370B , , PICT , T8054209

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	56	P
02.	DATA COMMUNICATION	PP	100	40	42	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	47	P
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	22	F
05.	THEORY OF COMPUTATION	PP	100	40	29	F
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	35	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	38	P
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	16	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	22	P
10.	HARDWARE LABORATORY	TW	25	10	19	P
11.	HARDWARE LABORATORY	PR	50	20	36	P

FIRST TERM TOTAL = 362/750.
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8054210 AVHAD PRATIK GULAB MAMTA , 71045377K , , PICT , T8054210

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	52	P
02.	DATA COMMUNICATION	PP	100	40	40	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	40	P
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	42	P
05.	THEORY OF COMPUTATION	PP	100	40	40	P
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	38	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	10	F
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	17	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	23	P
10.	HARDWARE LABORATORY	TW	25	10	20	P
11.	HARDWARE LABORATORY	PR	50	20	08	F

FIRST TERM TOTAL = 330/750.
ORDN. 1 MARKS :

T8054211 AYACHIT DUSHYANT PRADEEPRAO PRATIBHA , 71132423K , , PICT , T8054211

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	65	P
02.	DATA COMMUNICATION	PP	100	40	53	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	75	P
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	40	P
05.	THEORY OF COMPUTATION	PP	100	40	41	P
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	43	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	42	P
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	18	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	30	P
10.	HARDWARE LABORATORY	TW	25	10	20	P
11.	HARDWARE LABORATORY	PR	50	20	40	P

FIRST TERM TOTAL = 467/750.
ORDN. 1 MARKS :

T8054212 BAHULIKAR CHAITALI SANJAY SNEHA , 71045383D , , PICT , T8054212

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	62	P
02.	DATA COMMUNICATION	PP	100	40	68	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	66	P
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	58	P
05.	THEORY OF COMPUTATION	PP	100	40	55	P
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	45	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	46	P
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	24	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	40	P
10.	HARDWARE LABORATORY	TW	25	10	23	P
11.	HARDWARE LABORATORY	PR	50	20	40	P

FIRST TERM TOTAL = 527/750.
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8054213 BANG GAURAV SUNIL SUNANDA , 71045384B , , PICT , T8054213

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	54	P
02.	DATA COMMUNICATION	PP	100	40	51	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	66	P
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	54	P
05.	THEORY OF COMPUTATION	PP	100	40	43	P
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	40	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	44	P
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	22	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	41	P
10.	HARDWARE LABORATORY	TW	25	10	18	P
11.	HARDWARE LABORATORY	PR	50	20	43	P

FIRST TERM TOTAL = 476/750.
ORDN. 1 MARKS :

T8054214 BHADADE MOHIT DNYANESH SHOBHA , 71045385L , , PICT , T8054214

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	66	P
02.	DATA COMMUNICATION	PP	100	40	58	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	71	P
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	50	P
05.	THEORY OF COMPUTATION	PP	100	40	52	P
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	39	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	42	P
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	21	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	40	P
10.	HARDWARE LABORATORY	TW	25	10	22	P
11.	HARDWARE LABORATORY	PR	50	20	37	P

FIRST TERM TOTAL = 498/750.
ORDN. 1 MARKS :

T8054215 BHANDARI MUKESH DHANPAL DEEPA , 70801363F , , PICT , T8054215

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	40	P
02.	DATA COMMUNICATION	PP	100	40	AA	F
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	07	F
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	14	F
05.	THEORY OF COMPUTATION	PP	100	40	AA	F
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	20	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	AA	F
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	14	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	20	P
10.	HARDWARE LABORATORY	TW	25	10	15	P
11.	HARDWARE LABORATORY	PR	50	20	AA	F

FIRST TERM TOTAL = 130/750.
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8054216 BHANDARKAR PRATIK PRAVIN KALPANA , 71132424H , , PICT , T8054216

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	62	P
02.	DATA COMMUNICATION	PP	100	40	54	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	68	P
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	41	P
05.	THEORY OF COMPUTATION	PP	100	40	40	P
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	39	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	42	P
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	21	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	38	P
10.	HARDWARE LABORATORY	TW	25	10	22	P
11.	HARDWARE LABORATORY	PR	50	20	39	P

FIRST TERM TOTAL = 466/750.
ORDN. 1 MARKS :

T8054217 BHANGALE PRATIK DIGAMBAR MEENAKSHI , 71132425F , , PICT , T8054217

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	58	P
02.	DATA COMMUNICATION	PP	100	40	59	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	71	P
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	40	P
05.	THEORY OF COMPUTATION	PP	100	40	45	P
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	42	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	35	P
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	18	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	29	P
10.	HARDWARE LABORATORY	TW	25	10	21	P
11.	HARDWARE LABORATORY	PR	50	20	44	P

FIRST TERM TOTAL = 462/750.
ORDN. 1 MARKS :

T8054218 BHAVSAR CHAITANYA SHYAM BHARATI , 71045389C , , PICT , T8054218

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	59	P
02.	DATA COMMUNICATION	PP	100	40	41	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	55	P
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	44	P
05.	THEORY OF COMPUTATION	PP	100	40	40	P
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	37	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	34	P
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	17	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	39	P
10.	HARDWARE LABORATORY	TW	25	10	19	P
11.	HARDWARE LABORATORY	PR	50	20	40	P

FIRST TERM TOTAL = 425/750.
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8054219 BHOIR SARIKA DAMU ANUSAYA , 71045391E , , PICT , T8054219

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	40	P
02.	DATA COMMUNICATION	PP	100	40	40	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	44	P
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	24	F
05.	THEORY OF COMPUTATION	PP	100	40	22	F
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	40	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	06	F
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	19	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	18	F
10.	HARDWARE LABORATORY	TW	25	10	21	P
11.	HARDWARE LABORATORY	PR	50	20	08	F

FIRST TERM TOTAL = 282/750.
ORDN. 1 MARKS :

T8054220 BHOSALE SHITAL MANOHAR JAYA , 71132426D , , PICT , T8054220

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	45	P
02.	DATA COMMUNICATION	PP	100	40	27	F
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	30	F
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	17	F
05.	THEORY OF COMPUTATION	PP	100	40	09	F
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	34	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	16	F
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	15	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	10	F
10.	HARDWARE LABORATORY	TW	25	10	18	P
11.	HARDWARE LABORATORY	PR	50	20	10	F

FIRST TERM TOTAL = 231/750.
ORDN. 1 MARKS :

T8054221 BHURKE POOJA MILIND GEETANJALI , 71045395H , , PICT , T8054221

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	54	P
02.	DATA COMMUNICATION	PP	100	40	49	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	55	P
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	47	P
05.	THEORY OF COMPUTATION	PP	100	40	40	P
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	34	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	32	P
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	16	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	18	F
10.	HARDWARE LABORATORY	TW	25	10	20	P
11.	HARDWARE LABORATORY	PR	50	20	42	P

FIRST TERM TOTAL = 407/750.
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8054222 BISWAJIT DEY TAPATI RANI , 71045397D , , PICT , T8054222

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	57	P
02.	DATA COMMUNICATION	PP	100	40	55	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	62	P
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	44	P
05.	THEORY OF COMPUTATION	PP	100	40	49	P
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	43	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	47	P
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	20	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	38	P
10.	HARDWARE LABORATORY	TW	25	10	21	P
11.	HARDWARE LABORATORY	PR	50	20	41	P

FIRST TERM TOTAL = 477/750.
ORDN. 1 MARKS :

T8054223 CHANDORKAR ANUJA ANIRUDDHA ARUNA , 71045401F , , PICT , T8054223

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	62	P
02.	DATA COMMUNICATION	PP	100	40	57	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	49	P
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	47	P
05.	THEORY OF COMPUTATION	PP	100	40	40	P
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	41	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	44	P
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	16	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	38	P
10.	HARDWARE LABORATORY	TW	25	10	22	P
11.	HARDWARE LABORATORY	PR	50	20	40	P

FIRST TERM TOTAL = 456/750.
ORDN. 1 MARKS :

T8054224 CHAUDHARI DIPTI KRISHNA SHARDA , 71045404L , , PICT , T8054224

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	42	P
02.	DATA COMMUNICATION	PP	100	40	46	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	43	P
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	40	P
05.	THEORY OF COMPUTATION	PP	100	40	42	P
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	33	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	28	P
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	15	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	24	P
10.	HARDWARE LABORATORY	TW	25	10	20	P
11.	HARDWARE LABORATORY	PR	50	20	23	P

FIRST TERM TOTAL = 356/750.
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8054225 CHAUDHARI RASHMI CHANDRAKANT KARUNA , 71045405J , , PICT , T8054225

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	52	P
02.	DATA COMMUNICATION	PP	100	40	53	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	63	P
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	54	P
05.	THEORY OF COMPUTATION	PP	100	40	49	P
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	44	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	36	P
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	19	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	35	P
10.	HARDWARE LABORATORY	TW	25	10	23	P
11.	HARDWARE LABORATORY	PR	50	20	42	P

FIRST TERM TOTAL = 470/750.
ORDN. 1 MARKS :

T8054226 CHAVAN ASHISH VIJAYKUMAR RAJESHREE , 70925380L , , PICT , T8054226

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	60	P
02.	DATA COMMUNICATION	PP	100	40	49	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	49	P
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	48	P
05.	THEORY OF COMPUTATION	PP	100	40	22	F
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	34	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	32	P
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	15	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	10	F
10.	HARDWARE LABORATORY	TW	25	10	16	P
11.	HARDWARE LABORATORY	PR	50	20	20	P

FIRST TERM TOTAL = 355/750.
ORDN. 1 MARKS :

T8054227 CHHAJED DARSHAN RAJKUMAR KUSUM , 71045407E , , PICT , T8054227

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	62	P
02.	DATA COMMUNICATION	PP	100	40	50	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	65	P
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	51	P
05.	THEORY OF COMPUTATION	PP	100	40	52	P
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	40	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	42	P
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	20	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	35	P
10.	HARDWARE LABORATORY	TW	25	10	21	P
11.	HARDWARE LABORATORY	PR	50	20	20	P

FIRST TERM TOTAL = 458/750.
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8054228 CHHAJED NIKHIL VINOD MANISHA , 71045408C , , PICT , T8054228

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	55	P
02.	DATA COMMUNICATION	PP	100	40	47	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	56	P
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	46	P
05.	THEORY OF COMPUTATION	PP	100	40	49	P
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	36	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	40	P
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	15	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	34	P
10.	HARDWARE LABORATORY	TW	25	10	18	P
11.	HARDWARE LABORATORY	PR	50	20	46	P

FIRST TERM TOTAL = 442/750.
ORDN. 1 MARKS :

T8054229 DESHMUKH MAYURAJ PRABHAKAR ANURADHA , 71045412M , , PICT , T8054229

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	40	P
02.	DATA COMMUNICATION	PP	100	40	40	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	43	P
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	13	F
05.	THEORY OF COMPUTATION	PP	100	40	40	P
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	20	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	30	P
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	10	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	27	P
10.	HARDWARE LABORATORY	TW	25	10	10	P
11.	HARDWARE LABORATORY	PR	50	20	30	P

FIRST TERM TOTAL = 303/750.
ORDN. 1 MARKS :

T8054230 DESHMUKH SMITA ARUN ASHLESHA , 71045413K , , PICT , T8054230

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	48	P
02.	DATA COMMUNICATION	PP	100	40	50	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	49	P
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	49	P
05.	THEORY OF COMPUTATION	PP	100	40	31	F
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	31	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	10	F
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	14	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	34	P
10.	HARDWARE LABORATORY	TW	25	10	19	P
11.	HARDWARE LABORATORY	PR	50	20	30	P

FIRST TERM TOTAL = 365/750.
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8054231 DISHA DAWANI MEENA , 71045419J , , PICT , T8054231

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	56	P
02.	DATA COMMUNICATION	PP	100	40	44	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	63	P
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	45	P
05.	THEORY OF COMPUTATION	PP	100	40	52	P
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	41	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	47	P
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	19	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	28	P
10.	HARDWARE LABORATORY	TW	25	10	21	P
11.	HARDWARE LABORATORY	PR	50	20	35	P

FIRST TERM TOTAL = 451/750.
ORDN. 1 MARKS :

T8054232 DUBEY AAKRITI MANOJ ARCHANA , 71045421L , , PICT , T8054232

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	57	P
02.	DATA COMMUNICATION	PP	100	40	56	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	60	P
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	46	P
05.	THEORY OF COMPUTATION	PP	100	40	29	F
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	45	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	47	P
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	21	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	28	P
10.	HARDWARE LABORATORY	TW	25	10	21	P
11.	HARDWARE LABORATORY	PR	50	20	43	P

FIRST TERM TOTAL = 453/750.
ORDN. 1 MARKS :

T8054233 DUBEY SHREYA CHANDRAMA TARA , 71045422J , , PICT , T8054233

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	55	P
02.	DATA COMMUNICATION	PP	100	40	46	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	64	P
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	48	P
05.	THEORY OF COMPUTATION	PP	100	40	50	P
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	40	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	42	P
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	19	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	30	P
10.	HARDWARE LABORATORY	TW	25	10	20	P
11.	HARDWARE LABORATORY	PR	50	20	35	P

FIRST TERM TOTAL = 449/750.
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8054234 DURGESH CHAPORKAR SUMEDHA , 71045423G , , PICT , T8054234

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	58	P
02.	DATA COMMUNICATION	PP	100	40	46	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	56	P
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	43	P
05.	THEORY OF COMPUTATION	PP	100	40	40	P
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	28	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	30	P
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	10	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	34	P
10.	HARDWARE LABORATORY	TW	25	10	10	P
11.	HARDWARE LABORATORY	PR	50	20	08	F

FIRST TERM TOTAL = 363/750.
ORDN. 1 MARKS :

T8054236 GADE ABHIJEET BHARAT MANGAL , 71132427B , , PICT , T8054236

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	70	P
02.	DATA COMMUNICATION	PP	100	40	58	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	69	P
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	49	P
05.	THEORY OF COMPUTATION	PP	100	40	53	P
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	43	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	38	P
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	17	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	34	P
10.	HARDWARE LABORATORY	TW	25	10	21	P
11.	HARDWARE LABORATORY	PR	50	20	25	P

FIRST TERM TOTAL = 477/750.
ORDN. 1 MARKS :

T8054237 GADIA SAURABH PRASHANT SUNITA , 71045428H , , PICT , T8054237

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	46	P
02.	DATA COMMUNICATION	PP	100	40	67	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	68	P
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	56	P
05.	THEORY OF COMPUTATION	PP	100	40	53	P
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	43	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	38	P
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	17	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	40	P
10.	HARDWARE LABORATORY	TW	25	10	21	P
11.	HARDWARE LABORATORY	PR	50	20	40	P

FIRST TERM TOTAL = 489/750.
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8054238 GADSING MAYURI LAXMAN SAVITA , 71045429F , , PICT , T8054238

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	51	P
02.	DATA COMMUNICATION	PP	100	40	47	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	55	P
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	49	P
05.	THEORY OF COMPUTATION	PP	100	40	57	P
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	38	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	28	P
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	16	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	41	P
10.	HARDWARE LABORATORY	TW	25	10	20	P
11.	HARDWARE LABORATORY	PR	50	20	35	P

FIRST TERM TOTAL = 437/750.
ORDN. 1 MARKS :

T8054239 GAIKWAD MAHESH EKNATH LATA , 71045431H , , PICT , T8054239

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	40	P
02.	DATA COMMUNICATION	PP	100	40	42	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	42	P
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	45	P
05.	THEORY OF COMPUTATION	PP	100	40	41	P
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	34	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	08	F
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	14	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	12	F
10.	HARDWARE LABORATORY	TW	25	10	13	P
11.	HARDWARE LABORATORY	PR	50	20	05	F

FIRST TERM TOTAL = 296/750.
ORDN. 1 MARKS :

T8054240 GAIKWAD PUSHAKAR KISHOR VAISHALI , 71045432F , , PICT , T8054240

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	61	P
02.	DATA COMMUNICATION	PP	100	40	62	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	61	P
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	53	P
05.	THEORY OF COMPUTATION	PP	100	40	57	P
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	42	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	44	P
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	18	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	20	P
10.	HARDWARE LABORATORY	TW	25	10	20	P
11.	HARDWARE LABORATORY	PR	50	20	35	P

FIRST TERM TOTAL = 473/750.
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8054241 GANDHI ANUJ VIDYUT SHARMILA , 71045433D , , PICT , T8054241

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	64	P
02.	DATA COMMUNICATION	PP	100	40	51	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	69	P
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	53	P
05.	THEORY OF COMPUTATION	PP	100	40	60	P
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	40	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	40	P
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	17	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	20	P
10.	HARDWARE LABORATORY	TW	25	10	20	P
11.	HARDWARE LABORATORY	PR	50	20	28	P

FIRST TERM TOTAL = 462/750.
ORDN. 1 MARKS :

T8054242 GAUTAM KUMAR BALA DEVI , 71045434B , , PICT , T8054242

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	63	P
02.	DATA COMMUNICATION	PP	100	40	59	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	75	P
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	59	P
05.	THEORY OF COMPUTATION	PP	100	40	59	P
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	34	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	30	P
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	16	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	34	P
10.	HARDWARE LABORATORY	TW	25	10	18	P
11.	HARDWARE LABORATORY	PR	50	20	34	P

FIRST TERM TOTAL = 481/750.
ORDN. 1 MARKS :

T8054243 GOKHALE GAURAV SUHAS SWATI , 71132428L , , PICT , T8054243

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	63	P
02.	DATA COMMUNICATION	PP	100	40	50	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	66	P
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	45	P
05.	THEORY OF COMPUTATION	PP	100	40	46	P
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	28	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	21	P
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	14	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	12	F
10.	HARDWARE LABORATORY	TW	25	10	13	P
11.	HARDWARE LABORATORY	PR	50	20	05	F

FIRST TERM TOTAL = 363/750.
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8054244 GUPTA TARUN SATYABHUSHAN KANAK , 71045442C , , PICT , T8054244

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	66	P
02.	DATA COMMUNICATION	PP	100	40	58	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	75	P
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	54	P
05.	THEORY OF COMPUTATION	PP	100	40	59	P
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	36	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	38	P
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	16	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	28	P
10.	HARDWARE LABORATORY	TW	25	10	17	P
11.	HARDWARE LABORATORY	PR	50	20	22	P

FIRST TERM TOTAL = 469/750.
ORDN. 1 MARKS :

T8054245 HARSHIT SINGHANIA SEEMA , 71045445H , , PICT , T8054245

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	45	P
02.	DATA COMMUNICATION	PP	100	40	54	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	44	P
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	40	P
05.	THEORY OF COMPUTATION	PP	100	40	40	P
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	34	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	45	P
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	12	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	10	F
10.	HARDWARE LABORATORY	TW	25	10	18	P
11.	HARDWARE LABORATORY	PR	50	20	22	P

FIRST TERM TOTAL = 364/750.
ORDN. 1 MARKS :

T8054246 JADHAV PRASHANT UTTAM ANITA , 71045454G , , PICT , T8054246

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	30	F
02.	DATA COMMUNICATION	PP	100	40	15	F
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	22	F
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	19	F
05.	THEORY OF COMPUTATION	PP	100	40	AA	F
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	24	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	06	F
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	11	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	20	P
10.	HARDWARE LABORATORY	TW	25	10	13	P
11.	HARDWARE LABORATORY	PR	50	20	AA	F

FIRST TERM TOTAL = 160/750.
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8054247 JAIN PUJAN PRAFULLAKUMAR JYOTI , 71045456C , , PICT , T8054247

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	57	P
02.	DATA COMMUNICATION	PP	100	40	43	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	51	P
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	45	P
05.	THEORY OF COMPUTATION	PP	100	40	42	P
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	35	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	32	P
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	17	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	24	P
10.	HARDWARE LABORATORY	TW	25	10	18	P
11.	HARDWARE LABORATORY	PR	50	20	26	P

FIRST TERM TOTAL = 390/750.
ORDN. 1 MARKS :

T8054248 JAIN SAKSHI PARAS JYOTI , 71045457M , , PICT , T8054248

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	67	P
02.	DATA COMMUNICATION	PP	100	40	57	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	56	P
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	46	P
05.	THEORY OF COMPUTATION	PP	100	40	58	P
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	42	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	41	P
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	19	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	33	P
10.	HARDWARE LABORATORY	TW	25	10	21	P
11.	HARDWARE LABORATORY	PR	50	20	39	P

FIRST TERM TOTAL = 479/750.
ORDN. 1 MARKS :

T8054249 JAISWAL PRATIK PRAKASH KALPANA , 71045458K , , PICT , T8054249

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	68	P
02.	DATA COMMUNICATION	PP	100	40	56	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	69	P
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	40	P
05.	THEORY OF COMPUTATION	PP	100	40	49	P
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	34	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	35	P
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	17	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	28	P
10.	HARDWARE LABORATORY	TW	25	10	15	P
11.	HARDWARE LABORATORY	PR	50	20	20	P

FIRST TERM TOTAL = 431/750.
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8054250 JAMES DADO ANIA , 71045460M , , PICT , T8054250

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	40	P
02.	DATA COMMUNICATION	PP	100	40	48	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	47	P
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	30	F
05.	THEORY OF COMPUTATION	PP	100	40	42	P
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	28	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	15	F
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	11	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	34	P
10.	HARDWARE LABORATORY	TW	25	10	13	P
11.	HARDWARE LABORATORY	PR	50	20	05	F

FIRST TERM TOTAL = 313/750.
ORDN. 1 MARKS :

T8054251 JASPREET SINGH HORA BHUPENDER , 71045461K , , PICT , T8054251

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	65	P
02.	DATA COMMUNICATION	PP	100	40	55	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	56	P
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	40	P
05.	THEORY OF COMPUTATION	PP	100	40	47	P
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	40	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	10	F
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	19	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	30	P
10.	HARDWARE LABORATORY	TW	25	10	17	P
11.	HARDWARE LABORATORY	PR	50	20	05	F

FIRST TERM TOTAL = 384/750.
ORDN. 1 MARKS :

T8054252 JONDHALE KANCHAN BABUNATH ALKA , 71132429J , , PICT , T8054252

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	61	P
02.	DATA COMMUNICATION	PP	100	40	53	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	63	P
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	45	P
05.	THEORY OF COMPUTATION	PP	100	40	40	P
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	41	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	46	P
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	19	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	35	P
10.	HARDWARE LABORATORY	TW	25	10	21	P
11.	HARDWARE LABORATORY	PR	50	20	20	P

FIRST TERM TOTAL = 444/750.
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8054253 JOSHI DHANANJAY SHRINIVAS SAVITA , 71132430B , , PICT , T8054253

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	67	P
02.	DATA COMMUNICATION	PP	100	40	61	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	71	P
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	49	P
05.	THEORY OF COMPUTATION	PP	100	40	57	P
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	45	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	46	P
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	23	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	31	P
10.	HARDWARE LABORATORY	TW	25	10	22	P
11.	HARDWARE LABORATORY	PR	50	20	39	P

FIRST TERM TOTAL = 511/750.
ORDN. 1 MARKS :

T8054254 KANADE ROHIT RAJARAM VIDYA , 71132431L , , PICT , T8054254

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	69	P
02.	DATA COMMUNICATION	PP	100	40	57	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	71	P
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	51	P
05.	THEORY OF COMPUTATION	PP	100	40	62	P
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	42	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	44	P
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	18	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	35	P
10.	HARDWARE LABORATORY	TW	25	10	21	P
11.	HARDWARE LABORATORY	PR	50	20	42	P

FIRST TERM TOTAL = 512/750.
ORDN. 1 MARKS :

T8054255 KANTROD SUKESHNI ANIL SUNITA , 71045468G , , PICT , T8054255

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	41	P
02.	DATA COMMUNICATION	PP	100	40	40	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	61	P
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	42	P
05.	THEORY OF COMPUTATION	PP	100	40	47	P
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	38	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	35	P
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	16	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	30	P
10.	HARDWARE LABORATORY	TW	25	10	18	P
11.	HARDWARE LABORATORY	PR	50	20	25	P

FIRST TERM TOTAL = 393/750.
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8054256 KAPLE MADHURA MUKESH VANITA , 71045469E , , PICT , T8054256

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	60	P
02.	DATA COMMUNICATION	PP	100	40	63	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	67	P
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	56	P
05.	THEORY OF COMPUTATION	PP	100	40	46	P
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	43	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	45	P
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	20	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	37	P
10.	HARDWARE LABORATORY	TW	25	10	20	P
11.	HARDWARE LABORATORY	PR	50	20	41	P

FIRST TERM TOTAL = 498/750.
ORDN. 1 MARKS :

T8054257 KARAD NEHA DASHRATH USHA , 71045471G , , PICT , T8054257

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	47	P
02.	DATA COMMUNICATION	PP	100	40	45	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	56	P
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	40	P
05.	THEORY OF COMPUTATION	PP	100	40	40	P
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	37	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	28	P
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	16	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	20	P
10.	HARDWARE LABORATORY	TW	25	10	17	P
11.	HARDWARE LABORATORY	PR	50	20	28	P

FIRST TERM TOTAL = 374/750.
ORDN. 1 MARKS :

T8054259 KATARIYA RAHUL PADAMKUMAR BHARATI , 71045476H , , PICT , T8054259

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	62	P
02.	DATA COMMUNICATION	PP	100	40	52	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	70	P
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	51	P
05.	THEORY OF COMPUTATION	PP	100	40	58	P
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	41	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	40	P
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	20	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	22	P
10.	HARDWARE LABORATORY	TW	25	10	20	P
11.	HARDWARE LABORATORY	PR	50	20	37	P

FIRST TERM TOTAL = 473/750.
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8054260 KAWADE SARANG JAIRAJ KARUNA , 71045477F , , PICT , T8054260

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	66	P
02.	DATA COMMUNICATION	PP	100	40	61	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	70	P
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	51	P
05.	THEORY OF COMPUTATION	PP	100	40	52	P
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	44	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	42	P
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	23	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	36	P
10.	HARDWARE LABORATORY	TW	25	10	21	P
11.	HARDWARE LABORATORY	PR	50	20	36	P

FIRST TERM TOTAL = 502/750.
ORDN. 1 MARKS :

T8054261 KAZI SABIYA FARUK SHAHEDA , 71132432J , , PICT , T8054261

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	72	P
02.	DATA COMMUNICATION	PP	100	40	58	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	66	P
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	51	P
05.	THEORY OF COMPUTATION	PP	100	40	62	P
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	46	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	44	P
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	20	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	34	P
10.	HARDWARE LABORATORY	TW	25	10	21	P
11.	HARDWARE LABORATORY	PR	50	20	43	P

FIRST TERM TOTAL = 517/750.
ORDN. 1 MARKS :

T8054262 KHADE PRERANA BALASAHEB TARABAI , 71045481D , , PICT , T8054262

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	61	P
02.	DATA COMMUNICATION	PP	100	40	50	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	51	P
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	47	P
05.	THEORY OF COMPUTATION	PP	100	40	42	P
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	32	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	30	P
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	18	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	20	P
10.	HARDWARE LABORATORY	TW	25	10	21	P
11.	HARDWARE LABORATORY	PR	50	20	38	P

FIRST TERM TOTAL = 410/750.
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8054264 KHARDE ABHIJIT DIGAMBER JYOTI , 71045483L , , PICT , T8054264

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	58	P
02.	DATA COMMUNICATION	PP	100	40	60	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	70	P
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	45	P
05.	THEORY OF COMPUTATION	PP	100	40	51	P
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	44	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	41	P
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	22	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	31	P
10.	HARDWARE LABORATORY	TW	25	10	20	P
11.	HARDWARE LABORATORY	PR	50	20	42	P

FIRST TERM TOTAL = 484/750.
ORDN. 1 MARKS :

T8054265 KHEDKAR PRITAM KISHOR SUREKHA , 71045484J , , PICT , T8054265

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	58	P
02.	DATA COMMUNICATION	PP	100	40	41	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	49	P
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	40	P
05.	THEORY OF COMPUTATION	PP	100	40	48	P
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	35	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	42	P
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	14	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	29	P
10.	HARDWARE LABORATORY	TW	25	10	16	P
11.	HARDWARE LABORATORY	PR	50	20	36	P

FIRST TERM TOTAL = 408/750.
ORDN. 1 MARKS :

T8054266 KONDHAWALE PRASHANT MHATARBA MANGAL , 70925487D , , PICT , T8054266

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	51	P
02.	DATA COMMUNICATION	PP	100	40	43	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	52	P
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	27	F
05.	THEORY OF COMPUTATION	PP	100	40	41	P
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	38	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	21	P
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	18	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	20	P
10.	HARDWARE LABORATORY	TW	25	10	21	P
11.	HARDWARE LABORATORY	PR	50	20	32	P

FIRST TERM TOTAL = 364/750.
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8054267 KOTE UDAY ARUN SUNITA , 70925488B , , PICT , T8054267

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	40	P
02.	DATA COMMUNICATION	PP	100	40	49	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	47	P
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	44	P
05.	THEORY OF COMPUTATION	PP	100	40	40	P
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	34	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	22	P
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	15	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	20	P
10.	HARDWARE LABORATORY	TW	25	10	17	P
11.	HARDWARE LABORATORY	PR	50	20	36	P

FIRST TERM TOTAL = 364/750.
ORDN. 1 MARKS :

T8054268 KSHATRIYA PURVA HEMANT RAJESHRI , 71045487C , , PICT , T8054268

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	52	P
02.	DATA COMMUNICATION	PP	100	40	54	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	61	P
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	43	P
05.	THEORY OF COMPUTATION	PP	100	40	59	P
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	42	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	32	P
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	19	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	24	P
10.	HARDWARE LABORATORY	TW	25	10	20	P
11.	HARDWARE LABORATORY	PR	50	20	39	P

FIRST TERM TOTAL = 445/750.
ORDN. 1 MARKS :

T8054269 KSHITIZ DANGE REKHA RANI , 71045488M , , PICT , T8054269

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	53	P
02.	DATA COMMUNICATION	PP	100	40	60	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	59	P
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	44	P
05.	THEORY OF COMPUTATION	PP	100	40	58	P
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	40	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	39	P
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	16	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	35	P
10.	HARDWARE LABORATORY	TW	25	10	21	P
11.	HARDWARE LABORATORY	PR	50	20	30	P

FIRST TERM TOTAL = 455/750.
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8054270 KULKARNI AKASH ABHAY ASHWINI , 71045489K , , PICT , T8054270

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	66	P
02.	DATA COMMUNICATION	PP	100	40	72	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	72	P
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	54	P
05.	THEORY OF COMPUTATION	PP	100	40	51	P
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	43	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	40	P
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	22	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	36	P
10.	HARDWARE LABORATORY	TW	25	10	20	P
11.	HARDWARE LABORATORY	PR	50	20	41	P

FIRST TERM TOTAL = 517/750.
ORDN. 1 MARKS :

T8054271 KULKARNI CHINMAYI AVINASH SHOBHA , 71045493H , , PICT , T8054271

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	58	P
02.	DATA COMMUNICATION	PP	100	40	40	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	40	P
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	40	P
05.	THEORY OF COMPUTATION	PP	100	40	40	P
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	36	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	42	P
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	16	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	30	P
10.	HARDWARE LABORATORY	TW	25	10	20	P
11.	HARDWARE LABORATORY	PR	50	20	20	P

FIRST TERM TOTAL = 382/750.
ORDN. 1 MARKS :

T8054272 KULKARNI DNYANESH VIJAYRAO MANGAL , 71045494F , , PICT , T8054272

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	71	P
02.	DATA COMMUNICATION	PP	100	40	55	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	59	P
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	58	P
05.	THEORY OF COMPUTATION	PP	100	40	47	P
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	43	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	38	P
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	21	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	37	P
10.	HARDWARE LABORATORY	TW	25	10	21	P
11.	HARDWARE LABORATORY	PR	50	20	21	P

FIRST TERM TOTAL = 471/750.
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8054273 KULKARNI MANASI MILIND JYOTI , 71045496B , , PICT , T8054273

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	61	P
02.	DATA COMMUNICATION	PP	100	40	45	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	44	P
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	40	P
05.	THEORY OF COMPUTATION	PP	100	40	51	P
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	30	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	34	P
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	17	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	30	P
10.	HARDWARE LABORATORY	TW	25	10	20	P
11.	HARDWARE LABORATORY	PR	50	20	30	P

FIRST TERM TOTAL = 402/750.
ORDN. 1 MARKS :

T8054274 KULKARNI PRATHAMESH DEEPAK VIDYA , 71132433G , , PICT , T8054274

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	66	P
02.	DATA COMMUNICATION	PP	100	40	48	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	52	P
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	50	P
05.	THEORY OF COMPUTATION	PP	100	40	59	P
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	41	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	42	P
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	19	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	31	P
10.	HARDWARE LABORATORY	TW	25	10	20	P
11.	HARDWARE LABORATORY	PR	50	20	43	P

FIRST TERM TOTAL = 471/750.
ORDN. 1 MARKS :

T8054275 KULKARNI SAURABH RAVINDRA SWATI , 71045498J , , PICT , T8054275

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	63	P
02.	DATA COMMUNICATION	PP	100	40	53	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	55	P
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	70	P
05.	THEORY OF COMPUTATION	PP	100	40	50	P
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	43	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	45	P
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	21	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	32	P
10.	HARDWARE LABORATORY	TW	25	10	20	P
11.	HARDWARE LABORATORY	PR	50	20	41	P

FIRST TERM TOTAL = 493/750.
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8054276 KUNTALWAD GNYANESH GAJJARAM SARASWATHI , 71045501B , , PICT , T8054276

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	40	P
02.	DATA COMMUNICATION	PP	100	40	40	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	30	F
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	44	P
05.	THEORY OF COMPUTATION	PP	100	40	44	P
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	30	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	22	P
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	14	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	36	P
10.	HARDWARE LABORATORY	TW	25	10	15	P
11.	HARDWARE LABORATORY	PR	50	20	08	F

FIRST TERM TOTAL = 323/750.
ORDN. 1 MARKS :

T8054277 LATIFI AAMASH IMTIYAZ AHMED SHAHERA , 70925492L , , PICT , T8054277

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	30	F
02.	DATA COMMUNICATION	PP	100	40	40	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	30	F
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	26	F
05.	THEORY OF COMPUTATION	PP	100	40	28	F
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	26	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	22	P
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	11	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	25	P
10.	HARDWARE LABORATORY	TW	25	10	13	P
11.	HARDWARE LABORATORY	PR	50	20	05	F

FIRST TERM TOTAL = 256/750.
ORDN. 1 MARKS :

T8054278 LOKHANDE VINAYA VISHWANATHRAO PRATIBHA , 71045504G , , PICT , T8054278

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	57	P
02.	DATA COMMUNICATION	PP	100	40	52	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	60	P
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	69	P
05.	THEORY OF COMPUTATION	PP	100	40	55	P
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	39	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	34	P
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	17	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	30	P
10.	HARDWARE LABORATORY	TW	25	10	22	P
11.	HARDWARE LABORATORY	PR	50	20	10	F

FIRST TERM TOTAL = 445/750.
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8054279 MAHARANWAR NAMITA ANILRAO RENUKA , 71045510M , , PICT , T8054279

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	57	P
02.	DATA COMMUNICATION	PP	100	40	47	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	57	P
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	46	P
05.	THEORY OF COMPUTATION	PP	100	40	61	P
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	40	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	08	F
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	19	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	22	P
10.	HARDWARE LABORATORY	TW	25	10	20	P
11.	HARDWARE LABORATORY	PR	50	20	26	P

FIRST TERM TOTAL = 403/750.
ORDN. 1 MARKS :

T8054280 MALANI MUKUND PARMANAND SUDHA , 71045513F , , PICT , T8054280

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	59	P
02.	DATA COMMUNICATION	PP	100	40	50	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	54	P
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	61	P
05.	THEORY OF COMPUTATION	PP	100	40	55	P
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	39	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	35	P
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	17	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	30	P
10.	HARDWARE LABORATORY	TW	25	10	18	P
11.	HARDWARE LABORATORY	PR	50	20	30	P

FIRST TERM TOTAL = 448/750.
ORDN. 1 MARKS :

T8054281 MALHOTRA VIJAY GULSHARAN ANJU , 71045515B , , PICT , T8054281

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	61	P
02.	DATA COMMUNICATION	PP	100	40	56	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	56	P
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	56	P
05.	THEORY OF COMPUTATION	PP	100	40	53	P
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	36	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	42	P
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	19	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	37	P
10.	HARDWARE LABORATORY	TW	25	10	16	P
11.	HARDWARE LABORATORY	PR	50	20	41	P

FIRST TERM TOTAL = 473/750.
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8054282 MANDLIWALA NAFISA OANALI RASHIDA , 71045516L , , PICT , T8054282

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	64	P
02.	DATA COMMUNICATION	PP	100	40	52	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	59	P
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	64	P
05.	THEORY OF COMPUTATION	PP	100	40	51	P
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	42	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	40	P
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	16	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	38	P
10.	HARDWARE LABORATORY	TW	25	10	21	P
11.	HARDWARE LABORATORY	PR	50	20	34	P

FIRST TERM TOTAL = 481/750.
ORDN. 1 MARKS :

T8054283 MANE OM ARUN RAJANI , 70925499H , , PICT , T8054283

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	40	P
02.	DATA COMMUNICATION	PP	100	40	40	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	40	P
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	31	F
05.	THEORY OF COMPUTATION	PP	100	40	19	F
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	24	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	22	P
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	11	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	10	F
10.	HARDWARE LABORATORY	TW	25	10	14	P
11.	HARDWARE LABORATORY	PR	50	20	05	F

FIRST TERM TOTAL = 256/750.
ORDN. 1 MARKS :

T8054284 MANE POOJA VASANT VARSHA , 71132434E , , PICT , T8054284

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	66	P
02.	DATA COMMUNICATION	PP	100	40	58	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	65	P
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	60	P
05.	THEORY OF COMPUTATION	PP	100	40	61	P
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	43	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	36	P
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	17	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	38	P
10.	HARDWARE LABORATORY	TW	25	10	21	P
11.	HARDWARE LABORATORY	PR	50	20	20	P

FIRST TERM TOTAL = 485/750.
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8054285 MANVI BHATIA ANJU , 71045520J , , PICT , T8054285

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	48	P
02.	DATA COMMUNICATION	PP	100	40	53	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	58	P
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	58	P
05.	THEORY OF COMPUTATION	PP	100	40	65	P
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	40	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	45	P
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	16	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	39	P
10.	HARDWARE LABORATORY	TW	25	10	21	P
11.	HARDWARE LABORATORY	PR	50	20	38	P

FIRST TERM TOTAL = 481/750.
ORDN. 1 MARKS :

T8054286 MASKE SHRUTI HIRALAL SANGEETA , 71045523C , , PICT , T8054286

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	40	P
02.	DATA COMMUNICATION	PP	100	40	44	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	47	P
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	42	P
05.	THEORY OF COMPUTATION	PP	100	40	48	P
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	40	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	15	F
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	19	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	36	P
10.	HARDWARE LABORATORY	TW	25	10	19	P
11.	HARDWARE LABORATORY	PR	50	20	27	P

FIRST TERM TOTAL = 377/750.
ORDN. 1 MARKS :

T8054287 MUNESHWAR PRANALI PRAKASH PRAMILA , 71045533L , , PICT , T8054287

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	49	P
02.	DATA COMMUNICATION	PP	100	40	44	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	45	P
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	57	P
05.	THEORY OF COMPUTATION	PP	100	40	52	P
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	40	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	42	P
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	18	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	36	P
10.	HARDWARE LABORATORY	TW	25	10	17	P
11.	HARDWARE LABORATORY	PR	50	20	08	F

FIRST TERM TOTAL = 408/750.
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8054288 MUSALE SAGAR RAJENDRA KALPANA , 71045534J , , PICT , T8054288

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	52	P
02.	DATA COMMUNICATION	PP	100	40	41	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	52	P
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	46	P
05.	THEORY OF COMPUTATION	PP	100	40	40	P
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	40	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	39	P
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	17	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	34	P
10.	HARDWARE LABORATORY	TW	25	10	19	P
11.	HARDWARE LABORATORY	PR	50	20	38	P

FIRST TERM TOTAL = 418/750.
ORDN. 1 MARKS :

T8054289 MUTALIK MADHUMITRA MADHAV MAITREYEE , 71045535G , , PICT , T8054289

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	67	P
02.	DATA COMMUNICATION	PP	100	40	53	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	65	P
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	71	P
05.	THEORY OF COMPUTATION	PP	100	40	61	P
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	40	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	44	P
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	18	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	32	P
10.	HARDWARE LABORATORY	TW	25	10	21	P
11.	HARDWARE LABORATORY	PR	50	20	41	P

FIRST TERM TOTAL = 513/750.
ORDN. 1 MARKS :

T8054290 MUTHA SURABHI SHASHIKUMAR UJJWALA , 71045536E , , PICT , T8054290

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	65	P
02.	DATA COMMUNICATION	PP	100	40	50	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	62	P
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	71	P
05.	THEORY OF COMPUTATION	PP	100	40	65	P
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	42	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	42	P
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	20	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	35	P
10.	HARDWARE LABORATORY	TW	25	10	22	P
11.	HARDWARE LABORATORY	PR	50	20	42	P

FIRST TERM TOTAL = 516/750.
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8054291 NAIR AJAY RADHAKRISHNAN GEETHA , 71045539K , , PICT , T8054291

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	62	P
02.	DATA COMMUNICATION	PP	100	40	53	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	49	P
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	62	P
05.	THEORY OF COMPUTATION	PP	100	40	48	P
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	36	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	38	P
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	18	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	29	P
10.	HARDWARE LABORATORY	TW	25	10	20	P
11.	HARDWARE LABORATORY	PR	50	20	40	P

FIRST TERM TOTAL = 455/750.
ORDN. 1 MARKS :

T8054292 NAJAN KAUSTUBH GORAKSHANATH MEERA , 71045540C , , PICT , T8054292

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	40	P
02.	DATA COMMUNICATION	PP	100	40	42	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	40	P
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	40	P
05.	THEORY OF COMPUTATION	PP	100	40	30	F
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	45	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	34	P
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	17	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	20	P
10.	HARDWARE LABORATORY	TW	25	10	20	P
11.	HARDWARE LABORATORY	PR	50	20	35	P

FIRST TERM TOTAL = 363/750.
ORDN. 1 MARKS :

T8054293 NANDKAR NEHA VIJAY DIPALI , 70925523D , , PICT , T8054293

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	62	P
02.	DATA COMMUNICATION	PP	100	40	49	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	53	P
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	53	P
05.	THEORY OF COMPUTATION	PP	100	40	61	P
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	32	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	15	F
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	15	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	20	P
10.	HARDWARE LABORATORY	TW	25	10	18	P
11.	HARDWARE LABORATORY	PR	50	20	30	P

FIRST TERM TOTAL = 408/750.
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8054294 NANWANI SHRUTI RAMESH KAMLESH , 71045541M , , PICT , T8054294

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	67	P
02.	DATA COMMUNICATION	PP	100	40	45	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	62	P
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	61	P
05.	THEORY OF COMPUTATION	PP	100	40	62	P
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	40	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	39	P
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	18	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	18	F
10.	HARDWARE LABORATORY	TW	25	10	20	P
11.	HARDWARE LABORATORY	PR	50	20	31	P

FIRST TERM TOTAL = 463/750.
ORDN. 1 MARKS :

T8054295 NARENDRA PAL SINGH KADAM , 71045542K , , PICT , T8054295

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	60	P
02.	DATA COMMUNICATION	PP	100	40	51	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	54	P
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	64	P
05.	THEORY OF COMPUTATION	PP	100	40	56	P
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	44	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	35	P
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	23	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	37	P
10.	HARDWARE LABORATORY	TW	25	10	23	P
11.	HARDWARE LABORATORY	PR	50	20	38	P

FIRST TERM TOTAL = 485/750.
ORDN. 1 MARKS :

T8054296 NEHA RANA SUDHA , 71045543H , , PICT , T8054296

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	66	P
02.	DATA COMMUNICATION	PP	100	40	62	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	57	P
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	66	P
05.	THEORY OF COMPUTATION	PP	100	40	59	P
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	42	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	40	P
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	20	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	30	P
10.	HARDWARE LABORATORY	TW	25	10	20	P
11.	HARDWARE LABORATORY	PR	50	20	33	P

FIRST TERM TOTAL = 495/750.
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8054297 NEMADE AJAY VILAS MANJUSHA , 71045544F , , PICT , T8054297

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	62	P
02.	DATA COMMUNICATION	PP	100	40	49	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	54	P
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	55	P
05.	THEORY OF COMPUTATION	PP	100	40	68	P
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	40	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	43	P
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	18	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	36	P
10.	HARDWARE LABORATORY	TW	25	10	19	P
11.	HARDWARE LABORATORY	PR	50	20	31	P

FIRST TERM TOTAL = 475/750.
ORDN. 1 MARKS :

T8054298 NERKAR NIKITA SUNIL AMITA , 71045545D , , PICT , T8054298

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	68	P
02.	DATA COMMUNICATION	PP	100	40	51	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	68	P
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	68	P
05.	THEORY OF COMPUTATION	PP	100	40	51	P
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	45	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	45	P
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	22	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	41	P
10.	HARDWARE LABORATORY	TW	25	10	23	P
11.	HARDWARE LABORATORY	PR	50	20	43	P

FIRST TERM TOTAL = 525/750.
ORDN. 1 MARKS :

T8054299 NITYA RAJ SARASWATHY , 71045550L , , PICT , T8054299

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	59	P
02.	DATA COMMUNICATION	PP	100	40	40	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	44	P
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	60	P
05.	THEORY OF COMPUTATION	PP	100	40	51	P
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	38	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	43	P
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	20	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	26	P
10.	HARDWARE LABORATORY	TW	25	10	20	P
11.	HARDWARE LABORATORY	PR	50	20	35	P

FIRST TERM TOTAL = 436/750.
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8054300 PANDYA PARTH VIPUL ASHA , 71045553E , , PICT , T8054300

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	50	P
02.	DATA COMMUNICATION	PP	100	40	41	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	48	P
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	40	P
05.	THEORY OF COMPUTATION	PP	100	40	60	P
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	28	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	33	P
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	14	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	33	P
10.	HARDWARE LABORATORY	TW	25	10	17	P
11.	HARDWARE LABORATORY	PR	50	20	25	P

FIRST TERM TOTAL = 389/750.
ORDN. 1 MARKS :

T8054301 PATE MAYUR SATISH MADHURI , 71132435C , , PICT , T8054301

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	65	P
02.	DATA COMMUNICATION	PP	100	40	50	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	65	P
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	63	P
05.	THEORY OF COMPUTATION	PP	100	40	62	P
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	40	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	43	P
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	18	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	40	P
10.	HARDWARE LABORATORY	TW	25	10	20	P
11.	HARDWARE LABORATORY	PR	50	20	40	P

FIRST TERM TOTAL = 506/750.
ORDN. 1 MARKS :

T8054302 PATHAK AVINASH NARAYAN NALINI , 71132436M , , PICT , T8054302

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	67	P
02.	DATA COMMUNICATION	PP	100	40	53	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	67	P
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	55	P
05.	THEORY OF COMPUTATION	PP	100	40	67	P
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	45	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	42	P
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	22	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	39	P
10.	HARDWARE LABORATORY	TW	25	10	23	P
11.	HARDWARE LABORATORY	PR	50	20	42	P

FIRST TERM TOTAL = 522/750.
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8054303 PATHAK VISHVAJIT HARISHCHANDRA PRATIBHA , 71045561F , , PICT , T8054303

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	45	P
02.	DATA COMMUNICATION	PP	100	40	46	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	40	P
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	71	P
05.	THEORY OF COMPUTATION	PP	100	40	45	P
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	41	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	42	P
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	18	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	35	P
10.	HARDWARE LABORATORY	TW	25	10	20	P
11.	HARDWARE LABORATORY	PR	50	20	35	P

FIRST TERM TOTAL = 438/750.
ORDN. 1 MARKS :

T8054304 PATIL AKSHAY JAYANT POOJA , 71045562D , , PICT , T8054304

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	56	P
02.	DATA COMMUNICATION	PP	100	40	59	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	53	P
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	67	P
05.	THEORY OF COMPUTATION	PP	100	40	43	P
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	40	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	38	P
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	20	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	40	P
10.	HARDWARE LABORATORY	TW	25	10	20	P
11.	HARDWARE LABORATORY	PR	50	20	40	P

FIRST TERM TOTAL = 476/750.
ORDN. 1 MARKS :

T8054305 PATIL SAGAR RAJESH USHA , 71045567E , , PICT , T8054305

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	63	P
02.	DATA COMMUNICATION	PP	100	40	50	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	57	P
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	67	P
05.	THEORY OF COMPUTATION	PP	100	40	59	P
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	44	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	44	P
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	22	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	40	P
10.	HARDWARE LABORATORY	TW	25	10	22	P
11.	HARDWARE LABORATORY	PR	50	20	43	P

FIRST TERM TOTAL = 511/750.
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8054306 PATIL SHALAKA VIJAY SHAILAJA , 71132437K , , PICT , T8054306

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	66	P
02.	DATA COMMUNICATION	PP	100	40	66	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	63	P
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	67	P
05.	THEORY OF COMPUTATION	PP	100	40	76	P
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	45	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	44	P
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	21	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	33	P
10.	HARDWARE LABORATORY	TW	25	10	21	P
11.	HARDWARE LABORATORY	PR	50	20	44	P

FIRST TERM TOTAL = 546/750.
ORDN. 1 MARKS :

T8054307 PAWAR AJINKYA NANDU VIDYA , 71045570E , , PICT , T8054307

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	45	P
02.	DATA COMMUNICATION	PP	100	40	51	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	41	P
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	31	F
05.	THEORY OF COMPUTATION	PP	100	40	26	F
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	24	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	28	P
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	11	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	10	F
10.	HARDWARE LABORATORY	TW	25	10	11	P
11.	HARDWARE LABORATORY	PR	50	20	22	P

FIRST TERM TOTAL = 300/750.
ORDN. 1 MARKS :

T8054308 PAWAR HARISH MANGESH RENUKA , 71132438H , , PICT , T8054308

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	50	P
02.	DATA COMMUNICATION	PP	100	40	49	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	52	P
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	28	F
05.	THEORY OF COMPUTATION	PP	100	40	40	P
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	35	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	38	P
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	14	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	25	P
10.	HARDWARE LABORATORY	TW	25	10	16	P
11.	HARDWARE LABORATORY	PR	50	20	05	F

FIRST TERM TOTAL = 352/750.
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8054309 PILAJI KEDARNATH BALAJI ANUJA , 71045574H , , PICT , T8054309

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	62	P
02.	DATA COMMUNICATION	PP	100	40	59	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	44	P
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	49	P
05.	THEORY OF COMPUTATION	PP	100	40	56	P
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	34	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	43	P
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	15	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	35	P
10.	HARDWARE LABORATORY	TW	25	10	17	P
11.	HARDWARE LABORATORY	PR	50	20	33	P

FIRST TERM TOTAL = 447/750.
ORDN. 1 MARKS :

T8054310 POORVI ARVIND DHARWAD ROHINI , 70925566H , , PICT , T8054310

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	26	F
02.	DATA COMMUNICATION	PP	100	40	40	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	27	F
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	22	F
05.	THEORY OF COMPUTATION	PP	100	40	11	F
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	32	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	26	P
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	20	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	10	F
10.	HARDWARE LABORATORY	TW	25	10	21	P
11.	HARDWARE LABORATORY	PR	50	20	05	F

FIRST TERM TOTAL = 240/750.
ORDN. 1 MARKS :

T8054311 PORE DIVYA KISHOR KALPANA , 71132439F , , PICT , T8054311

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	62	P
02.	DATA COMMUNICATION	PP	100	40	59	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	58	P
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	51	P
05.	THEORY OF COMPUTATION	PP	100	40	55	P
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	42	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	44	P
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	21	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	34	P
10.	HARDWARE LABORATORY	TW	25	10	21	P
11.	HARDWARE LABORATORY	PR	50	20	30	P

FIRST TERM TOTAL = 477/750.
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8054312 POTDAR SHANTANU RATNAKAR ANITA , 71132440K , , PICT , T8054312

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	68	P
02.	DATA COMMUNICATION	PP	100	40	68	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	68	P
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	73	P
05.	THEORY OF COMPUTATION	PP	100	40	68	P
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	45	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	47	P
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	24	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	40	P
10.	HARDWARE LABORATORY	TW	25	10	23	P
11.	HARDWARE LABORATORY	PR	50	20	46	P

FIRST TERM TOTAL = 570/750.
ORDN. 1 MARKS :

T8054313 PRANIDHYA KHANDELWAL NISHA , 71045580B , , PICT , T8054313

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	60	P
02.	DATA COMMUNICATION	PP	100	40	56	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	48	P
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	46	P
05.	THEORY OF COMPUTATION	PP	100	40	44	P
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	38	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	43	P
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	16	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	25	P
10.	HARDWARE LABORATORY	TW	25	10	17	P
11.	HARDWARE LABORATORY	PR	50	20	34	P

FIRST TERM TOTAL = 427/750.
ORDN. 1 MARKS :

T8054314 PRATIVINDHYA MISHRA ANURAG , 71045581L , , PICT , T8054314

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	62	P
02.	DATA COMMUNICATION	PP	100	40	58	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	56	P
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	67	P
05.	THEORY OF COMPUTATION	PP	100	40	60	P
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	44	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	43	P
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	24	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	42	P
10.	HARDWARE LABORATORY	TW	25	10	22	P
11.	HARDWARE LABORATORY	PR	50	20	42	P

FIRST TERM TOTAL = 520/750.
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8054315 RAHEJA ROHAN VINOD POOJA , 71045585C , , PICT , T8054315

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	48	P
02.	DATA COMMUNICATION	PP	100	40	42	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	31	F
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	40	P
05.	THEORY OF COMPUTATION	PP	100	40	43	P
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	36	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	42	P
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	16	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	23	P
10.	HARDWARE LABORATORY	TW	25	10	17	P
11.	HARDWARE LABORATORY	PR	50	20	35	P

FIRST TERM TOTAL = 373/750.
ORDN. 1 MARKS :

T8054316 RANE RAHUL DILIP VIDYA , 71045586M , , PICT , T8054316

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	67	P
02.	DATA COMMUNICATION	PP	100	40	53	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	58	P
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	40	P
05.	THEORY OF COMPUTATION	PP	100	40	65	P
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	33	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	43	P
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	16	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	23	P
10.	HARDWARE LABORATORY	TW	25	10	19	P
11.	HARDWARE LABORATORY	PR	50	20	34	P

FIRST TERM TOTAL = 451/750.
ORDN. 1 MARKS :

T8054317 RAWALE SUHAS SUDAMRAO RATNMALA , 71045588H , , PICT , T8054317

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	40	P
02.	DATA COMMUNICATION	PP	100	40	43	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	40	P
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	26	F
05.	THEORY OF COMPUTATION	PP	100	40	40	P
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	35	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	12	F
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	14	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	15	F
10.	HARDWARE LABORATORY	TW	25	10	13	P
11.	HARDWARE LABORATORY	PR	50	20	08	F

FIRST TERM TOTAL = 286/750.
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8054318 SAKORE MITHILA RAMNATH MADHURI , 71045593D , , PICT , T8054318

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	71	P
02.	DATA COMMUNICATION	PP	100	40	63	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	49	P
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	40	P
05.	THEORY OF COMPUTATION	PP	100	40	65	P
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	39	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	28	P
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	18	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	24	P
10.	HARDWARE LABORATORY	TW	25	10	19	P
11.	HARDWARE LABORATORY	PR	50	20	21	P

FIRST TERM TOTAL = 437/750.
ORDN. 1 MARKS :

T8054319 SANDEEP AGARWAL RITA , 70925587L , , PICT , T8054319

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	47	P
02.	DATA COMMUNICATION	PP	100	40	40	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	11	F
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	15	F
05.	THEORY OF COMPUTATION	PP	100	40	50	P
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	34	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	12	F
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	15	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	22	P
10.	HARDWARE LABORATORY	TW	25	10	17	P
11.	HARDWARE LABORATORY	PR	50	20	30	P

FIRST TERM TOTAL = 293/750.
ORDN. 1 MARKS :

T8054320 SANGHVI PALAK RAJESH NEHA , 71045595L , , PICT , T8054320

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	59	P
02.	DATA COMMUNICATION	PP	100	40	54	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	52	P
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	57	P
05.	THEORY OF COMPUTATION	PP	100	40	54	P
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	45	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	38	P
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	23	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	22	P
10.	HARDWARE LABORATORY	TW	25	10	23	P
11.	HARDWARE LABORATORY	PR	50	20	40	P

FIRST TERM TOTAL = 467/750.
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8054321 SANGLIKAR PRATIK SANTOSH ANJALI , 71045597G , , PICT , T8054321

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	56	P
02.	DATA COMMUNICATION	PP	100	40	56	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	40	P
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	40	P
05.	THEORY OF COMPUTATION	PP	100	40	40	P
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	40	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	42	P
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	16	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	22	P
10.	HARDWARE LABORATORY	TW	25	10	19	P
11.	HARDWARE LABORATORY	PR	50	20	20	P

FIRST TERM TOTAL = 391/750.
ORDN. 1 MARKS :

T8054322 SARNAIK ABHISHEK MANOJ KAVITA , 70925592G , , PICT , T8054322

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	55	P
02.	DATA COMMUNICATION	PP	100	40	47	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	46	P
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	27	F
05.	THEORY OF COMPUTATION	PP	100	40	54	P
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	30	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	05	F
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	15	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	18	F
10.	HARDWARE LABORATORY	TW	25	10	17	P
11.	HARDWARE LABORATORY	PR	50	20	27	P

FIRST TERM TOTAL = 341/750.
ORDN. 1 MARKS :

T8054323 SAVLA SANKET HARISH JYOTI , 71045602G , , PICT , T8054323

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	40	P
02.	DATA COMMUNICATION	PP	100	40	52	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	31	F
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	44	P
05.	THEORY OF COMPUTATION	PP	100	40	46	P
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	36	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	40	P
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	15	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	33	P
10.	HARDWARE LABORATORY	TW	25	10	14	P
11.	HARDWARE LABORATORY	PR	50	20	28	P

FIRST TERM TOTAL = 379/750.
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8054324 SHAH DIMPLE PIYUSH BINAL , 71045606K , , PICT , T8054324

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	41	P
02.	DATA COMMUNICATION	PP	100	40	61	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	40	P
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	48	P
05.	THEORY OF COMPUTATION	PP	100	40	55	P
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	36	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	32	P
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	17	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	39	P
10.	HARDWARE LABORATORY	TW	25	10	19	P
11.	HARDWARE LABORATORY	PR	50	20	40	P

FIRST TERM TOTAL = 428/750.
ORDN. 1 MARKS :

T8054325 SHAH YASH MANESH RAJASHRI , 71045608F , , PICT , T8054325

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	55	P
02.	DATA COMMUNICATION	PP	100	40	53	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	59	P
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	45	P
05.	THEORY OF COMPUTATION	PP	100	40	54	P
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	40	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	40	P
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	19	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	36	P
10.	HARDWARE LABORATORY	TW	25	10	19	P
11.	HARDWARE LABORATORY	PR	50	20	34	P

FIRST TERM TOTAL = 454/750.
ORDN. 1 MARKS :

T8054326 SHASHANK KUMAR GUPTA ANJANI , 70925598F , , PICT , T8054326

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	44	P
02.	DATA COMMUNICATION	PP	100	40	44	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	40	P
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	AA	F
05.	THEORY OF COMPUTATION	PP	100	40	45	P
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	25	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	33	P
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	12	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	20	P
10.	HARDWARE LABORATORY	TW	25	10	16	P
11.	HARDWARE LABORATORY	PR	50	20	05	F

FIRST TERM TOTAL = 284/750.
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8054327 SHEKHAR SAMIR SHAILESH KUMAR GUNJAN PUSHPA RANI GUNJAN , 71045609D , , PICT , T8054327

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	54	P
02.	DATA COMMUNICATION	PP	100	40	49	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	46	P
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	52	P
05.	THEORY OF COMPUTATION	PP	100	40	40	P
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	34	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	39	P
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	15	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	21	P
10.	HARDWARE LABORATORY	TW	25	10	19	P
11.	HARDWARE LABORATORY	PR	50	20	36	P

FIRST TERM TOTAL = 405/750.
ORDN. 1 MARKS :

T8054328 SHERKAR ANIRUDH UMAKANT SMITA , 70925601K , , PICT , T8054328

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	40	P
02.	DATA COMMUNICATION	PP	100	40	40	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	40	P
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	28	F
05.	THEORY OF COMPUTATION	PP	100	40	42	P
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	40	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	42	P
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	19	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	32	P
10.	HARDWARE LABORATORY	TW	25	10	21	P
11.	HARDWARE LABORATORY	PR	50	20	33	P

FIRST TERM TOTAL = 377/750.
ORDN. 1 MARKS :

T8054329 SHINDE AMIT KEDARNATH SANGEETA , 71045614L , , PICT , T8054329

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	52	P
02.	DATA COMMUNICATION	PP	100	40	42	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	29	F
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	26	F
05.	THEORY OF COMPUTATION	PP	100	40	45	P
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	38	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	28	P
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	15	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	20	P
10.	HARDWARE LABORATORY	TW	25	10	17	P
11.	HARDWARE LABORATORY	PR	50	20	36	P

FIRST TERM TOTAL = 348/750.
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8054330 SHREYAS KUMAR PANIGRAHI MINATI , 71045618C , , PICT , T8054330

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	48	P
02.	DATA COMMUNICATION	PP	100	40	55	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	43	P
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	40	P
05.	THEORY OF COMPUTATION	PP	100	40	41	P
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	28	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	38	P
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	16	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	27	P
10.	HARDWARE LABORATORY	TW	25	10	17	P
11.	HARDWARE LABORATORY	PR	50	20	34	P

FIRST TERM TOTAL = 387/750.
ORDN. 1 MARKS :

T8054331 SHRUTI SWAGATIKA SUJATA , 71045619M , , PICT , T8054331

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	50	P
02.	DATA COMMUNICATION	PP	100	40	50	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	55	P
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	55	P
05.	THEORY OF COMPUTATION	PP	100	40	46	P
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	40	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	39	P
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	19	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	29	P
10.	HARDWARE LABORATORY	TW	25	10	19	P
11.	HARDWARE LABORATORY	PR	50	20	23	P

FIRST TERM TOTAL = 425/750.
ORDN. 1 MARKS :

T8054332 SIDDHA MONIKA SUNIL JAYASHREE , 71045623K , , PICT , T8054332

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	64	P
02.	DATA COMMUNICATION	PP	100	40	63	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	63	P
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	53	P
05.	THEORY OF COMPUTATION	PP	100	40	60	P
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	41	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	38	P
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	21	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	30	P
10.	HARDWARE LABORATORY	TW	25	10	21	P
11.	HARDWARE LABORATORY	PR	50	20	37	P

FIRST TERM TOTAL = 491/750.
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8054333 SINARE NUPOOR SANJAY KALPANA , 71045626D , , PICT , T8054333

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	61	P
02.	DATA COMMUNICATION	PP	100	40	54	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	62	P
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	52	P
05.	THEORY OF COMPUTATION	PP	100	40	56	P
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	40	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	34	P
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	20	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	33	P
10.	HARDWARE LABORATORY	TW	25	10	20	P
11.	HARDWARE LABORATORY	PR	50	20	30	P

FIRST TERM TOTAL = 462/750.
ORDN. 1 MARKS :

T8054334 SINGHAL PANKAJ SHRIBHAGWAN ANITA , 71045627B , , PICT , T8054334

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	69	P
02.	DATA COMMUNICATION	PP	100	40	56	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	58	P
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	54	P
05.	THEORY OF COMPUTATION	PP	100	40	65	P
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	36	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	40	P
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	20	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	34	P
10.	HARDWARE LABORATORY	TW	25	10	18	P
11.	HARDWARE LABORATORY	PR	50	20	35	P

FIRST TERM TOTAL = 485/750.
ORDN. 1 MARKS :

T8054335 SONAWANE KALINDI DATTATRAYA MAYA , 71045631L , , PICT , T8054335

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	49	P
02.	DATA COMMUNICATION	PP	100	40	55	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	44	P
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	45	P
05.	THEORY OF COMPUTATION	PP	100	40	43	P
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	37	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	10	F
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	16	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	31	P
10.	HARDWARE LABORATORY	TW	25	10	19	P
11.	HARDWARE LABORATORY	PR	50	20	33	P

FIRST TERM TOTAL = 382/750.
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8054336 SOUNDANKAR KOMAL RAJENDRA KALPANA , 71045634E , , PICT , T8054336

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	22	F
02.	DATA COMMUNICATION	PP	100	40	42	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	42	P
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	40	P
05.	THEORY OF COMPUTATION	PP	100	40	40	P
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	34	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	24	P
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	15	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	21	P
10.	HARDWARE LABORATORY	TW	25	10	19	P
11.	HARDWARE LABORATORY	PR	50	20	25	P

FIRST TERM TOTAL = 324/750.
ORDN. 1 MARKS :

T8054337 SUKHANI KUMAR DAYARAM ARTI , 71045636M , , PICT , T8054337

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	46	P
02.	DATA COMMUNICATION	PP	100	40	56	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	54	P
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	48	P
05.	THEORY OF COMPUTATION	PP	100	40	51	P
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	40	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	45	P
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	16	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	21	P
10.	HARDWARE LABORATORY	TW	25	10	19	P
11.	HARDWARE LABORATORY	PR	50	20	39	P

FIRST TERM TOTAL = 435/750.
ORDN. 1 MARKS :

T8054338 TAHAKIK AVINASH ABASAHEB SHAKUNTALA , 71045638H , , PICT , T8054338

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	53	P
02.	DATA COMMUNICATION	PP	100	40	55	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	59	P
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	57	P
05.	THEORY OF COMPUTATION	PP	100	40	62	P
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	32	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	15	F
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	15	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	32	P
10.	HARDWARE LABORATORY	TW	25	10	17	P
11.	HARDWARE LABORATORY	PR	50	20	40	P

FIRST TERM TOTAL = 437/750.
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8054339 TANPURE AJAY PRAKASH SUNITA , 71045639F , , PICT , T8054339

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	52	P
02.	DATA COMMUNICATION	PP	100	40	54	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	54	P
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	52	P
05.	THEORY OF COMPUTATION	PP	100	40	46	P
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	37	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	37	P
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	15	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	33	P
10.	HARDWARE LABORATORY	TW	25	10	18	P
11.	HARDWARE LABORATORY	PR	50	20	26	P

FIRST TERM TOTAL = 424/750.
ORDN. 1 MARKS :

T8054340 TARGE SOURABH SURESH MANISHA , 71132441H , , PICT , T8054340

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	60	P
02.	DATA COMMUNICATION	PP	100	40	60	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	66	P
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	45	P
05.	THEORY OF COMPUTATION	PP	100	40	58	P
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	45	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	42	P
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	14	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	25	P
10.	HARDWARE LABORATORY	TW	25	10	20	P
11.	HARDWARE LABORATORY	PR	50	20	35	P

FIRST TERM TOTAL = 470/750.
ORDN. 1 MARKS :

T8054341 TATTI ADITI ANIL ASHWINI , 71045641H , , PICT , T8054341

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	54	P
02.	DATA COMMUNICATION	PP	100	40	50	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	57	P
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	53	P
05.	THEORY OF COMPUTATION	PP	100	40	54	P
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	42	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	45	P
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	19	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	31	P
10.	HARDWARE LABORATORY	TW	25	10	19	P
11.	HARDWARE LABORATORY	PR	50	20	28	P

FIRST TERM TOTAL = 452/750.
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8054342 THAKARE DHANRAJSINGH SURESH JAYA , 71045646J , , PICT , T8054342

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	45	P
02.	DATA COMMUNICATION	PP	100	40	50	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	48	P
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	40	P
05.	THEORY OF COMPUTATION	PP	100	40	52	P
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	42	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	28	P
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	15	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	18	F
10.	HARDWARE LABORATORY	TW	25	10	20	P
11.	HARDWARE LABORATORY	PR	50	20	32	P

FIRST TERM TOTAL = 390/750.
ORDN. 1 MARKS :

T8054343 THAKARE SAGAR SATISH JAYASHREE , 70925632K , , PICT , T8054343

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	21	F
02.	DATA COMMUNICATION	PP	100	40	47	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	16	F
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	21	F
05.	THEORY OF COMPUTATION	PP	100	40	27	F
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	30	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	AA	F
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	12	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	15	F
10.	HARDWARE LABORATORY	TW	25	10	15	P
11.	HARDWARE LABORATORY	PR	50	20	08	F

FIRST TERM TOTAL = 212/750.
ORDN. 1 MARKS :

T8054344 THAKKAR BHAVIN PRANAY VARSHA , 71045647G , , PICT , T8054344

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	56	P
02.	DATA COMMUNICATION	PP	100	40	63	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	52	P
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	40	P
05.	THEORY OF COMPUTATION	PP	100	40	31	F
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	34	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	41	P
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	15	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	30	P
10.	HARDWARE LABORATORY	TW	25	10	18	P
11.	HARDWARE LABORATORY	PR	50	20	37	P

FIRST TERM TOTAL = 417/750.
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8054346 UTKARSH MISHRA MAMTA , 71045654K , , PICT , T8054346

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	56	P
02.	DATA COMMUNICATION	PP	100	40	62	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	40	P
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	50	P
05.	THEORY OF COMPUTATION	PP	100	40	46	P
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	20	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	26	P
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	14	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	20	P
10.	HARDWARE LABORATORY	TW	25	10	16	P
11.	HARDWARE LABORATORY	PR	50	20	05	F

FIRST TERM TOTAL = 355/750.
ORDN. 1 MARKS :

T8054348 VAYKOLE TEJAS ANIL KALPANA , 71045656F , , PICT , T8054348

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	43	P
02.	DATA COMMUNICATION	PP	100	40	47	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	58	P
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	40	P
05.	THEORY OF COMPUTATION	PP	100	40	44	P
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	41	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	32	P
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	18	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	32	P
10.	HARDWARE LABORATORY	TW	25	10	20	P
11.	HARDWARE LABORATORY	PR	50	20	08	F

FIRST TERM TOTAL = 383/750.
ORDN. 1 MARKS :

T8054349 VEER ANJALI SUNDARDAS URMILA , 71132442F , , PICT , T8054349

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	67	P
02.	DATA COMMUNICATION	PP	100	40	54	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	64	P
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	52	P
05.	THEORY OF COMPUTATION	PP	100	40	51	P
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	36	P
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	39	P
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	22	P
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	27	P
10.	HARDWARE LABORATORY	TW	25	10	22	P
11.	HARDWARE LABORATORY	PR	50	20	35	P

FIRST TERM TOTAL = 469/750.
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8054350 VEERKAR PARTHA PRABODH MEDHA , 71045657D , , PICT , T8054350

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	40	P	
02. DATA COMMUNICATION	PP	100	40	44	P	
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	55	P	
04. DIGITAL SIGNAL PROCESSING	PP	100	40	46	P	
05. THEORY OF COMPUTATION	PP	100	40	44	P	
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	41	P	
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	43	P	
08. SIGNAL PROCESSING LABORATORY	TW	25	10	20	P	
09. SIGNAL PROCESSING LABORATORY	OR	50	20	20	P	
10. HARDWARE LABORATORY	TW	25	10	20	P	
11. HARDWARE LABORATORY	PR	50	20	33	P	

FIRST TERM TOTAL = 406/750.
ORDN. 1 MARKS :

T8054351 WAJE ROHIT GITARAM PARVATI , 71045664G , , PICT , T8054351

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	52	P	
02. DATA COMMUNICATION	PP	100	40	58	P	
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	55	P	
04. DIGITAL SIGNAL PROCESSING	PP	100	40	56	P	
05. THEORY OF COMPUTATION	PP	100	40	53	P	
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	41	P	
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	42	P	
08. SIGNAL PROCESSING LABORATORY	TW	25	10	22	P	
09. SIGNAL PROCESSING LABORATORY	OR	50	20	26	P	
10. HARDWARE LABORATORY	TW	25	10	22	P	
11. HARDWARE LABORATORY	PR	50	20	36	P	

FIRST TERM TOTAL = 463/750.
ORDN. 1 MARKS :

T8054352 ARASHDEEP SINGH HEIR RUPINDER , 70925338K , T8054208 , PICT , T8054352

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	40	P C	12. PRINCIPLES OF PROGRAMMING LANG.	PP	100	40	AA	F	
02. DATA COMMUNICATION	PP	100	40	51	P	13. COMPUTER NETWORKS	PP	100	40	40	P	C
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	40	P C	14. FINANCE & MANAGEMENT INFORMA.SYS.	PP	100	40	AA	F	
04. DIGITAL SIGNAL PROCESSING	PP	100	40	40	P	15. SYSTEMS PROGRAMMING & OPERA.SYS.	PP	100	40	AA	F	
05. THEORY OF COMPUTATION	PP	100	40	29	F	16. SOFTWARE ENGINEERING	PP	100	40	14	F	
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	20	P C	17. SOFTWARE LABORATORY	TW	25	10	10	P	C
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	20	P	18. SOFTWARE LABORATORY	PR	50	20	08	F	
08. SIGNAL PROCESSING LABORATORY	TW	25	10	10	P C	19. COMPUTER NETWORK	TW	25	10	12	P	C
09. SIGNAL PROCESSING LABORATORY	OR	50	20	22	P C	20. COMPUTER NETWORK	OR	50	20	24	P	C
10. HARDWARE LABORATORY	TW	25	10	10	P C	21. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	25	P	C
11. HARDWARE LABORATORY	PR	50	20	26	P C	22. SEMINAR AND TECHNICAL COMMUNI.	TW	50	20	20	P	C

GRAND TOTAL = 461/1500, RESULT: FAILS
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8054353 ARCHANA KUMAR SHYAMALA , 71072124C , T8054209 , PICT , T8054353

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	44	P C	12.	PRINCIPLES OF PROGRAMMING LANG.	PP	100	40	63	P C
02.	DATA COMMUNICATION	PP	100	40	57	P C	13.	COMPUTER NETWORKS	PP	100	40	71	P C
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	66	P C	14.	FINANCE & MANAGEMENT INFORMA.SYS.	PP	100	40	70	P C
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	43	P C	15.	SYSTEMS PROGRAMMING & OPERA.SYS.	PP	100	40	60	P C
05.	THEORY OF COMPUTATION	PP	100	40	63	P C	16.	SOFTWARE ENGINEERING	PP	100	40	52	P C
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	47	P C	17.	SOFTWARE LABORATORY	TW	25	10	22	P C
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	36	P C	18.	SOFTWARE LABORATORY	PR	50	20	34	P C
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	23	P C	19.	COMPUTER NETWORK	TW	25	10	21	P C
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	20	P C	20.	COMPUTER NETWORK	OR	50	20	36	P
10.	HARDWARE LABORATORY	TW	25	10	23	P C	21.	SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	43	P C
11.	HARDWARE LABORATORY	PR	50	20	35	P C	22.	SEMINAR AND TECHNICAL COMMUNI.	TW	50	20	45	P C

GRAND TOTAL = 974/1500, RESULT: FIRST CLASS
ORDN. 1 MARKS :

T8054354 ARUNDHATI NAVADA REKHA , 70925340M , T8054210 , PICT , T8054354

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	41	P C	12.	PRINCIPLES OF PROGRAMMING LANG.	PP	100	40	52	P C
02.	DATA COMMUNICATION	PP	100	40	40	P C	13.	COMPUTER NETWORKS	PP	100	40	57	P C
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	40	P C	14.	FINANCE & MANAGEMENT INFORMA.SYS.	PP	100	40	54	P C
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	52	P	15.	SYSTEMS PROGRAMMING & OPERA.SYS.	PP	100	40	47	P C
05.	THEORY OF COMPUTATION	PP	100	40	62	P C	16.	SOFTWARE ENGINEERING	PP	100	40	40	P C
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	27	P C	17.	SOFTWARE LABORATORY	TW	25	10	14	P C
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	30	P C	18.	SOFTWARE LABORATORY	PR	50	20	35	P C
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	11	P C	19.	COMPUTER NETWORK	TW	25	10	14	P C
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	28	P C	20.	COMPUTER NETWORK	OR	50	20	26	P
10.	HARDWARE LABORATORY	TW	25	10	11	P C	21.	SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	28	P C
11.	HARDWARE LABORATORY	PR	50	20	34	P C	22.	SEMINAR AND TECHNICAL COMMUNI.	TW	50	20	36	P C

GRAND TOTAL = 779/1500, RESULT: SECOND CLASS RESULT RESERVED FOR BKLG
ORDN. 1 MARKS :

T8054355 AVLEEN UPPAL INDERDEEP , 70801350D , T8054213 , PICT , T8054355

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	46	P C	12.	PRINCIPLES OF PROGRAMMING LANG.	PP	100	40	AA	F
02.	DATA COMMUNICATION	PP	100	40	AA	F	13.	COMPUTER NETWORKS	PP	100	40	52	P C
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	40	P C	14.	FINANCE & MANAGEMENT INFORMA.SYS.	PP	100	40	53	P C
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	AA	F	15.	SYSTEMS PROGRAMMING & OPERA.SYS.	PP	100	40	40	P C
05.	THEORY OF COMPUTATION	PP	100	40	40	P C	16.	SOFTWARE ENGINEERING	PP	100	40	AA	F
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	22	P C	17.	SOFTWARE LABORATORY	TW	25	10	13	P C
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	AA	F	18.	SOFTWARE LABORATORY	PR	50	20	AA	F
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	15	P C	19.	COMPUTER NETWORK	TW	25	10	12	P C
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	27	P C	20.	COMPUTER NETWORK	OR	50	20	AA	F
10.	HARDWARE LABORATORY	TW	25	10	16	P C	21.	SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	27	P C
11.	HARDWARE LABORATORY	PR	50	20	AA	F	22.	SEMINAR AND TECHNICAL COMMUNI.	TW	50	20	38	P C

GRAND TOTAL = 441/1500, RESULT: FAILS RESULT RESERVED FOR BKLG
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8054356 BAGADE PRASANNA MADHURAJE SINDHU , 70925350J , T8054214 , PICT , T8054356

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	52	P C	12.	PRINCIPLES OF PROGRAMMING LANG.	PP	100	40	43 P C
02.	DATA COMMUNICATION	PP	100	40	40	P C	13.	COMPUTER NETWORKS	PP	100	40	41 P C
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	48	P C	14.	FINANCE & MANAGEMENT INFORMA.SYS.	PP	100	40	49 P C
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	43	P C	15.	SYSTEMS PROGRAMMING & OPERA.SYS.	PP	100	40	56 P C
05.	THEORY OF COMPUTATION	PP	100	40	40	P C	16.	SOFTWARE ENGINEERING	PP	100	40	40 P C
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	39	P C	17.	SOFTWARE LABORATORY	TW	25	10	19 P C
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	36	P C	18.	SOFTWARE LABORATORY	PR	50	20	36 P
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	19	P C	19.	COMPUTER NETWORK	TW	25	10	19 P C
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	34	P C	20.	COMPUTER NETWORK	OR	50	20	30 P C
10.	HARDWARE LABORATORY	TW	25	10	18	P C	21.	SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	37 P C
11.	HARDWARE LABORATORY	PR	50	20	45	P C	22.	SEMINAR AND TECHNICAL COMMUNI.	TW	50	20	38 P C

GRAND TOTAL = 822+03/1500, RESULT: HIGHER SECOND CLASS [O.2]
ORDN. 1 MARKS :

T8054357 BHATEWARA SAGAR RAJMAL NALINI , 70925364J , T8054219 , PICT , T8054357

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	43	P C	12.	PRINCIPLES OF PROGRAMMING LANG.	PP	100	40	44 P C
02.	DATA COMMUNICATION	PP	100	40	54	P C	13.	COMPUTER NETWORKS	PP	100	40	45 P C
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	57	P C	14.	FINANCE & MANAGEMENT INFORMA.SYS.	PP	100	40	61 P C
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	58	P C	15.	SYSTEMS PROGRAMMING & OPERA.SYS.	PP	100	40	66 P C
05.	THEORY OF COMPUTATION	PP	100	40	56	P C	16.	SOFTWARE ENGINEERING	PP	100	40	47 P C
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	40	P C	17.	SOFTWARE LABORATORY	TW	25	10	19 P C
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	42	P C	18.	SOFTWARE LABORATORY	PR	50	20	30 P C
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	18	P C	19.	COMPUTER NETWORK	TW	25	10	21 P C
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	20	P C	20.	COMPUTER NETWORK	OR	50	20	32 P
10.	HARDWARE LABORATORY	TW	25	10	19	P C	21.	SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	31 P C
11.	HARDWARE LABORATORY	PR	50	20	38	P C	22.	SEMINAR AND TECHNICAL COMMUNI.	TW	50	20	39 P C

GRAND TOTAL = 880/1500, RESULT: HIGHER SECOND CLASS
ORDN. 1 MARKS :

T8054358 CHITALE MANDAR SUHAS NEELA , 70701524D , T8054223 , PICT , T8054358

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	42	P	12.	PRINCIPLES OF PROGRAMMING LANG.	PP	100	40	41 P C
02.	DATA COMMUNICATION	PP	100	40	41	P C	13.	COMPUTER NETWORKS	PP	100	40	40 P C
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	53	P C	14.	FINANCE & MANAGEMENT INFORMA.SYS.	PP	100	40	47 P C
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	40	P C	15.	SYSTEMS PROGRAMMING & OPERA.SYS.	PP	100	40	51 P
05.	THEORY OF COMPUTATION	PP	100	40	44	P C	16.	SOFTWARE ENGINEERING	PP	100	40	40 P C
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	42	P C	17.	SOFTWARE LABORATORY	TW	25	10	20 P C
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	25	P	18.	SOFTWARE LABORATORY	PR	50	20	25 P
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	22	P C	19.	COMPUTER NETWORK	TW	25	10	20 P C
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	37	P C	20.	COMPUTER NETWORK	OR	50	20	35 P C
10.	HARDWARE LABORATORY	TW	25	10	20	P C	21.	SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	39 P C
11.	HARDWARE LABORATORY	PR	50	20	24	P C	22.	SEMINAR AND TECHNICAL COMMUNI.	TW	50	20	38 P C

GRAND TOTAL = 786/1500, RESULT: SECOND CLASS
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8054359 DESHPANDE KAIVALYA ARUN SARITA , 70925397E , T8054227 , PICT , T8054359

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	43	P C	12.	PRINCIPLES OF PROGRAMMING LANG.	PP	100	40	43	P
02.	DATA COMMUNICATION	PP	100	40	55	P C	13.	COMPUTER NETWORKS	PP	100	40	65	P C
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	61	P C	14.	FINANCE & MANAGEMENT INFORMA.SYS.	PP	100	40	58	P C
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	62	P C	15.	SYSTEMS PROGRAMMING & OPERA.SYS.	PP	100	40	69	P C
05.	THEORY OF COMPUTATION	PP	100	40	46	P C	16.	SOFTWARE ENGINEERING	PP	100	40	40	P C
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	34	P C	17.	SOFTWARE LABORATORY	TW	25	10	18	P C
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	30	P C	18.	SOFTWARE LABORATORY	PR	50	20	38	P C
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	20	P C	19.	COMPUTER NETWORK	TW	25	10	18	P C
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	30	P C	20.	COMPUTER NETWORK	OR	50	20	44	P C
10.	HARDWARE LABORATORY	TW	25	10	17	P C	21.	SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	30	P C
11.	HARDWARE LABORATORY	PR	50	20	36	P C	22.	SEMINAR AND TECHNICAL COMMUNI.	TW	50	20	33	P C

GRAND TOTAL = 890+10/1500, RESULT: FIRST CLASS [0.2]
ORDN. 1 MARKS :

T8054360 DHENDE ADITYA BARMA LATA , 70925401G , T8054229 , PICT , T8054360

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	47	P C	12.	PRINCIPLES OF PROGRAMMING LANG.	PP	100	40	53	P C
02.	DATA COMMUNICATION	PP	100	40	50	P C	13.	COMPUTER NETWORKS	PP	100	40	66	P C
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	63	P C	14.	FINANCE & MANAGEMENT INFORMA.SYS.	PP	100	40	62	P C
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	45	P C	15.	SYSTEMS PROGRAMMING & OPERA.SYS.	PP	100	40	48	P C
05.	THEORY OF COMPUTATION	PP	100	40	46	P C	16.	SOFTWARE ENGINEERING	PP	100	40	42	P C
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	28	P C	17.	SOFTWARE LABORATORY	TW	25	10	12	P C
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	30	P C	18.	SOFTWARE LABORATORY	PR	50	20	20	P
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	19	P C	19.	COMPUTER NETWORK	TW	25	10	16	P C
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	27	P C	20.	COMPUTER NETWORK	OR	50	20	28	P C
10.	HARDWARE LABORATORY	TW	25	10	18	P C	21.	SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	28	P C
11.	HARDWARE LABORATORY	PR	50	20	20	P C	22.	SEMINAR AND TECHNICAL COMMUNI.	TW	50	20	34	P C

GRAND TOTAL = 802/1500, RESULT: SECOND CLASS
ORDN. 1 MARKS :

T8054361 DONGRE PALLAVI TRYAMAKRAO KUSUM , 70925410F , T8054232 , PICT , T8054361

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	47	P C	12.	PRINCIPLES OF PROGRAMMING LANG.	PP	100	40	62	P C
02.	DATA COMMUNICATION	PP	100	40	52	P C	13.	COMPUTER NETWORKS	PP	100	40	61	P C
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	70	P C	14.	FINANCE & MANAGEMENT INFORMA.SYS.	PP	100	40	70	P C
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	49	P C	15.	SYSTEMS PROGRAMMING & OPERA.SYS.	PP	100	40	40	P C
05.	THEORY OF COMPUTATION	PP	100	40	51	P C	16.	SOFTWARE ENGINEERING	PP	100	40	47	P C
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	35	P C	17.	SOFTWARE LABORATORY	TW	25	10	20	P C
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	34	P C	18.	SOFTWARE LABORATORY	PR	50	20	25	P C
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	19	P C	19.	COMPUTER NETWORK	TW	25	10	22	P C
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	27	P C	20.	COMPUTER NETWORK	OR	50	20	37	P
10.	HARDWARE LABORATORY	TW	25	10	18	P C	21.	SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	36	P C
11.	HARDWARE LABORATORY	PR	50	20	35	P C	22.	SEMINAR AND TECHNICAL COMMUNI.	TW	50	20	33	P C

GRAND TOTAL = 890+10/1500, RESULT: FIRST CLASS [0.2]
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8054362 GADIA VARUN RAJESH SADHANA , 70925415G , T8054233 , PICT , T8054362

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	41	P C	12.	PRINCIPLES OF PROGRAMMING LANG.	PP	100	40	53 P C
02.	DATA COMMUNICATION	PP	100	40	44	P C	13.	COMPUTER NETWORKS	PP	100	40	72 P C
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	52	P C	14.	FINANCE & MANAGEMENT INFORMA.SYS.	PP	100	40	65 P C
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	51	P C	15.	SYSTEMS PROGRAMMING & OPERA.SYS.	PP	100	40	43 P C
05.	THEORY OF COMPUTATION	PP	100	40	62	P C	16.	SOFTWARE ENGINEERING	PP	100	40	58 P C
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	28	P C	17.	SOFTWARE LABORATORY	TW	25	10	17 P C
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	30	P C	18.	SOFTWARE LABORATORY	PR	50	20	37 P
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	15	P C	19.	COMPUTER NETWORK	TW	25	10	19 P C
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	35	P C	20.	COMPUTER NETWORK	OR	50	20	37 P
10.	HARDWARE LABORATORY	TW	25	10	11	P C	21.	SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	28 P C
11.	HARDWARE LABORATORY	PR	50	20	34	P C	22.	SEMINAR AND TECHNICAL COMMUNI.	TW	50	20	42 P C

GRAND TOTAL = 874/1500, RESULT: HIGHER SECOND CLASS
ORDN. 1 MARKS :

T8054363 GAIKWAD SHIVRAJ VITTHALRAO SINDHU , 70925417C , T8054235 , PICT , T8054363

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	43	P C	12.	PRINCIPLES OF PROGRAMMING LANG.	PP	100	40	40 P C
02.	DATA COMMUNICATION	PP	100	40	47	P C	13.	COMPUTER NETWORKS	PP	100	40	50 P C
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	45	P C	14.	FINANCE & MANAGEMENT INFORMA.SYS.	PP	100	40	50 P C
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	54	P C	15.	SYSTEMS PROGRAMMING & OPERA.SYS.	PP	100	40	43 P C
05.	THEORY OF COMPUTATION	PP	100	40	46	P C	16.	SOFTWARE ENGINEERING	PP	100	40	05 F
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	20	P C	17.	SOFTWARE LABORATORY	TW	25	10	10 P C
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	21	P	18.	SOFTWARE LABORATORY	PR	50	20	05 F
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	16	P C	19.	COMPUTER NETWORK	TW	25	10	14 P C
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	40	P C	20.	COMPUTER NETWORK	OR	50	20	42 P C
10.	HARDWARE LABORATORY	TW	25	10	11	P C	21.	SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	27 P C
11.	HARDWARE LABORATORY	PR	50	20	28	P C	22.	SEMINAR AND TECHNICAL COMMUNI.	TW	50	20	28 P C

GRAND TOTAL = 685/1500, RESULT: FAILS A.T.K.T.
ORDN. 1 MARKS :

T8054364 GALANDE SAURABH SANJAY SUNANDA , 70925420C , T8054236 , PICT , T8054364

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	49	P C	12.	PRINCIPLES OF PROGRAMMING LANG.	PP	100	40	58 P C
02.	DATA COMMUNICATION	PP	100	40	48	P C	13.	COMPUTER NETWORKS	PP	100	40	57 P C
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	74	P C	14.	FINANCE & MANAGEMENT INFORMA.SYS.	PP	100	40	57 P C
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	63	P C	15.	SYSTEMS PROGRAMMING & OPERA.SYS.	PP	100	40	60 P C
05.	THEORY OF COMPUTATION	PP	100	40	69	P C	16.	SOFTWARE ENGINEERING	PP	100	40	40 P C
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	40	P C	17.	SOFTWARE LABORATORY	TW	25	10	19 P C
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	38	P C	18.	SOFTWARE LABORATORY	PR	50	20	35 P
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	20	P C	19.	COMPUTER NETWORK	TW	25	10	20 P C
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	37	P C	20.	COMPUTER NETWORK	OR	50	20	25 P C
10.	HARDWARE LABORATORY	TW	25	10	17	P C	21.	SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	37 P C
11.	HARDWARE LABORATORY	PR	50	20	39	P C	22.	SEMINAR AND TECHNICAL COMMUNI.	TW	50	20	42 P C

GRAND TOTAL = 944/1500, RESULT: FIRST CLASS
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8054365 GHULE CHAITALI CHANDRASHEKHAR RAJESHREE , 70925431J , T8054243 , PICT , T8054365

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	54	P C	12.	PRINCIPLES OF PROGRAMMING LANG.	PP	100	40	59 P C
02.	DATA COMMUNICATION	PP	100	40	49	P C	13.	COMPUTER NETWORKS	PP	100	40	56 P C
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	65	P C	14.	FINANCE & MANAGEMENT INFORMA.SYS.	PP	100	40	60 P C
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	40	P C	15.	SYSTEMS PROGRAMMING & OPERA.SYS.	PP	100	40	65 P C
05.	THEORY OF COMPUTATION	PP	100	40	64	P C	16.	SOFTWARE ENGINEERING	PP	100	40	40 P C
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	27	P C	17.	SOFTWARE LABORATORY	TW	25	10	18 P C
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	30	P C	18.	SOFTWARE LABORATORY	PR	50	20	30 P
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	16	P C	19.	COMPUTER NETWORK	TW	25	10	16 P C
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	28	P C	20.	COMPUTER NETWORK	OR	50	20	38 P C
10.	HARDWARE LABORATORY	TW	25	10	15	P C	21.	SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	33 P C
11.	HARDWARE LABORATORY	PR	50	20	28	P C	22.	SEMINAR AND TECHNICAL COMMUNI.	TW	50	20	38 P C

GRAND TOTAL = 869/1500, RESULT: HIGHER SECOND CLASS
ORDN. 1 MARKS :

T8054366 GIDWANI RAVI ASHOK KANCHAN , 70925432G , T8054244 , PICT , T8054366

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	40	P C	12.	PRINCIPLES OF PROGRAMMING LANG.	PP	100	40	47 P C
02.	DATA COMMUNICATION	PP	100	40	41	P C	13.	COMPUTER NETWORKS	PP	100	40	46 P C
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	54	P C	14.	FINANCE & MANAGEMENT INFORMA.SYS.	PP	100	40	40 P C
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	59	P C	15.	SYSTEMS PROGRAMMING & OPERA.SYS.	PP	100	40	55 P C
05.	THEORY OF COMPUTATION	PP	100	40	50	P C	16.	SOFTWARE ENGINEERING	PP	100	40	AA F
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	39	P C	17.	SOFTWARE LABORATORY	TW	25	10	18 P C
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	28	P C	18.	SOFTWARE LABORATORY	PR	50	20	28 P C
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	18	P C	19.	COMPUTER NETWORK	TW	25	10	13 P C
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	38	P C	20.	COMPUTER NETWORK	OR	50	20	36 P C
10.	HARDWARE LABORATORY	TW	25	10	20	P C	21.	SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	34 P C
11.	HARDWARE LABORATORY	PR	50	20	42	P C	22.	SEMINAR AND TECHNICAL COMMUNI.	TW	50	20	36 P C

GRAND TOTAL = 782/1500, RESULT: FAILS A.T.K.T.
ORDN. 1 MARKS :

T8054367 HIRAN KALPESH SURESH KALPANA , 70925438F , T8054248 , PICT , T8054367

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	40	P C	12.	PRINCIPLES OF PROGRAMMING LANG.	PP	100	40	40 P C
02.	DATA COMMUNICATION	PP	100	40	40	P C	13.	COMPUTER NETWORKS	PP	100	40	47 P C
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	40	P C	14.	FINANCE & MANAGEMENT INFORMA.SYS.	PP	100	40	40 P C
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	44	P C	15.	SYSTEMS PROGRAMMING & OPERA.SYS.	PP	100	40	40 P C
05.	THEORY OF COMPUTATION	PP	100	40	56	P C	16.	SOFTWARE ENGINEERING	PP	100	40	40 P
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	30	P C	17.	SOFTWARE LABORATORY	TW	25	10	14 P C
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	28	P C	18.	SOFTWARE LABORATORY	PR	50	20	28 P C
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	15	P C	19.	COMPUTER NETWORK	TW	25	10	13 P C
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	21	P C	20.	COMPUTER NETWORK	OR	50	20	20$ P
10.	HARDWARE LABORATORY	TW	25	10	16	P C	21.	SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	27 P C
11.	HARDWARE LABORATORY	PR	50	20	32	P C	22.	SEMINAR AND TECHNICAL COMMUNI.	TW	50	20	23 P C

GRAND TOTAL = 694/1500, RESULT: PASS CLASS [$ O.1]
ORDN. 1 MARKS : (20)2,

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8054368 JAHAGIRDAR SOUMYA SUDHINDRA SUKANYA , 70701467M , T8054252 , PICT , T8054368

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	46	P	12.	PRINCIPLES OF PROGRAMMING LANG.	PP	100	40	40 P C
02.	DATA COMMUNICATION	PP	100	40	42	P C	13.	COMPUTER NETWORKS	PP	100	40	58 P C
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	54	P C	14.	FINANCE & MANAGEMENT INFORMA.SYS.	PP	100	40	59 P C
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	40	P C	15.	SYSTEMS PROGRAMMING & OPERA.SYS.	PP	100	40	43 P C
05.	THEORY OF COMPUTATION	PP	100	40	47	P C	16.	SOFTWARE ENGINEERING	PP	100	40	42 P C
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	35	P C	17.	SOFTWARE LABORATORY	TW	25	10	16 P C
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	25	P C	18.	SOFTWARE LABORATORY	PR	50	20	21 P
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	15	P C	19.	COMPUTER NETWORK	TW	25	10	16 P C
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	24	P C	20.	COMPUTER NETWORK	OR	50	20	28 P
10.	HARDWARE LABORATORY	TW	25	10	17	P C	21.	SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	28 P C
11.	HARDWARE LABORATORY	PR	50	20	38	P C	22.	SEMINAR AND TECHNICAL COMMUNI.	TW	50	20	31 P C

GRAND TOTAL = 765/1500, RESULT: SECOND CLASS
ORDN. 1 MARKS :

T8054369 JAJU PRATIKSHA VIJAYKUMAR SULOCHANA , 70925450E , T8054254 , PICT , T8054369

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	42	P C	12.	PRINCIPLES OF PROGRAMMING LANG.	PP	100	40	51 P C
02.	DATA COMMUNICATION	PP	100	40	40	P C	13.	COMPUTER NETWORKS	PP	100	40	57 P C
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	54	P C	14.	FINANCE & MANAGEMENT INFORMA.SYS.	PP	100	40	53 P C
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	46	P C	15.	SYSTEMS PROGRAMMING & OPERA.SYS.	PP	100	40	55 P C
05.	THEORY OF COMPUTATION	PP	100	40	53	P C	16.	SOFTWARE ENGINEERING	PP	100	40	40 P C
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	32	P C	17.	SOFTWARE LABORATORY	TW	25	10	20 P C
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	30	P C	18.	SOFTWARE LABORATORY	PR	50	20	36 P C
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	16	P C	19.	COMPUTER NETWORK	TW	25	10	13 P C
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	30	P C	20.	COMPUTER NETWORK	OR	50	20	35 P
10.	HARDWARE LABORATORY	TW	25	10	17	P C	21.	SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	34 P C
11.	HARDWARE LABORATORY	PR	50	20	30	P C	22.	SEMINAR AND TECHNICAL COMMUNI.	TW	50	20	31 P C

GRAND TOTAL = 815+10/1500, RESULT: HIGHER SECOND CLASS [O.2]
ORDN. 1 MARKS :

T8054370 KADAM SHRINIWAS MUKUNDRAO URMILA , 70925458L , T8054257 , PICT , T8054370

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	47	P C	12.	PRINCIPLES OF PROGRAMMING LANG.	PP	100	40	51 P C
02.	DATA COMMUNICATION	PP	100	40	40	P C	13.	COMPUTER NETWORKS	PP	100	40	49 P C
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	43	P C	14.	FINANCE & MANAGEMENT INFORMA.SYS.	PP	100	40	48 P C
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	40	P C	15.	SYSTEMS PROGRAMMING & OPERA.SYS.	PP	100	40	46 P C
05.	THEORY OF COMPUTATION	PP	100	40	49	P C	16.	SOFTWARE ENGINEERING	PP	100	40	46 P
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	22	P C	17.	SOFTWARE LABORATORY	TW	25	10	12 P C
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	20	P C	18.	SOFTWARE LABORATORY	PR	50	20	25 P
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	11	P C	19.	COMPUTER NETWORK	TW	25	10	14 P C
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	32	P C	20.	COMPUTER NETWORK	OR	50	20	32 P C
10.	HARDWARE LABORATORY	TW	25	10	12	P C	21.	SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	23 P C
11.	HARDWARE LABORATORY	PR	50	20	29	P C	22.	SEMINAR AND TECHNICAL COMMUNI.	TW	50	20	25 P C

GRAND TOTAL = 716/1500, RESULT: PASS CLASS
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8054371 KATDARE SANCHIT VINAYAK ALKA , 70925470K , T8054263 , PICT , T8054371

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	48	P C	12.	PRINCIPLES OF PROGRAMMING LANG.	PP	100	40	52	P C
02.	DATA COMMUNICATION	PP	100	40	40	P C	13.	COMPUTER NETWORKS	PP	100	40	58	P C
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	40	P C	14.	FINANCE & MANAGEMENT INFORMA.SYS.	PP	100	40	43	P C
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	45	P C	15.	SYSTEMS PROGRAMMING & OPERA.SYS.	PP	100	40	45	P C
05.	THEORY OF COMPUTATION	PP	100	40	53	P C	16.	SOFTWARE ENGINEERING	PP	100	40	40	P C
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	36	P C	17.	SOFTWARE LABORATORY	TW	25	10	18	P C
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	40	P C	18.	SOFTWARE LABORATORY	PR	50	20	36	P C
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	18	P C	19.	COMPUTER NETWORK	TW	25	10	16	P C
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	23	P C	20.	COMPUTER NETWORK	OR	50	20	36	P
10.	HARDWARE LABORATORY	TW	25	10	20	P C	21.	SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	31	P C
11.	HARDWARE LABORATORY	PR	50	20	32	P C	22.	SEMINAR AND TECHNICAL COMMUNI.	TW	50	20	36	P C

GRAND TOTAL = 806/1500, RESULT: SECOND CLASS
ORDN. 1 MARKS :

T8054372 KAVITKE INDRAJEET ARVIND SUREKHA , 70925471H , T8054264 , PICT , T8054372

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	60	P C	12.	PRINCIPLES OF PROGRAMMING LANG.	PP	100	40	59	P C
02.	DATA COMMUNICATION	PP	100	40	47	P	13.	COMPUTER NETWORKS	PP	100	40	55	P C
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	55	P C	14.	FINANCE & MANAGEMENT INFORMA.SYS.	PP	100	40	51	P C
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	40	P	15.	SYSTEMS PROGRAMMING & OPERA.SYS.	PP	100	40	52	P C
05.	THEORY OF COMPUTATION	PP	100	40	40	P C	16.	SOFTWARE ENGINEERING	PP	100	40	40	P C
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	37	P C	17.	SOFTWARE LABORATORY	TW	25	10	19	P C
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	36	P C	18.	SOFTWARE LABORATORY	PR	50	20	30	P C
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	18	P C	19.	COMPUTER NETWORK	TW	25	10	18	P C
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	28	P C	20.	COMPUTER NETWORK	OR	50	20	35	P C
10.	HARDWARE LABORATORY	TW	25	10	18	P C	21.	SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	35	P C
11.	HARDWARE LABORATORY	PR	50	20	40	P C	22.	SEMINAR AND TECHNICAL COMMUNI.	TW	50	20	38	P C

GRAND TOTAL = 851/1500, RESULT: HIGHER SECOND CLASS
ORDN. 1 MARKS :

T8054373 KHUNE VIKRANT SUNIL CHARU , 70925481E , T8054270 , PICT , T8054373

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	52	P C	12.	PRINCIPLES OF PROGRAMMING LANG.	PP	100	40	41	P C
02.	DATA COMMUNICATION	PP	100	40	40	P C	13.	COMPUTER NETWORKS	PP	100	40	44	P C
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	47	P C	14.	FINANCE & MANAGEMENT INFORMA.SYS.	PP	100	40	52	P C
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	40	P C	15.	SYSTEMS PROGRAMMING & OPERA.SYS.	PP	100	40	59	P C
05.	THEORY OF COMPUTATION	PP	100	40	43	P C	16.	SOFTWARE ENGINEERING	PP	100	40	40	P C
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	24	P C	17.	SOFTWARE LABORATORY	TW	25	10	17	P C
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	31	P C	18.	SOFTWARE LABORATORY	PR	50	20	34	P C
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	12	P C	19.	COMPUTER NETWORK	TW	25	10	15	P C
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	27	P C	20.	COMPUTER NETWORK	OR	50	20	34	P
10.	HARDWARE LABORATORY	TW	25	10	12	P C	21.	SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	33	P C
11.	HARDWARE LABORATORY	PR	50	20	22	P C	22.	SEMINAR AND TECHNICAL COMMUNI.	TW	50	20	25	P C

GRAND TOTAL = 744+06/1500, RESULT: SECOND CLASS [O.2]
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8054374 KSHIRSAGAR RUTUL ULHASRAO SANGITA , 70925489L , T8054276 , PICT , T8054374

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	53	P C	12.	PRINCIPLES OF PROGRAMMING LANG.	PP	100	40	61 P C
02.	DATA COMMUNICATION	PP	100	40	60	P C	13.	COMPUTER NETWORKS	PP	100	40	42 P C
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	63	P C	14.	FINANCE & MANAGEMENT INFORMA.SYS.	PP	100	40	57 P C
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	53	P C	15.	SYSTEMS PROGRAMMING & OPERA.SYS.	PP	100	40	62 P C
05.	THEORY OF COMPUTATION	PP	100	40	66	P C	16.	SOFTWARE ENGINEERING	PP	100	40	51 P C
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	20	P C	17.	SOFTWARE LABORATORY	TW	25	10	17 P C
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	20	P C	18.	SOFTWARE LABORATORY	PR	50	20	42 P C
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	10	P C	19.	COMPUTER NETWORK	TW	25	10	12 P C
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	25	P C	20.	COMPUTER NETWORK	OR	50	20	33 P
10.	HARDWARE LABORATORY	TW	25	10	10	P C	21.	SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	27 P C
11.	HARDWARE LABORATORY	PR	50	20	34	P C	22.	SEMINAR AND TECHNICAL COMMUNI.	TW	50	20	28 P C

GRAND TOTAL = 846/1500, RESULT: HIGHER SECOND CLASS
ORDN. 1 MARKS :

T8054375 KUMAR HARISH BHIM CHANDRAVATIDEVI , 70925491B , T8054277 , PICT , T8054375

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	45	P C	12.	PRINCIPLES OF PROGRAMMING LANG.	PP	100	40	50 P C
02.	DATA COMMUNICATION	PP	100	40	63	P C	13.	COMPUTER NETWORKS	PP	100	40	49 P C
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	53	P C	14.	FINANCE & MANAGEMENT INFORMA.SYS.	PP	100	40	47 P C
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	55	P C	15.	SYSTEMS PROGRAMMING & OPERA.SYS.	PP	100	40	55 P C
05.	THEORY OF COMPUTATION	PP	100	40	62	P C	16.	SOFTWARE ENGINEERING	PP	100	40	42 P C
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	35	P C	17.	SOFTWARE LABORATORY	TW	25	10	18 P C
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	30	P C	18.	SOFTWARE LABORATORY	PR	50	20	40 P C
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	18	P C	19.	COMPUTER NETWORK	TW	25	10	15 P C
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	34	P C	20.	COMPUTER NETWORK	OR	50	20	36 P
10.	HARDWARE LABORATORY	TW	25	10	18	P C	21.	SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	36 P C
11.	HARDWARE LABORATORY	PR	50	20	36	P C	22.	SEMINAR AND TECHNICAL COMMUNI.	TW	50	20	39 P C

GRAND TOTAL = 876/1500, RESULT: HIGHER SECOND CLASS
ORDN. 1 MARKS :

T8054376 MAHAJAN VAIBHAV ANUP ARTI , 71072133B , T8054282 , PICT , T8054376

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	40	P C	12.	PRINCIPLES OF PROGRAMMING LANG.	PP	100	40	60 P C
02.	DATA COMMUNICATION	PP	100	40	48	P C	13.	COMPUTER NETWORKS	PP	100	40	40 P C
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	51	P C	14.	FINANCE & MANAGEMENT INFORMA.SYS.	PP	100	40	40 P C
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	40	P C	15.	SYSTEMS PROGRAMMING & OPERA.SYS.	PP	100	40	47 P C
05.	THEORY OF COMPUTATION	PP	100	40	47	P C	16.	SOFTWARE ENGINEERING	PP	100	40	40 P C
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	36	P C	17.	SOFTWARE LABORATORY	TW	25	10	18 P C
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	22	P	18.	SOFTWARE LABORATORY	PR	50	20	26 P
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	14	P C	19.	COMPUTER NETWORK	TW	25	10	21 P C
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	26	P C	20.	COMPUTER NETWORK	OR	50	20	30 P C
10.	HARDWARE LABORATORY	TW	25	10	15	P C	21.	SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	38 P C
11.	HARDWARE LABORATORY	PR	50	20	32	P C	22.	SEMINAR AND TECHNICAL COMMUNI.	TW	50	20	39 P C

GRAND TOTAL = 770/1500, RESULT: SECOND CLASS
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8054377 MUNDHADA ABHISHEK HARISH HEMLATA , 70925516M , T8054288 , PICT , T8054377

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	59	P C	12.	PRINCIPLES OF PROGRAMMING LANG.	PP	100	40	61	P C
02.	DATA COMMUNICATION	PP	100	40	67	P C	13.	COMPUTER NETWORKS	PP	100	40	47	P C
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	64	P C	14.	FINANCE & MANAGEMENT INFORMA.SYS.	PP	100	40	47	P C
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	40	P C	15.	SYSTEMS PROGRAMMING & OPERA.SYS.	PP	100	40	63	P C
05.	THEORY OF COMPUTATION	PP	100	40	63	P C	16.	SOFTWARE ENGINEERING	PP	100	40	44	P C
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	24	P C	17.	SOFTWARE LABORATORY	TW	25	10	13	P C
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	31	P	18.	SOFTWARE LABORATORY	PR	50	20	22	P
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	13	P C	19.	COMPUTER NETWORK	TW	25	10	15	P C
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	23	P C	20.	COMPUTER NETWORK	OR	50	20	30	P C
10.	HARDWARE LABORATORY	TW	25	10	14	P C	21.	SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	29	P C
11.	HARDWARE LABORATORY	PR	50	20	20	P C	22.	SEMINAR AND TECHNICAL COMMUNI.	TW	50	20	30	P C

GRAND TOTAL = 819+06/1500, RESULT: HIGHER SECOND CLASS [O.2]
ORDN. 1 MARKS :

T8054378 PATIL HARSHAL PRAVINSING SANDHYA , 70925546C , T8054299 , PICT , T8054378

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	40	P C	12.	PRINCIPLES OF PROGRAMMING LANG.	PP	100	40	45	P
02.	DATA COMMUNICATION	PP	100	40	43	P C	13.	COMPUTER NETWORKS	PP	100	40	41	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	43	P C	14.	FINANCE & MANAGEMENT INFORMA.SYS.	PP	100	40	40	P
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	43	P C	15.	SYSTEMS PROGRAMMING & OPERA.SYS.	PP	100	40	47	P C
05.	THEORY OF COMPUTATION	PP	100	40	45	P C	16.	SOFTWARE ENGINEERING	PP	100	40	40	P
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	21	P C	17.	SOFTWARE LABORATORY	TW	25	10	16	P C
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	20	P	18.	SOFTWARE LABORATORY	PR	50	20	20	P
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	13	P C	19.	COMPUTER NETWORK	TW	25	10	10	P C
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	26	P C	20.	COMPUTER NETWORK	OR	50	20	22	P C
10.	HARDWARE LABORATORY	TW	25	10	13	P C	21.	SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	25	P C
11.	HARDWARE LABORATORY	PR	50	20	25	P C	22.	SEMINAR AND TECHNICAL COMMUNI.	TW	50	20	37	P C

GRAND TOTAL = 675/1500, RESULT: PASS CLASS
ORDN. 1 MARKS :

T8054379 PATIL PRIYANKA ARUN ARUNA , 70925550M , T8054302 , PICT , T8054379

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	44	P C	12.	PRINCIPLES OF PROGRAMMING LANG.	PP	100	40	61	P C
02.	DATA COMMUNICATION	PP	100	40	45	P C	13.	COMPUTER NETWORKS	PP	100	40	46	P C
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	58	P C	14.	FINANCE & MANAGEMENT INFORMA.SYS.	PP	100	40	49	P C
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	40	P C	15.	SYSTEMS PROGRAMMING & OPERA.SYS.	PP	100	40	50	P C
05.	THEORY OF COMPUTATION	PP	100	40	53	P C	16.	SOFTWARE ENGINEERING	PP	100	40	48	P C
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	36	P C	17.	SOFTWARE LABORATORY	TW	25	10	16	P C
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	20	P C	18.	SOFTWARE LABORATORY	PR	50	20	22	P
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	22	P C	19.	COMPUTER NETWORK	TW	25	10	22	P C
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	20	P C	20.	COMPUTER NETWORK	OR	50	20	43	P C
10.	HARDWARE LABORATORY	TW	25	10	19	P C	21.	SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	39	P C
11.	HARDWARE LABORATORY	PR	50	20	21	P C	22.	SEMINAR AND TECHNICAL COMMUNI.	TW	50	20	35	P C

GRAND TOTAL = 809/1500, RESULT: SECOND CLASS
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8054380 PATIL SWAPNIL PRABHAKAR URMILA , 70601435K , T8054303 , PICT , T8054380

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	40	P	12.	PRINCIPLES OF PROGRAMMING LANG.	PP	100	40	40	P C
02.	DATA COMMUNICATION	PP	100	40	40	P	13.	COMPUTER NETWORKS	PP	100	40	20	F
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	15	F	14.	FINANCE & MANAGEMENT INFORMA.SYS.	PP	100	40	46	P
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	07	F	15.	SYSTEMS PROGRAMMING & OPERA.SYS.	PP	100	40	42	P C
05.	THEORY OF COMPUTATION	PP	100	40	AA	F	16.	SOFTWARE ENGINEERING	PP	100	40	40	P C
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	21	P C	17.	SOFTWARE LABORATORY	TW	25	10	10	P C
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	25	P C	18.	SOFTWARE LABORATORY	PR	50	20	05	F
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	13	P C	19.	COMPUTER NETWORK	TW	25	10	14	P C
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	21	P C	20.	COMPUTER NETWORK	OR	50	20	22	P C
10.	HARDWARE LABORATORY	TW	25	10	14	P C	21.	SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	32	P C
11.	HARDWARE LABORATORY	PR	50	20	24	P C	22.	SEMINAR AND TECHNICAL COMMUNI.	TW	50	20	37	P C

GRAND TOTAL = 528/1500, RESULT: FAILS
ORDN. 1 MARKS :

T8054381 PATIL VINEET JAYANT PRAJAKTA , 70601438D , T8054304 , PICT , T8054381

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	AA	F	12.	PRINCIPLES OF PROGRAMMING LANG.	PP	100	40	45	P
02.	DATA COMMUNICATION	PP	100	40	AA	F	13.	COMPUTER NETWORKS	PP	100	40	AA	F
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	AA	F	14.	FINANCE & MANAGEMENT INFORMA.SYS.	PP	100	40	AA	F
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	27	F	15.	SYSTEMS PROGRAMMING & OPERA.SYS.	PP	100	40	AA	F
05.	THEORY OF COMPUTATION	PP	100	40	AA	F	16.	SOFTWARE ENGINEERING	PP	100	40	40	P
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	20	P C	17.	SOFTWARE LABORATORY	TW	25	10	10	P C
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	AA	F	18.	SOFTWARE LABORATORY	PR	50	20	AA	F
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	11	P C	19.	COMPUTER NETWORK	TW	25	10	12	P C
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	22	P	20.	COMPUTER NETWORK	OR	50	20	AA	F
10.	HARDWARE LABORATORY	TW	25	10	10	P C	21.	SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	21	P C
11.	HARDWARE LABORATORY	PR	50	20	27	P C	22.	SEMINAR AND TECHNICAL COMMUNI.	TW	50	20	21	P C

GRAND TOTAL = 266/1500, RESULT: FAILS
ORDN. 1 MARKS :

T8054383 PRANAV THAKUR SUNITA , 70801567M , T8054309 , PICT , T8054383

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	45	P C	12.	PRINCIPLES OF PROGRAMMING LANG.	PP	100	40	AA	F
02.	DATA COMMUNICATION	PP	100	40	AA	F	13.	COMPUTER NETWORKS	PP	100	40	AA	F
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	AA	F	14.	FINANCE & MANAGEMENT INFORMA.SYS.	PP	100	40	AA	F
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	AA	F	15.	SYSTEMS PROGRAMMING & OPERA.SYS.	PP	100	40	41	P C
05.	THEORY OF COMPUTATION	PP	100	40	43	P C	16.	SOFTWARE ENGINEERING	PP	100	40	AA	F
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	20	P C	17.	SOFTWARE LABORATORY	TW	25	10	13	P C
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	AA	F	18.	SOFTWARE LABORATORY	PR	50	20	AA	F
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	10	P C	19.	COMPUTER NETWORK	TW	25	10	10	P C
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	29	P C	20.	COMPUTER NETWORK	OR	50	20	25	P C
10.	HARDWARE LABORATORY	TW	25	10	10	P C	21.	SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	21	P C
11.	HARDWARE LABORATORY	PR	50	20	26	P C	22.	SEMINAR AND TECHNICAL COMMUNI.	TW	50	20	20	P C

GRAND TOTAL = 313/1500, RESULT: FAILS RESULT RESERVED FOR BKLG
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8054384 ROKADE VISHALSAGAR DEVIDAS VANDANA , 70701610L , T8054314 , PICT , T8054384

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	41	P C	12. PRINCIPLES OF PROGRAMMING LANG.	PP	100	40	55	P C
02. DATA COMMUNICATION	PP	100	40	48	P C	13. COMPUTER NETWORKS	PP	100	40	50	P C
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	53	P C	14. FINANCE & MANAGEMENT INFORMA.SYS.	PP	100	40	44	P C
04. DIGITAL SIGNAL PROCESSING	PP	100	40	63	P C	15. SYSTEMS PROGRAMMING & OPERA.SYS.	PP	100	40	59	P C
05. THEORY OF COMPUTATION	PP	100	40	43	P C	16. SOFTWARE ENGINEERING	PP	100	40	21	F
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	20	P C	17. SOFTWARE LABORATORY	TW	25	10	11	P C
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	21	P	18. SOFTWARE LABORATORY	PR	50	20	05	F
08. SIGNAL PROCESSING LABORATORY	TW	25	10	11	P C	19. COMPUTER NETWORK	TW	25	10	15	P C
09. SIGNAL PROCESSING LABORATORY	OR	50	20	27	P C	20. COMPUTER NETWORK	OR	50	20	20$	P
10. HARDWARE LABORATORY	TW	25	10	10	P C	21. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	20	P C
11. HARDWARE LABORATORY	PR	50	20	26	P C	22. SEMINAR AND TECHNICAL COMMUNI.	TW	50	20	21	P C

GRAND TOTAL = 684/1500, RESULT: FAILS A.T.K.T. [$ 0.1] RESULT RESERVED FOR BKLG
ORDN. 1 MARKS : (20)2,

T8054385 SHANTANU SINGHAL SHIKHA , 70504046B , T8054320 , PICT , T8054385

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	56	P C	12. PRINCIPLES OF PROGRAMMING LANG.	PP	100	40	74	P C
02. DATA COMMUNICATION	PP	100	40	45	P C	13. COMPUTER NETWORKS	PP	100	40	45	P C
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	43	P	14. FINANCE & MANAGEMENT INFORMA.SYS.	PP	100	40	46	P C
04. DIGITAL SIGNAL PROCESSING	PP	100	40	47	P C	15. SYSTEMS PROGRAMMING & OPERA.SYS.	PP	100	40	53	P C
05. THEORY OF COMPUTATION	PP	100	40	40	P C	16. SOFTWARE ENGINEERING	PP	100	40	52	P C
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	22	P C	17. SOFTWARE LABORATORY	TW	25	10	22	P C
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	28	P C	18. SOFTWARE LABORATORY	PR	50	20	38	P C
08. SIGNAL PROCESSING LABORATORY	TW	25	10	11	P C	19. COMPUTER NETWORK	TW	25	10	12	P C
09. SIGNAL PROCESSING LABORATORY	OR	50	20	21	P C	20. COMPUTER NETWORK	OR	50	20	30	P C
10. HARDWARE LABORATORY	TW	25	10	10	P C	21. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	28	P C
11. HARDWARE LABORATORY	PR	50	20	45	P C	22. SEMINAR AND TECHNICAL COMMUNI.	TW	50	20	25	P C

GRAND TOTAL = 793/1500, RESULT: SECOND CLASS RESULT RESERVED FOR BKLG
ORDN. 1 MARKS :

T8054386 SHARMA AKANKSHA ANUPAM SHIKHA , 70925597H , T8054321 , PICT , T8054386

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	43	P C	12. PRINCIPLES OF PROGRAMMING LANG.	PP	100	40	54	P C
02. DATA COMMUNICATION	PP	100	40	60	P C	13. COMPUTER NETWORKS	PP	100	40	57	P C
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	45	P C	14. FINANCE & MANAGEMENT INFORMA.SYS.	PP	100	40	46	P C
04. DIGITAL SIGNAL PROCESSING	PP	100	40	64	P C	15. SYSTEMS PROGRAMMING & OPERA.SYS.	PP	100	40	55	P C
05. THEORY OF COMPUTATION	PP	100	40	40	P C	16. SOFTWARE ENGINEERING	PP	100	40	49	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	34	P C	17. SOFTWARE LABORATORY	TW	25	10	16	P C
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	40	P C	18. SOFTWARE LABORATORY	PR	50	20	29	P C
08. SIGNAL PROCESSING LABORATORY	TW	25	10	18	P C	19. COMPUTER NETWORK	TW	25	10	17	P C
09. SIGNAL PROCESSING LABORATORY	OR	50	20	30	P C	20. COMPUTER NETWORK	OR	50	20	30	P C
10. HARDWARE LABORATORY	TW	25	10	17	P C	21. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	37	P C
11. HARDWARE LABORATORY	PR	50	20	32	P C	22. SEMINAR AND TECHNICAL COMMUNI.	TW	50	20	24	P C

GRAND TOTAL = 837/1500, RESULT: HIGHER SECOND CLASS
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8054387 SHUBHAM SINGH JAISHREE , 70601498H , T8054326 , PICT , T8054387

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	AA	F	
02. DATA COMMUNICATION	PP	100	40	AA	F	
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	19	F	
04. DIGITAL SIGNAL PROCESSING	PP	100	40	16	F	
05. THEORY OF COMPUTATION	PP	100	40	AA	F	
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	20	P	C
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	AA	F	
08. SIGNAL PROCESSING LABORATORY	TW	25	10	11	P	C
09. SIGNAL PROCESSING LABORATORY	OR	50	20	20	P	
10. HARDWARE LABORATORY	TW	25	10	10	P	C
11. HARDWARE LABORATORY	PR	50	20	20	P	

FIRST TERM TOTAL = 116/750.
ORDN. 1 MARKS :

T8054388 SIDDHARTH JAIN RENU , 70925611G , T8054327 , PICT , T8054388

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	43	P	C	12. PRINCIPLES OF PROGRAMMING LANG.	PP	100	40	45	P C
02. DATA COMMUNICATION	PP	100	40	48	P	C	13. COMPUTER NETWORKS	PP	100	40	40	P C
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	43	P	C	14. FINANCE & MANAGEMENT INFORMA.SYS.	PP	100	40	40	P C
04. DIGITAL SIGNAL PROCESSING	PP	100	40	40	P	C	15. SYSTEMS PROGRAMMING & OPERA.SYS.	PP	100	40	46	P C
05. THEORY OF COMPUTATION	PP	100	40	20	F		16. SOFTWARE ENGINEERING	PP	100	40	40	P C
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	25	P	C	17. SOFTWARE LABORATORY	TW	25	10	14	P C
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	30	P	C	18. SOFTWARE LABORATORY	PR	50	20	35	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	16	P	C	19. COMPUTER NETWORK	TW	25	10	15	P C
09. SIGNAL PROCESSING LABORATORY	OR	50	20	36	P	C	20. COMPUTER NETWORK	OR	50	20	30	P C
10. HARDWARE LABORATORY	TW	25	10	16	P	C	21. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	29	P C
11. HARDWARE LABORATORY	PR	50	20	30	P	C	22. SEMINAR AND TECHNICAL COMMUNI.	TW	50	20	27	P C

GRAND TOTAL = 708/1500, RESULT: FAILS A.T.K.T.
ORDN. 1 MARKS :

T8054389 TAYDE PRAFULLA ANILRAO VANDANA , 70925629K , T8054336 , PICT , T8054389

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	40	P	C	12. PRINCIPLES OF PROGRAMMING LANG.	PP	100	40	56	P C
02. DATA COMMUNICATION	PP	100	40	53	P	C	13. COMPUTER NETWORKS	PP	100	40	46	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	52	P	C	14. FINANCE & MANAGEMENT INFORMA.SYS.	PP	100	40	51	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	41	P	C	15. SYSTEMS PROGRAMMING & OPERA.SYS.	PP	100	40	52	P C
05. THEORY OF COMPUTATION	PP	100	40	40	P	C	16. SOFTWARE ENGINEERING	PP	100	40	40	P C
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	31	P	C	17. SOFTWARE LABORATORY	TW	25	10	19	P C
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	20	P	C	18. SOFTWARE LABORATORY	PR	50	20	37	P C
08. SIGNAL PROCESSING LABORATORY	TW	25	10	17	P	C	19. COMPUTER NETWORK	TW	25	10	11	P C
09. SIGNAL PROCESSING LABORATORY	OR	50	20	29	P	C	20. COMPUTER NETWORK	OR	50	20	28	P C
10. HARDWARE LABORATORY	TW	25	10	19	P	C	21. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	39	P C
11. HARDWARE LABORATORY	PR	50	20	21	P	C	22. SEMINAR AND TECHNICAL COMMUNI.	TW	50	20	38	P C

GRAND TOTAL = 780/1500, RESULT: SECOND CLASS
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8054390 VAIDYA MANDAR RAGHUNATH CHHAYA , 71072141C , T8054342 , PICT , T8054390

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	50	P C	12.	PRINCIPLES OF PROGRAMMING LANG.	PP	100	40	67	P C
02.	DATA COMMUNICATION	PP	100	40	45	P C	13.	COMPUTER NETWORKS	PP	100	40	55	P C
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	56	P C	14.	FINANCE & MANAGEMENT INFORMA.SYS.	PP	100	40	61	P C
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	50	P C	15.	SYSTEMS PROGRAMMING & OPERA.SYS.	PP	100	40	67	P C
05.	THEORY OF COMPUTATION	PP	100	40	42	P C	16.	SOFTWARE ENGINEERING	PP	100	40	46	P C
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	45	P C	17.	SOFTWARE LABORATORY	TW	25	10	21	P C
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	34	P C	18.	SOFTWARE LABORATORY	PR	50	20	26	P C
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	21	P C	19.	COMPUTER NETWORK	TW	25	10	20	P C
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	28	P C	20.	COMPUTER NETWORK	OR	50	20	32	P
10.	HARDWARE LABORATORY	TW	25	10	21	P C	21.	SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	42	P C
11.	HARDWARE LABORATORY	PR	50	20	22	P C	22.	SEMINAR AND TECHNICAL COMMUNI.	TW	50	20	43	P C

GRAND TOTAL = 894+06/1500, RESULT: FIRST CLASS [O.2]
ORDN. 1 MARKS :

T8054391 WAGHMODE PRIYA SHIVAJI MEERA , 70925655J , T8054347 , PICT , T8054391

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	40	P C	12.	PRINCIPLES OF PROGRAMMING LANG.	PP	100	40	52	P C
02.	DATA COMMUNICATION	PP	100	40	43	P C	13.	COMPUTER NETWORKS	PP	100	40	40	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	54	P C	14.	FINANCE & MANAGEMENT INFORMA.SYS.	PP	100	40	50	P C
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	40	P C	15.	SYSTEMS PROGRAMMING & OPERA.SYS.	PP	100	40	59	P C
05.	THEORY OF COMPUTATION	PP	100	40	42	P C	16.	SOFTWARE ENGINEERING	PP	100	40	40	P C
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	31	P C	17.	SOFTWARE LABORATORY	TW	25	10	14	P C
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	22	P C	18.	SOFTWARE LABORATORY	PR	50	20	25	P C
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	18	P C	19.	COMPUTER NETWORK	TW	25	10	14	P C
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	27	P C	20.	COMPUTER NETWORK	OR	50	20	36	P C
10.	HARDWARE LABORATORY	TW	25	10	18	P C	21.	SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	32	P C
11.	HARDWARE LABORATORY	PR	50	20	36	P C	22.	SEMINAR AND TECHNICAL COMMUNI.	TW	50	20	35	P C

GRAND TOTAL = 768/1500, RESULT: SECOND CLASS
ORDN. 1 MARKS :

T8054392 YUVRAJ BHARDWAJ SANGEETA , 70701703D , T8054348 , PICT , T8054392

01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	44	P	12.	PRINCIPLES OF PROGRAMMING LANG.	PP	100	40	27	F
02.	DATA COMMUNICATION	PP	100	40	40	P C	13.	COMPUTER NETWORKS	PP	100	40	40	P
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	21	F	14.	FINANCE & MANAGEMENT INFORMA.SYS.	PP	100	40	40	P C
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	26	F	15.	SYSTEMS PROGRAMMING & OPERA.SYS.	PP	100	40	46	P C
05.	THEORY OF COMPUTATION	PP	100	40	AA	F	16.	SOFTWARE ENGINEERING	PP	100	40	13	F
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	20	P C	17.	SOFTWARE LABORATORY	TW	25	10	10	P C
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	22	P C	18.	SOFTWARE LABORATORY	PR	50	20	05	F
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	10	P C	19.	COMPUTER NETWORK	TW	25	10	10	P C
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	22	P C	20.	COMPUTER NETWORK	OR	50	20	30	P C
10.	HARDWARE LABORATORY	TW	25	10	10	P C	21.	SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	21	P C
11.	HARDWARE LABORATORY	PR	50	20	20	P	22.	SEMINAR AND TECHNICAL COMMUNI.	TW	50	20	20	P C

GRAND TOTAL = 497/1500, RESULT: FAILS
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8058501 AGHAV ISHWARI JAGANNATH VAISHALI , 71045363K , , PICT , T8058501

01.	OPERATING SYSTEM	PP	100	40	63	P
02.	THEORY OF COMPUTATION	PP	100	40	56	P
03.	COMPUTER NETWORK TECHNOLOGY	PP	100	40	80	P
04.	DATBASE MANAGEMENT SYSTEMS	PP	100	40	65	P
05.	SOFTWARE ENGINEERING	PP	100	40	66	P
06.	OPERATING SYSTEM DESIGN LAB.	TW	25	10	22	P
07.	OPERATING SYSTEM DESIGN LAB.	PR	50	20	38	P
08.	INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	19	P
09.	INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	42	P
10.	NETWORK LABORATORY	TW	25	10	19	P
11.	NETWORK LABORATORY	OR	50	20	41	P

FIRST TERM TOTAL = 534/750.
ORDN. 1 MARKS :

T8058502 AGRAWAL NILIMA OMPRAKASH LATA , 71134941L , , PICT , T8058502

01.	OPERATING SYSTEM	PP	100	40	52	P
02.	THEORY OF COMPUTATION	PP	100	40	55	P
03.	COMPUTER NETWORK TECHNOLOGY	PP	100	40	63	P
04.	DATBASE MANAGEMENT SYSTEMS	PP	100	40	60	P
05.	SOFTWARE ENGINEERING	PP	100	40	53	P
06.	OPERATING SYSTEM DESIGN LAB.	TW	25	10	23	P
07.	OPERATING SYSTEM DESIGN LAB.	PR	50	20	35	P
08.	INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	23	P
09.	INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	37	P
10.	NETWORK LABORATORY	TW	25	10	21	P
11.	NETWORK LABORATORY	OR	50	20	36	P

FIRST TERM TOTAL = 481/750.
ORDN. 1 MARKS :

T8058503 AHER SHUBHAM SURESH LATA , 71134942J , , PICT , T8058503

01.	OPERATING SYSTEM	PP	100	40	54	P
02.	THEORY OF COMPUTATION	PP	100	40	55	P
03.	COMPUTER NETWORK TECHNOLOGY	PP	100	40	85	P
04.	DATBASE MANAGEMENT SYSTEMS	PP	100	40	65	P
05.	SOFTWARE ENGINEERING	PP	100	40	70	P
06.	OPERATING SYSTEM DESIGN LAB.	TW	25	10	23	P
07.	OPERATING SYSTEM DESIGN LAB.	PR	50	20	40	P
08.	INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	22	P
09.	INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	37	P
10.	NETWORK LABORATORY	TW	25	10	22	P
11.	NETWORK LABORATORY	OR	50	20	39	P

FIRST TERM TOTAL = 534/750.
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8058504 ALWAL ANNAPURNA RAMESH RENUKA , 71134943G , , PICT , T8058504

01. OPERATING SYSTEM	PP	100	40	55	P
02. THEORY OF COMPUTATION	PP	100	40	45	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	80	P
04. DATBASE MANAGEMENT SYSTEMS	PP	100	40	67	P
05. SOFTWARE ENGINEERING	PP	100	40	66	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	19	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	22	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	19	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	38	P
10. NETWORK LABORATORY	TW	25	10	19	P
11. NETWORK LABORATORY	OR	50	20	35	P

FIRST TERM TOTAL = 486/750.
ORDN. 1 MARKS :

T8058505 ANKIT SHARMA ANITA , 71045372J , , PICT , T8058505

01. OPERATING SYSTEM	PP	100	40	40	P
02. THEORY OF COMPUTATION	PP	100	40	40	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	43	P
04. DATBASE MANAGEMENT SYSTEMS	PP	100	40	50	P
05. SOFTWARE ENGINEERING	PP	100	40	51	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	16	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	30	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	20	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	38	P
10. NETWORK LABORATORY	TW	25	10	17	P
11. NETWORK LABORATORY	OR	50	20	36	P

FIRST TERM TOTAL = 398/750.
ORDN. 1 MARKS :

T8058506 ANUJA WANGNOO AMBICA , 71045373G , , PICT , T8058506

01. OPERATING SYSTEM	PP	100	40	40	P
02. THEORY OF COMPUTATION	PP	100	40	44	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	41	P
04. DATBASE MANAGEMENT SYSTEMS	PP	100	40	45	P
05. SOFTWARE ENGINEERING	PP	100	40	40	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	15	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	22	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	15	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	21	P
10. NETWORK LABORATORY	TW	25	10	16	P
11. NETWORK LABORATORY	OR	50	20	25	P

FIRST TERM TOTAL = 341/750.
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8058507 ASHISH SANADHYA MADHU , 71045374E , , PICT , T8058507

01.	OPERATING SYSTEM	PP	100	40	53	P
02.	THEORY OF COMPUTATION	PP	100	40	49	P
03.	COMPUTER NETWORK TECHNOLOGY	PP	100	40	71	P
04.	DATBASE MANAGEMENT SYSTEMS	PP	100	40	62	P
05.	SOFTWARE ENGINEERING	PP	100	40	60	P
06.	OPERATING SYSTEM DESIGN LAB.	TW	25	10	20	P
07.	OPERATING SYSTEM DESIGN LAB.	PR	50	20	39	P
08.	INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	19	P
09.	INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	30	P
10.	NETWORK LABORATORY	TW	25	10	21	P
11.	NETWORK LABORATORY	OR	50	20	44	P

FIRST TERM TOTAL = 489/750.
ORDN. 1 MARKS :

T8058508 ASHTEKAR KRUTTIKA CHANDRAKANT SUREKHA , 71045375C , , PICT , T8058508

01.	OPERATING SYSTEM	PP	100	40	40	P
02.	THEORY OF COMPUTATION	PP	100	40	45	P
03.	COMPUTER NETWORK TECHNOLOGY	PP	100	40	52	P
04.	DATBASE MANAGEMENT SYSTEMS	PP	100	40	57	P
05.	SOFTWARE ENGINEERING	PP	100	40	40	P
06.	OPERATING SYSTEM DESIGN LAB.	TW	25	10	21	P
07.	OPERATING SYSTEM DESIGN LAB.	PR	50	20	32	P
08.	INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	21	P
09.	INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	32	P
10.	NETWORK LABORATORY	TW	25	10	20	P
11.	NETWORK LABORATORY	OR	50	20	37	P

FIRST TERM TOTAL = 420/750.
ORDN. 1 MARKS :

T8058509 BADGUJAR PRATIKSHA BHASKAR ANITA , 71045380K , , PICT , T8058509

01.	OPERATING SYSTEM	PP	100	40	52	P
02.	THEORY OF COMPUTATION	PP	100	40	44	P
03.	COMPUTER NETWORK TECHNOLOGY	PP	100	40	65	P
04.	DATBASE MANAGEMENT SYSTEMS	PP	100	40	55	P
05.	SOFTWARE ENGINEERING	PP	100	40	55	P
06.	OPERATING SYSTEM DESIGN LAB.	TW	25	10	20	P
07.	OPERATING SYSTEM DESIGN LAB.	PR	50	20	21	P
08.	INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	19	P
09.	INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	27	P
10.	NETWORK LABORATORY	TW	25	10	21	P
11.	NETWORK LABORATORY	OR	50	20	32	P

FIRST TERM TOTAL = 432/750.
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8058510 BHALERAO MANGESH GANESH NANDA , 70925361D , , PICT , T8058510

01. OPERATING SYSTEM	PP	100	40	40	P
02. THEORY OF COMPUTATION	PP	100	40	40	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	40	P
04. DATBASE MANAGEMENT SYSTEMS	PP	100	40	40	P
05. SOFTWARE ENGINEERING	PP	100	40	40	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	15	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	08	F
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	15	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	41	P
10. NETWORK LABORATORY	TW	25	10	16	P
11. NETWORK LABORATORY	OR	50	20	32	P

FIRST TERM TOTAL = 348/750.
ORDN. 1 MARKS :

T8058511 BHARTENDU BHARTI MANISHA DEVI , 71045387G , , PICT , T8058511

01. OPERATING SYSTEM	PP	100	40	41	P
02. THEORY OF COMPUTATION	PP	100	40	45	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	65	P
04. DATBASE MANAGEMENT SYSTEMS	PP	100	40	63	P
05. SOFTWARE ENGINEERING	PP	100	40	40	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	18	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	40	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	18	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	33	P
10. NETWORK LABORATORY	TW	25	10	17	P
11. NETWORK LABORATORY	OR	50	20	10	F

FIRST TERM TOTAL = 411/750.
ORDN. 1 MARKS :

T8058512 BHINTADE VIVEK ASHOK SANGEETA , 71134944E , , PICT , T8058512

01. OPERATING SYSTEM	PP	100	40	52	P
02. THEORY OF COMPUTATION	PP	100	40	56	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	84	P
04. DATBASE MANAGEMENT SYSTEMS	PP	100	40	62	P
05. SOFTWARE ENGINEERING	PP	100	40	64	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	18	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	41	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	19	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	10	F
10. NETWORK LABORATORY	TW	25	10	16	P
11. NETWORK LABORATORY	OR	50	20	34	P

FIRST TERM TOTAL = 477/750.
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8058513 BHOSALE SHREEYA DEEPAKRAO JYOTSNA , 71045394K , , PICT , T8058513

01.	OPERATING SYSTEM	PP	100	40	56	P
02.	THEORY OF COMPUTATION	PP	100	40	53	P
03.	COMPUTER NETWORK TECHNOLOGY	PP	100	40	78	P
04.	DATBASE MANAGEMENT SYSTEMS	PP	100	40	59	P
05.	SOFTWARE ENGINEERING	PP	100	40	60	P
06.	OPERATING SYSTEM DESIGN LAB.	TW	25	10	23	P
07.	OPERATING SYSTEM DESIGN LAB.	PR	50	20	40	P
08.	INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	23	P
09.	INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	32	P
10.	NETWORK LABORATORY	TW	25	10	23	P
11.	NETWORK LABORATORY	OR	50	20	34	P

FIRST TERM TOTAL = 504/750.
ORDN. 1 MARKS :

T8058514 BORDE ROHAN SATISHRAO UJWALA , 71045398B , , PICT , T8058514

01.	OPERATING SYSTEM	PP	100	40	51	P
02.	THEORY OF COMPUTATION	PP	100	40	61	P
03.	COMPUTER NETWORK TECHNOLOGY	PP	100	40	68	P
04.	DATBASE MANAGEMENT SYSTEMS	PP	100	40	65	P
05.	SOFTWARE ENGINEERING	PP	100	40	59	P
06.	OPERATING SYSTEM DESIGN LAB.	TW	25	10	19	P
07.	OPERATING SYSTEM DESIGN LAB.	PR	50	20	30	P
08.	INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	19	P
09.	INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	10	F
10.	NETWORK LABORATORY	TW	25	10	19	P
11.	NETWORK LABORATORY	OR	50	20	40	P

FIRST TERM TOTAL = 461/750.
ORDN. 1 MARKS :

T8058515 BRAHME NACHIKET SHRIKANT VIDULA , 71134945C , , PICT , T8058515

01.	OPERATING SYSTEM	PP	100	40	46	P
02.	THEORY OF COMPUTATION	PP	100	40	46	P
03.	COMPUTER NETWORK TECHNOLOGY	PP	100	40	72	P
04.	DATBASE MANAGEMENT SYSTEMS	PP	100	40	56	P
05.	SOFTWARE ENGINEERING	PP	100	40	55	P
06.	OPERATING SYSTEM DESIGN LAB.	TW	25	10	21	P
07.	OPERATING SYSTEM DESIGN LAB.	PR	50	20	42	P
08.	INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	19	P
09.	INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	36	P
10.	NETWORK LABORATORY	TW	25	10	22	P
11.	NETWORK LABORATORY	OR	50	20	35	P

FIRST TERM TOTAL = 471/750.
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8058516 CHAKRAVARTHY ROHAN KUMAR MONICA , 71045400H , , PICT , T8058516

01.	OPERATING SYSTEM	PP	100	40	44	P
02.	THEORY OF COMPUTATION	PP	100	40	55	P
03.	COMPUTER NETWORK TECHNOLOGY	PP	100	40	63	P
04.	DATBASE MANAGEMENT SYSTEMS	PP	100	40	64	P
05.	SOFTWARE ENGINEERING	PP	100	40	51	P
06.	OPERATING SYSTEM DESIGN LAB.	TW	25	10	15	P
07.	OPERATING SYSTEM DESIGN LAB.	PR	50	20	34	P
08.	INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	15	P
09.	INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	21	P
10.	NETWORK LABORATORY	TW	25	10	12	P
11.	NETWORK LABORATORY	OR	50	20	40	P

FIRST TERM TOTAL = 431/750.
ORDN. 1 MARKS :

T8058517 CHANDAK RASHMI VIJAYKUMARJI SHYAMA , 71134946M , , PICT , T8058517

01.	OPERATING SYSTEM	PP	100	40	45	P
02.	THEORY OF COMPUTATION	PP	100	40	58	P
03.	COMPUTER NETWORK TECHNOLOGY	PP	100	40	68	P
04.	DATBASE MANAGEMENT SYSTEMS	PP	100	40	53	P
05.	SOFTWARE ENGINEERING	PP	100	40	57	P
06.	OPERATING SYSTEM DESIGN LAB.	TW	25	10	23	P
07.	OPERATING SYSTEM DESIGN LAB.	PR	50	20	25	P
08.	INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	23	P
09.	INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	36	P
10.	NETWORK LABORATORY	TW	25	10	22	P
11.	NETWORK LABORATORY	OR	50	20	34	P

FIRST TERM TOTAL = 466/750.
ORDN. 1 MARKS :

T8058518 CHANGEDIYA SUNNY SANJAY ASHA , 71058517K , , PICT , T8058518

01.	OPERATING SYSTEM	PP	100	40	44	P
02.	THEORY OF COMPUTATION	PP	100	40	57	P
03.	COMPUTER NETWORK TECHNOLOGY	PP	100	40	69	P
04.	DATBASE MANAGEMENT SYSTEMS	PP	100	40	69	P
05.	SOFTWARE ENGINEERING	PP	100	40	58	P
06.	OPERATING SYSTEM DESIGN LAB.	TW	25	10	20	P
07.	OPERATING SYSTEM DESIGN LAB.	PR	50	20	10	F
08.	INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	20	P
09.	INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	43	P
10.	NETWORK LABORATORY	TW	25	10	22	P
11.	NETWORK LABORATORY	OR	50	20	39	P

FIRST TERM TOTAL = 472/750.
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8058519 CHAUDHAR SATISH RAOSAHEB SANGITA , 71045402D , , PICT , T8058519

01.	OPERATING SYSTEM	PP	100	40	51	P
02.	THEORY OF COMPUTATION	PP	100	40	49	P
03.	COMPUTER NETWORK TECHNOLOGY	PP	100	40	77	P
04.	DATBASE MANAGEMENT SYSTEMS	PP	100	40	69	P
05.	SOFTWARE ENGINEERING	PP	100	40	55	P
06.	OPERATING SYSTEM DESIGN LAB.	TW	25	10	21	P
07.	OPERATING SYSTEM DESIGN LAB.	PR	50	20	27	P
08.	INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	21	P
09.	INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	10	F
10.	NETWORK LABORATORY	TW	25	10	20	P
11.	NETWORK LABORATORY	OR	50	20	40	P

FIRST TERM TOTAL = 461/750.
ORDN. 1 MARKS :

T8058520 CHAUDHARI AMAR SHRIKANT SHUBHANGI , 71045403B , , PICT , T8058520

01.	OPERATING SYSTEM	PP	100	40	58	P
02.	THEORY OF COMPUTATION	PP	100	40	46	P
03.	COMPUTER NETWORK TECHNOLOGY	PP	100	40	81	P
04.	DATBASE MANAGEMENT SYSTEMS	PP	100	40	55	P
05.	SOFTWARE ENGINEERING	PP	100	40	55	P
06.	OPERATING SYSTEM DESIGN LAB.	TW	25	10	16	P
07.	OPERATING SYSTEM DESIGN LAB.	PR	50	20	44	P
08.	INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	18	P
09.	INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	37	P
10.	NETWORK LABORATORY	TW	25	10	17	P
11.	NETWORK LABORATORY	OR	50	20	40	P

FIRST TERM TOTAL = 484/750.
ORDN. 1 MARKS :

T8058521 CHAUDHARI MAHESH PRABHAKAR MANGALA , 70925378J , , PICT , T8058521

01.	OPERATING SYSTEM	PP	100	40	46	P
02.	THEORY OF COMPUTATION	PP	100	40	44	P
03.	COMPUTER NETWORK TECHNOLOGY	PP	100	40	68	P
04.	DATBASE MANAGEMENT SYSTEMS	PP	100	40	55	P
05.	SOFTWARE ENGINEERING	PP	100	40	48	P
06.	OPERATING SYSTEM DESIGN LAB.	TW	25	10	19	P
07.	OPERATING SYSTEM DESIGN LAB.	PR	50	20	43	P
08.	INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	17	P
09.	INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	37	P
10.	NETWORK LABORATORY	TW	25	10	21	P
11.	NETWORK LABORATORY	OR	50	20	37	P

FIRST TERM TOTAL = 455/750.
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8058522 CHAVAN PUSHPAK VILAS SUREKHA , 71134947K , , PICT , T8058522

01. OPERATING SYSTEM	PP	100	40	57	P
02. THEORY OF COMPUTATION	PP	100	40	48	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	64	P
04. DATBASE MANAGEMENT SYSTEMS	PP	100	40	57	P
05. SOFTWARE ENGINEERING	PP	100	40	69	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	19	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	34	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	17	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	10	F
10. NETWORK LABORATORY	TW	25	10	17	P
11. NETWORK LABORATORY	OR	50	20	35	P

FIRST TERM TOTAL = 448/750.
ORDN. 1 MARKS :

T8058523 DARP YOGESH RAMCHANDRA SANDHYA , 71134948H , , PICT , T8058523

01. OPERATING SYSTEM	PP	100	40	50	P
02. THEORY OF COMPUTATION	PP	100	40	40	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	56	P
04. DATBASE MANAGEMENT SYSTEMS	PP	100	40	60	P
05. SOFTWARE ENGINEERING	PP	100	40	58	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	18	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	30	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	18	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	40	P
10. NETWORK LABORATORY	TW	25	10	19	P
11. NETWORK LABORATORY	OR	50	20	37	P

FIRST TERM TOTAL = 445/750.
ORDN. 1 MARKS :

T8058524 DAWANGE VISHAL SANJAY KALPANA , 71134949F , , PICT , T8058524

01. OPERATING SYSTEM	PP	100	40	53	P
02. THEORY OF COMPUTATION	PP	100	40	66	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	69	P
04. DATBASE MANAGEMENT SYSTEMS	PP	100	40	67	P
05. SOFTWARE ENGINEERING	PP	100	40	63	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	22	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	40	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	22	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	38	P
10. NETWORK LABORATORY	TW	25	10	21	P
11. NETWORK LABORATORY	OR	50	20	37	P

FIRST TERM TOTAL = 521/750.
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8058525 DEORE NIKHIL BAPUSAHEB MANDAKINI , 70801397L , , PICT , T8058525

01. OPERATING SYSTEM	PP	100	40	42	P
02. THEORY OF COMPUTATION	PP	100	40	40	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	50	P
04. DATBASE MANAGEMENT SYSTEMS	PP	100	40	41	P
05. SOFTWARE ENGINEERING	PP	100	40	43	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	14	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	06	F
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	13	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	35	P
10. NETWORK LABORATORY	TW	25	10	12	P
11. NETWORK LABORATORY	OR	50	20	11	F

FIRST TERM TOTAL = 327/750.
ORDN. 1 MARKS :

T8058526 DESHPANDE KIRTI VINOD SHALINI , 71134950K , , PICT , T8058526

01. OPERATING SYSTEM	PP	100	40	54	P
02. THEORY OF COMPUTATION	PP	100	40	55	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	68	P
04. DATBASE MANAGEMENT SYSTEMS	PP	100	40	63	P
05. SOFTWARE ENGINEERING	PP	100	40	67	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	18	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	38	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	18	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	34	P
10. NETWORK LABORATORY	TW	25	10	19	P
11. NETWORK LABORATORY	OR	50	20	40	P

FIRST TERM TOTAL = 492/750.
ORDN. 1 MARKS :

T8058527 DESHPANDE NACHIKET CHANDRAKANT KISHORI , 71134951H , , PICT , T8058527

01. OPERATING SYSTEM	PP	100	40	54	P
02. THEORY OF COMPUTATION	PP	100	40	46	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	62	P
04. DATBASE MANAGEMENT SYSTEMS	PP	100	40	61	P
05. SOFTWARE ENGINEERING	PP	100	40	57	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	22	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	32	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	21	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	33	P
10. NETWORK LABORATORY	TW	25	10	20	P
11. NETWORK LABORATORY	OR	50	20	40	P

FIRST TERM TOTAL = 470/750.
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8058528 DHADIWAL KOMAL AJIT ASHA , 71134952F , , PICT , T8058528

01. OPERATING SYSTEM	PP	100	40	61	P
02. THEORY OF COMPUTATION	PP	100	40	58	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	77	P
04. DATBASE MANAGEMENT SYSTEMS	PP	100	40	64	P
05. SOFTWARE ENGINEERING	PP	100	40	74	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	21	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	35	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	20	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	25	P
10. NETWORK LABORATORY	TW	25	10	22	P
11. NETWORK LABORATORY	OR	50	20	44	P

FIRST TERM TOTAL = 524/750.
ORDN. 1 MARKS :

T8058529 DHULE AKSHAYKUMAR MAHADEVAPPA SHIVANI , 71045416D , , PICT , T8058529

01. OPERATING SYSTEM	PP	100	40	45	P
02. THEORY OF COMPUTATION	PP	100	40	56	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	74	P
04. DATBASE MANAGEMENT SYSTEMS	PP	100	40	59	P
05. SOFTWARE ENGINEERING	PP	100	40	68	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	20	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	00	F
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	21	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	33	P
10. NETWORK LABORATORY	TW	25	10	18	P
11. NETWORK LABORATORY	OR	50	20	34	P

FIRST TERM TOTAL = 449/750.
ORDN. 1 MARKS :

T8058530 DIKE ASHISH VIJAY RAJANI , 71045418L , , PICT , T8058530

01. OPERATING SYSTEM	PP	100	40	19	F
02. THEORY OF COMPUTATION	PP	100	40	09	F
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	45	P
04. DATBASE MANAGEMENT SYSTEMS	PP	100	40	40	P
05. SOFTWARE ENGINEERING	PP	100	40	46	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	12	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	AA	F
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	10	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	07	F
10. NETWORK LABORATORY	TW	25	10	10	P
11. NETWORK LABORATORY	OR	50	20	AA	F

FIRST TERM TOTAL = 213/750.
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8058531 DIVEKAR SANDIP LAXMAN SHOBHA , 71134953D , , PICT , T8058531

01. OPERATING SYSTEM	PP	100	40	54	P
02. THEORY OF COMPUTATION	PP	100	40	44	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	68	P
04. DATBASE MANAGEMENT SYSTEMS	PP	100	40	69	P
05. SOFTWARE ENGINEERING	PP	100	40	53	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	21	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	38	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	20	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	39	P
10. NETWORK LABORATORY	TW	25	10	22	P
11. NETWORK LABORATORY	OR	50	20	36	P

FIRST TERM TOTAL = 486/750.
ORDN. 1 MARKS :

T8058532 DUMAVAT KUNAL BHAVARLAL VIDYA , 71134954B , , PICT , T8058532

01. OPERATING SYSTEM	PP	100	40	52	P
02. THEORY OF COMPUTATION	PP	100	40	40	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	66	P
04. DATBASE MANAGEMENT SYSTEMS	PP	100	40	66	P
05. SOFTWARE ENGINEERING	PP	100	40	59	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	20	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	32	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	19	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	28	P
10. NETWORK LABORATORY	TW	25	10	18	P
11. NETWORK LABORATORY	OR	50	20	35	P

FIRST TERM TOTAL = 456/750.
ORDN. 1 MARKS :

T8058533 EKHANDE DNYANESHWAR KACHARU VITHABAI , 71134955L , , PICT , T8058533

01. OPERATING SYSTEM	PP	100	40	54	P
02. THEORY OF COMPUTATION	PP	100	40	52	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	79	P
04. DATBASE MANAGEMENT SYSTEMS	PP	100	40	59	P
05. SOFTWARE ENGINEERING	PP	100	40	69	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	23	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	41	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	23	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	38	P
10. NETWORK LABORATORY	TW	25	10	22	P
11. NETWORK LABORATORY	OR	50	20	40	P

FIRST TERM TOTAL = 522/750.
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8058534 GADKARI ASHISH VASANTRAO SANGITA , 71134956J , , PICT , T8058534

01.	OPERATING SYSTEM	PP	100	40	52	P
02.	THEORY OF COMPUTATION	PP	100	40	60	P
03.	COMPUTER NETWORK TECHNOLOGY	PP	100	40	78	P
04.	DATBASE MANAGEMENT SYSTEMS	PP	100	40	63	P
05.	SOFTWARE ENGINEERING	PP	100	40	64	P
06.	OPERATING SYSTEM DESIGN LAB.	TW	25	10	24	P
07.	OPERATING SYSTEM DESIGN LAB.	PR	50	20	36	P
08.	INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	22	P
09.	INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	43	P
10.	NETWORK LABORATORY	TW	25	10	24	P
11.	NETWORK LABORATORY	OR	50	20	37	P

FIRST TERM TOTAL = 526/750.
ORDN. 1 MARKS :

T8058535 GAIKWAD AKSHAY SHAHAJI MAHANANDA , 71045430K , , PICT , T8058535

01.	OPERATING SYSTEM	PP	100	40	45	P
02.	THEORY OF COMPUTATION	PP	100	40	48	P
03.	COMPUTER NETWORK TECHNOLOGY	PP	100	40	AA	F
04.	DATBASE MANAGEMENT SYSTEMS	PP	100	40	52	P
05.	SOFTWARE ENGINEERING	PP	100	40	44	P
06.	OPERATING SYSTEM DESIGN LAB.	TW	25	10	10	P
07.	OPERATING SYSTEM DESIGN LAB.	PR	50	20	AA	F
08.	INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	10	P
09.	INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	06	F
10.	NETWORK LABORATORY	TW	25	10	10	P
11.	NETWORK LABORATORY	OR	50	20	AA	F

FIRST TERM TOTAL = 240/750.
ORDN. 1 MARKS :

T8058536 GAIKWAD SAGAR ANAND SUREKHA , 71045773B , , PICT , T8058536

01.	OPERATING SYSTEM	PP	100	40	54	P
02.	THEORY OF COMPUTATION	PP	100	40	63	P
03.	COMPUTER NETWORK TECHNOLOGY	PP	100	40	68	P
04.	DATBASE MANAGEMENT SYSTEMS	PP	100	40	50	P
05.	SOFTWARE ENGINEERING	PP	100	40	43	P
06.	OPERATING SYSTEM DESIGN LAB.	TW	25	10	21	P
07.	OPERATING SYSTEM DESIGN LAB.	PR	50	20	44	P
08.	INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	20	P
09.	INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	37	P
10.	NETWORK LABORATORY	TW	25	10	22	P
11.	NETWORK LABORATORY	OR	50	20	40	P

FIRST TERM TOTAL = 482/750.
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8058537 GEHANI ASHISH MOHAN BHARTI , 71134957G , , PICT , T8058537

01.	OPERATING SYSTEM	PP	100	40	64	P
02.	THEORY OF COMPUTATION	PP	100	40	81	P
03.	COMPUTER NETWORK TECHNOLOGY	PP	100	40	90	P
04.	DATBASE MANAGEMENT SYSTEMS	PP	100	40	69	P
05.	SOFTWARE ENGINEERING	PP	100	40	69	P
06.	OPERATING SYSTEM DESIGN LAB.	TW	25	10	24	P
07.	OPERATING SYSTEM DESIGN LAB.	PR	50	20	48	P
08.	INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	24	P
09.	INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	37	P
10.	NETWORK LABORATORY	TW	25	10	23	P
11.	NETWORK LABORATORY	OR	50	20	45	P

FIRST TERM TOTAL = 597/750.
ORDN. 1 MARKS :

T8058538 GHADGE TEJAL RAJENDRA ROHINI , 71134958E , , PICT , T8058538

01.	OPERATING SYSTEM	PP	100	40	65	P
02.	THEORY OF COMPUTATION	PP	100	40	59	P
03.	COMPUTER NETWORK TECHNOLOGY	PP	100	40	81	P
04.	DATBASE MANAGEMENT SYSTEMS	PP	100	40	65	P
05.	SOFTWARE ENGINEERING	PP	100	40	61	P
06.	OPERATING SYSTEM DESIGN LAB.	TW	25	10	24	P
07.	OPERATING SYSTEM DESIGN LAB.	PR	50	20	45	P
08.	INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	24	P
09.	INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	42	P
10.	NETWORK LABORATORY	TW	25	10	24	P
11.	NETWORK LABORATORY	OR	50	20	46	P

FIRST TERM TOTAL = 559/750.
ORDN. 1 MARKS :

T8058539 GUNDECHA ANIKET PRAKASH UJWALA , 71134959C , , PICT , T8058539

01.	OPERATING SYSTEM	PP	100	40	49	P
02.	THEORY OF COMPUTATION	PP	100	40	40	P
03.	COMPUTER NETWORK TECHNOLOGY	PP	100	40	69	P
04.	DATBASE MANAGEMENT SYSTEMS	PP	100	40	61	P
05.	SOFTWARE ENGINEERING	PP	100	40	48	P
06.	OPERATING SYSTEM DESIGN LAB.	TW	25	10	18	P
07.	OPERATING SYSTEM DESIGN LAB.	PR	50	20	35	P
08.	INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	15	P
09.	INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	28	P
10.	NETWORK LABORATORY	TW	25	10	16	P
11.	NETWORK LABORATORY	OR	50	20	39	P

FIRST TERM TOTAL = 439/750.
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8058540 GUPTA VANI JITENDRA RITU , 71045443M , , PICT , T8058540

01.	OPERATING SYSTEM	PP	100	40	55	P
02.	THEORY OF COMPUTATION	PP	100	40	73	P
03.	COMPUTER NETWORK TECHNOLOGY	PP	100	40	77	P
04.	DATBASE MANAGEMENT SYSTEMS	PP	100	40	72	P
05.	SOFTWARE ENGINEERING	PP	100	40	43	P
06.	OPERATING SYSTEM DESIGN LAB.	TW	25	10	21	P
07.	OPERATING SYSTEM DESIGN LAB.	PR	50	20	42	P
08.	INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	20	P
09.	INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	41	P
10.	NETWORK LABORATORY	TW	25	10	21	P
11.	NETWORK LABORATORY	OR	50	20	44	P

FIRST TERM TOTAL = 531/750.
ORDN. 1 MARKS :

T8058541 HIRAN PRANAV PRAMODKUMAR REKHA , 71045447D , , PICT , T8058541

01.	OPERATING SYSTEM	PP	100	40	40	P
02.	THEORY OF COMPUTATION	PP	100	40	55	P
03.	COMPUTER NETWORK TECHNOLOGY	PP	100	40	50	P
04.	DATBASE MANAGEMENT SYSTEMS	PP	100	40	61	P
05.	SOFTWARE ENGINEERING	PP	100	40	42	P
06.	OPERATING SYSTEM DESIGN LAB.	TW	25	10	20	P
07.	OPERATING SYSTEM DESIGN LAB.	PR	50	20	30	P
08.	INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	20	P
09.	INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	41	P
10.	NETWORK LABORATORY	TW	25	10	20	P
11.	NETWORK LABORATORY	OR	50	20	36	P

FIRST TERM TOTAL = 436/750.
ORDN. 1 MARKS :

T8058542 HIRE PRATIK PARASHURAM MANGALA , 71045448B , , PICT , T8058542

01.	OPERATING SYSTEM	PP	100	40	49	P
02.	THEORY OF COMPUTATION	PP	100	40	56	P
03.	COMPUTER NETWORK TECHNOLOGY	PP	100	40	57	P
04.	DATBASE MANAGEMENT SYSTEMS	PP	100	40	52	P
05.	SOFTWARE ENGINEERING	PP	100	40	40	P
06.	OPERATING SYSTEM DESIGN LAB.	TW	25	10	20	P
07.	OPERATING SYSTEM DESIGN LAB.	PR	50	20	37	P
08.	INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	20	P
09.	INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	10	F
10.	NETWORK LABORATORY	TW	25	10	18	P
11.	NETWORK LABORATORY	OR	50	20	36	P

FIRST TERM TOTAL = 415/750.
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8058543 INAMDAR MOHSIN IMTIYAZ SHAHENAZ , 71045449L , , PICT , T8058543

01. OPERATING SYSTEM	PP	100	40	46	P
02. THEORY OF COMPUTATION	PP	100	40	44	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	40	P
04. DATBASE MANAGEMENT SYSTEMS	PP	100	40	45	P
05. SOFTWARE ENGINEERING	PP	100	40	21	F
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	19	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	42	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	20	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	37	P
10. NETWORK LABORATORY	TW	25	10	22	P
11. NETWORK LABORATORY	OR	50	20	32	P

FIRST TERM TOTAL = 386/750.
ORDN. 1 MARKS :

T8058544 INGALE KOMAL RAJENDRA USHA , 71045450D , , PICT , T8058544

01. OPERATING SYSTEM	PP	100	40	62	P
02. THEORY OF COMPUTATION	PP	100	40	73	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	76	P
04. DATBASE MANAGEMENT SYSTEMS	PP	100	40	62	P
05. SOFTWARE ENGINEERING	PP	100	40	46	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	23	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	39	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	23	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	39	P
10. NETWORK LABORATORY	TW	25	10	22	P
11. NETWORK LABORATORY	OR	50	20	32	P

FIRST TERM TOTAL = 519/750.
ORDN. 1 MARKS :

T8058545 INGOLE PRACHI VASANT NILIMA , 71045451B , , PICT , T8058545

01. OPERATING SYSTEM	PP	100	40	59	P
02. THEORY OF COMPUTATION	PP	100	40	60	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	66	P
04. DATBASE MANAGEMENT SYSTEMS	PP	100	40	64	P
05. SOFTWARE ENGINEERING	PP	100	40	49	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	21	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	25	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	20	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	35	P
10. NETWORK LABORATORY	TW	25	10	22	P
11. NETWORK LABORATORY	OR	50	20	33	P

FIRST TERM TOTAL = 474/750.
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8058546 ISHAN AGARWAL MAMTA , 71045452L , , PICT , T8058546

01. OPERATING SYSTEM	PP	100	40	48	P
02. THEORY OF COMPUTATION	PP	100	40	47	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	67	P
04. DATBASE MANAGEMENT SYSTEMS	PP	100	40	47	P
05. SOFTWARE ENGINEERING	PP	100	40	31	F
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	19	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	41	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	19	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	28	P
10. NETWORK LABORATORY	TW	25	10	19	P
11. NETWORK LABORATORY	OR	50	20	37	P

FIRST TERM TOTAL = 420/750.
ORDN. 1 MARKS :

T8058547 JADHAV DHANRAJ KHANDERAO CHHAYA , 71134960G , , PICT , T8058547

01. OPERATING SYSTEM	PP	100	40	59	P
02. THEORY OF COMPUTATION	PP	100	40	72	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	58	P
04. DATBASE MANAGEMENT SYSTEMS	PP	100	40	57	P
05. SOFTWARE ENGINEERING	PP	100	40	60	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	22	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	41	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	22	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	43	P
10. NETWORK LABORATORY	TW	25	10	22	P
11. NETWORK LABORATORY	OR	50	20	37	P

FIRST TERM TOTAL = 516/750.
ORDN. 1 MARKS :

T8058548 JADHAV MAYUR JAGANNATH JANABAI , 71134961E , , PICT , T8058548

01. OPERATING SYSTEM	PP	100	40	52	P
02. THEORY OF COMPUTATION	PP	100	40	72	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	73	P
04. DATBASE MANAGEMENT SYSTEMS	PP	100	40	51	P
05. SOFTWARE ENGINEERING	PP	100	40	50	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	22	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	42	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	22	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	40	P
10. NETWORK LABORATORY	TW	25	10	20	P
11. NETWORK LABORATORY	OR	50	20	38	P

FIRST TERM TOTAL = 504/750.
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8058549 JODHWANI SAHIL PRADEEP BABITA , 71045462H , , PICT , T8058549

01.	OPERATING SYSTEM	PP	100	40	63	P
02.	THEORY OF COMPUTATION	PP	100	40	59	P
03.	COMPUTER NETWORK TECHNOLOGY	PP	100	40	67	P
04.	DATBASE MANAGEMENT SYSTEMS	PP	100	40	55	P
05.	SOFTWARE ENGINEERING	PP	100	40	40	P
06.	OPERATING SYSTEM DESIGN LAB.	TW	25	10	19	P
07.	OPERATING SYSTEM DESIGN LAB.	PR	50	20	37	P
08.	INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	19	P
09.	INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	41	P
10.	NETWORK LABORATORY	TW	25	10	22	P
11.	NETWORK LABORATORY	OR	50	20	25	P

FIRST TERM TOTAL = 464/750.
ORDN. 1 MARKS :

T8058550 JUVVADI MANOBHIRAM J VIJAYASHRI , 71057070J , , PICT , T8058550

01.	OPERATING SYSTEM	PP	100	40	49	P
02.	THEORY OF COMPUTATION	PP	100	40	43	P
03.	COMPUTER NETWORK TECHNOLOGY	PP	100	40	62	P
04.	DATBASE MANAGEMENT SYSTEMS	PP	100	40	54	P
05.	SOFTWARE ENGINEERING	PP	100	40	40	P
06.	OPERATING SYSTEM DESIGN LAB.	TW	25	10	18	P
07.	OPERATING SYSTEM DESIGN LAB.	PR	50	20	40	P
08.	INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	17	P
09.	INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	35	P
10.	NETWORK LABORATORY	TW	25	10	18	P
11.	NETWORK LABORATORY	OR	50	20	39	P

FIRST TERM TOTAL = 431/750.
ORDN. 1 MARKS :

T8058551 KADAM AMIT ASHOKRAO MANDAKINI , 71134962C , , PICT , T8058551

01.	OPERATING SYSTEM	PP	100	40	43	P
02.	THEORY OF COMPUTATION	PP	100	40	53	P
03.	COMPUTER NETWORK TECHNOLOGY	PP	100	40	54	P
04.	DATBASE MANAGEMENT SYSTEMS	PP	100	40	49	P
05.	SOFTWARE ENGINEERING	PP	100	40	45	P
06.	OPERATING SYSTEM DESIGN LAB.	TW	25	10	21	P
07.	OPERATING SYSTEM DESIGN LAB.	PR	50	20	08	F
08.	INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	21	P
09.	INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	40	P
10.	NETWORK LABORATORY	TW	25	10	22	P
11.	NETWORK LABORATORY	OR	50	20	25	P

FIRST TERM TOTAL = 403/750.
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8058552 KADAM PRADNYA PRADEEP PRANITA , 71134963M , , PICT , T8058552

01. OPERATING SYSTEM	PP	100	40	48	P
02. THEORY OF COMPUTATION	PP	100	40	45	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	62	P
04. DATBASE MANAGEMENT SYSTEMS	PP	100	40	62	P
05. SOFTWARE ENGINEERING	PP	100	40	58	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	23	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	35	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	23	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	32	P
10. NETWORK LABORATORY	TW	25	10	21	P
11. NETWORK LABORATORY	OR	50	20	45	P

FIRST TERM TOTAL = 476/750.
ORDN. 1 MARKS :

T8058553 KADAM RAHUL DNYANESHWAR INDUBAI , 71134964K , , PICT , T8058553

01. OPERATING SYSTEM	PP	100	40	61	P
02. THEORY OF COMPUTATION	PP	100	40	69	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	78	P
04. DATBASE MANAGEMENT SYSTEMS	PP	100	40	67	P
05. SOFTWARE ENGINEERING	PP	100	40	53	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	21	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	41	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	20	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	37	P
10. NETWORK LABORATORY	TW	25	10	22	P
11. NETWORK LABORATORY	OR	50	20	43	P

FIRST TERM TOTAL = 534/750.
ORDN. 1 MARKS :

T8058554 KAKADE POOJA DILIPRAO SUNITA , 71134965H , , PICT , T8058554

01. OPERATING SYSTEM	PP	100	40	59	P
02. THEORY OF COMPUTATION	PP	100	40	64	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	74	P
04. DATBASE MANAGEMENT SYSTEMS	PP	100	40	58	P
05. SOFTWARE ENGINEERING	PP	100	40	55	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	23	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	37	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	24	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	32	P
10. NETWORK LABORATORY	TW	25	10	22	P
11. NETWORK LABORATORY	OR	50	20	36	P

FIRST TERM TOTAL = 507/750.
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8058555 KALBANDE GAURI RAJENDRA PRABHA , 71134966F , , PICT , T8058555

01.	OPERATING SYSTEM	PP	100	40	60	P
02.	THEORY OF COMPUTATION	PP	100	40	60	P
03.	COMPUTER NETWORK TECHNOLOGY	PP	100	40	73	P
04.	DATBASE MANAGEMENT SYSTEMS	PP	100	40	60	P
05.	SOFTWARE ENGINEERING	PP	100	40	45	P
06.	OPERATING SYSTEM DESIGN LAB.	TW	25	10	20	P
07.	OPERATING SYSTEM DESIGN LAB.	PR	50	20	42	P
08.	INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	22	P
09.	INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	38	P
10.	NETWORK LABORATORY	TW	25	10	21	P
11.	NETWORK LABORATORY	OR	50	20	42	P

FIRST TERM TOTAL = 506/750.
ORDN. 1 MARKS :

T8058556 KALRA JASBIRKAUR AMARJIT SINGH MEENA , 71134967D , , PICT , T8058556

01.	OPERATING SYSTEM	PP	100	40	62	P
02.	THEORY OF COMPUTATION	PP	100	40	51	P
03.	COMPUTER NETWORK TECHNOLOGY	PP	100	40	68	P
04.	DATBASE MANAGEMENT SYSTEMS	PP	100	40	50	P
05.	SOFTWARE ENGINEERING	PP	100	40	48	P
06.	OPERATING SYSTEM DESIGN LAB.	TW	25	10	23	P
07.	OPERATING SYSTEM DESIGN LAB.	PR	50	20	44	P
08.	INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	22	P
09.	INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	33	P
10.	NETWORK LABORATORY	TW	25	10	21	P
11.	NETWORK LABORATORY	OR	50	20	40	P

FIRST TERM TOTAL = 484/750.
ORDN. 1 MARKS :

T8058557 KAMBLE JITESH SHAMRAO PUSHPA , 71045466L , , PICT , T8058557

01.	OPERATING SYSTEM	PP	100	40	54	P
02.	THEORY OF COMPUTATION	PP	100	40	68	P
03.	COMPUTER NETWORK TECHNOLOGY	PP	100	40	78	P
04.	DATBASE MANAGEMENT SYSTEMS	PP	100	40	51	P
05.	SOFTWARE ENGINEERING	PP	100	40	45	P
06.	OPERATING SYSTEM DESIGN LAB.	TW	25	10	21	P
07.	OPERATING SYSTEM DESIGN LAB.	PR	50	20	42	P
08.	INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	20	P
09.	INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	41	P
10.	NETWORK LABORATORY	TW	25	10	20	P
11.	NETWORK LABORATORY	OR	50	20	45	P

FIRST TERM TOTAL = 505/750.
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8058558 KARACHIWALA HAMZA SHABBIR DURRIYA , 71045470J , , PICT , T8058558

01.	OPERATING SYSTEM	PP	100	40	50	P
02.	THEORY OF COMPUTATION	PP	100	40	74	P
03.	COMPUTER NETWORK TECHNOLOGY	PP	100	40	71	P
04.	DATBASE MANAGEMENT SYSTEMS	PP	100	40	58	P
05.	SOFTWARE ENGINEERING	PP	100	40	44	P
06.	OPERATING SYSTEM DESIGN LAB.	TW	25	10	18	P
07.	OPERATING SYSTEM DESIGN LAB.	PR	50	20	42	P
08.	INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	19	P
09.	INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	41	P
10.	NETWORK LABORATORY	TW	25	10	19	P
11.	NETWORK LABORATORY	OR	50	20	39	P

FIRST TERM TOTAL = 495/750.
ORDN. 1 MARKS :

T8058559 KARISHMA GARG KAVITA , 71045474M , , PICT , T8058559

01.	OPERATING SYSTEM	PP	100	40	41	P
02.	THEORY OF COMPUTATION	PP	100	40	59	P
03.	COMPUTER NETWORK TECHNOLOGY	PP	100	40	56	P
04.	DATBASE MANAGEMENT SYSTEMS	PP	100	40	58	P
05.	SOFTWARE ENGINEERING	PP	100	40	41	P
06.	OPERATING SYSTEM DESIGN LAB.	TW	25	10	23	P
07.	OPERATING SYSTEM DESIGN LAB.	PR	50	20	42	P
08.	INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	23	P
09.	INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	39	P
10.	NETWORK LABORATORY	TW	25	10	22	P
11.	NETWORK LABORATORY	OR	50	20	40	P

FIRST TERM TOTAL = 467/750.
ORDN. 1 MARKS :

T8058560 KAZI SANA NAFIS FARJANA , 71045478D , , PICT , T8058560

01.	OPERATING SYSTEM	PP	100	40	47	P
02.	THEORY OF COMPUTATION	PP	100	40	73	P
03.	COMPUTER NETWORK TECHNOLOGY	PP	100	40	72	P
04.	DATBASE MANAGEMENT SYSTEMS	PP	100	40	59	P
05.	SOFTWARE ENGINEERING	PP	100	40	45	P
06.	OPERATING SYSTEM DESIGN LAB.	TW	25	10	23	P
07.	OPERATING SYSTEM DESIGN LAB.	PR	50	20	40	P
08.	INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	22	P
09.	INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	31	P
10.	NETWORK LABORATORY	TW	25	10	22	P
11.	NETWORK LABORATORY	OR	50	20	23	P

FIRST TERM TOTAL = 479/750.
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8058561 KELKAR ANUJA MILIND SUPRIYA , 71045480F , , PICT , T8058561

01. OPERATING SYSTEM	PP	100	40	53	P
02. THEORY OF COMPUTATION	PP	100	40	58	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	67	P
04. DATBASE MANAGEMENT SYSTEMS	PP	100	40	60	P
05. SOFTWARE ENGINEERING	PP	100	40	56	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	23	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	40	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	22	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	42	P
10. NETWORK LABORATORY	TW	25	10	23	P
11. NETWORK LABORATORY	OR	50	20	47	P

FIRST TERM TOTAL = 514/750.
ORDN. 1 MARKS :

T8058562 KHANDAGALE SURAJ PANDURANG SUNITA , 70925476J , , PICT , T8058562

01. OPERATING SYSTEM	PP	100	40	45	P
02. THEORY OF COMPUTATION	PP	100	40	57	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	62	P
04. DATBASE MANAGEMENT SYSTEMS	PP	100	40	51	P
05. SOFTWARE ENGINEERING	PP	100	40	40	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	19	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	28	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	19	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	40	P
10. NETWORK LABORATORY	TW	25	10	18	P
11. NETWORK LABORATORY	OR	50	20	37	P

FIRST TERM TOTAL = 433/750.
ORDN. 1 MARKS :

T8058563 KOTHAWALE AKASH SANJAY ANITA , 71045486E , , PICT , T8058563

01. OPERATING SYSTEM	PP	100	40	52	P
02. THEORY OF COMPUTATION	PP	100	40	61	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	69	P
04. DATBASE MANAGEMENT SYSTEMS	PP	100	40	42	P
05. SOFTWARE ENGINEERING	PP	100	40	51	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	23	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	26	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	22	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	41	P
10. NETWORK LABORATORY	TW	25	10	22	P
11. NETWORK LABORATORY	OR	50	20	37	P

FIRST TERM TOTAL = 468/750.
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8058564 KULKARNI AMITA DILIP MEGHA , 71045492K , , PICT , T8058564

01.	OPERATING SYSTEM	PP	100	40	50	P
02.	THEORY OF COMPUTATION	PP	100	40	69	P
03.	COMPUTER NETWORK TECHNOLOGY	PP	100	40	64	P
04.	DATBASE MANAGEMENT SYSTEMS	PP	100	40	57	P
05.	SOFTWARE ENGINEERING	PP	100	40	40	P
06.	OPERATING SYSTEM DESIGN LAB.	TW	25	10	23	P
07.	OPERATING SYSTEM DESIGN LAB.	PR	50	20	38	P
08.	INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	23	P
09.	INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	31	P
10.	NETWORK LABORATORY	TW	25	10	21	P
11.	NETWORK LABORATORY	OR	50	20	33	P

FIRST TERM TOTAL = 470/750.
ORDN. 1 MARKS :

T8058565 KULKARNI PRAMOD SHIVAJIRAO SUMATI , 71045672H , , PICT , T8058565

01.	OPERATING SYSTEM	PP	100	40	40	P
02.	THEORY OF COMPUTATION	PP	100	40	40	P
03.	COMPUTER NETWORK TECHNOLOGY	PP	100	40	61	P
04.	DATBASE MANAGEMENT SYSTEMS	PP	100	40	49	P
05.	SOFTWARE ENGINEERING	PP	100	40	40	P
06.	OPERATING SYSTEM DESIGN LAB.	TW	25	10	12	P
07.	OPERATING SYSTEM DESIGN LAB.	PR	50	20	06	F
08.	INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	10	P
09.	INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	25	P
10.	NETWORK LABORATORY	TW	25	10	10	P
11.	NETWORK LABORATORY	OR	50	20	12	F

FIRST TERM TOTAL = 317/750.
ORDN. 1 MARKS :

T8058566 LAKHEPATIL AJINKYA RAMRAJE HEMLATA , 71045502L , , PICT , T8058566

01.	OPERATING SYSTEM	PP	100	40	49	P
02.	THEORY OF COMPUTATION	PP	100	40	65	P
03.	COMPUTER NETWORK TECHNOLOGY	PP	100	40	67	P
04.	DATBASE MANAGEMENT SYSTEMS	PP	100	40	63	P
05.	SOFTWARE ENGINEERING	PP	100	40	44	P
06.	OPERATING SYSTEM DESIGN LAB.	TW	25	10	24	P
07.	OPERATING SYSTEM DESIGN LAB.	PR	50	20	39	P
08.	INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	23	P
09.	INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	35	P
10.	NETWORK LABORATORY	TW	25	10	22	P
11.	NETWORK LABORATORY	OR	50	20	36	P

FIRST TERM TOTAL = 490/750.
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8058567 LONKAR GORAKH SUBHASH SHOBHA , 71134968B , , PICT , T8058567

01. OPERATING SYSTEM	PP	100	40	55	P	
02. THEORY OF COMPUTATION	PP	100	40	66	P	
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	78	P	
04. DATBASE MANAGEMENT SYSTEMS	PP	100	40	58	P	
05. SOFTWARE ENGINEERING	PP	100	40	56	P	
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	24	P	
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	42	P	
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	24	P	
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	39	P	
10. NETWORK LABORATORY	TW	25	10	24	P	
11. NETWORK LABORATORY	OR	50	20	47	P	

FIRST TERM TOTAL = 536/750.
ORDN. 1 MARKS :

T8058568 MAHAJAN AKSHATA DEVENDRA JAISHREE , 71045507M , , PICT , T8058568

01. OPERATING SYSTEM	PP	100	40	51	P	
02. THEORY OF COMPUTATION	PP	100	40	75	P	
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	66	P	
04. DATBASE MANAGEMENT SYSTEMS	PP	100	40	62	P	
05. SOFTWARE ENGINEERING	PP	100	40	53	P	
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	21	P	
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	41	P	
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	20	P	
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	40	P	
10. NETWORK LABORATORY	TW	25	10	22	P	
11. NETWORK LABORATORY	OR	50	20	44	P	

FIRST TERM TOTAL = 516/750.
ORDN. 1 MARKS :

T8058570 MAHALPURE SHRUTI SATISH SUSHMA , 71045509H , , PICT , T8058570

01. OPERATING SYSTEM	PP	100	40	40	P	
02. THEORY OF COMPUTATION	PP	100	40	53	P	
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	65	P	
04. DATBASE MANAGEMENT SYSTEMS	PP	100	40	56	P	
05. SOFTWARE ENGINEERING	PP	100	40	45	P	
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	24	P	
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	41	P	
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	24	P	
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	31	P	
10. NETWORK LABORATORY	TW	25	10	22	P	
11. NETWORK LABORATORY	OR	50	20	40	P	

FIRST TERM TOTAL = 463/750.
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8058571 MARDA SAKSHI BRIJMOHAN JYOTI , 71045521G , , PICT , T8058571

01.	OPERATING SYSTEM	PP	100	40	40	P
02.	THEORY OF COMPUTATION	PP	100	40	57	P
03.	COMPUTER NETWORK TECHNOLOGY	PP	100	40	61	P
04.	DATBASE MANAGEMENT SYSTEMS	PP	100	40	55	P
05.	SOFTWARE ENGINEERING	PP	100	40	41	P
06.	OPERATING SYSTEM DESIGN LAB.	TW	25	10	22	P
07.	OPERATING SYSTEM DESIGN LAB.	PR	50	20	38	P
08.	INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	22	P
09.	INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	39	P
10.	NETWORK LABORATORY	TW	25	10	21	P
11.	NETWORK LABORATORY	OR	50	20	32	P

FIRST TERM TOTAL = 450/750.
ORDN. 1 MARKS :

T8058572 MARWA MAYUR RAJESH ASHA , 71045522E , , PICT , T8058572

01.	OPERATING SYSTEM	PP	100	40	40	P
02.	THEORY OF COMPUTATION	PP	100	40	29	F
03.	COMPUTER NETWORK TECHNOLOGY	PP	100	40	58	P
04.	DATBASE MANAGEMENT SYSTEMS	PP	100	40	45	P
05.	SOFTWARE ENGINEERING	PP	100	40	40	P
06.	OPERATING SYSTEM DESIGN LAB.	TW	25	10	11	P
07.	OPERATING SYSTEM DESIGN LAB.	PR	50	20	28	P
08.	INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	12	P
09.	INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	AA	F
10.	NETWORK LABORATORY	TW	25	10	10	P
11.	NETWORK LABORATORY	OR	50	20	10	F

FIRST TERM TOTAL = 295/750.
ORDN. 1 MARKS :

T8058573 MAYANK SINGH ARCHANA , 71045524M , , PICT , T8058573

01.	OPERATING SYSTEM	PP	100	40	40	P
02.	THEORY OF COMPUTATION	PP	100	40	55	P
03.	COMPUTER NETWORK TECHNOLOGY	PP	100	40	45	P
04.	DATBASE MANAGEMENT SYSTEMS	PP	100	40	61	P
05.	SOFTWARE ENGINEERING	PP	100	40	47	P
06.	OPERATING SYSTEM DESIGN LAB.	TW	25	10	17	P
07.	OPERATING SYSTEM DESIGN LAB.	PR	50	20	37	P
08.	INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	17	P
09.	INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	24	P
10.	NETWORK LABORATORY	TW	25	10	16	P
11.	NETWORK LABORATORY	OR	50	20	39	P

FIRST TERM TOTAL = 408/750.
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8058574 MOHAMMED AAQUIB ANSARI MOHAMMED YUSUF SUFIYA , 71045526H , , PICT , T8058574

01.	OPERATING SYSTEM	PP	100	40	28	F
02.	THEORY OF COMPUTATION	PP	100	40	40	P
03.	COMPUTER NETWORK TECHNOLOGY	PP	100	40	45	P
04.	DATBASE MANAGEMENT SYSTEMS	PP	100	40	40	P
05.	SOFTWARE ENGINEERING	PP	100	40	40	P
06.	OPERATING SYSTEM DESIGN LAB.	TW	25	10	10	P
07.	OPERATING SYSTEM DESIGN LAB.	PR	50	20	AA	F
08.	INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	10	P
09.	INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	AA	F
10.	NETWORK LABORATORY	TW	25	10	10	P
11.	NETWORK LABORATORY	OR	50	20	11	F

FIRST TERM TOTAL = 246/750.
ORDN. 1 MARKS :

T8058575 MULAY AMIT YESHWANT SNEHAL , 71045530F , , PICT , T8058575

01.	OPERATING SYSTEM	PP	100	40	40	P
02.	THEORY OF COMPUTATION	PP	100	40	52	P
03.	COMPUTER NETWORK TECHNOLOGY	PP	100	40	58	P
04.	DATBASE MANAGEMENT SYSTEMS	PP	100	40	48	P
05.	SOFTWARE ENGINEERING	PP	100	40	46	P
06.	OPERATING SYSTEM DESIGN LAB.	TW	25	10	15	P
07.	OPERATING SYSTEM DESIGN LAB.	PR	50	20	05	F
08.	INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	15	P
09.	INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	10	F
10.	NETWORK LABORATORY	TW	25	10	14	P
11.	NETWORK LABORATORY	OR	50	20	12	F

FIRST TERM TOTAL = 325/750.
ORDN. 1 MARKS :

T8058576 MULEY PRASAD MUKUNDRAO SUSHMA , 71134969L , , PICT , T8058576

01.	OPERATING SYSTEM	PP	100	40	40	P
02.	THEORY OF COMPUTATION	PP	100	40	58	P
03.	COMPUTER NETWORK TECHNOLOGY	PP	100	40	70	P
04.	DATBASE MANAGEMENT SYSTEMS	PP	100	40	55	P
05.	SOFTWARE ENGINEERING	PP	100	40	46	P
06.	OPERATING SYSTEM DESIGN LAB.	TW	25	10	23	P
07.	OPERATING SYSTEM DESIGN LAB.	PR	50	20	33	P
08.	INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	23	P
09.	INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	41	P
10.	NETWORK LABORATORY	TW	25	10	22	P
11.	NETWORK LABORATORY	OR	50	20	40	P

FIRST TERM TOTAL = 473/750.
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8058578 MURKUTE DEVYANI BALASAHEB KALPANA , 71134971B , , PICT , T8058578

01.	OPERATING SYSTEM	PP	100	40	47	P
02.	THEORY OF COMPUTATION	PP	100	40	50	P
03.	COMPUTER NETWORK TECHNOLOGY	PP	100	40	65	P
04.	DATBASE MANAGEMENT SYSTEMS	PP	100	40	48	P
05.	SOFTWARE ENGINEERING	PP	100	40	47	P
06.	OPERATING SYSTEM DESIGN LAB.	TW	25	10	22	P
07.	OPERATING SYSTEM DESIGN LAB.	PR	50	20	38	P
08.	INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	21	P
09.	INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	20	P
10.	NETWORK LABORATORY	TW	25	10	21	P
11.	NETWORK LABORATORY	OR	50	20	38	P

FIRST TERM TOTAL = 439/750.
ORDN. 1 MARKS :

T8058579 NAIKNAWARE UTKARSH MARUTI MAHANANDA , 71045538M , , PICT , T8058579

01.	OPERATING SYSTEM	PP	100	40	40	P
02.	THEORY OF COMPUTATION	PP	100	40	48	P
03.	COMPUTER NETWORK TECHNOLOGY	PP	100	40	52	P
04.	DATBASE MANAGEMENT SYSTEMS	PP	100	40	50	P
05.	SOFTWARE ENGINEERING	PP	100	40	40	P
06.	OPERATING SYSTEM DESIGN LAB.	TW	25	10	23	P
07.	OPERATING SYSTEM DESIGN LAB.	PR	50	20	32	P
08.	INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	20	P
09.	INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	30	P
10.	NETWORK LABORATORY	TW	25	10	22	P
11.	NETWORK LABORATORY	OR	50	20	35	P

FIRST TERM TOTAL = 413/750.
ORDN. 1 MARKS :

T8058580 NIKAM SONALI JIVAN ARUNA , 71134972L , , PICT , T8058580

01.	OPERATING SYSTEM	PP	100	40	47	P
02.	THEORY OF COMPUTATION	PP	100	40	62	P
03.	COMPUTER NETWORK TECHNOLOGY	PP	100	40	74	P
04.	DATBASE MANAGEMENT SYSTEMS	PP	100	40	68	P
05.	SOFTWARE ENGINEERING	PP	100	40	60	P
06.	OPERATING SYSTEM DESIGN LAB.	TW	25	10	24	P
07.	OPERATING SYSTEM DESIGN LAB.	PR	50	20	32	P
08.	INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	21	P
09.	INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	27	P
10.	NETWORK LABORATORY	TW	25	10	23	P
11.	NETWORK LABORATORY	OR	50	20	25	P

FIRST TERM TOTAL = 483/750.
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8058581 NILESH D PHADTARE KUNDA , 70925526J , , PICT , T8058581

01. OPERATING SYSTEM	PP	100	40	40	P
02. THEORY OF COMPUTATION	PP	100	40	45	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	45	P
04. DATBASE MANAGEMENT SYSTEMS	PP	100	40	40	P
05. SOFTWARE ENGINEERING	PP	100	40	31	F
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	20	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	28	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	21	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	31	P
10. NETWORK LABORATORY	TW	25	10	20	P
11. NETWORK LABORATORY	OR	50	20	32	P

FIRST TERM TOTAL = 376/750.
ORDN. 1 MARKS :

T8058582 NIMBALKAR ABHIJIT SHIRISH SUREKHA , 71057179J , , PICT , T8058582

01. OPERATING SYSTEM	PP	100	40	53	P
02. THEORY OF COMPUTATION	PP	100	40	51	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	68	P
04. DATBASE MANAGEMENT SYSTEMS	PP	100	40	57	P
05. SOFTWARE ENGINEERING	PP	100	40	58	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	22	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	38	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	22	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	40	P
10. NETWORK LABORATORY	TW	25	10	21	P
11. NETWORK LABORATORY	OR	50	20	40	P

FIRST TERM TOTAL = 492/750.
ORDN. 1 MARKS :

T8058583 NY GARIN YI MALIVATTEY , 70925519F , , PICT , T8058583

01. OPERATING SYSTEM	PP	100	40	23	F
02. THEORY OF COMPUTATION	PP	100	40	AA	F
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	46	P
04. DATBASE MANAGEMENT SYSTEMS	PP	100	40	40	P
05. SOFTWARE ENGINEERING	PP	100	40	30	F
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	19	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	27	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	20	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	31	P
10. NETWORK LABORATORY	TW	25	10	18	P
11. NETWORK LABORATORY	OR	50	20	08	F

FIRST TERM TOTAL = 281/750.
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8058584 PANDIT DIVYA SYAMANTAKMANI VEENA , 71045552G , , PICT , T8058584

01.	OPERATING SYSTEM	PP	100	40	53	P
02.	THEORY OF COMPUTATION	PP	100	40	40	P
03.	COMPUTER NETWORK TECHNOLOGY	PP	100	40	60	P
04.	DATBASE MANAGEMENT SYSTEMS	PP	100	40	50	P
05.	SOFTWARE ENGINEERING	PP	100	40	48	P
06.	OPERATING SYSTEM DESIGN LAB.	TW	25	10	19	P
07.	OPERATING SYSTEM DESIGN LAB.	PR	50	20	38	P
08.	INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	18	P
09.	INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	15	F
10.	NETWORK LABORATORY	TW	25	10	19	P
11.	NETWORK LABORATORY	OR	50	20	40	P

FIRST TERM TOTAL = 418/750.
ORDN. 1 MARKS :

T8058585 PANSARE ROHAN SAMPAT MANGAL , 71045554C , , PICT , T8058585

01.	OPERATING SYSTEM	PP	100	40	50	P
02.	THEORY OF COMPUTATION	PP	100	40	49	P
03.	COMPUTER NETWORK TECHNOLOGY	PP	100	40	69	P
04.	DATBASE MANAGEMENT SYSTEMS	PP	100	40	60	P
05.	SOFTWARE ENGINEERING	PP	100	40	49	P
06.	OPERATING SYSTEM DESIGN LAB.	TW	25	10	22	P
07.	OPERATING SYSTEM DESIGN LAB.	PR	50	20	39	P
08.	INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	20	P
09.	INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	28	P
10.	NETWORK LABORATORY	TW	25	10	20	P
11.	NETWORK LABORATORY	OR	50	20	10	F

FIRST TERM TOTAL = 437/750.
ORDN. 1 MARKS :

T8058586 PARADKAR SUPRIYA SUNIL CHITRA , 71045555M , , PICT , T8058586

01.	OPERATING SYSTEM	PP	100	40	50	P
02.	THEORY OF COMPUTATION	PP	100	40	65	P
03.	COMPUTER NETWORK TECHNOLOGY	PP	100	40	61	P
04.	DATBASE MANAGEMENT SYSTEMS	PP	100	40	51	P
05.	SOFTWARE ENGINEERING	PP	100	40	45	P
06.	OPERATING SYSTEM DESIGN LAB.	TW	25	10	21	P
07.	OPERATING SYSTEM DESIGN LAB.	PR	50	20	38	P
08.	INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	20	P
09.	INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	27	P
10.	NETWORK LABORATORY	TW	25	10	21	P
11.	NETWORK LABORATORY	OR	50	20	30	P

FIRST TERM TOTAL = 449/750.
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8058587 PARAKH KUNAL KAILAS MEENA , 71045556K , , PICT , T8058587

01.	OPERATING SYSTEM	PP	100	40	55	P
02.	THEORY OF COMPUTATION	PP	100	40	56	P
03.	COMPUTER NETWORK TECHNOLOGY	PP	100	40	79	P
04.	DATBASE MANAGEMENT SYSTEMS	PP	100	40	56	P
05.	SOFTWARE ENGINEERING	PP	100	40	53	P
06.	OPERATING SYSTEM DESIGN LAB.	TW	25	10	21	P
07.	OPERATING SYSTEM DESIGN LAB.	PR	50	20	42	P
08.	INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	20	P
09.	INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	32	P
10.	NETWORK LABORATORY	TW	25	10	21	P
11.	NETWORK LABORATORY	OR	50	20	38	P

FIRST TERM TOTAL = 494/750.
ORDN. 1 MARKS :

T8058588 PAREKH SONALI SUNIL REKHA , 71045558F , , PICT , T8058588

01.	OPERATING SYSTEM	PP	100	40	48	P
02.	THEORY OF COMPUTATION	PP	100	40	47	P
03.	COMPUTER NETWORK TECHNOLOGY	PP	100	40	67	P
04.	DATBASE MANAGEMENT SYSTEMS	PP	100	40	54	P
05.	SOFTWARE ENGINEERING	PP	100	40	44	P
06.	OPERATING SYSTEM DESIGN LAB.	TW	25	10	18	P
07.	OPERATING SYSTEM DESIGN LAB.	PR	50	20	43	P
08.	INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	19	P
09.	INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	26	P
10.	NETWORK LABORATORY	TW	25	10	18	P
11.	NETWORK LABORATORY	OR	50	20	35	P

FIRST TERM TOTAL = 436/750.
ORDN. 1 MARKS :

T8058589 PARTH SARTHI PIPLANI SUREKHA , 71045560H , , PICT , T8058589

01.	OPERATING SYSTEM	PP	100	40	29	F
02.	THEORY OF COMPUTATION	PP	100	40	40	P
03.	COMPUTER NETWORK TECHNOLOGY	PP	100	40	40	P
04.	DATBASE MANAGEMENT SYSTEMS	PP	100	40	40	P
05.	SOFTWARE ENGINEERING	PP	100	40	41	P
06.	OPERATING SYSTEM DESIGN LAB.	TW	25	10	16	P
07.	OPERATING SYSTEM DESIGN LAB.	PR	50	20	10	F
08.	INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	10	P
09.	INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	AA	F
10.	NETWORK LABORATORY	TW	25	10	12	P
11.	NETWORK LABORATORY	OR	50	20	07	F

FIRST TERM TOTAL = 260/750.
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8058590 PATEL NAZNIN JAVED MAHERUNISSA , 71134973J , , PICT , T8058590

01. OPERATING SYSTEM	PP	100	40	40	P
02. THEORY OF COMPUTATION	PP	100	40	30	F
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	61	P
04. DATBASE MANAGEMENT SYSTEMS	PP	100	40	48	P
05. SOFTWARE ENGINEERING	PP	100	40	40	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	21	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	15	F
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	19	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	20	P
10. NETWORK LABORATORY	TW	25	10	21	P
11. NETWORK LABORATORY	OR	50	20	28	P

FIRST TERM TOTAL = 362/750.
ORDN. 1 MARKS :

T8058591 PATIL DNYANESHWARI PRAKASH SANGITA , 71045563B , , PICT , T8058591

01. OPERATING SYSTEM	PP	100	40	45	P
02. THEORY OF COMPUTATION	PP	100	40	70	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	69	P
04. DATBASE MANAGEMENT SYSTEMS	PP	100	40	46	P
05. SOFTWARE ENGINEERING	PP	100	40	46	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	21	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	35	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	19	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	33	P
10. NETWORK LABORATORY	TW	25	10	20	P
11. NETWORK LABORATORY	OR	50	20	25	P

FIRST TERM TOTAL = 451/750.
ORDN. 1 MARKS :

T8058592 PATIL KHUSHBU PRAKASH CHHAYA , 71045565J , , PICT , T8058592

01. OPERATING SYSTEM	PP	100	40	48	P
02. THEORY OF COMPUTATION	PP	100	40	76	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	62	P
04. DATBASE MANAGEMENT SYSTEMS	PP	100	40	63	P
05. SOFTWARE ENGINEERING	PP	100	40	40	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	19	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	31	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	18	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	27	P
10. NETWORK LABORATORY	TW	25	10	19	P
11. NETWORK LABORATORY	OR	50	20	28	P

FIRST TERM TOTAL = 448/750.
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8058593 PAWAR BHANUPRIYA VITTHAL SHAKUNTALA , 71045571C , , PICT , T8058593

01. OPERATING SYSTEM	PP	100	40	41	P
02. THEORY OF COMPUTATION	PP	100	40	55	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	44	P
04. DATBASE MANAGEMENT SYSTEMS	PP	100	40	42	P
05. SOFTWARE ENGINEERING	PP	100	40	41	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	20	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	32	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	18	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	28	P
10. NETWORK LABORATORY	TW	25	10	19	P
11. NETWORK LABORATORY	OR	50	20	25	P

FIRST TERM TOTAL = 383/750.
ORDN. 1 MARKS :

T8058594 PHUTANE AMEY UDAY SARITA , 71054577M , , PICT , T8058594

01. OPERATING SYSTEM	PP	100	40	52	P
02. THEORY OF COMPUTATION	PP	100	40	61	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	74	P
04. DATBASE MANAGEMENT SYSTEMS	PP	100	40	55	P
05. SOFTWARE ENGINEERING	PP	100	40	52	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	18	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	41	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	19	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	38	P
10. NETWORK LABORATORY	TW	25	10	17	P
11. NETWORK LABORATORY	OR	50	20	34	P

FIRST TERM TOTAL = 478/750.
ORDN. 1 MARKS :

T8058595 PINGLE ADITYA ULHAS ARCHANA , 71134974G , , PICT , T8058595

01. OPERATING SYSTEM	PP	100	40	48	P
02. THEORY OF COMPUTATION	PP	100	40	55	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	74	P
04. DATBASE MANAGEMENT SYSTEMS	PP	100	40	55	P
05. SOFTWARE ENGINEERING	PP	100	40	50	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	23	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	42	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	21	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	40	P
10. NETWORK LABORATORY	TW	25	10	22	P
11. NETWORK LABORATORY	OR	50	20	44	P

FIRST TERM TOTAL = 495/750.
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8058596 POLE AKSHAYKUMAR RAHUL SHEELA , 71045576D , , PICT , T8058596

01. OPERATING SYSTEM	PP	100	40	40	P
02. THEORY OF COMPUTATION	PP	100	40	20	F
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	55	P
04. DATBASE MANAGEMENT SYSTEMS	PP	100	40	42	P
05. SOFTWARE ENGINEERING	PP	100	40	40	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	12	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	20	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	10	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	10	F
10. NETWORK LABORATORY	TW	25	10	19	P
11. NETWORK LABORATORY	OR	50	20	08	F

FIRST TERM TOTAL = 296/750.
ORDN. 1 MARKS :

T8058597 POTE SMITA RAGHUNATH VIJAYA , 71045577B , , PICT , T8058597

01. OPERATING SYSTEM	PP	100	40	48	P
02. THEORY OF COMPUTATION	PP	100	40	46	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	49	P
04. DATBASE MANAGEMENT SYSTEMS	PP	100	40	40	P
05. SOFTWARE ENGINEERING	PP	100	40	41	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	20	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	32	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	20	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	38	P
10. NETWORK LABORATORY	TW	25	10	21	P
11. NETWORK LABORATORY	OR	50	20	30	P

FIRST TERM TOTAL = 404/750.
ORDN. 1 MARKS :

T8058598 POTEY AKSHAY MADHUKAR NILIMA , 71045578L , , PICT , T8058598

01. OPERATING SYSTEM	PP	100	40	27	F
02. THEORY OF COMPUTATION	PP	100	40	42	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	66	P
04. DATBASE MANAGEMENT SYSTEMS	PP	100	40	47	P
05. SOFTWARE ENGINEERING	PP	100	40	40	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	18	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	30	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	15	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	AA	F
10. NETWORK LABORATORY	TW	25	10	11	P
11. NETWORK LABORATORY	OR	50	20	AA	F

FIRST TERM TOTAL = 311/750.
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8058599 PRIYA KASHYAP REETA , 71045582J , , PICT , T8058599

01.	OPERATING SYSTEM	PP	100	40	44	P
02.	THEORY OF COMPUTATION	PP	100	40	64	P
03.	COMPUTER NETWORK TECHNOLOGY	PP	100	40	51	P
04.	DATBASE MANAGEMENT SYSTEMS	PP	100	40	44	P
05.	SOFTWARE ENGINEERING	PP	100	40	54	P
06.	OPERATING SYSTEM DESIGN LAB.	TW	25	10	20	P
07.	OPERATING SYSTEM DESIGN LAB.	PR	50	20	20	P
08.	INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	20	P
09.	INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	20	P
10.	NETWORK LABORATORY	TW	25	10	20	P
11.	NETWORK LABORATORY	OR	50	20	30	P

FIRST TERM TOTAL = 404/750.
ORDN. 1 MARKS :

T8058600 PUNTAMBEKAR SHREYA SHAILESH SEEMA , 71045584E , , PICT , T8058600

01.	OPERATING SYSTEM	PP	100	40	31	F
02.	THEORY OF COMPUTATION	PP	100	40	19	F
03.	COMPUTER NETWORK TECHNOLOGY	PP	100	40	46	P
04.	DATBASE MANAGEMENT SYSTEMS	PP	100	40	40	P
05.	SOFTWARE ENGINEERING	PP	100	40	25	F
06.	OPERATING SYSTEM DESIGN LAB.	TW	25	10	18	P
07.	OPERATING SYSTEM DESIGN LAB.	PR	50	20	15	F
08.	INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	18	P
09.	INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	38	P
10.	NETWORK LABORATORY	TW	25	10	19	P
11.	NETWORK LABORATORY	OR	50	20	28	P

FIRST TERM TOTAL = 314/750.
ORDN. 1 MARKS :

T8058601 RAIJADE DHANVANTARI BALKRISHANA RATNAPRABHA , 71134975E , , PICT , T8058601

01.	OPERATING SYSTEM	PP	100	40	46	P
02.	THEORY OF COMPUTATION	PP	100	40	47	P
03.	COMPUTER NETWORK TECHNOLOGY	PP	100	40	55	P
04.	DATBASE MANAGEMENT SYSTEMS	PP	100	40	47	P
05.	SOFTWARE ENGINEERING	PP	100	40	48	P
06.	OPERATING SYSTEM DESIGN LAB.	TW	25	10	23	P
07.	OPERATING SYSTEM DESIGN LAB.	PR	50	20	36	P
08.	INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	20	P
09.	INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	25	P
10.	NETWORK LABORATORY	TW	25	10	22	P
11.	NETWORK LABORATORY	OR	50	20	28	P

FIRST TERM TOTAL = 416/750.
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8058602 RAJOLE VISHAL VILASRAO SUNITA , 71134976C , , PICT , T8058602

01. OPERATING SYSTEM	PP	100	40	53	P
02. THEORY OF COMPUTATION	PP	100	40	72	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	71	P
04. DATBASE MANAGEMENT SYSTEMS	PP	100	40	60	P
05. SOFTWARE ENGINEERING	PP	100	40	60	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	22	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	41	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	21	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	40	P
10. NETWORK LABORATORY	TW	25	10	22	P
11. NETWORK LABORATORY	OR	50	20	35	P

FIRST TERM TOTAL = 517/750.
ORDN. 1 MARKS :

T8058603 RISHABH PANDITA USHA , 71045589F , , PICT , T8058603

01. OPERATING SYSTEM	PP	100	40	52	P
02. THEORY OF COMPUTATION	PP	100	40	59	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	67	P
04. DATBASE MANAGEMENT SYSTEMS	PP	100	40	60	P
05. SOFTWARE ENGINEERING	PP	100	40	46	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	21	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	41	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	21	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	30	P
10. NETWORK LABORATORY	TW	25	10	20	P
11. NETWORK LABORATORY	OR	50	20	44	P

FIRST TERM TOTAL = 482/750.
ORDN. 1 MARKS :

T8058604 ROHAN MARWADI MEENAKSHI , 71045590K , , PICT , T8058604

01. OPERATING SYSTEM	PP	100	40	31	F
02. THEORY OF COMPUTATION	PP	100	40	27	F
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	31	F
04. DATBASE MANAGEMENT SYSTEMS	PP	100	40	28	F
05. SOFTWARE ENGINEERING	PP	100	40	45	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	17	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	12	F
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	19	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	40	P
10. NETWORK LABORATORY	TW	25	10	19	P
11. NETWORK LABORATORY	OR	50	20	22	P

FIRST TERM TOTAL = 309/750.
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8058605 SACHIN SUKHLECHA SANGEETA , 71045592F , , PICT , T8058605

01. OPERATING SYSTEM	PP	100	40	40	P	
02. THEORY OF COMPUTATION	PP	100	40	65	P	
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	44	P	
04. DATBASE MANAGEMENT SYSTEMS	PP	100	40	41	P	
05. SOFTWARE ENGINEERING	PP	100	40	45	P	
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	20	P	
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	37	P	
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	16	P	
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	33	P	
10. NETWORK LABORATORY	TW	25	10	20	P	
11. NETWORK LABORATORY	OR	50	20	32	P	

FIRST TERM TOTAL = 414/750.
ORDN. 1 MARKS :

T8058606 SALVE AJINKYA PRABHAKAR KAVITA , 71045594B , , PICT , T8058606

01. OPERATING SYSTEM	PP	100	40	51	P	
02. THEORY OF COMPUTATION	PP	100	40	61	P	
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	72	P	
04. DATBASE MANAGEMENT SYSTEMS	PP	100	40	51	P	
05. SOFTWARE ENGINEERING	PP	100	40	55	P	
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	16	P	
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	44	P	
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	17	P	
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	41	P	
10. NETWORK LABORATORY	TW	25	10	15	P	
11. NETWORK LABORATORY	OR	50	20	30	P	

FIRST TERM TOTAL = 467/750.
ORDN. 1 MARKS :

T8058607 SANJAY SINGH PREMA , 71053988G , , PICT , T8058607

01. OPERATING SYSTEM	PP	100	40	64	P	
02. THEORY OF COMPUTATION	PP	100	40	56	P	
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	77	P	
04. DATBASE MANAGEMENT SYSTEMS	PP	100	40	52	P	
05. SOFTWARE ENGINEERING	PP	100	40	50	P	
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	21	P	
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	44	P	
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	19	P	
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	41	P	
10. NETWORK LABORATORY	TW	25	10	20	P	
11. NETWORK LABORATORY	OR	50	20	32	P	

FIRST TERM TOTAL = 495/750.
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8058608 SARTHAK MAJITHIA POOJA , 71045598E , , PICT , T8058608

01. OPERATING SYSTEM	PP	100	40	40	P
02. THEORY OF COMPUTATION	PP	100	40	09	F
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	AA	F
04. DATBASE MANAGEMENT SYSTEMS	PP	100	40	AA	F
05. SOFTWARE ENGINEERING	PP	100	40	24	F
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	11	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	22	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	12	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	AA	F
10. NETWORK LABORATORY	TW	25	10	10	P
11. NETWORK LABORATORY	OR	50	20	AA	F

FIRST TERM TOTAL = 146/750.
ORDN. 1 MARKS :

T8058610 SATPUTE ABHINANDAN ASHOK SAVITA , 71134977M , , PICT , T8058610

01. OPERATING SYSTEM	PP	100	40	52	P
02. THEORY OF COMPUTATION	PP	100	40	62	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	80	P
04. DATBASE MANAGEMENT SYSTEMS	PP	100	40	65	P
05. SOFTWARE ENGINEERING	PP	100	40	62	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	23	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	36	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	21	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	37	P
10. NETWORK LABORATORY	TW	25	10	23	P
11. NETWORK LABORATORY	OR	50	20	40	P

FIRST TERM TOTAL = 520/750.
ORDN. 1 MARKS :

T8058611 SATPUTE SWAPNIL BHASKAR SUREKHA , 71045600L , , PICT , T8058611

01. OPERATING SYSTEM	PP	100	40	46	P
02. THEORY OF COMPUTATION	PP	100	40	42	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	50	P
04. DATBASE MANAGEMENT SYSTEMS	PP	100	40	44	P
05. SOFTWARE ENGINEERING	PP	100	40	40	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	15	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	38	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	15	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	30	P
10. NETWORK LABORATORY	TW	25	10	15	P
11. NETWORK LABORATORY	OR	50	20	35	P

FIRST TERM TOTAL = 389/750.
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8058612 SETHI SUPREET SATISHKUMAR PREETI , 71045605M , , PICT , T8058612

01.	OPERATING SYSTEM	PP	100	40	43	P
02.	THEORY OF COMPUTATION	PP	100	40	41	P
03.	COMPUTER NETWORK TECHNOLOGY	PP	100	40	29	F
04.	DATBASE MANAGEMENT SYSTEMS	PP	100	40	30	F
05.	SOFTWARE ENGINEERING	PP	100	40	AA	F
06.	OPERATING SYSTEM DESIGN LAB.	TW	25	10	14	P
07.	OPERATING SYSTEM DESIGN LAB.	PR	50	20	39	P
08.	INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	18	P
09.	INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	39	P
10.	NETWORK LABORATORY	TW	25	10	11	P
11.	NETWORK LABORATORY	OR	50	20	06	F

FIRST TERM TOTAL = 282/750.
ORDN. 1 MARKS :

T8058613 SHAH KUNAL ARVIND PRAMILA , 71045607H , , PICT , T8058613

01.	OPERATING SYSTEM	PP	100	40	28	F
02.	THEORY OF COMPUTATION	PP	100	40	49	P
03.	COMPUTER NETWORK TECHNOLOGY	PP	100	40	59	P
04.	DATBASE MANAGEMENT SYSTEMS	PP	100	40	45	P
05.	SOFTWARE ENGINEERING	PP	100	40	40	P
06.	OPERATING SYSTEM DESIGN LAB.	TW	25	10	21	P
07.	OPERATING SYSTEM DESIGN LAB.	PR	50	20	38	P
08.	INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	20	P
09.	INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	32	P
10.	NETWORK LABORATORY	TW	25	10	21	P
11.	NETWORK LABORATORY	OR	50	20	30	P

FIRST TERM TOTAL = 403/750.
ORDN. 1 MARKS :

T8058614 SHAIKH SHEEBANRAZA ZAHEER NASEEM , 71134978K , , PICT , T8058614

01.	OPERATING SYSTEM	PP	100	40	50	P
02.	THEORY OF COMPUTATION	PP	100	40	56	P
03.	COMPUTER NETWORK TECHNOLOGY	PP	100	40	73	P
04.	DATBASE MANAGEMENT SYSTEMS	PP	100	40	55	P
05.	SOFTWARE ENGINEERING	PP	100	40	56	P
06.	OPERATING SYSTEM DESIGN LAB.	TW	25	10	21	P
07.	OPERATING SYSTEM DESIGN LAB.	PR	50	20	40	P
08.	INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	22	P
09.	INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	32	P
10.	NETWORK LABORATORY	TW	25	10	19	P
11.	NETWORK LABORATORY	OR	50	20	35	P

FIRST TERM TOTAL = 480/750.
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8058615 SHEIKH LINA JAMIL AHMED NISHAT , 71134979H , , PICT , T8058615

01.	OPERATING SYSTEM	PP	100	40	58	P
02.	THEORY OF COMPUTATION	PP	100	40	53	P
03.	COMPUTER NETWORK TECHNOLOGY	PP	100	40	78	P
04.	DATBASE MANAGEMENT SYSTEMS	PP	100	40	72	P
05.	SOFTWARE ENGINEERING	PP	100	40	61	P
06.	OPERATING SYSTEM DESIGN LAB.	TW	25	10	22	P
07.	OPERATING SYSTEM DESIGN LAB.	PR	50	20	41	P
08.	INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	20	P
09.	INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	10	F
10.	NETWORK LABORATORY	TW	25	10	21	P
11.	NETWORK LABORATORY	OR	50	20	30	P

FIRST TERM TOTAL = 485/750.
ORDN. 1 MARKS :

T8058616 SHETTY SHEFALI RAVINDRA VANDANA , 71045612D , , PICT , T8058616

01.	OPERATING SYSTEM	PP	100	40	50	P
02.	THEORY OF COMPUTATION	PP	100	40	51	P
03.	COMPUTER NETWORK TECHNOLOGY	PP	100	40	62	P
04.	DATBASE MANAGEMENT SYSTEMS	PP	100	40	53	P
05.	SOFTWARE ENGINEERING	PP	100	40	42	P
06.	OPERATING SYSTEM DESIGN LAB.	TW	25	10	22	P
07.	OPERATING SYSTEM DESIGN LAB.	PR	50	20	42	P
08.	INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	23	P
09.	INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	28	P
10.	NETWORK LABORATORY	TW	25	10	21	P
11.	NETWORK LABORATORY	OR	50	20	30	P

FIRST TERM TOTAL = 445/750.
ORDN. 1 MARKS :

T8058617 SHEWALE ANULA TUSHAR NEETA , 71045613B , , PICT , T8058617

01.	OPERATING SYSTEM	PP	100	40	40	P
02.	THEORY OF COMPUTATION	PP	100	40	40	P
03.	COMPUTER NETWORK TECHNOLOGY	PP	100	40	49	P
04.	DATBASE MANAGEMENT SYSTEMS	PP	100	40	40	P
05.	SOFTWARE ENGINEERING	PP	100	40	29	F
06.	OPERATING SYSTEM DESIGN LAB.	TW	25	10	13	P
07.	OPERATING SYSTEM DESIGN LAB.	PR	50	20	25	P
08.	INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	14	P
09.	INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	30	P
10.	NETWORK LABORATORY	TW	25	10	16	P
11.	NETWORK LABORATORY	OR	50	20	28	P

FIRST TERM TOTAL = 345/750.
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8058618 SHINDE AKASH ARUN SHOBHA , 71134980M , , PICT , T8058618

01.	OPERATING SYSTEM	PP	100	40	59	P
02.	THEORY OF COMPUTATION	PP	100	40	52	P
03.	COMPUTER NETWORK TECHNOLOGY	PP	100	40	72	P
04.	DATBASE MANAGEMENT SYSTEMS	PP	100	40	50	P
05.	SOFTWARE ENGINEERING	PP	100	40	55	P
06.	OPERATING SYSTEM DESIGN LAB.	TW	25	10	21	P
07.	OPERATING SYSTEM DESIGN LAB.	PR	50	20	38	P
08.	INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	20	P
09.	INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	32	P
10.	NETWORK LABORATORY	TW	25	10	18	P
11.	NETWORK LABORATORY	OR	50	20	24	P

FIRST TERM TOTAL = 458/750.
ORDN. 1 MARKS :

T8058619 SHINGANE ANKUSH DINANATH VEENA , 71134981K , , PICT , T8058619

01.	OPERATING SYSTEM	PP	100	40	62	P
02.	THEORY OF COMPUTATION	PP	100	40	57	P
03.	COMPUTER NETWORK TECHNOLOGY	PP	100	40	66	P
04.	DATBASE MANAGEMENT SYSTEMS	PP	100	40	62	P
05.	SOFTWARE ENGINEERING	PP	100	40	56	P
06.	OPERATING SYSTEM DESIGN LAB.	TW	25	10	17	P
07.	OPERATING SYSTEM DESIGN LAB.	PR	50	20	39	P
08.	INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	17	P
09.	INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	40	P
10.	NETWORK LABORATORY	TW	25	10	17	P
11.	NETWORK LABORATORY	OR	50	20	22	P

FIRST TERM TOTAL = 477/750.
ORDN. 1 MARKS :

T8058620 SHIRUDE NEHA RAJIV SMITA , 71059492F , , PICT , T8058620

01.	OPERATING SYSTEM	PP	100	40	47	P
02.	THEORY OF COMPUTATION	PP	100	40	42	P
03.	COMPUTER NETWORK TECHNOLOGY	PP	100	40	65	P
04.	DATBASE MANAGEMENT SYSTEMS	PP	100	40	53	P
05.	SOFTWARE ENGINEERING	PP	100	40	46	P
06.	OPERATING SYSTEM DESIGN LAB.	TW	25	10	22	P
07.	OPERATING SYSTEM DESIGN LAB.	PR	50	20	40	P
08.	INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	20	P
09.	INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	32	P
10.	NETWORK LABORATORY	TW	25	10	21	P
11.	NETWORK LABORATORY	OR	50	20	30	P

FIRST TERM TOTAL = 438/750.
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8058621 SHIVADE PRATHAMESH SHRIKANT NILIMA , 70925608G , , PICT , T8058621

01. OPERATING SYSTEM	PP	100	40	27	F
02. THEORY OF COMPUTATION	PP	100	40	27	F
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	50	P
04. DATBASE MANAGEMENT SYSTEMS	PP	100	40	40	P
05. SOFTWARE ENGINEERING	PP	100	40	40	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	14	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	40	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	15	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	41	P
10. NETWORK LABORATORY	TW	25	10	14	P
11. NETWORK LABORATORY	OR	50	20	24	P

FIRST TERM TOTAL = 352/750.
ORDN. 1 MARKS :

T8058622 SHRIKANTH JAIKUMAR BHUVNA , 70801622H , , PICT , T8058622

01. OPERATING SYSTEM	PP	100	40	41	P
02. THEORY OF COMPUTATION	PP	100	40	40	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	62	P
04. DATBASE MANAGEMENT SYSTEMS	PP	100	40	47	P
05. SOFTWARE ENGINEERING	PP	100	40	40	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	22	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	45	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	20	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	38	P
10. NETWORK LABORATORY	TW	25	10	22	P
11. NETWORK LABORATORY	OR	50	20	30	P

FIRST TERM TOTAL = 427/750.
ORDN. 1 MARKS :

T8058623 SHUBHI YEDE ANJU , 71045620E , , PICT , T8058623

01. OPERATING SYSTEM	PP	100	40	50	P
02. THEORY OF COMPUTATION	PP	100	40	50	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	61	P
04. DATBASE MANAGEMENT SYSTEMS	PP	100	40	45	P
05. SOFTWARE ENGINEERING	PP	100	40	52	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	18	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	42	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	19	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	31	P
10. NETWORK LABORATORY	TW	25	10	18	P
11. NETWORK LABORATORY	OR	50	20	30	P

FIRST TERM TOTAL = 436/750.
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8058624 SINGH PRABHAKAR KRISHNAKUMAR GAYTRIDEVI , 71134982H , , PICT , T8058624

01.	OPERATING SYSTEM	PP	100	40	52	P
02.	THEORY OF COMPUTATION	PP	100	40	66	P
03.	COMPUTER NETWORK TECHNOLOGY	PP	100	40	73	P
04.	DATBASE MANAGEMENT SYSTEMS	PP	100	40	60	P
05.	SOFTWARE ENGINEERING	PP	100	40	63	P
06.	OPERATING SYSTEM DESIGN LAB.	TW	25	10	23	P
07.	OPERATING SYSTEM DESIGN LAB.	PR	50	20	43	P
08.	INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	23	P
09.	INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	32	P
10.	NETWORK LABORATORY	TW	25	10	23	P
11.	NETWORK LABORATORY	OR	50	20	30	P

FIRST TERM TOTAL = 510/750.
ORDN. 1 MARKS :

T8058625 SOMANI NEHA NANDKISHOR ARATI , 71045628L , , PICT , T8058625

01.	OPERATING SYSTEM	PP	100	40	52	P
02.	THEORY OF COMPUTATION	PP	100	40	48	P
03.	COMPUTER NETWORK TECHNOLOGY	PP	100	40	57	P
04.	DATBASE MANAGEMENT SYSTEMS	PP	100	40	49	P
05.	SOFTWARE ENGINEERING	PP	100	40	40	P
06.	OPERATING SYSTEM DESIGN LAB.	TW	25	10	19	P
07.	OPERATING SYSTEM DESIGN LAB.	PR	50	20	41	P
08.	INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	18	P
09.	INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	35	P
10.	NETWORK LABORATORY	TW	25	10	19	P
11.	NETWORK LABORATORY	OR	50	20	32	P

FIRST TERM TOTAL = 431/750.
ORDN. 1 MARKS :

T8058626 SOMWANSHI HARSHAL SURESH NUTAN , 71045629J , , PICT , T8058626

01.	OPERATING SYSTEM	PP	100	40	48	P
02.	THEORY OF COMPUTATION	PP	100	40	50	P
03.	COMPUTER NETWORK TECHNOLOGY	PP	100	40	56	P
04.	DATBASE MANAGEMENT SYSTEMS	PP	100	40	46	P
05.	SOFTWARE ENGINEERING	PP	100	40	40	P
06.	OPERATING SYSTEM DESIGN LAB.	TW	25	10	19	P
07.	OPERATING SYSTEM DESIGN LAB.	PR	50	20	39	P
08.	INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	20	P
09.	INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	20	P
10.	NETWORK LABORATORY	TW	25	10	19	P
11.	NETWORK LABORATORY	OR	50	20	30	P

FIRST TERM TOTAL = 406/750.
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8058627 SONAWANE JAYANT SHAM RAJESHRI , 71045630B , , PICT , T8058627

01.	OPERATING SYSTEM	PP	100	40	28	F
02.	THEORY OF COMPUTATION	PP	100	40	56	P
03.	COMPUTER NETWORK TECHNOLOGY	PP	100	40	64	P
04.	DATBASE MANAGEMENT SYSTEMS	PP	100	40	60	P
05.	SOFTWARE ENGINEERING	PP	100	40	47	P
06.	OPERATING SYSTEM DESIGN LAB.	TW	25	10	18	P
07.	OPERATING SYSTEM DESIGN LAB.	PR	50	20	37	P
08.	INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	17	P
09.	INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	35	P
10.	NETWORK LABORATORY	TW	25	10	18	P
11.	NETWORK LABORATORY	OR	50	20	25	P

FIRST TERM TOTAL = 424/750.
ORDN. 1 MARKS :

T8058628 SONY PRERNA ASHOK BHAGWATI , 71134983F , , PICT , T8058628

01.	OPERATING SYSTEM	PP	100	40	50	P
02.	THEORY OF COMPUTATION	PP	100	40	41	P
03.	COMPUTER NETWORK TECHNOLOGY	PP	100	40	57	P
04.	DATBASE MANAGEMENT SYSTEMS	PP	100	40	53	P
05.	SOFTWARE ENGINEERING	PP	100	40	40	P
06.	OPERATING SYSTEM DESIGN LAB.	TW	25	10	22	P
07.	OPERATING SYSTEM DESIGN LAB.	PR	50	20	35	P
08.	INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	20	P
09.	INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	38	P
10.	NETWORK LABORATORY	TW	25	10	20	P
11.	NETWORK LABORATORY	OR	50	20	24	P

FIRST TERM TOTAL = 420/750.
ORDN. 1 MARKS :

T8058629 SURADKAR SWAPNIL RAJENDRA SANGITA , 71134984D , , PICT , T8058629

01.	OPERATING SYSTEM	PP	100	40	53	P
02.	THEORY OF COMPUTATION	PP	100	40	30	F
03.	COMPUTER NETWORK TECHNOLOGY	PP	100	40	63	P
04.	DATBASE MANAGEMENT SYSTEMS	PP	100	40	58	P
05.	SOFTWARE ENGINEERING	PP	100	40	46	P
06.	OPERATING SYSTEM DESIGN LAB.	TW	25	10	20	P
07.	OPERATING SYSTEM DESIGN LAB.	PR	50	20	30	P
08.	INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	21	P
09.	INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	31	P
10.	NETWORK LABORATORY	TW	25	10	18	P
11.	NETWORK LABORATORY	OR	50	20	24	P

FIRST TERM TOTAL = 413/750.
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8058630 SURYAWANSHI NIRAJ NITIN NEHA , 70701667D , , PICT , T8058630

01.	OPERATING SYSTEM	PP	100	40	28	F
02.	THEORY OF COMPUTATION	PP	100	40	22	F
03.	COMPUTER NETWORK TECHNOLOGY	PP	100	40	40	P
04.	DATBASE MANAGEMENT SYSTEMS	PP	100	40	44	P
05.	SOFTWARE ENGINEERING	PP	100	40	25	F
06.	OPERATING SYSTEM DESIGN LAB.	TW	25	10	12	P
07.	OPERATING SYSTEM DESIGN LAB.	PR	50	20	AA	F
08.	INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	14	P
09.	INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	20	P
10.	NETWORK LABORATORY	TW	25	10	11	P
11.	NETWORK LABORATORY	OR	50	20	28	P

FIRST TERM TOTAL = 262/750.
ORDN. 1 MARKS :

T8058631 SUYASH PANDEY SHAILA , 70925625G , , PICT , T8058631

01.	OPERATING SYSTEM	PP	100	40	31	F
02.	THEORY OF COMPUTATION	PP	100	40	30	F
03.	COMPUTER NETWORK TECHNOLOGY	PP	100	40	29	F
04.	DATBASE MANAGEMENT SYSTEMS	PP	100	40	AA	F
05.	SOFTWARE ENGINEERING	PP	100	40	40	P
06.	OPERATING SYSTEM DESIGN LAB.	TW	25	10	11	P
07.	OPERATING SYSTEM DESIGN LAB.	PR	50	20	AA	F
08.	INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	14	P
09.	INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	28	P
10.	NETWORK LABORATORY	TW	25	10	10	P
11.	NETWORK LABORATORY	OR	50	20	AA	F

FIRST TERM TOTAL = 208/750.
ORDN. 1 MARKS :

T8058632 TAORI SANIKA SANJAY RUPALI , 71045640K , , PICT , T8058632

01.	OPERATING SYSTEM	PP	100	40	52	P
02.	THEORY OF COMPUTATION	PP	100	40	48	P
03.	COMPUTER NETWORK TECHNOLOGY	PP	100	40	59	P
04.	DATBASE MANAGEMENT SYSTEMS	PP	100	40	65	P
05.	SOFTWARE ENGINEERING	PP	100	40	47	P
06.	OPERATING SYSTEM DESIGN LAB.	TW	25	10	23	P
07.	OPERATING SYSTEM DESIGN LAB.	PR	50	20	39	P
08.	INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	21	P
09.	INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	32	P
10.	NETWORK LABORATORY	TW	25	10	23	P
11.	NETWORK LABORATORY	OR	50	20	34	P

FIRST TERM TOTAL = 465/750.
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8058633 TATHE VISHAL BHAGWAT KALPANA , 71134985B , , PICT , T8058633

01.	OPERATING SYSTEM	PP	100	40	52	P
02.	THEORY OF COMPUTATION	PP	100	40	55	P
03.	COMPUTER NETWORK TECHNOLOGY	PP	100	40	71	P
04.	DATBASE MANAGEMENT SYSTEMS	PP	100	40	61	P
05.	SOFTWARE ENGINEERING	PP	100	40	60	P
06.	OPERATING SYSTEM DESIGN LAB.	TW	25	10	23	P
07.	OPERATING SYSTEM DESIGN LAB.	PR	50	20	41	P
08.	INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	23	P
09.	INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	31	P
10.	NETWORK LABORATORY	TW	25	10	22	P
11.	NETWORK LABORATORY	OR	50	20	30	P

FIRST TERM TOTAL = 490/750.
ORDN. 1 MARKS :

T8058634 THAKUR BHAVANA PRAKASH ARCHANA , 70925633H , , PICT , T8058634

01.	OPERATING SYSTEM	PP	100	40	43	P
02.	THEORY OF COMPUTATION	PP	100	40	52	P
03.	COMPUTER NETWORK TECHNOLOGY	PP	100	40	46	P
04.	DATBASE MANAGEMENT SYSTEMS	PP	100	40	41	P
05.	SOFTWARE ENGINEERING	PP	100	40	41	P
06.	OPERATING SYSTEM DESIGN LAB.	TW	25	10	17	P
07.	OPERATING SYSTEM DESIGN LAB.	PR	50	20	35	P
08.	INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	17	P
09.	INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	12	F
10.	NETWORK LABORATORY	TW	25	10	18	P
11.	NETWORK LABORATORY	OR	50	20	24	P

FIRST TERM TOTAL = 367/750.
ORDN. 1 MARKS :

T8058635 THOMBRE PRIYANKA MILIND JAYMALA , 71045649C , , PICT , T8058635

01.	OPERATING SYSTEM	PP	100	40	16	F
02.	THEORY OF COMPUTATION	PP	100	40	56	P
03.	COMPUTER NETWORK TECHNOLOGY	PP	100	40	27	F
04.	DATBASE MANAGEMENT SYSTEMS	PP	100	40	44	P
05.	SOFTWARE ENGINEERING	PP	100	40	42	P
06.	OPERATING SYSTEM DESIGN LAB.	TW	25	10	17	P
07.	OPERATING SYSTEM DESIGN LAB.	PR	50	20	35	P
08.	INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	15	P
09.	INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	38	P
10.	NETWORK LABORATORY	TW	25	10	18	P
11.	NETWORK LABORATORY	OR	50	20	08	F

FIRST TERM TOTAL = 336/750.
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8058636 TIWARI ROHIT VINOD SHAKUNTALA , 71045652C , , PICT , T8058636

01.	OPERATING SYSTEM	PP	100	40	40	P
02.	THEORY OF COMPUTATION	PP	100	40	20	F
03.	COMPUTER NETWORK TECHNOLOGY	PP	100	40	43	P
04.	DATBASE MANAGEMENT SYSTEMS	PP	100	40	30	F
05.	SOFTWARE ENGINEERING	PP	100	40	29	F
06.	OPERATING SYSTEM DESIGN LAB.	TW	25	10	15	P
07.	OPERATING SYSTEM DESIGN LAB.	PR	50	20	AA	F
08.	INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	17	P
09.	INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	28	P
10.	NETWORK LABORATORY	TW	25	10	14	P
11.	NETWORK LABORATORY	OR	50	20	10	F

FIRST TERM TOTAL = 262/750.
ORDN. 1 MARKS :

T8058637 UKARANDE SNEHAL ANIL JAYASHRI , 71134986L , , PICT , T8058637

01.	OPERATING SYSTEM	PP	100	40	48	P
02.	THEORY OF COMPUTATION	PP	100	40	43	P
03.	COMPUTER NETWORK TECHNOLOGY	PP	100	40	58	P
04.	DATBASE MANAGEMENT SYSTEMS	PP	100	40	61	P
05.	SOFTWARE ENGINEERING	PP	100	40	42	P
06.	OPERATING SYSTEM DESIGN LAB.	TW	25	10	22	P
07.	OPERATING SYSTEM DESIGN LAB.	PR	50	20	30	P
08.	INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	23	P
09.	INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	31	P
10.	NETWORK LABORATORY	TW	25	10	22	P
11.	NETWORK LABORATORY	OR	50	20	31	P

FIRST TERM TOTAL = 432/750.
ORDN. 1 MARKS :

T8058638 UPPOD BALAJI SAMBHAJI SHOBHA , 71045653M , , PICT , T8058638

01.	OPERATING SYSTEM	PP	100	40	40	P
02.	THEORY OF COMPUTATION	PP	100	40	40	P
03.	COMPUTER NETWORK TECHNOLOGY	PP	100	40	54	P
04.	DATBASE MANAGEMENT SYSTEMS	PP	100	40	41	P
05.	SOFTWARE ENGINEERING	PP	100	40	27	F
06.	OPERATING SYSTEM DESIGN LAB.	TW	25	10	15	P
07.	OPERATING SYSTEM DESIGN LAB.	PR	50	20	28	P
08.	INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	17	P
09.	INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	41	P
10.	NETWORK LABORATORY	TW	25	10	15	P
11.	NETWORK LABORATORY	OR	50	20	08	F

FIRST TERM TOTAL = 346/750.
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8058639 VADASADAWALA SHABNAM WAHID BHAI FARIDA , 71045655H , , PICT , T8058639

01. OPERATING SYSTEM	PP	100	40	53	P
02. THEORY OF COMPUTATION	PP	100	40	58	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	69	P
04. DATBASE MANAGEMENT SYSTEMS	PP	100	40	51	P
05. SOFTWARE ENGINEERING	PP	100	40	43	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	21	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	38	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	21	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	28	P
10. NETWORK LABORATORY	TW	25	10	19	P
11. NETWORK LABORATORY	OR	50	20	33	P

FIRST TERM TOTAL = 454/750.
ORDN. 1 MARKS :

T8058640 VARSHA SINHA SEEMA SINHA , 71058776H , , PICT , T8058640

01. OPERATING SYSTEM	PP	100	40	58	P
02. THEORY OF COMPUTATION	PP	100	40	68	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	69	P
04. DATBASE MANAGEMENT SYSTEMS	PP	100	40	67	P
05. SOFTWARE ENGINEERING	PP	100	40	54	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	24	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	40	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	22	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	10	F
10. NETWORK LABORATORY	TW	25	10	23	P
11. NETWORK LABORATORY	OR	50	20	24	P

FIRST TERM TOTAL = 482/750.
ORDN. 1 MARKS :

T8058641 VISHWAS JAIN GEETA , 71045658B , , PICT , T8058641

01. OPERATING SYSTEM	PP	100	40	40	P
02. THEORY OF COMPUTATION	PP	100	40	45	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	57	P
04. DATBASE MANAGEMENT SYSTEMS	PP	100	40	57	P
05. SOFTWARE ENGINEERING	PP	100	40	50	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	23	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	37	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	20	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	37	P
10. NETWORK LABORATORY	TW	25	10	22	P
11. NETWORK LABORATORY	OR	50	20	28	P

FIRST TERM TOTAL = 438/750.
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8058642 VIVEK GIRIDHAR KANNAKE PUSHPA , 71045659L , , PICT , T8058642

01.	OPERATING SYSTEM	PP	100	40	22	F
02.	THEORY OF COMPUTATION	PP	100	40	41	P
03.	COMPUTER NETWORK TECHNOLOGY	PP	100	40	49	P
04.	DATBASE MANAGEMENT SYSTEMS	PP	100	40	55	P
05.	SOFTWARE ENGINEERING	PP	100	40	40	P
06.	OPERATING SYSTEM DESIGN LAB.	TW	25	10	20	P
07.	OPERATING SYSTEM DESIGN LAB.	PR	50	20	27	P
08.	INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	21	P
09.	INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	31	P
10.	NETWORK LABORATORY	TW	25	10	20	P
11.	NETWORK LABORATORY	OR	50	20	24	P

FIRST TERM TOTAL = 370/750.
ORDN. 1 MARKS :

T8058643 VIVEK KUMAR SUMAN , 70925651F , , PICT , T8058643

01.	OPERATING SYSTEM	PP	100	40	46	P
02.	THEORY OF COMPUTATION	PP	100	40	53	P
03.	COMPUTER NETWORK TECHNOLOGY	PP	100	40	59	P
04.	DATBASE MANAGEMENT SYSTEMS	PP	100	40	52	P
05.	SOFTWARE ENGINEERING	PP	100	40	40	P
06.	OPERATING SYSTEM DESIGN LAB.	TW	25	10	21	P
07.	OPERATING SYSTEM DESIGN LAB.	PR	50	20	39	P
08.	INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	18	P
09.	INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	28	P
10.	NETWORK LABORATORY	TW	25	10	20	P
11.	NETWORK LABORATORY	OR	50	20	30	P

FIRST TERM TOTAL = 426/750.
ORDN. 1 MARKS :

T8058644 VIVERT JAIN NIDHI , 71045660D , , PICT , T8058644

01.	OPERATING SYSTEM	PP	100	40	AA	F
02.	THEORY OF COMPUTATION	PP	100	40	45	P
03.	COMPUTER NETWORK TECHNOLOGY	PP	100	40	62	P
04.	DATBASE MANAGEMENT SYSTEMS	PP	100	40	52	P
05.	SOFTWARE ENGINEERING	PP	100	40	57	P
06.	OPERATING SYSTEM DESIGN LAB.	TW	25	10	21	P
07.	OPERATING SYSTEM DESIGN LAB.	PR	50	20	12	F
08.	INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	20	P
09.	INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	28	P
10.	NETWORK LABORATORY	TW	25	10	21	P
11.	NETWORK LABORATORY	OR	50	20	32	P

FIRST TERM TOTAL = 371/750.
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8058646 WAGHMODE SACHIN DADASO SHOBHA , 71045663J , , PICT , T8058646

01.	OPERATING SYSTEM	PP	100	40	40	P
02.	THEORY OF COMPUTATION	PP	100	40	60	P
03.	COMPUTER NETWORK TECHNOLOGY	PP	100	40	58	P
04.	DATBASE MANAGEMENT SYSTEMS	PP	100	40	65	P
05.	SOFTWARE ENGINEERING	PP	100	40	62	P
06.	OPERATING SYSTEM DESIGN LAB.	TW	25	10	19	P
07.	OPERATING SYSTEM DESIGN LAB.	PR	50	20	28	P
08.	INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	21	P
09.	INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	28	P
10.	NETWORK LABORATORY	TW	25	10	20	P
11.	NETWORK LABORATORY	OR	50	20	10	F

FIRST TERM TOTAL = 431/750.
ORDN. 1 MARKS :

T8058647 WAYKOLE VRUSHALI PRAVIN ARUNA , 71045666C , , PICT , T8058647

01.	OPERATING SYSTEM	PP	100	40	42	P
02.	THEORY OF COMPUTATION	PP	100	40	50	P
03.	COMPUTER NETWORK TECHNOLOGY	PP	100	40	47	P
04.	DATBASE MANAGEMENT SYSTEMS	PP	100	40	50	P
05.	SOFTWARE ENGINEERING	PP	100	40	43	P
06.	OPERATING SYSTEM DESIGN LAB.	TW	25	10	23	P
07.	OPERATING SYSTEM DESIGN LAB.	PR	50	20	39	P
08.	INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	21	P
09.	INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	12	F
10.	NETWORK LABORATORY	TW	25	10	22	P
11.	NETWORK LABORATORY	OR	50	20	24	P

FIRST TERM TOTAL = 393/750.
ORDN. 1 MARKS :

T8058648 YADAV SNEHA HANAMANT NANDA , 71134987J , , PICT , T8058648

01.	OPERATING SYSTEM	PP	100	40	55	P
02.	THEORY OF COMPUTATION	PP	100	40	68	P
03.	COMPUTER NETWORK TECHNOLOGY	PP	100	40	70	P
04.	DATBASE MANAGEMENT SYSTEMS	PP	100	40	68	P
05.	SOFTWARE ENGINEERING	PP	100	40	65	P
06.	OPERATING SYSTEM DESIGN LAB.	TW	25	10	22	P
07.	OPERATING SYSTEM DESIGN LAB.	PR	50	20	36	P
08.	INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	23	P
09.	INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	31	P
10.	NETWORK LABORATORY	TW	25	10	22	P
11.	NETWORK LABORATORY	OR	50	20	30	P

FIRST TERM TOTAL = 511/750.
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8058649 ZAJRIYA AASHAY MAHESH CHHAYA , 71045670M , , PICT , T8058649

01. OPERATING SYSTEM	PP	100	40	45	P	
02. THEORY OF COMPUTATION	PP	100	40	50	P	
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	63	P	
04. DATBASE MANAGEMENT SYSTEMS	PP	100	40	58	P	
05. SOFTWARE ENGINEERING	PP	100	40	53	P	
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	23	P	
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	41	P	
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	24	P	
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	20	P	
10. NETWORK LABORATORY	TW	25	10	23	P	
11. NETWORK LABORATORY	OR	50	20	32	P	

FIRST TERM TOTAL = 454/750.
ORDN. 1 MARKS :

T8058650 ABHINAV CHATURVEDI ABHA , 70801315F , T8058501 , PICT , T8058650

01. OPERATING SYSTEM	PP	100	40	40	P	13. SYSTEM SOFTWARE PROGRAMMING	PP	100	40	AA	F
02. THEORY OF COMPUTATION	PP	100	40	41	P	14. MANAGEMENT INFORMATION SYSTEMS	PP	100	40	48	P C
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	41	P	15. PROGRAMMING PARADIGMS	PP	100	40	41	P C
04. DATBASE MANAGEMENT SYSTEMS	PP	100	40	27	F	16. DESIGN & ANALYSIS OF ALGORITHMS	PP	100	40	45	P
05. SOFTWARE ENGINEERING	PP	100	40	40	P C	17. HUMAN COMPU.INTERACTION & USABI.	PP	100	40	47	P C
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	10	P C	18. SOFTWARE DESIGN LABORATORY	TW	50	20	20	P C
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	AA	F	19. SOFTWARE DESIGN LABORATORY	PR	50	20	24	P C
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	10	P C	20. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	20	P C
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	27	P C	21. SOFTWARE DEVELOPMENT TOOLS LAB.	OR	50	20	32	P
10. NETWORK LABORATORY	TW	25	10	10	P C	22. SEMINAR AND TECHNICAL COMMUN.	TW	50	20	20	P C
11. NETWORK LABORATORY	OR	50	20	26	P C						

GRAND TOTAL = 579/1500, RESULT: FAILS A.T.K.T.
ORDN. 1 MARKS :

T8058651 ABNAVE NRUPEN PRAKASH HEMLATA , 70925316J , T8058502 , PICT , T8058651

01. OPERATING SYSTEM	PP	100	40	40	P C	13. SYSTEM SOFTWARE PROGRAMMING	PP	100	40	43	P C
02. THEORY OF COMPUTATION	PP	100	40	40	P C	14. MANAGEMENT INFORMATION SYSTEMS	PP	100	40	41	P C
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	40	P C	15. PROGRAMMING PARADIGMS	PP	100	40	47	P
04. DATBASE MANAGEMENT SYSTEMS	PP	100	40	40	P C	16. DESIGN & ANALYSIS OF ALGORITHMS	PP	100	40	40	P
05. SOFTWARE ENGINEERING	PP	100	40	40	P C	17. HUMAN COMPU.INTERACTION & USABI.	PP	100	40	50	P C
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	10	P C	18. SOFTWARE DESIGN LABORATORY	TW	50	20	20	P C
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	28	P C	19. SOFTWARE DESIGN LABORATORY	PR	50	20	32	P C
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	10	P C	20. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	20	P C
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	32	P C	21. SOFTWARE DEVELOPMENT TOOLS LAB.	OR	50	20	20	P
10. NETWORK LABORATORY	TW	25	10	10	P C	22. SEMINAR AND TECHNICAL COMMUN.	TW	50	20	27	P C
11. NETWORK LABORATORY	OR	50	20	21	P						

GRAND TOTAL = 661/1500, RESULT: PASS CLASS
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8058652 AGARWAL AYUSH ATUL NISHI , 70503791G , T8058504 , PICT , T8058652

01. OPERATING SYSTEM	PP	100	40	44	P C	13. SYSTEM SOFTWARE PROGRAMMING	PP	100	40	58	P C
02. THEORY OF COMPUTATION	PP	100	40	16	F	14. MANAGEMENT INFORMATION SYSTEMS	PP	100	40	45	P C
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	43	P C	15. PROGRAMMING PARADIGMS	PP	100	40	AA	F
04. DATBASE MANAGEMENT SYSTEMS	PP	100	40	44	P C	16. DESIGN & ANALYSIS OF ALGORITHMS	PP	100	40	40	P C
05. SOFTWARE ENGINEERING	PP	100	40	40	P C	17. HUMAN COMPU.INTERACTION & USABI.	PP	100	40	43	P C
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	19	P C	18. SOFTWARE DESIGN LABORATORY	TW	50	20	34	P C
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	42	P C	19. SOFTWARE DESIGN LABORATORY	PR	50	20	40	P C
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	18	P C	20. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	33	P C
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	38	P C	21. SOFTWARE DEVELOPMENT TOOLS LAB.	OR	50	20	30	P C
10. NETWORK LABORATORY	TW	25	10	19	P C	22. SEMINAR AND TECHNICAL COMMUN.	TW	50	20	27	P C
11. NETWORK LABORATORY	OR	50	20	38	P C						

GRAND TOTAL = 731/1500, RESULT: FAILS A.T.K.T.
ORDN. 1 MARKS :

T8058653 AKSHAT GOEL TOOLIKA , 70925323M , T8058507 , PICT , T8058653

01. OPERATING SYSTEM	PP	100	40	55	P C	13. SYSTEM SOFTWARE PROGRAMMING	PP	100	40	75	P C
02. THEORY OF COMPUTATION	PP	100	40	60	P C	14. MANAGEMENT INFORMATION SYSTEMS	PP	100	40	62	P C
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	58	P C	15. PROGRAMMING PARADIGMS	PP	100	40	53	P C
04. DATBASE MANAGEMENT SYSTEMS	PP	100	40	41	P C	16. DESIGN & ANALYSIS OF ALGORITHMS	PP	100	40	50	P C
05. SOFTWARE ENGINEERING	PP	100	40	54	P C	17. HUMAN COMPU.INTERACTION & USABI.	PP	100	40	69	P C
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	10	P C	18. SOFTWARE DESIGN LABORATORY	TW	50	20	20	P C
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	32	P C	19. SOFTWARE DESIGN LABORATORY	PR	50	20	21	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	10	P C	20. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	25	P C
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	38	P C	21. SOFTWARE DEVELOPMENT TOOLS LAB.	OR	50	20	34	P
10. NETWORK LABORATORY	TW	25	10	10	P C	22. SEMINAR AND TECHNICAL COMMUN.	TW	50	20	34	P C
11. NETWORK LABORATORY	OR	50	20	35	P C						

GRAND TOTAL = 856/1500, RESULT: HIGHER SECOND CLASS
ORDN. 1 MARKS :

T8058654 AKSHAY MADHOGARIA ANITA , 70925325H , T8058509 , PICT , T8058654

01. OPERATING SYSTEM	PP	100	40	40	P C	13. SYSTEM SOFTWARE PROGRAMMING	PP	100	40	49	P C
02. THEORY OF COMPUTATION	PP	100	40	55	P C	14. MANAGEMENT INFORMATION SYSTEMS	PP	100	40	50	P C
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	55	P C	15. PROGRAMMING PARADIGMS	PP	100	40	52	P
04. DATBASE MANAGEMENT SYSTEMS	PP	100	40	40	P C	16. DESIGN & ANALYSIS OF ALGORITHMS	PP	100	40	48	P C
05. SOFTWARE ENGINEERING	PP	100	40	40	P C	17. HUMAN COMPU.INTERACTION & USABI.	PP	100	40	59	P C
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	10	P C	18. SOFTWARE DESIGN LABORATORY	TW	50	20	20	P C
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	35	P C	19. SOFTWARE DESIGN LABORATORY	PR	50	20	30	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	10	P C	20. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	23	P C
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	32	P C	21. SOFTWARE DEVELOPMENT TOOLS LAB.	OR	50	20	22	P
10. NETWORK LABORATORY	TW	25	10	10	P C	22. SEMINAR AND TECHNICAL COMMUN.	TW	50	20	25	P C
11. NETWORK LABORATORY	OR	50	20	22	P C						

GRAND TOTAL = 737/1500, RESULT: PASS CLASS
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8058655 ANIKET PALLEWAD NEELAWATI , 70925330D , T8058511 , PICT , T8058655

01. OPERATING SYSTEM	PP	100	40	50	P C	13. SYSTEM SOFTWARE PROGRAMMING	PP	100	40	57	P C
02. THEORY OF COMPUTATION	PP	100	40	44	P C	14. MANAGEMENT INFORMATION SYSTEMS	PP	100	40	56	P C
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	45	P C	15. PROGRAMMING PARADIGMS	PP	100	40	46	P C
04. DATBASE MANAGEMENT SYSTEMS	PP	100	40	41	P C	16. DESIGN & ANALYSIS OF ALGORITHMS	PP	100	40	40	P C
05. SOFTWARE ENGINEERING	PP	100	40	44	P C	17. HUMAN COMPU.INTERACTION & USABI.	PP	100	40	58	P C
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	17	P C	18. SOFTWARE DESIGN LABORATORY	TW	50	20	25	P C
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	25	P C	19. SOFTWARE DESIGN LABORATORY	PR	50	20	38	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	17	P C	20. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	22	P C
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	25	P C	21. SOFTWARE DEVELOPMENT TOOLS LAB.	OR	50	20	08	F
10. NETWORK LABORATORY	TW	25	10	15	P C	22. SEMINAR AND TECHNICAL COMMUN.	TW	50	20	20	P C
11. NETWORK LABORATORY	OR	50	20	23	P C						

GRAND TOTAL = 737/1500, RESULT: FAILS A.T.K.T.
ORDN. 1 MARKS :

T8058656 ANIL KUMAR NAINA DEVI , 70925331B , T8058512 , PICT , T8058656

01. OPERATING SYSTEM	PP	100	40	40	P C	13. SYSTEM SOFTWARE PROGRAMMING	PP	100	40	64	P C
02. THEORY OF COMPUTATION	PP	100	40	43	P C	14. MANAGEMENT INFORMATION SYSTEMS	PP	100	40	51	P C
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	55	P C	15. PROGRAMMING PARADIGMS	PP	100	40	40	P C
04. DATBASE MANAGEMENT SYSTEMS	PP	100	40	40	P C	16. DESIGN & ANALYSIS OF ALGORITHMS	PP	100	40	55	P C
05. SOFTWARE ENGINEERING	PP	100	40	46	P C	17. HUMAN COMPU.INTERACTION & USABI.	PP	100	40	61	P C
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	14	P C	18. SOFTWARE DESIGN LABORATORY	TW	50	20	20	P C
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	36	P C	19. SOFTWARE DESIGN LABORATORY	PR	50	20	20	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	14	P C	20. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	24	P C
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	30	P C	21. SOFTWARE DEVELOPMENT TOOLS LAB.	OR	50	20	28	P
10. NETWORK LABORATORY	TW	25	10	15	P C	22. SEMINAR AND TECHNICAL COMMUN.	TW	50	20	30	P C
11. NETWORK LABORATORY	OR	50	20	28	P C						

GRAND TOTAL = 772/1500, RESULT: SECOND CLASS
ORDN. 1 MARKS :

T8058657 ARJAN BAJAJ BANDANA , 70701364L , T8058516 , PICT , T8058657

01. OPERATING SYSTEM	PP	100	40	40	P C	13. SYSTEM SOFTWARE PROGRAMMING	PP	100	40	66	P C
02. THEORY OF COMPUTATION	PP	100	40	46	P C	14. MANAGEMENT INFORMATION SYSTEMS	PP	100	40	44	P C
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	55	P C	15. PROGRAMMING PARADIGMS	PP	100	40	44	P C
04. DATBASE MANAGEMENT SYSTEMS	PP	100	40	40	P C	16. DESIGN & ANALYSIS OF ALGORITHMS	PP	100	40	44	P C
05. SOFTWARE ENGINEERING	PP	100	40	41	P C	17. HUMAN COMPU.INTERACTION & USABI.	PP	100	40	56	P C
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	10	P C	18. SOFTWARE DESIGN LABORATORY	TW	50	20	34	P C
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	28	P C	19. SOFTWARE DESIGN LABORATORY	PR	50	20	21	P C
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	10	P C	20. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	30	P C
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	30	P C	21. SOFTWARE DEVELOPMENT TOOLS LAB.	OR	50	20	26	P
10. NETWORK LABORATORY	TW	25	10	10	P C	22. SEMINAR AND TECHNICAL COMMUN.	TW	50	20	24	P C
11. NETWORK LABORATORY	OR	50	20	21	P						

GRAND TOTAL = 730/1500, RESULT: PASS CLASS RESULT RESERVED FOR BKLG
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8058658 AVASARE ARUN RAKHAMAJI PADMINI , 70925345B , T8058517 , PICT , T8058658

01. OPERATING SYSTEM	PP	100	40	40	P C	13. SYSTEM SOFTWARE PROGRAMMING	PP	100	40	48	P C
02. THEORY OF COMPUTATION	PP	100	40	40	P C	14. MANAGEMENT INFORMATION SYSTEMS	PP	100	40	AA	F
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	42	P C	15. PROGRAMMING PARADIGMS	PP	100	40	40	P
04. DATBASE MANAGEMENT SYSTEMS	PP	100	40	40	P C	16. DESIGN & ANALYSIS OF ALGORITHMS	PP	100	40	47	P
05. SOFTWARE ENGINEERING	PP	100	40	40	P C	17. HUMAN COMPU.INTERACTION & USABI.	PP	100	40	47	P C
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	14	P C	18. SOFTWARE DESIGN LABORATORY	TW	50	20	20	P C
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	37	P C	19. SOFTWARE DESIGN LABORATORY	PR	50	20	34	P C
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	12	P C	20. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	23	P C
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	40	P C	21. SOFTWARE DEVELOPMENT TOOLS LAB.	OR	50	20	25	P
10. NETWORK LABORATORY	TW	25	10	16	P C	22. SEMINAR AND TECHNICAL COMMUN.	TW	50	20	34	P C
11. NETWORK LABORATORY	OR	50	20	35	P C						

GRAND TOTAL = 692/1500, RESULT: FAILS A.T.K.T.
ORDN. 1 MARKS :

T8058659 BERDE SIDDESH VISHWANATH VAISHALI , 71073822G , T8058523 , PICT , T8058659

01. OPERATING SYSTEM	PP	100	40	45	P C	13. SYSTEM SOFTWARE PROGRAMMING	PP	100	40	55	P C
02. THEORY OF COMPUTATION	PP	100	40	40	P C	14. MANAGEMENT INFORMATION SYSTEMS	PP	100	40	51	P C
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	56	P C	15. PROGRAMMING PARADIGMS	PP	100	40	44	P C
04. DATBASE MANAGEMENT SYSTEMS	PP	100	40	40	P C	16. DESIGN & ANALYSIS OF ALGORITHMS	PP	100	40	AA	F
05. SOFTWARE ENGINEERING	PP	100	40	61	P C	17. HUMAN COMPU.INTERACTION & USABI.	PP	100	40	68	P C
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	12	P C	18. SOFTWARE DESIGN LABORATORY	TW	50	20	20	P C
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	37	P C	19. SOFTWARE DESIGN LABORATORY	PR	50	20	37	P C
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	11	P C	20. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	30	P C
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	32	P C	21. SOFTWARE DEVELOPMENT TOOLS LAB.	OR	50	20	30	P C
10. NETWORK LABORATORY	TW	25	10	12	P C	22. SEMINAR AND TECHNICAL COMMUN.	TW	50	20	35	P C
11. NETWORK LABORATORY	OR	50	20	33	P C						

GRAND TOTAL = 770/1500, RESULT: FAILS A.T.K.T. RESULT RESERVED FOR BKLG
ORDN. 1 MARKS :

T8058660 BHANGALE DEEPAK SATISH SEEMA , 70503833F , T8058525 , PICT , T8058660

01. OPERATING SYSTEM	PP	100	40	46	P C	13. SYSTEM SOFTWARE PROGRAMMING	PP	100	40	58	P C
02. THEORY OF COMPUTATION	PP	100	40	42	P C	14. MANAGEMENT INFORMATION SYSTEMS	PP	100	40	49	P C
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	40	P	15. PROGRAMMING PARADIGMS	PP	100	40	40	P C
04. DATBASE MANAGEMENT SYSTEMS	PP	100	40	43	P	16. DESIGN & ANALYSIS OF ALGORITHMS	PP	100	40	46	P
05. SOFTWARE ENGINEERING	PP	100	40	40	P C	17. HUMAN COMPU.INTERACTION & USABI.	PP	100	40	49	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	20	P C	18. SOFTWARE DESIGN LABORATORY	TW	50	20	41	P C
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	32	P C	19. SOFTWARE DESIGN LABORATORY	PR	50	20	43	P C
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	18	P C	20. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	39	P C
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	40	P C	21. SOFTWARE DEVELOPMENT TOOLS LAB.	OR	50	20	30	P C
10. NETWORK LABORATORY	TW	25	10	18	P C	22. SEMINAR AND TECHNICAL COMMUN.	TW	50	20	30	P C
11. NETWORK LABORATORY	OR	50	20	28	P C						

GRAND TOTAL = 812/1500, RESULT: SECOND CLASS
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8058661 GHODE PRAVIN EKNATH YAMUNABAI , 70801429B , T8058553 , PICT , T8058661

01. OPERATING SYSTEM	PP	100	40	47	P C	13. SYSTEM SOFTWARE PROGRAMMING	PP	100	40	58	P C
02. THEORY OF COMPUTATION	PP	100	40	51	P C	14. MANAGEMENT INFORMATION SYSTEMS	PP	100	40	48	P C
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	58	P C	15. PROGRAMMING PARADIGMS	PP	100	40	40	P C
04. DATBASE MANAGEMENT SYSTEMS	PP	100	40	40	P C	16. DESIGN & ANALYSIS OF ALGORITHMS	PP	100	40	42	P C
05. SOFTWARE ENGINEERING	PP	100	40	45	P C	17. HUMAN COMPU.INTERACTION & USABI.	PP	100	40	59	P C
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	12	P C	18. SOFTWARE DESIGN LABORATORY	TW	50	20	20	P C
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	12	F	19. SOFTWARE DESIGN LABORATORY	PR	50	20	20	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	12	P C	20. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	20	P C
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	29	P C	21. SOFTWARE DEVELOPMENT TOOLS LAB.	OR	50	20	20	P
10. NETWORK LABORATORY	TW	25	10	10	P C	22. SEMINAR AND TECHNICAL COMMUN.	TW	50	20	34	P C
11. NETWORK LABORATORY	OR	50	20	35	P C						

GRAND TOTAL = 731/1500, RESULT: FAILS A.T.K.T.
ORDN. 1 MARKS :

T8058662 JADHAV DEEPANJAN MADHAV ARUNA , 70925445J , T8058557 , PICT , T8058662

01. OPERATING SYSTEM	PP	100	40	45	P C	13. SYSTEM SOFTWARE PROGRAMMING	PP	100	40	59	P C
02. THEORY OF COMPUTATION	PP	100	40	52	P C	14. MANAGEMENT INFORMATION SYSTEMS	PP	100	40	47	P C
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	72	P C	15. PROGRAMMING PARADIGMS	PP	100	40	40	P C
04. DATBASE MANAGEMENT SYSTEMS	PP	100	40	53	P	16. DESIGN & ANALYSIS OF ALGORITHMS	PP	100	40	43	P C
05. SOFTWARE ENGINEERING	PP	100	40	47	P C	17. HUMAN COMPU.INTERACTION & USABI.	PP	100	40	55	P C
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	13	P C	18. SOFTWARE DESIGN LABORATORY	TW	50	20	24	P C
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	AA	F	19. SOFTWARE DESIGN LABORATORY	PR	50	20	AA	F
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	12	P C	20. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	20	P C
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	22	P C	21. SOFTWARE DEVELOPMENT TOOLS LAB.	OR	50	20	AA	F
10. NETWORK LABORATORY	TW	25	10	16	P C	22. SEMINAR AND TECHNICAL COMMUN.	TW	50	20	34	P C
11. NETWORK LABORATORY	OR	50	20	42	P C						

GRAND TOTAL = 712/1500, RESULT: FAILS A.T.K.T. RESULT RESERVED FOR BKLG
ORDN. 1 MARKS :

T8058663 KAKADE SHASHIKANT AVINASH NANDA , 70925460B , T8058565 , PICT , T8058663

01. OPERATING SYSTEM	PP	100	40	48	P C	13. SYSTEM SOFTWARE PROGRAMMING	PP	100	40	50	P C
02. THEORY OF COMPUTATION	PP	100	40	51	P C	14. MANAGEMENT INFORMATION SYSTEMS	PP	100	40	45	P C
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	47	P C	15. PROGRAMMING PARADIGMS	PP	100	40	45	P
04. DATBASE MANAGEMENT SYSTEMS	PP	100	40	40	P C	16. DESIGN & ANALYSIS OF ALGORITHMS	PP	100	40	41	P
05. SOFTWARE ENGINEERING	PP	100	40	40	P C	17. HUMAN COMPU.INTERACTION & USABI.	PP	100	40	49	P C
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	14	P C	18. SOFTWARE DESIGN LABORATORY	TW	50	20	22	P C
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	35	P C	19. SOFTWARE DESIGN LABORATORY	PR	50	20	38	P C
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	12	P C	20. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	24	P C
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	34	P C	21. SOFTWARE DEVELOPMENT TOOLS LAB.	OR	50	20	35	P C
10. NETWORK LABORATORY	TW	25	10	12	P C	22. SEMINAR AND TECHNICAL COMMUN.	TW	50	20	26	P C
11. NETWORK LABORATORY	OR	50	20	20	P						

GRAND TOTAL = 740+10/1500, RESULT: SECOND CLASS [O.2]
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8058664 MADGULWAR PRASHANT ASHOKRAO VIJAYALAKSHMI , 70503949J , T8058576 , PICT , T8058664

01. OPERATING SYSTEM	PP	100	40	40	P C	13. SYSTEM SOFTWARE PROGRAMMING	PP	100	40	43	P C
02. THEORY OF COMPUTATION	PP	100	40	40	P C	14. MANAGEMENT INFORMATION SYSTEMS	PP	100	40	40	P C
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	62	P C	15. PROGRAMMING PARADIGMS	PP	100	40	40	P C
04. DATBASE MANAGEMENT SYSTEMS	PP	100	40	40	P C	16. DESIGN & ANALYSIS OF ALGORITHMS	PP	100	40	40	P C
05. SOFTWARE ENGINEERING	PP	100	40	41	P C	17. HUMAN COMPU.INTERACTION & USABI.	PP	100	40	61	P C
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	12	P C	18. SOFTWARE DESIGN LABORATORY	TW	50	20	25	P C
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	22	P C	19. SOFTWARE DESIGN LABORATORY	PR	50	20	15*	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	12	P C	20. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	20	P C
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	29	P C	21. SOFTWARE DEVELOPMENT TOOLS LAB.	OR	50	20	22	P
10. NETWORK LABORATORY	TW	25	10	10	P C	22. SEMINAR AND TECHNICAL COMMUN.	TW	50	20	20	P C
11. NETWORK LABORATORY	OR	50	20	22	P						

GRAND TOTAL = 666/1500, RESULT: PASS CLASS * [0.4]
ORDN. 1 MARKS :

T8058665 MAGAR ASHWINI DEELIP SHALINI , 71073835J , T8058577 , PICT , T8058665

01. OPERATING SYSTEM	PP	100	40	45	P C	13. SYSTEM SOFTWARE PROGRAMMING	PP	100	40	62	P C
02. THEORY OF COMPUTATION	PP	100	40	48	P C	14. MANAGEMENT INFORMATION SYSTEMS	PP	100	40	59	P C
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	68	P C	15. PROGRAMMING PARADIGMS	PP	100	40	61	P C
04. DATBASE MANAGEMENT SYSTEMS	PP	100	40	40	P C	16. DESIGN & ANALYSIS OF ALGORITHMS	PP	100	40	44	P C
05. SOFTWARE ENGINEERING	PP	100	40	42	P C	17. HUMAN COMPU.INTERACTION & USABI.	PP	100	40	61	P C
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	19	P C	18. SOFTWARE DESIGN LABORATORY	TW	50	20	38	P C
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	AA	F	19. SOFTWARE DESIGN LABORATORY	PR	50	20	38	P C
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	18	P C	20. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	43	P C
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	32	P C	21. SOFTWARE DEVELOPMENT TOOLS LAB.	OR	50	20	35	P C
10. NETWORK LABORATORY	TW	25	10	20	P C	22. SEMINAR AND TECHNICAL COMMUN.	TW	50	20	38	P C
11. NETWORK LABORATORY	OR	50	20	40	P C						

GRAND TOTAL = 871/1500, RESULT: FAILS A.T.K.T.
ORDN. 1 MARKS :

T8058666 MANDORA BHAGYASHREE SATISH SAPANA , 70925498K , T8058578 , PICT , T8058666

01. OPERATING SYSTEM	PP	100	40	47	P C	13. SYSTEM SOFTWARE PROGRAMMING	PP	100	40	59	P C
02. THEORY OF COMPUTATION	PP	100	40	58	P C	14. MANAGEMENT INFORMATION SYSTEMS	PP	100	40	40	P C
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	57	P C	15. PROGRAMMING PARADIGMS	PP	100	40	44	P C
04. DATBASE MANAGEMENT SYSTEMS	PP	100	40	51	P C	16. DESIGN & ANALYSIS OF ALGORITHMS	PP	100	40	41	P C
05. SOFTWARE ENGINEERING	PP	100	40	56	P C	17. HUMAN COMPU.INTERACTION & USABI.	PP	100	40	57	P C
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	16	P C	18. SOFTWARE DESIGN LABORATORY	TW	50	20	26	P C
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	32	P C	19. SOFTWARE DESIGN LABORATORY	PR	50	20	30	P C
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	14	P C	20. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	20	P C
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	36	P C	21. SOFTWARE DEVELOPMENT TOOLS LAB.	OR	50	20	35	P
10. NETWORK LABORATORY	TW	25	10	16	P C	22. SEMINAR AND TECHNICAL COMMUN.	TW	50	20	32	P C
11. NETWORK LABORATORY	OR	50	20	36	P C						

GRAND TOTAL = 820+05/1500, RESULT: HIGHER SECOND CLASS [0.2]
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8058667 MAYANK BHATNAGAR SHEETAL , 70925502M , T8058580 , PICT , T8058667

01. OPERATING SYSTEM	PP	100	40	46	P C	13. SYSTEM SOFTWARE PROGRAMMING	PP	100	40	53	P C
02. THEORY OF COMPUTATION	PP	100	40	45	P C	14. MANAGEMENT INFORMATION SYSTEMS	PP	100	40	41	P C
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	62	P C	15. PROGRAMMING PARADIGMS	PP	100	40	50	P C
04. DATBASE MANAGEMENT SYSTEMS	PP	100	40	43	P C	16. DESIGN & ANALYSIS OF ALGORITHMS	PP	100	40	54	P
05. SOFTWARE ENGINEERING	PP	100	40	43	P C	17. HUMAN COMPU.INTERACTION & USABI.	PP	100	40	56	P C
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	18	P C	18. SOFTWARE DESIGN LABORATORY	TW	50	20	35	P C
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	38	P C	19. SOFTWARE DESIGN LABORATORY	PR	50	20	36	P C
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	17	P C	20. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	39	P C
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	42	P C	21. SOFTWARE DEVELOPMENT TOOLS LAB.	OR	50	20	32	P
10. NETWORK LABORATORY	TW	25	10	19	P C	22. SEMINAR AND TECHNICAL COMMUN.	TW	50	20	37	P C
11. NETWORK LABORATORY	OR	50	20	42	P C						

GRAND TOTAL = 867/1500, RESULT: HIGHER SECOND CLASS
ORDN. 1 MARKS :

T8058668 MOHD ZEESHAN MOHD ROSHAN SHAIKH SHAMIM PARVIN , 70925506D , T8058581 , PICT , T8058668

01. OPERATING SYSTEM	PP	100	40	48	P C	13. SYSTEM SOFTWARE PROGRAMMING	PP	100	40	65	P C
02. THEORY OF COMPUTATION	PP	100	40	44	P C	14. MANAGEMENT INFORMATION SYSTEMS	PP	100	40	48	P C
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	61	P C	15. PROGRAMMING PARADIGMS	PP	100	40	59	P C
04. DATBASE MANAGEMENT SYSTEMS	PP	100	40	40	P C	16. DESIGN & ANALYSIS OF ALGORITHMS	PP	100	40	49	P C
05. SOFTWARE ENGINEERING	PP	100	40	40	P C	17. HUMAN COMPU.INTERACTION & USABI.	PP	100	40	63	P C
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	17	P C	18. SOFTWARE DESIGN LABORATORY	TW	50	20	38	P C
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	32	P C	19. SOFTWARE DESIGN LABORATORY	PR	50	20	40	P C
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	15	P C	20. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	39	P C
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	29	P C	21. SOFTWARE DEVELOPMENT TOOLS LAB.	OR	50	20	28	P
10. NETWORK LABORATORY	TW	25	10	16	P C	22. SEMINAR AND TECHNICAL COMMUN.	TW	50	20	37	P C
11. NETWORK LABORATORY	OR	50	20	32	P C						

GRAND TOTAL = 859/1500, RESULT: HIGHER SECOND CLASS
ORDN. 1 MARKS :

T8058669 MONISH PATEL NEETA , 70925508L , T8058582 , PICT , T8058669

01. OPERATING SYSTEM	PP	100	40	40	P C	13. SYSTEM SOFTWARE PROGRAMMING	PP	100	40	45	P C
02. THEORY OF COMPUTATION	PP	100	40	40	P C	14. MANAGEMENT INFORMATION SYSTEMS	PP	100	40	50	P C
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	42	P C	15. PROGRAMMING PARADIGMS	PP	100	40	AA	F
04. DATBASE MANAGEMENT SYSTEMS	PP	100	40	42	P C	16. DESIGN & ANALYSIS OF ALGORITHMS	PP	100	40	AA	F
05. SOFTWARE ENGINEERING	PP	100	40	48	P C	17. HUMAN COMPU.INTERACTION & USABI.	PP	100	40	53	P C
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	10	P C	18. SOFTWARE DESIGN LABORATORY	TW	50	20	20	P C
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	20	P C	19. SOFTWARE DESIGN LABORATORY	PR	50	20	25	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	10	P C	20. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	20	P C
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	21	P C	21. SOFTWARE DEVELOPMENT TOOLS LAB.	OR	50	20	07	F
10. NETWORK LABORATORY	TW	25	10	10	P C	22. SEMINAR AND TECHNICAL COMMUN.	TW	50	20	20	P C
11. NETWORK LABORATORY	OR	50	20	24	P C							

GRAND TOTAL = 557/1500, RESULT: FAILS A.T.K.T. RESULT RESERVED FOR BKLG
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8058670 PALAK AGRAWAL PRITI , 70925531E , T8058591 , PICT , T8058670

01. OPERATING SYSTEM	PP	100	40	47	P	C	13. SYSTEM SOFTWARE PROGRAMMING	PP	100	40	55	P C
02. THEORY OF COMPUTATION	PP	100	40	40	P	C	14. MANAGEMENT INFORMATION SYSTEMS	PP	100	40	53	P C
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	66	P	C	15. PROGRAMMING PARADIGMS	PP	100	40	51	P C
04. DATBASE MANAGEMENT SYSTEMS	PP	100	40	40	P	C	16. DESIGN & ANALYSIS OF ALGORITHMS	PP	100	40	40	P C
05. SOFTWARE ENGINEERING	PP	100	40	59	P	C	17. HUMAN COMPU.INTERACTION & USABI.	PP	100	40	72	P C
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	10	P	C	18. SOFTWARE DESIGN LABORATORY	TW	50	20	20	P C
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	28	P	C	19. SOFTWARE DESIGN LABORATORY	PR	50	20	15*	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	10	P	C	20. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	30	P C
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	20	P	C	21. SOFTWARE DEVELOPMENT TOOLS LAB.	OR	50	20	20	P
10. NETWORK LABORATORY	TW	25	10	10	P	C	22. SEMINAR AND TECHNICAL COMMUN.	TW	50	20	30	P C
11. NETWORK LABORATORY	OR	50	20	23	P	C						

GRAND TOTAL = 754/1500, RESULT: SECOND CLASS * [0.4]
ORDN. 1 MARKS :

T8058672 PATHAK ANIKET SANJAY ASHA , 70925542L , T8058599 , PICT , T8058672

01. OPERATING SYSTEM	PP	100	40	40	P	C	13. SYSTEM SOFTWARE PROGRAMMING	PP	100	40	56	P C
02. THEORY OF COMPUTATION	PP	100	40	AA	F		14. MANAGEMENT INFORMATION SYSTEMS	PP	100	40	49	P C
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	57	P	C	15. PROGRAMMING PARADIGMS	PP	100	40	40	P C
04. DATBASE MANAGEMENT SYSTEMS	PP	100	40	40	P	C	16. DESIGN & ANALYSIS OF ALGORITHMS	PP	100	40	42	P C
05. SOFTWARE ENGINEERING	PP	100	40	47	P	C	17. HUMAN COMPU.INTERACTION & USABI.	PP	100	40	51	P C
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	12	P	C	18. SOFTWARE DESIGN LABORATORY	TW	50	20	26	P C
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	20	P	C	19. SOFTWARE DESIGN LABORATORY	PR	50	20	AA	F
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	12	P	C	20. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	24	P C
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	33	P	C	21. SOFTWARE DEVELOPMENT TOOLS LAB.	OR	50	20	36	P C
10. NETWORK LABORATORY	TW	25	10	12	P	C	22. SEMINAR AND TECHNICAL COMMUN.	TW	50	20	32	P C
11. NETWORK LABORATORY	OR	50	20	30	P	C						

GRAND TOTAL = 677/1500, RESULT: FAILS A.T.K.T.
ORDN. 1 MARKS :

T8058674 PRIYANKA PANDE RASIKA , 70925570F , T8058607 , PICT , T8058674

01. OPERATING SYSTEM	PP	100	40	50	P	C	13. SYSTEM SOFTWARE PROGRAMMING	PP	100	40	69	P C
02. THEORY OF COMPUTATION	PP	100	40	59	P	C	14. MANAGEMENT INFORMATION SYSTEMS	PP	100	40	50	P C
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	66	P	C	15. PROGRAMMING PARADIGMS	PP	100	40	45	P C
04. DATBASE MANAGEMENT SYSTEMS	PP	100	40	44	P	C	16. DESIGN & ANALYSIS OF ALGORITHMS	PP	100	40	43	P C
05. SOFTWARE ENGINEERING	PP	100	40	57	P	C	17. HUMAN COMPU.INTERACTION & USABI.	PP	100	40	70	P C
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	14	P	C	18. SOFTWARE DESIGN LABORATORY	TW	50	20	33	P C
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	33	P	C	19. SOFTWARE DESIGN LABORATORY	PR	50	20	42	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	15	P	C	20. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	36	P C
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	44	P	C	21. SOFTWARE DEVELOPMENT TOOLS LAB.	OR	50	20	37	P C
10. NETWORK LABORATORY	TW	25	10	16	P	C	22. SEMINAR AND TECHNICAL COMMUN.	TW	50	20	37	P C
11. NETWORK LABORATORY	OR	50	20	25	P	C						

GRAND TOTAL = 901/1500, RESULT: FIRST CLASS
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8058675 RAHUL RANJAN KUMKUM , 70701594E , T8058610 , PICT , T8058675

01. OPERATING SYSTEM	PP	100	40	28	F	13. SYSTEM SOFTWARE PROGRAMMING	PP	100	40	40	P C
02. THEORY OF COMPUTATION	PP	100	40	40	P C	14. MANAGEMENT INFORMATION SYSTEMS	PP	100	40	AA	F
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	42	P C	15. PROGRAMMING PARADIGMS	PP	100	40	40	P C
04. DATBASE MANAGEMENT SYSTEMS	PP	100	40	41	P C	16. DESIGN & ANALYSIS OF ALGORITHMS	PP	100	40	AA	F
05. SOFTWARE ENGINEERING	PP	100	40	31	F	17. HUMAN COMPU.INTERACTION & USABI.	PP	100	40	44	P C
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	10	P C	18. SOFTWARE DESIGN LABORATORY	TW	50	20	20	P C
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	AA	F	19. SOFTWARE DESIGN LABORATORY	PR	50	20	22	P C
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	10	P C	20. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	20	P C
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	23	P C	21. SOFTWARE DEVELOPMENT TOOLS LAB.	OR	50	20	AA	F
10. NETWORK LABORATORY	TW	25	10	10	P C	22. SEMINAR AND TECHNICAL COMMUN.	TW	50	20	20	P C
11. NETWORK LABORATORY	OR	50	20	20	P						

GRAND TOTAL = 471/1500, RESULT: FAILS
ORDN. 1 MARKS :

T8058676 SHIRA ANJALI BHAGWANSING MALTABAI , 70801618K , T8058620 , PICT , T8058676

01. OPERATING SYSTEM	PP	100	40	42	P C	13. SYSTEM SOFTWARE PROGRAMMING	PP	100	40	45	P C
02. THEORY OF COMPUTATION	PP	100	40	41	P C	14. MANAGEMENT INFORMATION SYSTEMS	PP	100	40	AA	F
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	51	P C	15. PROGRAMMING PARADIGMS	PP	100	40	41	P C
04. DATBASE MANAGEMENT SYSTEMS	PP	100	40	40	P C	16. DESIGN & ANALYSIS OF ALGORITHMS	PP	100	40	40	P C
05. SOFTWARE ENGINEERING	PP	100	40	40	P C	17. HUMAN COMPU.INTERACTION & USABI.	PP	100	40	47	P C
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	18	P C	18. SOFTWARE DESIGN LABORATORY	TW	50	20	31	P C
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	15	F	19. SOFTWARE DESIGN LABORATORY	PR	50	20	07	F
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	15	P C	20. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	33	P C
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	25	P C	21. SOFTWARE DEVELOPMENT TOOLS LAB.	OR	50	20	08	F
10. NETWORK LABORATORY	TW	25	10	17	P C	22. SEMINAR AND TECHNICAL COMMUN.	TW	50	20	38	P C
11. NETWORK LABORATORY	OR	50	20	34	P C						

GRAND TOTAL = 649/1500, RESULT: FAILS A.T.K.T.
ORDN. 1 MARKS :

T8058677 TANAWADE AMOL SHAMRAO MANDA , 70801648M , T8058629 , PICT , T8058677

01. OPERATING SYSTEM	PP	100	40	40	P C	13. SYSTEM SOFTWARE PROGRAMMING	PP	100	40	59	P C
02. THEORY OF COMPUTATION	PP	100	40	40	P C	14. MANAGEMENT INFORMATION SYSTEMS	PP	100	40	45	P C
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	43	P C	15. PROGRAMMING PARADIGMS	PP	100	40	41	P C
04. DATBASE MANAGEMENT SYSTEMS	PP	100	40	40	P C	16. DESIGN & ANALYSIS OF ALGORITHMS	PP	100	40	40	P C
05. SOFTWARE ENGINEERING	PP	100	40	40	P C	17. HUMAN COMPU.INTERACTION & USABI.	PP	100	40	57	P C
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	12	P C	18. SOFTWARE DESIGN LABORATORY	TW	50	20	39	P C
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	05	F	19. SOFTWARE DESIGN LABORATORY	PR	50	20	30	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	12	P C	20. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	36	P C
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	27	P C	21. SOFTWARE DEVELOPMENT TOOLS LAB.	OR	50	20	36	P C
10. NETWORK LABORATORY	TW	25	10	10	P C	22. SEMINAR AND TECHNICAL COMMUN.	TW	50	20	41	P C
11. NETWORK LABORATORY	OR	50	20	33	P C						

GRAND TOTAL = 740/1500, RESULT: FAILS A.T.K.T.
ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8058678 TATHE SANDEEP HARIDAS REKHA , 70701670D , T8058630 , PICT , T8058678

01. OPERATING SYSTEM	PP	100	40	49	P C	13. SYSTEM SOFTWARE PROGRAMMING	PP	100	40	40	P C
02. THEORY OF COMPUTATION	PP	100	40	40	P C	14. MANAGEMENT INFORMATION SYSTEMS	PP	100	40	57	P C
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	44	P C	15. PROGRAMMING PARADIGMS	PP	100	40	50	P C
04. DATBASE MANAGEMENT SYSTEMS	PP	100	40	40	P C	16. DESIGN & ANALYSIS OF ALGORITHMS	PP	100	40	40	P C
05. SOFTWARE ENGINEERING	PP	100	40	57	P C	17. HUMAN COMPU.INTERACTION & USABI.	PP	100	40	40	P C
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	10	P C	18. SOFTWARE DESIGN LABORATORY	TW	50	20	20	P C
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	AA	F	19. SOFTWARE DESIGN LABORATORY	PR	50	20	33	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	10	P C	20. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	20	P C
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	28	P C	21. SOFTWARE DEVELOPMENT TOOLS LAB.	OR	50	20	25	P
10. NETWORK LABORATORY	TW	25	10	10	P C	22. SEMINAR AND TECHNICAL COMMUN.	TW	50	20	22	P C
11. NETWORK LABORATORY	OR	50	20	29	P C						

GRAND TOTAL = 674/1500, RESULT: FAILS A.T.K.T. RESULT RESERVED FOR BKLG
ORDN. 1 MARKS :

T8058679 VED PRAKASH SINGH RAJ LAXMI , 70801665M , T8058635 , PICT , T8058679

01. OPERATING SYSTEM	PP	100	40	AA	F	13. SYSTEM SOFTWARE PROGRAMMING	PP	100	40	40	P C
02. THEORY OF COMPUTATION	PP	100	40	52	P C	14. MANAGEMENT INFORMATION SYSTEMS	PP	100	40	46	P C
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	46	P C	15. PROGRAMMING PARADIGMS	PP	100	40	AA	F
04. DATBASE MANAGEMENT SYSTEMS	PP	100	40	AA	F	16. DESIGN & ANALYSIS OF ALGORITHMS	PP	100	40	AA	F
05. SOFTWARE ENGINEERING	PP	100	40	40	P C	17. HUMAN COMPU.INTERACTION & USABI.	PP	100	40	44	P C
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	10	P C	18. SOFTWARE DESIGN LABORATORY	TW	50	20	21	P C
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	20	P C	19. SOFTWARE DESIGN LABORATORY	PR	50	20	AA	F
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	10	P C	20. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	20	P C
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	21	P C	21. SOFTWARE DEVELOPMENT TOOLS LAB.	OR	50	20	AA	F
10. NETWORK LABORATORY	TW	25	10	10	P C	22. SEMINAR AND TECHNICAL COMMUN.	TW	50	20	22	P C
11. NETWORK LABORATORY	OR	50	20	26	P C						

GRAND TOTAL = 438/1500, RESULT: FAILS
ORDN. 1 MARKS :

T8058680 WAKHARE PRACHI PRAKASH NIRMALA , 70925658C , T8058644 , PICT , T8058680

01. OPERATING SYSTEM	PP	100	40	50	P C	13. SYSTEM SOFTWARE PROGRAMMING	PP	100	40	75	P C
02. THEORY OF COMPUTATION	PP	100	40	51	P C	14. MANAGEMENT INFORMATION SYSTEMS	PP	100	40	58	P C
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	78	P C	15. PROGRAMMING PARADIGMS	PP	100	40	72	P C
04. DATBASE MANAGEMENT SYSTEMS	PP	100	40	41	P C	16. DESIGN & ANALYSIS OF ALGORITHMS	PP	100	40	54	P C
05. SOFTWARE ENGINEERING	PP	100	40	71	P C	17. HUMAN COMPU.INTERACTION & USABI.	PP	100	40	63	P C
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	21	P C	18. SOFTWARE DESIGN LABORATORY	TW	50	20	34	P C
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	35	P C	19. SOFTWARE DESIGN LABORATORY	PR	50	20	30	P C
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	19	P C	20. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	39	P C
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	39	P C	21. SOFTWARE DEVELOPMENT TOOLS LAB.	OR	50	20	35	P
10. NETWORK LABORATORY	TW	25	10	22	P C	22. SEMINAR AND TECHNICAL COMMUN.	TW	50	20	44	P C
11. NETWORK LABORATORY	OR	50	20	38	P C							

GRAND TOTAL = 991/1500, RESULT: FIRST CLASS WITH DISTINCTION
ORDN. 1 MARKS :