

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80053001 ABHISHEK ANAND SINDHU , 71200748C , , PICT , T80053001

01. CONTROL SYSTEMS	PP	100	40	60	P
02. DIGITAL COMMUNICATION	PP	100	40	45	P
03. DIGITAL COMMUNICATION	PR	50	20	28	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	48	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	32	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	43	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	28	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	49	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	38	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	26	P

FIRST TERM TOTAL = 397/750.

ORDN. 1 MARKS :

T80053002 AGRAWAL MUKUL RAVINDRA ASHA , 71200751C , , PICT , T80053002

01. CONTROL SYSTEMS	PP	100	40	79	P
02. DIGITAL COMMUNICATION	PP	100	40	65	P
03. DIGITAL COMMUNICATION	PR	50	20	36	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	77	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	40	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	68	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	32	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	70	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	39	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	40	P

FIRST TERM TOTAL = 546/750.

ORDN. 1 MARKS :

T80053003 AGRAWAL PRANAT ADITYA ASHA , 71100721H , T80053007 , PICT , T80053003

01. CONTROL SYSTEMS	PP	100	40	47	P C	11. SIGNAL CODING & ESTIMATION THEORY	PP	100	40	45	P C
02. DIGITAL COMMUNICATION	PP	100	40	40	P C	12. SIGNAL CODING & ESTIMATION THEORY	PR	50	20	32	P C
03. DIGITAL COMMUNICATION	PR	50	20	20	P C	13. SYSTEM PROGRA.& OPERATING SYS.	PP	100	40	54	P C
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	47	P C	14. SYSTEM PROGRA.& OPERATING SYS.	TW	50	20	30	P C
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	36	P C	15. COMPUTER ORGANIZATION & ARCHITEC	PP	100	40	46	P C
06. MICROCONTROLLERS & APPLICATION	PP	100	40	47	P C	16. INDUSTRIAL MANAGEMENT	PP	100	40	58	P C
07. MICROCONTROLLERS & APPLICATION	PR	50	20	21	P C	17. WAVE THEORY & ANTENNA	PP	100	40	58	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	42	P C	18. WAVE THEORY & ANTENNA	PR	50	20	23	P C
09. DIGITAL SIGNAL PROCESSING	OR	50	20	30	P C	19. MINI PROJECT & SEMINAR	OR	50	20	35	P C
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	24	P C	20. TEST & MEASUREMENT TECHNIQUES	OR	50	20	32	P C

GRAND TOTAL = 767/1500, RESULT: SECOND CLASS

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.  
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80053004 AHIRE BHUSHAN SURESH LAXMIBAI , 71344345G , PICT , T80053004

01. CONTROL SYSTEMS	PP	100	40	60	P
02. DIGITAL COMMUNICATION	PP	100	40	43	P
03. DIGITAL COMMUNICATION	PR	50	20	35	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	63	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	36	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	56	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	31	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	32	F
09. DIGITAL SIGNAL PROCESSING	OR	50	20	37	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	35	P

FIRST TERM TOTAL = 428/750.

ORDN. 1 MARKS :

T80053005 ALGUR GAURAV ISHWAR VASANTI , 71200757B , PICT , T80053005

01. CONTROL SYSTEMS	PP	100	40	52	P
02. DIGITAL COMMUNICATION	PP	100	40	AA	F
03. DIGITAL COMMUNICATION	PR	50	20	06	F
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	AA	F
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	28	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	15	F
07. MICROCONTROLLERS & APPLICATION	PR	50	20	28	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	27	F
09. DIGITAL SIGNAL PROCESSING	OR	50	20	36	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	22	P

FIRST TERM TOTAL = 214/750.

ORDN. 1 MARKS :

T80053006 ALI ASGER MODI ZEENAT , 71100724B , T80053009 , PICT , T80053006

01. CONTROL SYSTEMS	PP	100	40	40	P C	11. SIGNAL CODING & ESTIMATION THEORY	PP	100	40	40	P
02. DIGITAL COMMUNICATION	PP	100	40	40	P C	12. SIGNAL CODING & ESTIMATION THEORY	PR	50	20	35	P C
03. DIGITAL COMMUNICATION	PR	50	20	30	P C	13. SYSTEM PROGRA.& OPERATING SYS.	PP	100	40	52	P C
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	48	P C	14. SYSTEM PROGRA.& OPERATING SYS.	TW	50	20	35	P C
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	34	P C	15. COMPUTER ORGANIZATION & ARCHITEC	PP	100	40	44	P C
06. MICROCONTROLLERS & APPLICATION	PP	100	40	56	P C	16. INDUSTRIAL MANAGEMENT	PP	100	40	63	P C
07. MICROCONTROLLERS & APPLICATION	PR	50	20	27	P C	17. WAVE THEORY & ANTENNA	PP	100	40	40	P C
08. DIGITAL SIGNAL PROCESSING	PP	100	40	52	P C	18. WAVE THEORY & ANTENNA	PR	50	20	30	P C
09. DIGITAL SIGNAL PROCESSING	OR	50	20	23	P C	19. MINI PROJECT & SEMINAR	OR	50	20	39	P C
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	22	P C	20. TEST & MEASUREMENT TECHNIQUES	OR	50	20	36	P C

GRAND TOTAL = 786/1500, RESULT: SECOND CLASS

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80053007 ANIRUDDHA SINGH CHOUHAN RENUKA , 71200761L , , PICT , T80053007

01. CONTROL SYSTEMS	PP	100	40	62	P
02. DIGITAL COMMUNICATION	PP	100	40	44	P
03. DIGITAL COMMUNICATION	PR	50	20	22	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	48	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	29	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	62	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	22	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	57	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	34	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	15	F

FIRST TERM TOTAL = 395/750.

ORDN. 1 MARKS :

T80053008 ASHISH VILAS PATIL SHAILA PATIL , 71344346E , , PICT , T80053008

01. CONTROL SYSTEMS	PP	100	40	82	P
02. DIGITAL COMMUNICATION	PP	100	40	76	P
03. DIGITAL COMMUNICATION	PR	50	20	45	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	83	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	47	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	76	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	40	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	74	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	41	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	44	P

FIRST TERM TOTAL = 608/750.

ORDN. 1 MARKS :

T80053009 ATHAWALE SHRADDHA MAHADEV MAYA , 71212091C , , PICT , T80053009

01. CONTROL SYSTEMS	PP	100	40	62	P
02. DIGITAL COMMUNICATION	PP	100	40	42	P
03. DIGITAL COMMUNICATION	PR	50	20	42	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	47	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	38	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	53	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	24	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	58	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	33	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	36	P

FIRST TERM TOTAL = 435/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80053010 AWACHAR MOHAN UDDHAO KAVERI , 71200767K , , PICT , T80053010

01. CONTROL SYSTEMS	PP	100	40	67	P
02. DIGITAL COMMUNICATION	PP	100	40	57	P
03. DIGITAL COMMUNICATION	PR	50	20	37	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	75	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	37	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	60	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	28	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	48	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	35	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	34	P

FIRST TERM TOTAL = 478/750.

ORDN. 1 MARKS :

T80053011 B KUMAR ANUPAM LAKSHMI PRIYA , 71200768H , , PICT , T80053011

01. CONTROL SYSTEMS	PP	100	40	43	P
02. DIGITAL COMMUNICATION	PP	100	40	29	F
03. DIGITAL COMMUNICATION	PR	50	20	20	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	40	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	29	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	32	F
07. MICROCONTROLLERS & APPLICATION	PR	50	20	05	F
08. DIGITAL SIGNAL PROCESSING	PP	100	40	46	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	33	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	12	F

FIRST TERM TOTAL = 289/750.

ORDN. 1 MARKS :

T80053012 BADE ABHIJIT ARJUN ASHA , 71200769F , , PICT , T80053012

01. CONTROL SYSTEMS	PP	100	40	66	P
02. DIGITAL COMMUNICATION	PP	100	40	51	P
03. DIGITAL COMMUNICATION	PR	50	20	30	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	61	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	36	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	56	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	24	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	48	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	37	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	40	P

FIRST TERM TOTAL = 449/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80053013 BAGDE PRAJAKTA ASHOK MANISHA , 71200770K , , PICT , T80053013

01. CONTROL SYSTEMS	PP	100	40	55	P
02. DIGITAL COMMUNICATION	PP	100	40	52	P
03. DIGITAL COMMUNICATION	PR	50	20	40	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	63	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	42	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	62	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	32	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	61	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	35	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	38	P

FIRST TERM TOTAL = 480/750.

ORDN. 1 MARKS :

T80053014 BAIRAGI SURAJ KACHARDAS SULOCHANA , 71200774B , , PICT , T80053014

01. CONTROL SYSTEMS	PP	100	40	78	P
02. DIGITAL COMMUNICATION	PP	100	40	71	P
03. DIGITAL COMMUNICATION	PR	50	20	44	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	82	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	45	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	71	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	40	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	69	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	38	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	38	P

FIRST TERM TOTAL = 576/750.

ORDN. 1 MARKS :

T80053015 BAJAJ POOJA BALAPRASAD SAVITA , 71200776J , , PICT , T80053015

01. CONTROL SYSTEMS	PP	100	40	70	P
02. DIGITAL COMMUNICATION	PP	100	40	25	F
03. DIGITAL COMMUNICATION	PR	50	20	20	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	44	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	36	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	47	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	22	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	50	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	32	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	39	P

FIRST TERM TOTAL = 385/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80053016 BAKRE ANUJ RAMESH JAYASHREE , 71200777G , , PICT , T80053016

01. CONTROL SYSTEMS	PP	100	40	76	P
02. DIGITAL COMMUNICATION	PP	100	40	72	P
03. DIGITAL COMMUNICATION	PR	50	20	39	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	65	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	38	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	70	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	36	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	73	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	39	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	39	P

FIRST TERM TOTAL = 547/750.

ORDN. 1 MARKS :

T80053017 BARKADE VIJAYA VASANT JAYASHRI , 71344347C , , PICT , T80053017

01. CONTROL SYSTEMS	PP	100	40	75	P
02. DIGITAL COMMUNICATION	PP	100	40	56	P
03. DIGITAL COMMUNICATION	PR	50	20	20	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	75	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	40	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	73	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	36	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	64	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	33	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	34	P

FIRST TERM TOTAL = 506/750.

ORDN. 1 MARKS :

T80053018 BASHIRABADKAR SAURABH MOHANKUMAR ANUPAMA , 71200785H , , PICT , T80053018

01. CONTROL SYSTEMS	PP	100	40	61	P
02. DIGITAL COMMUNICATION	PP	100	40	60	P
03. DIGITAL COMMUNICATION	PR	50	20	27	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	49	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	35	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	50	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	29	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	54	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	35	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	26	P

FIRST TERM TOTAL = 426/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80053019 BAVKAR INDRANEEL NANDKUMAR RADHIKA , 71200787D , , PICT , T80053019

01. CONTROL SYSTEMS	PP	100	40	64	P
02. DIGITAL COMMUNICATION	PP	100	40	40	P
03. DIGITAL COMMUNICATION	PR	50	20	33	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	47	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	39	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	51	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	30	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	55	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	40	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	36	P

FIRST TERM TOTAL = 435/750.

ORDN. 1 MARKS :

T80053020 BAYAS DIPALI SUNILSING URMILA , 71344348M , , PICT , T80053020

01. CONTROL SYSTEMS	PP	100	40	78	P
02. DIGITAL COMMUNICATION	PP	100	40	60	P
03. DIGITAL COMMUNICATION	PR	50	20	32	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	76	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	42	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	72	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	31	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	62	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	36	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	38	P

FIRST TERM TOTAL = 527/750.

ORDN. 1 MARKS :

T80053021 BERAD SUPRIYA LAXMIKANT SUNANDA , 71200788B , , PICT , T80053021

01. CONTROL SYSTEMS	PP	100	40	68	P
02. DIGITAL COMMUNICATION	PP	100	40	63	P
03. DIGITAL COMMUNICATION	PR	50	20	20	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	75	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	43	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	71	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	33	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	66	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	38	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	34	P

FIRST TERM TOTAL = 511/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80053022 BHALERAO MAHESH GOPAL SANGITA , 71344349K , , PICT , T80053022

01. CONTROL SYSTEMS	PP	100	40	67	P
02. DIGITAL COMMUNICATION	PP	100	40	46	P
03. DIGITAL COMMUNICATION	PR	50	20	35	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	61	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	39	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	70	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	30	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	68	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	32	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	38	P

FIRST TERM TOTAL = 486/750.

ORDN. 1 MARKS :

T80053023 BHANDARI AISHWARYA ABHAY SANGITA , 71200791B , , PICT , T80053023

01. CONTROL SYSTEMS	PP	100	40	60	P
02. DIGITAL COMMUNICATION	PP	100	40	62	P
03. DIGITAL COMMUNICATION	PR	50	20	33	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	40	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	37	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	73	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	38	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	74	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	39	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	38	P

FIRST TERM TOTAL = 494/750.

ORDN. 1 MARKS :

T80053024 BHARGANDE SAURABH VIJAYKUMAR SUSHMA , 71200794G , , PICT , T80053024

01. CONTROL SYSTEMS	PP	100	40	63	P
02. DIGITAL COMMUNICATION	PP	100	40	27	F
03. DIGITAL COMMUNICATION	PR	50	20	34	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	50	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	35	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	44	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	06	F
08. DIGITAL SIGNAL PROCESSING	PP	100	40	40	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	38	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	38	P

FIRST TERM TOTAL = 375/750.

ORDN. 1 MARKS :



NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80053025 BHAVSAR RUCHIKA KISHOR SAVITA , 71344350C , , PICT , T80053025

01. CONTROL SYSTEMS	PP	100	40	67	P
02. DIGITAL COMMUNICATION	PP	100	40	78	P
03. DIGITAL COMMUNICATION	PR	50	20	38	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	72	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	45	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	67	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	38	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	58	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	39	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	34	P

FIRST TERM TOTAL = 536/750.

ORDN. 1 MARKS :

T80053026 BHINGE ABHIJEET SOMESHWAR RUPALI , 71200795E , , PICT , T80053026

01. CONTROL SYSTEMS	PP	100	40	75	P
02. DIGITAL COMMUNICATION	PP	100	40	73	P
03. DIGITAL COMMUNICATION	PR	50	20	45	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	79	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	47	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	70	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	44	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	77	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	44	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	45	P

FIRST TERM TOTAL = 599/750.

ORDN. 1 MARKS :

T80053027 BHORDE MONALI MARUTI SUNANDA , 71344351M , , PICT , T80053027

01. CONTROL SYSTEMS	PP	100	40	71	P
02. DIGITAL COMMUNICATION	PP	100	40	83	P
03. DIGITAL COMMUNICATION	PR	50	20	44	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	71	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	40	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	72	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	41	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	63	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	39	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	44	P

FIRST TERM TOTAL = 568/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80053028 BHOSALE JUILEE UMESH ANITA , 71200797M , , PICT , T80053028

01. CONTROL SYSTEMS	PP	100	40	74	P
02. DIGITAL COMMUNICATION	PP	100	40	67	P
03. DIGITAL COMMUNICATION	PR	50	20	43	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	72	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	39	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	55	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	41	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	70	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	38	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	37	P

FIRST TERM TOTAL = 536/750.

ORDN. 1 MARKS :

T80053029 BIDE DADA VITHOBA JIJABAI , 71200799H , , PICT , T80053029

01. CONTROL SYSTEMS	PP	100	40	57	P
02. DIGITAL COMMUNICATION	PP	100	40	AA	F
03. DIGITAL COMMUNICATION	PR	50	20	07	F
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	57	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	36	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	67	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	07	F
08. DIGITAL SIGNAL PROCESSING	PP	100	40	AA	F
09. DIGITAL SIGNAL PROCESSING	OR	50	20	34	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	13	F

FIRST TERM TOTAL = 278/750.

ORDN. 1 MARKS :

T80053030 BOBADE AJINKYA CHANDRAKANT CHHAYA , 71200802M , , PICT , T80053030

01. CONTROL SYSTEMS	PP	100	40	55	P
02. DIGITAL COMMUNICATION	PP	100	40	30	F
03. DIGITAL COMMUNICATION	PR	50	20	40	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	33	F
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	38	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	45	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	26	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	50	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	35	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	28	P

FIRST TERM TOTAL = 380/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80053031 BONDE KAJAL SURESH KALPANA , 71344352K , , PICT , T80053031

01. CONTROL SYSTEMS	PP	100	40	59	P
02. DIGITAL COMMUNICATION	PP	100	40	40	P
03. DIGITAL COMMUNICATION	PR	50	20	39	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	49	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	39	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	58	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	34	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	45	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	34	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	42	P

FIRST TERM TOTAL = 439/750.

ORDN. 1 MARKS :

T80053032 BOOB SURABHI RAMDHAN SANGEETA , 71200803K , , PICT , T80053032

01. CONTROL SYSTEMS	PP	100	40	65	P
02. DIGITAL COMMUNICATION	PP	100	40	64	P
03. DIGITAL COMMUNICATION	PR	50	20	41	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	66	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	39	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	72	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	41	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	71	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	40	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	42	P

FIRST TERM TOTAL = 541/750.

ORDN. 1 MARKS :

T80053033 BORSE SWATI NANA KALPANA , 71100754D , , PICT , T80053033

01. CONTROL SYSTEMS	PP	100	40	40	P
02. DIGITAL COMMUNICATION	PP	100	40	40	P
03. DIGITAL COMMUNICATION	PR	50	20	25	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	53	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	32	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	31	F
07. MICROCONTROLLERS & APPLICATION	PR	50	20	AA	F
08. DIGITAL SIGNAL PROCESSING	PP	100	40	40	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	33	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	28	P

FIRST TERM TOTAL = 322/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80053034 CHANDAK ANKIT PANDURANGJI MAYA , 71100760J , PICT , T80053034

01. CONTROL SYSTEMS	PP	100	40	42	P
02. DIGITAL COMMUNICATION	PP	100	40	07	F
03. DIGITAL COMMUNICATION	PR	50	20	34	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	45	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	32	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	24	F
07. MICROCONTROLLERS & APPLICATION	PR	50	20	03	F
08. DIGITAL SIGNAL PROCESSING	PP	100	40	28	F
09. DIGITAL SIGNAL PROCESSING	OR	50	20	34	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	26	P

FIRST TERM TOTAL = 275/750.

ORDN. 1 MARKS :

T80053035 CHANDAN PANCHLORIA SUMITRADEVI , 71100761G , T80053020 , PICT , T80053035

01. CONTROL SYSTEMS	PP	100	40	45	P C	11. SIGNAL CODING & ESTIMATION THEORY	PP	100	40	40	P C
02. DIGITAL COMMUNICATION	PP	100	40	43	P C	12. SIGNAL CODING & ESTIMATION THEORY	PR	50	20	27	P
03. DIGITAL COMMUNICATION	PR	50	20	32	P C	13. SYSTEM PROGRA.& OPERATING SYS.	PP	100	40	63	P C
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	58	P C	14. SYSTEM PROGRA.& OPERATING SYS.	TW	50	20	33	P C
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	40	P C	15. COMPUTER ORGANIZATION & ARCHITEC	PP	100	40	48	P C
06. MICROCONTROLLERS & APPLICATION	PP	100	40	40	P C	16. INDUSTRIAL MANAGEMENT	PP	100	40	70	P C
07. MICROCONTROLLERS & APPLICATION	PR	50	20	33	P C	17. WAVE THEORY & ANTENNA	PP	100	40	40	P C
08. DIGITAL SIGNAL PROCESSING	PP	100	40	44	P C	18. WAVE THEORY & ANTENNA	PR	50	20	20	P C
09. DIGITAL SIGNAL PROCESSING	OR	50	20	21	P C	19. MINI PROJECT & SEMINAR	OR	50	20	38	P C
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	28	P C	20. TEST & MEASUREMENT TECHNIQUES	OR	50	20	32	P C

GRAND TOTAL = 795/1500, RESULT: SECOND CLASS

ORDN. 1 MARKS :

T80053036 CHASKAR DIPALI SHRIDHAR ALKA , 71200810B , PICT , T80053036

01. CONTROL SYSTEMS	PP	100	40	58	P
02. DIGITAL COMMUNICATION	PP	100	40	40	P
03. DIGITAL COMMUNICATION	PR	50	20	36	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	50	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	39	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	56	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	22	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	59	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	33	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	28	P

FIRST TERM TOTAL = 421/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80053037 CHAVAN ROHIT UTTAM MEENA , 71200816M , PICT , T80053037

01. CONTROL SYSTEMS	PP	100	40	66	P
02. DIGITAL COMMUNICATION	PP	100	40	85	P
03. DIGITAL COMMUNICATION	PR	50	20	35	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	70	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	43	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	82	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	41	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	79	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	38	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	38	P

FIRST TERM TOTAL = 577/750.

ORDN. 1 MARKS :

T80053038 CHIRANIYA ANKITA GOPAL RAKHI , 71200821H , PICT , T80053038

01. CONTROL SYSTEMS	PP	100	40	61	P
02. DIGITAL COMMUNICATION	PP	100	40	40	P
03. DIGITAL COMMUNICATION	PR	50	20	29	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	62	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	29	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	45	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	05	F
08. DIGITAL SIGNAL PROCESSING	PP	100	40	59	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	35	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	26	P

FIRST TERM TOTAL = 391/750.

ORDN. 1 MARKS :

T80053039 CHITALE JANHAVI GOVIND VARSHA , 71100769B , T80053025 , PICT , T80053039

01. CONTROL SYSTEMS	PP	100	40	56	P C	11. SIGNAL CODING & ESTIMATION THEORY	PP	100	40	40	P C
02. DIGITAL COMMUNICATION	PP	100	40	48	P C	12. SIGNAL CODING & ESTIMATION THEORY	PR	50	20	39	P C
03. DIGITAL COMMUNICATION	PR	50	20	41	P C	13. SYSTEM PROGRA.& OPERATING SYS.	PP	100	40	53	P C
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	55	P C	14. SYSTEM PROGRA.& OPERATING SYS.	TW	50	20	40	P C
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	42	P C	15. COMPUTER ORGANIZATION & ARCHITEC	PP	100	40	52	P C
06. MICROCONTROLLERS & APPLICATION	PP	100	40	57	P C	16. INDUSTRIAL MANAGEMENT	PP	100	40	61	P C
07. MICROCONTROLLERS & APPLICATION	PR	50	20	38	P C	17. WAVE THEORY & ANTENNA	PP	100	40	61	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	47	P C	18. WAVE THEORY & ANTENNA	PR	50	20	38	P C
09. DIGITAL SIGNAL PROCESSING	OR	50	20	42	P C	19. MINI PROJECT & SEMINAR	OR	50	20	42	P C
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	25	P C	20. TEST & MEASUREMENT TECHNIQUES	OR	50	20	41	P C

GRAND TOTAL = 918/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80053040 CHOWDHARY NOOPUR YUVRAJKUMAR HARSHA , 71200823D , , PICT , T80053040

01. CONTROL SYSTEMS	PP	100	40	62	P
02. DIGITAL COMMUNICATION	PP	100	40	44	P
03. DIGITAL COMMUNICATION	PR	50	20	24	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	56	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	38	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	49	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	06	F
08. DIGITAL SIGNAL PROCESSING	PP	100	40	51	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	36	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	36	P

FIRST TERM TOTAL = 402/750.

ORDN. 1 MARKS :

T80053041 DAKE ABOLI BALASAHEB ARUNA , 71200826J , , PICT , T80053041

01. CONTROL SYSTEMS	PP	100	40	64	P
02. DIGITAL COMMUNICATION	PP	100	40	40	P
03. DIGITAL COMMUNICATION	PR	50	20	08	F
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	67	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	35	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	57	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	26	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	64	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	28	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	26	P

FIRST TERM TOTAL = 415/750.

ORDN. 1 MARKS :

T80053042 DESHMUKH ASHUTOSH VILASRAO SUNITA , 71100780C , , PICT , T80053042

01. CONTROL SYSTEMS	PP	100	40	60	P
02. DIGITAL COMMUNICATION	PP	100	40	43	P
03. DIGITAL COMMUNICATION	PR	50	20	AA	F
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	49	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	20	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	26	F
07. MICROCONTROLLERS & APPLICATION	PR	50	20	AA	F
08. DIGITAL SIGNAL PROCESSING	PP	100	40	48	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	AA	F
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	AA	F

FIRST TERM TOTAL = 246/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80053043	DESHPANDE ADITI VIDHYADHAR			ALKA		, 71100781M		T80053028		PICT		T80053043
01. CONTROL SYSTEMS	PP	100	40	58	P C	11. SIGNAL CODING & ESTIMATION THEORY	PP	100	40	40	P C	
02. DIGITAL COMMUNICATION	PP	100	40	42	P C	12. SIGNAL CODING & ESTIMATION THEORY	PR	50	20	38	P C	
03. DIGITAL COMMUNICATION	PR	50	20	27	P C	13. SYSTEM PROGRA.& OPERATING SYS.	PP	100	40	54	P C	
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	46	P C	14. SYSTEM PROGRA.& OPERATING SYS.	TW	50	20	40	P C	
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	38	P C	15. COMPUTER ORGANIZATION & ARCHITEC	PP	100	40	53	P C	
06. MICROCONTROLLERS & APPLICATION	PP	100	40	51	P C	16. INDUSTRIAL MANAGEMENT	PP	100	40	61	P C	
07. MICROCONTROLLERS & APPLICATION	PR	50	20	30	P C	17. WAVE THEORY & ANTENNA	PP	100	40	52	P C	
08. DIGITAL SIGNAL PROCESSING	PP	100	40	51	P C	18. WAVE THEORY & ANTENNA	PR	50	20	22	P C	
09. DIGITAL SIGNAL PROCESSING	OR	50	20	26	P C	19. MINI PROJECT & SEMINAR	OR	50	20	39	P C	
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	34	P C	20. TEST & MEASUREMENT TECHNIQUES	OR	50	20	25	P	

GRAND TOTAL = 827/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

T80053044	DESHPANDE CHAITANYA DHANANJAY			SWATI		, 71200832C				PICT		T80053044
01. CONTROL SYSTEMS	PP	100	40	70	P							
02. DIGITAL COMMUNICATION	PP	100	40	56	P							
03. DIGITAL COMMUNICATION	PR	50	20	43	P							
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	64	P							
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	41	P							
06. MICROCONTROLLERS & APPLICATION	PP	100	40	68	P							
07. MICROCONTROLLERS & APPLICATION	PR	50	20	44	P							
08. DIGITAL SIGNAL PROCESSING	PP	100	40	67	P							
09. DIGITAL SIGNAL PROCESSING	OR	50	20	36	P							
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	34	P							

FIRST TERM TOTAL = 523/750.

ORDN. 1 MARKS :

T80053045	DESHPANDE KAUSTUBH SURYAKANT			MANIK		, 71200833M				PICT		T80053045
01. CONTROL SYSTEMS	PP	100	40	63	P							
02. DIGITAL COMMUNICATION	PP	100	40	48	P							
03. DIGITAL COMMUNICATION	PR	50	20	39	P							
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	62	P							
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	36	P							
06. MICROCONTROLLERS & APPLICATION	PP	100	40	55	P							
07. MICROCONTROLLERS & APPLICATION	PR	50	20	30	P							
08. DIGITAL SIGNAL PROCESSING	PP	100	40	52	P							
09. DIGITAL SIGNAL PROCESSING	OR	50	20	36	P							
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	38	P							

FIRST TERM TOTAL = 459/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80053046 DESHPANDE NIKITA KISHOR NEETA , 71200834K , , PICT , T80053046

01. CONTROL SYSTEMS	PP	100	40	55	P
02. DIGITAL COMMUNICATION	PP	100	40	46	P
03. DIGITAL COMMUNICATION	PR	50	20	30	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	59	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	33	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	56	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	31	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	51	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	32	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	22	P

FIRST TERM TOTAL = 415/750.

ORDN. 1 MARKS :

T80053047 DESHPANDE SUBODH SANJAYRAO SUPRIYA , 71200835H , , PICT , T80053047

01. CONTROL SYSTEMS	PP	100	40	57	P
02. DIGITAL COMMUNICATION	PP	100	40	43	P
03. DIGITAL COMMUNICATION	PR	50	20	32	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	55	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	36	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	50	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	33	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	40	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	38	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	37	P

FIRST TERM TOTAL = 421/750.

ORDN. 1 MARKS :

T80053048 DHAMAL NAMRATA MOHAN SAVITA , 71344353H , , PICT , T80053048

01. CONTROL SYSTEMS	PP	100	40	74	P
02. DIGITAL COMMUNICATION	PP	100	40	57	P
03. DIGITAL COMMUNICATION	PR	50	20	35	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	55	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	43	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	71	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	40	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	66	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	38	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	38	P

FIRST TERM TOTAL = 517/750.

ORDN. 1 MARKS :



NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80053049 DHODI SHRADDHA MADHU ARUNA , 71045415F , , PICT , T80053049

01. CONTROL SYSTEMS	PP	100	40	44	P
02. DIGITAL COMMUNICATION	PP	100	40	23	F
03. DIGITAL COMMUNICATION	PR	50	20	04	F
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	48	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	27	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	32	F
07. MICROCONTROLLERS & APPLICATION	PR	50	20	05	F
08. DIGITAL SIGNAL PROCESSING	PP	100	40	40	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	37	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	14	F

FIRST TERM TOTAL = 274/750.

ORDN. 1 MARKS :

T80053050 DHORE PRASHANT SHRIKRISHNA RANJANA , 71200842L , , PICT , T80053050

01. CONTROL SYSTEMS	PP	100	40	60	P
02. DIGITAL COMMUNICATION	PP	100	40	31	F
03. DIGITAL COMMUNICATION	PR	50	20	27	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	57	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	28	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	43	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	30	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	54	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	38	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	34	P

FIRST TERM TOTAL = 402/750.

ORDN. 1 MARKS :

T80053051 DHOTEY RUTVIJ SUDHIR SANGITA , 71200843J , , PICT , T80053051

01. CONTROL SYSTEMS	PP	100	40	56	P
02. DIGITAL COMMUNICATION	PP	100	40	51	P
03. DIGITAL COMMUNICATION	PR	50	20	33	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	64	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	39	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	53	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	30	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	61	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	34	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	38	P

FIRST TERM TOTAL = 459/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80053052 DOLAS LUBDHA RAJU SUNITA , 71236310G , , PICT , T80053052

01. CONTROL SYSTEMS	PP	100	40	57	P
02. DIGITAL COMMUNICATION	PP	100	40	25	F
03. DIGITAL COMMUNICATION	PR	50	20	23	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	51	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	34	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	47	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	05	F
08. DIGITAL SIGNAL PROCESSING	PP	100	40	46	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	32	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	32	P

FIRST TERM TOTAL = 352/750.

ORDN. 1 MARKS :

T80053053 GAIKWAD GAYATRI DILIP DIPALI , 71100792G , , PICT , T80053053

01. CONTROL SYSTEMS	PP	100	40	40	P
02. DIGITAL COMMUNICATION	PP	100	40	30	F
03. DIGITAL COMMUNICATION	PR	50	20	06	F
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	40	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	36	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	40	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	26	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	47	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	28	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	24	P

FIRST TERM TOTAL = 317/750.

ORDN. 1 MARKS :

T80053054 GAIKWAD VAISHALI RAMESH SHITAL , 71344354F , , PICT , T80053054

01. CONTROL SYSTEMS	PP	100	40	56	P
02. DIGITAL COMMUNICATION	PP	100	40	41	P
03. DIGITAL COMMUNICATION	PR	50	20	22	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	52	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	41	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	54	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	30	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	63	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	31	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	34	P

FIRST TERM TOTAL = 424/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80053055 GANGAL AKSHAY PARAG RAJASHREE , 71200857J , PICT , T80053055

01. CONTROL SYSTEMS	PP	100	40	77	P
02. DIGITAL COMMUNICATION	PP	100	40	57	P
03. DIGITAL COMMUNICATION	PR	50	20	37	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	77	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	46	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	73	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	44	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	76	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	40	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	42	P

FIRST TERM TOTAL = 569/750.

ORDN. 1 MARKS :

T80053056 GAVADE SACHIN SHIVAJI SNEHAL , 71100798F , T80053034 , PICT , T80053056

01. CONTROL SYSTEMS	PP	100	40	49	P C	11. SIGNAL CODING & ESTIMATION THEORY	PP	100	40	60	P C
02. DIGITAL COMMUNICATION	PP	100	40	40	P C	12. SIGNAL CODING & ESTIMATION THEORY	PR	50	20	20	P C
03. DIGITAL COMMUNICATION	PR	50	20	30	P C	13. SYSTEM PROGRA.& OPERATING SYS.	PP	100	40	46	P C
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	50	P C	14. SYSTEM PROGRA.& OPERATING SYS.	TW	50	20	34	P C
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	39	P C	15. COMPUTER ORGANIZATION & ARCHITEC	PP	100	40	AA	F
06. MICROCONTROLLERS & APPLICATION	PP	100	40	48	P C	16. INDUSTRIAL MANAGEMENT	PP	100	40	49	P C
07. MICROCONTROLLERS & APPLICATION	PR	50	20	27	P C	17. WAVE THEORY & ANTENNA	PP	100	40	41	P C
08. DIGITAL SIGNAL PROCESSING	PP	100	40	AA	F	18. WAVE THEORY & ANTENNA	PR	50	20	21	P C
09. DIGITAL SIGNAL PROCESSING	OR	50	20	34	P C	19. MINI PROJECT & SEMINAR	OR	50	20	38	P C
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	27	P C	20. TEST & MEASUREMENT TECHNIQUES	OR	50	20	36	P C

GRAND TOTAL = 689/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

T80053057 GENGAJE PRAJAKTA SHANTARAM SUREKHA , 71236312C , PICT , T80053057

01. CONTROL SYSTEMS	PP	100	40	49	P
02. DIGITAL COMMUNICATION	PP	100	40	29	F
03. DIGITAL COMMUNICATION	PR	50	20	08	F
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	29	F
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	41	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	46	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	21	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	49	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	25	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	26	P

FIRST TERM TOTAL = 323/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80053058 GHADGE GITANJALI SUBHASH MANJUSHA , 71200863C , PICT , T80053058

01. CONTROL SYSTEMS	PP	100	40	53	P
02. DIGITAL COMMUNICATION	PP	100	40	44	P
03. DIGITAL COMMUNICATION	PR	50	20	40	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	58	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	41	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	50	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	35	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	60	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	26	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	28	P

FIRST TERM TOTAL = 435/750.

ORDN. 1 MARKS :

T80053059 GHARDE SAGAR BHIMRAO SHARDA , 71236313M , T80053035 , PICT , T80053059

01. CONTROL SYSTEMS	PP	100	40	40	P C	11. SIGNAL CODING & ESTIMATION THEORY	PP	100	40	24	F
02. DIGITAL COMMUNICATION	PP	100	40	48	P C	12. SIGNAL CODING & ESTIMATION THEORY	PR	50	20	27	P C
03. DIGITAL COMMUNICATION	PR	50	20	28	P C	13. SYSTEM PROGRA.& OPERATING SYS.	PP	100	40	53	P C
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	41	P C	14. SYSTEM PROGRA.& OPERATING SYS.	TW	50	20	39	P C
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	39	P C	15. COMPUTER ORGANIZATION & ARCHITEC	PP	100	40	41	P C
06. MICROCONTROLLERS & APPLICATION	PP	100	40	57	P C	16. INDUSTRIAL MANAGEMENT	PP	100	40	47	P C
07. MICROCONTROLLERS & APPLICATION	PR	50	20	35	P C	17. WAVE THEORY & ANTENNA	PP	100	40	40	P C
08. DIGITAL SIGNAL PROCESSING	PP	100	40	40	P C	18. WAVE THEORY & ANTENNA	PR	50	20	35	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	22	P C	19. MINI PROJECT & SEMINAR	OR	50	20	33	P C
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	29	P C	20. TEST & MEASUREMENT TECHNIQUES	OR	50	20	26	P C

GRAND TOTAL = 744/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

T80053060 GIRI PRATIK CHANDRAKANT SANDHYA GIRI , 71344355D , PICT , T80053060

01. CONTROL SYSTEMS	PP	100	40	81	P
02. DIGITAL COMMUNICATION	PP	100	40	61	P
03. DIGITAL COMMUNICATION	PR	50	20	42	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	53	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	45	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	68	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	35	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	62	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	38	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	38	P

FIRST TERM TOTAL = 523/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80053061 GOKHALE SATYAJIT SHASHANK JAYASHREE , 71200866H , , PICT , T80053061

01. CONTROL SYSTEMS	PP	100	40	50	P
02. DIGITAL COMMUNICATION	PP	100	40	40	P
03. DIGITAL COMMUNICATION	PR	50	20	43	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	52	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	41	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	57	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	36	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	48	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	33	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	36	P

FIRST TERM TOTAL = 436/750.

ORDN. 1 MARKS :

T80053062 GOSAVI ABHISHEK MAHESH MANISHA , 71344356B , , PICT , T80053062

01. CONTROL SYSTEMS	PP	100	40	82	P
02. DIGITAL COMMUNICATION	PP	100	40	64	P
03. DIGITAL COMMUNICATION	PR	50	20	43	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	59	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	37	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	68	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	34	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	65	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	33	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	32	P

FIRST TERM TOTAL = 517/750.

ORDN. 1 MARKS :

T80053063 GUMBADE MAYURI RAGHUNATH LATA , 71344357L , , PICT , T80053063

01. CONTROL SYSTEMS	PP	100	40	57	P
02. DIGITAL COMMUNICATION	PP	100	40	50	P
03. DIGITAL COMMUNICATION	PR	50	20	26	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	56	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	39	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	61	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	40	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	52	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	28	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	32	P

FIRST TERM TOTAL = 441/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80053064 GUTTE KISHOR SHIVAJI USHABAI , 71200874J , , PICT , T80053064

01. CONTROL SYSTEMS	PP	100	40	69	P
02. DIGITAL COMMUNICATION	PP	100	40	49	P
03. DIGITAL COMMUNICATION	PR	50	20	30	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	53	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	43	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	56	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	38	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	66	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	36	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	34	P

FIRST TERM TOTAL = 474/750.

ORDN. 1 MARKS :

T80053065 HEGDE GAUTAMI GOPALKRISHNA APARNA , 71200880C , , PICT , T80053065

01. CONTROL SYSTEMS	PP	100	40	71	P
02. DIGITAL COMMUNICATION	PP	100	40	61	P
03. DIGITAL COMMUNICATION	PR	50	20	36	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	71	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	41	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	67	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	31	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	69	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	30	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	42	P

FIRST TERM TOTAL = 519/750.

ORDN. 1 MARKS :

T80053066 HIMANSHU JAIN RACHANA , 71100821D , T80053039 , PICT , T80053066

01. CONTROL SYSTEMS	PP	100	40	53	P C	11. SIGNAL CODING & ESTIMATION THEORY	PP	100	40	51	P
02. DIGITAL COMMUNICATION	PP	100	40	44	P C	12. SIGNAL CODING & ESTIMATION THEORY	PR	50	20	32	P C
03. DIGITAL COMMUNICATION	PR	50	20	37	P C	13. SYSTEM PROGRA.& OPERATING SYS.	PP	100	40	54	P C
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	42	P C	14. SYSTEM PROGRA.& OPERATING SYS.	TW	50	20	28	P C
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	25	P C	15. COMPUTER ORGANIZATION & ARCHITEC	PP	100	40	50	P C
06. MICROCONTROLLERS & APPLICATION	PP	100	40	56	P C	16. INDUSTRIAL MANAGEMENT	PP	100	40	41	P C
07. MICROCONTROLLERS & APPLICATION	PR	50	20	23	P C	17. WAVE THEORY & ANTENNA	PP	100	40	46	P C
08. DIGITAL SIGNAL PROCESSING	PP	100	40	40	P C	18. WAVE THEORY & ANTENNA	PR	50	20	25	P C
09. DIGITAL SIGNAL PROCESSING	OR	50	20	32	P C	19. MINI PROJECT & SEMINAR	OR	50	20	34	P C
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	25	P C	20. TEST & MEASUREMENT TECHNIQUES	OR	50	20	34	P C

GRAND TOTAL = 772/1500, RESULT: SECOND CLASS

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80053067 HINGANE SOURABH MADHUKAR MADHURI , 71200881M , , PICT , T80053067

01. CONTROL SYSTEMS	PP	100	40	73	P
02. DIGITAL COMMUNICATION	PP	100	40	47	P
03. DIGITAL COMMUNICATION	PR	50	20	40	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	62	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	42	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	59	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	33	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	59	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	34	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	40	P

FIRST TERM TOTAL = 489/750.

ORDN. 1 MARKS :

T80053068 HOWNI SAMAN WANN LAMBOK , 71200883H , , PICT , T80053068

01. CONTROL SYSTEMS	PP	100	40	46	P
02. DIGITAL COMMUNICATION	PP	100	40	32	F
03. DIGITAL COMMUNICATION	PR	50	20	20	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	29	F
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	28	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	21	F
07. MICROCONTROLLERS & APPLICATION	PR	50	20	05	F
08. DIGITAL SIGNAL PROCESSING	PP	100	40	40	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	30	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	12	F

FIRST TERM TOTAL = 263/750.

ORDN. 1 MARKS :

T80053069 INGLE BHAVANA KRUSHNAJI SHAILA , 71344358J , , PICT , T80053069

01. CONTROL SYSTEMS	PP	100	40	67	P
02. DIGITAL COMMUNICATION	PP	100	40	41	P
03. DIGITAL COMMUNICATION	PR	50	20	40	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	58	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	41	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	70	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	36	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	61	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	30	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	38	P

FIRST TERM TOTAL = 482/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80053070	ISHTH PRAKASH BHAGVAN				KAMAL				, 70925442D			T80053040		PICT		T80053070
01. CONTROL SYSTEMS	PP	100	40	40	P C	11. SIGNAL CODING & ESTIMATION THEORY	PP	100	40	40	P C					
02. DIGITAL COMMUNICATION	PP	100	40	40	P C	12. SIGNAL CODING & ESTIMATION THEORY	PR	50	20	23	P					
03. DIGITAL COMMUNICATION	PR	50	20	20	P C	13. SYSTEM PROGRA.& OPERATING SYS.	PP	100	40	43	P C					
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	52	P C	14. SYSTEM PROGRA.& OPERATING SYS.	TW	50	20	28	P C					
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	33	P C	15. COMPUTER ORGANIZATION & ARCHITECT	PP	100	40	45	P C					
06. MICROCONTROLLERS & APPLICATION	PP	100	40	40	P C	16. INDUSTRIAL MANAGEMENT	PP	100	40	40	P C					
07. MICROCONTROLLERS & APPLICATION	PR	50	20	28	P C	17. WAVE THEORY & ANTENNA	PP	100	40	46	P C					
08. DIGITAL SIGNAL PROCESSING	PP	100	40	40	P C	18. WAVE THEORY & ANTENNA	PR	50	20	22	P C					
09. DIGITAL SIGNAL PROCESSING	OR	50	20	28	P C	19. MINI PROJECT & SEMINAR	OR	50	20	21	P					
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	22	P C	20. TEST & MEASUREMENT TECHNIQUES	OR	50	20	22	P					

GRAND TOTAL = 673/1500, RESULT: PASS CLASS

ORDN. 1 MARKS :

T80053071	JADHAV POONAM SANJAY				SANGITA				, 71344359G					PICT		T80053071
01. CONTROL SYSTEMS	PP	100	40	55	P											
02. DIGITAL COMMUNICATION	PP	100	40	46	P											
03. DIGITAL COMMUNICATION	PR	50	20	40	P											
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	52	P											
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	40	P											
06. MICROCONTROLLERS & APPLICATION	PP	100	40	57	P											
07. MICROCONTROLLERS & APPLICATION	PR	50	20	33	P											
08. DIGITAL SIGNAL PROCESSING	PP	100	40	40	P											
09. DIGITAL SIGNAL PROCESSING	OR	50	20	28	P											
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	38	P											

FIRST TERM TOTAL = 429/750.

ORDN. 1 MARKS :

T80053072	JAGTAP MANJIRI ARVIND				SHUBHANGI				, 71100825G			T80053041		PICT		T80053072
01. CONTROL SYSTEMS	PP	100	40	43	P C	11. SIGNAL CODING & ESTIMATION THEORY	PP	100	40	40	P C					
02. DIGITAL COMMUNICATION	PP	100	40	25	F	12. SIGNAL CODING & ESTIMATION THEORY	PR	50	20	30	P C					
03. DIGITAL COMMUNICATION	PR	50	20	21	P C	13. SYSTEM PROGRA.& OPERATING SYS.	PP	100	40	43	P					
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	52	P C	14. SYSTEM PROGRA.& OPERATING SYS.	TW	50	20	34	P C					
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	36	P C	15. COMPUTER ORGANIZATION & ARCHITECT	PP	100	40	49	P C					
06. MICROCONTROLLERS & APPLICATION	PP	100	40	46	P C	16. INDUSTRIAL MANAGEMENT	PP	100	40	44	P C					
07. MICROCONTROLLERS & APPLICATION	PR	50	20	25	P C	17. WAVE THEORY & ANTENNA	PP	100	40	50	P C					
08. DIGITAL SIGNAL PROCESSING	PP	100	40	43	P C	18. WAVE THEORY & ANTENNA	PR	50	20	28	P C					
09. DIGITAL SIGNAL PROCESSING	OR	50	20	26	P C	19. MINI PROJECT & SEMINAR	OR	50	20	37	P C					
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	22	P C	20. TEST & MEASUREMENT TECHNIQUES	OR	50	20	20	P C					

GRAND TOTAL = 714/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :



NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80053073 JAGTAP SHITAL LAXMAN LATA , 71344360L , , PICT , T80053073

01. CONTROL SYSTEMS	PP	100	40	65	P
02. DIGITAL COMMUNICATION	PP	100	40	44	P
03. DIGITAL COMMUNICATION	PR	50	20	37	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	61	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	43	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	62	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	35	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	60	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	31	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	36	P

FIRST TERM TOTAL = 474/750.

ORDN. 1 MARKS :

T80053074 JAIN KAJAL HEMANTKUMAR RAJANI , 71200887L , , PICT , T80053074

01. CONTROL SYSTEMS	PP	100	40	71	P
02. DIGITAL COMMUNICATION	PP	100	40	52	P
03. DIGITAL COMMUNICATION	PR	50	20	32	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	56	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	36	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	53	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	21	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	58	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	30	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	29	P

FIRST TERM TOTAL = 438/750.

ORDN. 1 MARKS :

T80053075 JAIN KRUNAL LALIT MANJU , 71200888J , , PICT , T80053075

01. CONTROL SYSTEMS	PP	100	40	64	P
02. DIGITAL COMMUNICATION	PP	100	40	63	P
03. DIGITAL COMMUNICATION	PR	50	20	44	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	53	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	42	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	63	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	40	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	71	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	40	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	36	P

FIRST TERM TOTAL = 516/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80053076 JAVANJAL MAUSAMI SHIVSHANKAR SANDHYA , 71344361J , , PICT , T80053076

01. CONTROL SYSTEMS	PP	100	40	65	P
02. DIGITAL COMMUNICATION	PP	100	40	54	P
03. DIGITAL COMMUNICATION	PR	50	20	29	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	57	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	36	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	70	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	39	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	59	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	36	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	30	P

FIRST TERM TOTAL = 475/750.

ORDN. 1 MARKS :

T80053077 JAWALIKAR AMEY RAVINDRA NIRMALA RAVINDRA JA , 71344362G , , PICT , T80053077

01. CONTROL SYSTEMS	PP	100	40	42	P
02. DIGITAL COMMUNICATION	PP	100	40	AA	F
03. DIGITAL COMMUNICATION	PR	50	20	28	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	40	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	33	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	48	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	35	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	48	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	38	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	36	P

FIRST TERM TOTAL = 348/750.

ORDN. 1 MARKS :

T80053078 JOSHI NEEL RAJEEV PADMAJA , 71200894C , , PICT , T80053078

01. CONTROL SYSTEMS	PP	100	40	58	P
02. DIGITAL COMMUNICATION	PP	100	40	44	P
03. DIGITAL COMMUNICATION	PR	50	20	44	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	56	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	38	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	55	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	39	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	66	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	39	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	42	P

FIRST TERM TOTAL = 481/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80053079 JOSHI NIKITA SANJAY MADHAVI , 71200895M , , PICT , T80053079

01. CONTROL SYSTEMS	PP	100	40	40	P
02. DIGITAL COMMUNICATION	PP	100	40	40	P
03. DIGITAL COMMUNICATION	PR	50	20	32	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	47	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	41	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	43	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	23	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	48	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	38	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	28	P

FIRST TERM TOTAL = 380/750.

ORDN. 1 MARKS :

T80053080 KADAM AKSHAY VITTHALRAO LALITA , 71200898F , , PICT , T80053080

01. CONTROL SYSTEMS	PP	100	40	85	P
02. DIGITAL COMMUNICATION	PP	100	40	63	P
03. DIGITAL COMMUNICATION	PR	50	20	40	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	55	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	45	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	64	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	38	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	68	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	38	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	40	P

FIRST TERM TOTAL = 536/750.

ORDN. 1 MARKS :

T80053081 KALE PRASHANT SUBHASH SHARDA , 71200902H , , PICT , T80053081

01. CONTROL SYSTEMS	PP	100	40	51	P
02. DIGITAL COMMUNICATION	PP	100	40	50	P
03. DIGITAL COMMUNICATION	PR	50	20	31	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	57	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	37	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	69	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	33	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	57	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	36	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	22	P

FIRST TERM TOTAL = 443/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80053082	KAMBLE DASHARATH SATAPPA				KAMAL				, 71236314K				T80053048		PICT			T80053082
01. CONTROL SYSTEMS	PP	100	40	50	P C	11. SIGNAL CODING & ESTIMATION THEORY	PP	100	40	40	P C							
02. DIGITAL COMMUNICATION	PP	100	40	40	P C	12. SIGNAL CODING & ESTIMATION THEORY	PR	50	20	24	P							
03. DIGITAL COMMUNICATION	PR	50	20	28	P C	13. SYSTEM PROGRA.& OPERATING SYS.	PP	100	40	58	P C							
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	55	P C	14. SYSTEM PROGRA.& OPERATING SYS.	TW	50	20	40	P C							
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	42	P C	15. COMPUTER ORGANIZATION & ARCHITECTURE	PP	100	40	53	P C							
06. MICROCONTROLLERS & APPLICATION	PP	100	40	62	P C	16. INDUSTRIAL MANAGEMENT	PP	100	40	58	P C							
07. MICROCONTROLLERS & APPLICATION	PR	50	20	25	P C	17. WAVE THEORY & ANTENNA	PP	100	40	59	P C							
08. DIGITAL SIGNAL PROCESSING	PP	100	40	45	P C	18. WAVE THEORY & ANTENNA	PR	50	20	35	P C							
09. DIGITAL SIGNAL PROCESSING	OR	50	20	25	P C	19. MINI PROJECT & SEMINAR	OR	50	20	41	P C							
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	25	P C	20. TEST & MEASUREMENT TECHNIQUES	OR	50	20	34	P C							

GRAND TOTAL = 839/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

T80053083	KAMBLE PRATIK RAVINDRA				SUMAN				, 71100845M				T80053049		PICT			T80053083
01. CONTROL SYSTEMS	PP	100	40	54	P C	11. SIGNAL CODING & ESTIMATION THEORY	PP	100	40	40	P C							
02. DIGITAL COMMUNICATION	PP	100	40	45	P C	12. SIGNAL CODING & ESTIMATION THEORY	PR	50	20	23	P							
03. DIGITAL COMMUNICATION	PR	50	20	30	P C	13. SYSTEM PROGRA.& OPERATING SYS.	PP	100	40	66	P C							
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	58	P C	14. SYSTEM PROGRA.& OPERATING SYS.	TW	50	20	27	P C							
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	26	P C	15. COMPUTER ORGANIZATION & ARCHITECTURE	PP	100	40	51	P C							
06. MICROCONTROLLERS & APPLICATION	PP	100	40	48	P C	16. INDUSTRIAL MANAGEMENT	PP	100	40	47	P C							
07. MICROCONTROLLERS & APPLICATION	PR	50	20	27	P C	17. WAVE THEORY & ANTENNA	PP	100	40	54	P C							
08. DIGITAL SIGNAL PROCESSING	PP	100	40	44	P C	18. WAVE THEORY & ANTENNA	PR	50	20	24	P C							
09. DIGITAL SIGNAL PROCESSING	OR	50	20	33	P C	19. MINI PROJECT & SEMINAR	OR	50	20	36	P C							
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	22	P C	20. TEST & MEASUREMENT TECHNIQUES	OR	50	20	28	P C							

GRAND TOTAL = 783/1500, RESULT: SECOND CLASS

ORDN. 1 MARKS :

T80053084	KAMBLE SANJEEVANI RAMESH				JAYASHREE				, 71200908G						PICT			T80053084
01. CONTROL SYSTEMS	PP	100	40	40	P													
02. DIGITAL COMMUNICATION	PP	100	40	32	F													
03. DIGITAL COMMUNICATION	PR	50	20	20	P													
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	49	P													
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	42	P													
06. MICROCONTROLLERS & APPLICATION	PP	100	40	40	P													
07. MICROCONTROLLERS & APPLICATION	PR	50	20	20	P													
08. DIGITAL SIGNAL PROCESSING	PP	100	40	40	P													
09. DIGITAL SIGNAL PROCESSING	OR	50	20	34	P													
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	22	P													

FIRST TERM TOTAL = 339/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80053085 KAMBLE VIJAY SANJAY SAVITRI , 71200909E , , PICT , T80053085

01. CONTROL SYSTEMS	PP	100	40	47	P
02. DIGITAL COMMUNICATION	PP	100	40	65	P
03. DIGITAL COMMUNICATION	PR	50	20	40	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	55	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	38	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	58	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	28	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	54	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	33	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	34	P

FIRST TERM TOTAL = 452/750.

ORDN. 1 MARKS :

T80053086 KAPRE SURAJ DNYANOBA JAYSHREE , 71200911G , , PICT , T80053086

01. CONTROL SYSTEMS	PP	100	40	61	P
02. DIGITAL COMMUNICATION	PP	100	40	29	F
03. DIGITAL COMMUNICATION	PR	50	20	36	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	55	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	39	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	50	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	28	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	57	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	37	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	30	P

FIRST TERM TOTAL = 422/750.

ORDN. 1 MARKS :

T80053087 KASAR SNEHAL NIVRUTTI SUREKHA , 71212196L , , PICT , T80053087

01. CONTROL SYSTEMS	PP	100	40	66	P
02. DIGITAL COMMUNICATION	PP	100	40	63	P
03. DIGITAL COMMUNICATION	PR	50	20	36	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	72	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	37	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	59	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	25	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	56	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	35	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	31	P

FIRST TERM TOTAL = 480/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80053088 KASLIKAR RASHMI MILIND SWATI , 71200915K , , PICT , T80053088

01. CONTROL SYSTEMS	PP	100	40	67	P
02. DIGITAL COMMUNICATION	PP	100	40	52	P
03. DIGITAL COMMUNICATION	PR	50	20	30	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	55	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	37	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	60	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	33	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	47	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	36	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	32	P

FIRST TERM TOTAL = 449/750.

ORDN. 1 MARKS :

T80053089 KHAIRE SHUBHAM SUNIL SANGITA , 71200921D , , PICT , T80053089

01. CONTROL SYSTEMS	PP	100	40	67	P
02. DIGITAL COMMUNICATION	PP	100	40	58	P
03. DIGITAL COMMUNICATION	PR	50	20	32	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	71	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	37	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	41	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	25	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	59	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	38	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	22	P

FIRST TERM TOTAL = 450/750.

ORDN. 1 MARKS :

T80053091 KORANNE RENUKA SUNIL SHUBHADA KORANNE , 71344363E , , PICT , T80053091

01. CONTROL SYSTEMS	PP	100	40	56	P
02. DIGITAL COMMUNICATION	PP	100	40	40	P
03. DIGITAL COMMUNICATION	PR	50	20	37	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	59	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	38	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	55	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	36	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	45	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	39	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	43	P

FIRST TERM TOTAL = 448/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80053092 KOTHARI GAURAO RAJENDRA VIJAYA , 71200932K , , PICT , T80053092

01. CONTROL SYSTEMS	PP	100	40	55	P
02. DIGITAL COMMUNICATION	PP	100	40	66	P
03. DIGITAL COMMUNICATION	PR	50	20	42	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	58	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	41	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	59	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	32	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	58	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	38	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	33	P

FIRST TERM TOTAL = 482/750.

ORDN. 1 MARKS :

T80053093 KRITI KESARWANI ALKA , 71200933H , , PICT , T80053093

01. CONTROL SYSTEMS	PP	100	40	60	P
02. DIGITAL COMMUNICATION	PP	100	40	51	P
03. DIGITAL COMMUNICATION	PR	50	20	35	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	74	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	39	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	55	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	28	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	57	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	39	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	28	P

FIRST TERM TOTAL = 466/750.

ORDN. 1 MARKS :

T80053094 KULKARNI PARIMAL SHRIKRISHNA VASUDHA , 71200936B , , PICT , T80053094

01. CONTROL SYSTEMS	PP	100	40	71	P
02. DIGITAL COMMUNICATION	PP	100	40	68	P
03. DIGITAL COMMUNICATION	PR	50	20	38	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	77	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	41	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	50	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	26	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	59	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	40	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	34	P

FIRST TERM TOTAL = 504/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80053095 KULKARNI VISHAL VINAYAK VARSHA , 71200939G , , PICT , T80053095

01. CONTROL SYSTEMS	PP	100	40	71	P
02. DIGITAL COMMUNICATION	PP	100	40	68	P
03. DIGITAL COMMUNICATION	PR	50	20	42	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	64	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	42	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	64	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	36	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	65	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	37	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	28	P

FIRST TERM TOTAL = 517/750.

ORDN. 1 MARKS :

T80053096 LADIKAR CHAITALI AVINASH NEETA , 71200942G , , PICT , T80053096

01. CONTROL SYSTEMS	PP	100	40	47	P
02. DIGITAL COMMUNICATION	PP	100	40	50	P
03. DIGITAL COMMUNICATION	PR	50	20	33	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	58	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	43	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	52	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	39	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	58	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	38	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	40	P

FIRST TERM TOTAL = 458/750.

ORDN. 1 MARKS :

T80053097 LASRADO VINOD WILFRED MARIA , 71200944C , , PICT , T80053097

01. CONTROL SYSTEMS	PP	100	40	62	P
02. DIGITAL COMMUNICATION	PP	100	40	70	P
03. DIGITAL COMMUNICATION	PR	50	20	46	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	81	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	43	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	63	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	41	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	70	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	39	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	45	P

FIRST TERM TOTAL = 560/750.

ORDN. 1 MARKS :



NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80053098 LOHIYA KRISHNA SUBHASH SUNITA , 71200945M , , PICT , T80053098

01. CONTROL SYSTEMS	PP	100	40	32	F
02. DIGITAL COMMUNICATION	PP	100	40	31	F
03. DIGITAL COMMUNICATION	PR	50	20	25	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	40	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	31	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	40	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	05	F
08. DIGITAL SIGNAL PROCESSING	PP	100	40	45	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	39	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	12	F

FIRST TERM TOTAL = 300/750.

ORDN. 1 MARKS :

T80053099 MANE MADHURI DILIP SHEELA , 71200954L , , PICT , T80053099

01. CONTROL SYSTEMS	PP	100	40	79	P
02. DIGITAL COMMUNICATION	PP	100	40	59	P
03. DIGITAL COMMUNICATION	PR	50	20	38	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	80	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	46	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	68	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	31	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	69	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	37	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	32	P

FIRST TERM TOTAL = 539/750.

ORDN. 1 MARKS :

T80053100 MANU MAKAR INDU , 71200958C , , PICT , T80053100

01. CONTROL SYSTEMS	PP	100	40	49	P
02. DIGITAL COMMUNICATION	PP	100	40	46	P
03. DIGITAL COMMUNICATION	PR	50	20	35	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	67	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	37	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	56	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	04	F
08. DIGITAL SIGNAL PROCESSING	PP	100	40	57	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	38	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	42	P

FIRST TERM TOTAL = 431/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80053101 MHETRE ROSHANEE RAJENDRA SANGITA , 71200960E , , PICT , T80053101

01. CONTROL SYSTEMS	PP	100	40	52	P
02. DIGITAL COMMUNICATION	PP	100	40	58	P
03. DIGITAL COMMUNICATION	PR	50	20	32	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	62	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	44	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	52	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	25	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	68	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	38	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	30	P

FIRST TERM TOTAL = 461/750.

ORDN. 1 MARKS :

T80053102 MIHIR BIDWAI URMILA , 71200961C , , PICT , T80053102

01. CONTROL SYSTEMS	PP	100	40	53	P
02. DIGITAL COMMUNICATION	PP	100	40	40	P
03. DIGITAL COMMUNICATION	PR	50	20	22	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	50	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	24	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	21	F
07. MICROCONTROLLERS & APPLICATION	PR	50	20	28	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	60	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	34	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	22	P

FIRST TERM TOTAL = 354/750.

ORDN. 1 MARKS :

T80053103 MUNNA KUMAR URMILADEVI , 71200970B , , PICT , T80053103

01. CONTROL SYSTEMS	PP	100	40	49	P
02. DIGITAL COMMUNICATION	PP	100	40	53	P
03. DIGITAL COMMUNICATION	PR	50	20	27	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	68	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	32	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	40	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	29	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	60	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	37	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	20	P

FIRST TERM TOTAL = 415/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80053104	NADEKAR SANJIV CHANDRAKANT			ALKA		, 71236321B		T80053071		PICT		T80053104
01. CONTROL SYSTEMS	PP	100	40	60	P C	11. SIGNAL CODING & ESTIMATION THEORY	PP	100	40	57	P C	
02. DIGITAL COMMUNICATION	PP	100	40	44	P C	12. SIGNAL CODING & ESTIMATION THEORY	PR	50	20	32	P C	
03. DIGITAL COMMUNICATION	PR	50	20	22	P C	13. SYSTEM PROGRA.& OPERATING SYS.	PP	100	40	58	P C	
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	50	P C	14. SYSTEM PROGRA.& OPERATING SYS.	TW	50	20	37	P C	
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	40	P C	15. COMPUTER ORGANIZATION & ARCHITEC	PP	100	40	51	P C	
06. MICROCONTROLLERS & APPLICATION	PP	100	40	44	P C	16. INDUSTRIAL MANAGEMENT	PP	100	40	48	P	
07. MICROCONTROLLERS & APPLICATION	PR	50	20	34	P C	17. WAVE THEORY & ANTENNA	PP	100	40	47	P C	
08. DIGITAL SIGNAL PROCESSING	PP	100	40	55	P C	18. WAVE THEORY & ANTENNA	PR	50	20	40	P	
09. DIGITAL SIGNAL PROCESSING	OR	50	20	35	P C	19. MINI PROJECT & SEMINAR	OR	50	20	40	P C	
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	27	P C	20. TEST & MEASUREMENT TECHNIQUES	OR	50	20	21	P C	

GRAND TOTAL = 842/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

T80053105	NAGARE PRERANA EKNATH			PUSHPA		, 71200975C				PICT		T80053105
01. CONTROL SYSTEMS	PP	100	40	73	P							
02. DIGITAL COMMUNICATION	PP	100	40	52	P							
03. DIGITAL COMMUNICATION	PR	50	20	20	P							
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	79	P							
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	41	P							
06. MICROCONTROLLERS & APPLICATION	PP	100	40	55	P							
07. MICROCONTROLLERS & APPLICATION	PR	50	20	25	P							
08. DIGITAL SIGNAL PROCESSING	PP	100	40	50	P							
09. DIGITAL SIGNAL PROCESSING	OR	50	20	38	P							
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	34	P							

FIRST TERM TOTAL = 467/750.

ORDN. 1 MARKS :

T80053106	NAIR PRATHMESH PRAKASH			PRIYA		, 71344364C				PICT		T80053106
01. CONTROL SYSTEMS	PP	100	40	50	P							
02. DIGITAL COMMUNICATION	PP	100	40	46	P							
03. DIGITAL COMMUNICATION	PR	50	20	42	P							
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	46	P							
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	41	P							
06. MICROCONTROLLERS & APPLICATION	PP	100	40	54	P							
07. MICROCONTROLLERS & APPLICATION	PR	50	20	29	P							
08. DIGITAL SIGNAL PROCESSING	PP	100	40	55	P							
09. DIGITAL SIGNAL PROCESSING	OR	50	20	40	P							
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	36	P							

FIRST TERM TOTAL = 439/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80053107 NAWATHALE MANOJ PANDURANG RAJANI , 71200982F , , PICT , T80053107

01. CONTROL SYSTEMS	PP	100	40	63	P
02. DIGITAL COMMUNICATION	PP	100	40	48	P
03. DIGITAL COMMUNICATION	PR	50	20	43	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	70	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	39	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	63	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	32	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	60	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	40	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	38	P

FIRST TERM TOTAL = 496/750.

ORDN. 1 MARKS :

T80053108 NEVPURKAR MANDAR SANTOSH ANAGHA , 71200985L , , PICT , T80053108

01. CONTROL SYSTEMS	PP	100	40	72	P
02. DIGITAL COMMUNICATION	PP	100	40	61	P
03. DIGITAL COMMUNICATION	PR	50	20	44	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	65	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	39	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	66	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	36	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	66	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	40	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	41	P

FIRST TERM TOTAL = 530/750.

ORDN. 1 MARKS :

T80053109 NIMISHA NITIN MISHRA KIRTI , 71205847J , , PICT , T80053109

01. CONTROL SYSTEMS	PP	100	40	64	P
02. DIGITAL COMMUNICATION	PP	100	40	60	P
03. DIGITAL COMMUNICATION	PR	50	20	43	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	70	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	40	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	70	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	39	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	62	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	37	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	43	P

FIRST TERM TOTAL = 528/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80053110	NITESH KR SINGH				RITA				, 70801532J				T80053154		PICT			T80053110
01. CONTROL SYSTEMS	PP	100	40	50	P C	11. SIGNAL CODING & ESTIMATION THEORY	PP	100	40	56	P							
02. DIGITAL COMMUNICATION	PP	100	40	42	P	12. SIGNAL CODING & ESTIMATION THEORY	PR	50	20	22	P C							
03. DIGITAL COMMUNICATION	PR	50	20	23	P C	13. SYSTEM PROGRA.& OPERATING SYS.	PP	100	40	40	P							
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	44	P C	14. SYSTEM PROGRA.& OPERATING SYS.	TW	50	20	26	P C							
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	22	P C	15. COMPUTER ORGANIZATION & ARCHITEC	PP	100	40	46	P C							
06. MICROCONTROLLERS & APPLICATION	PP	100	40	44	P C	16. INDUSTRIAL MANAGEMENT	PP	100	40	49	P C							
07. MICROCONTROLLERS & APPLICATION	PR	50	20	28	P C	17. WAVE THEORY & ANTENNA	PP	100	40	48	P C							
08. DIGITAL SIGNAL PROCESSING	PP	100	40	40	P C	18. WAVE THEORY & ANTENNA	PR	50	20	25	P C							
09. DIGITAL SIGNAL PROCESSING	OR	50	20	26	P C	19. MINI PROJECT & SEMINAR	OR	50	20	20	P C							
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	28	P C	20. TEST & MEASUREMENT TECHNIQUES	OR	50	20	30	P C							

GRAND TOTAL = 709/1500, RESULT: PASS CLASS

ORDN. 1 MARKS :

T80053111	OMKAR NAGESHKAR				KRUPA				, 71200991E						PICT			T80053111
01. CONTROL SYSTEMS	PP	100	40	60	P													
02. DIGITAL COMMUNICATION	PP	100	40	53	P													
03. DIGITAL COMMUNICATION	PR	50	20	44	P													
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	59	P													
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	39	P													
06. MICROCONTROLLERS & APPLICATION	PP	100	40	40	P													
07. MICROCONTROLLERS & APPLICATION	PR	50	20	39	P													
08. DIGITAL SIGNAL PROCESSING	PP	100	40	54	P													
09. DIGITAL SIGNAL PROCESSING	OR	50	20	40	P													
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	40	P													

FIRST TERM TOTAL = 468/750.

ORDN. 1 MARKS :

T80053112	OSWAL ARCHA AMRUT				ALKA OSWAL				, 71344365M						PICT			T80053112
01. CONTROL SYSTEMS	PP	100	40	48	P													
02. DIGITAL COMMUNICATION	PP	100	40	26	F													
03. DIGITAL COMMUNICATION	PR	50	20	08	F													
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	18	F													
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	36	P													
06. MICROCONTROLLERS & APPLICATION	PP	100	40	21	F													
07. MICROCONTROLLERS & APPLICATION	PR	50	20	24	P													
08. DIGITAL SIGNAL PROCESSING	PP	100	40	46	P													
09. DIGITAL SIGNAL PROCESSING	OR	50	20	25	P													
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	23	P													

FIRST TERM TOTAL = 275/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80053113	PANDILWAR RAMESHWAR KISHORE			SUJATA		, 71100928H		T80053083		PICT		T80053113
01. CONTROL SYSTEMS	PP	100	40	61	P C	11. SIGNAL CODING & ESTIMATION THEORY	PP	100	40	40	P C	
02. DIGITAL COMMUNICATION	PP	100	40	40	P C	12. SIGNAL CODING & ESTIMATION THEORY	PR	50	20	37	P C	
03. DIGITAL COMMUNICATION	PR	50	20	37	P C	13. SYSTEM PROGRA.& OPERATING SYS.	PP	100	40	46	P C	
04. NETWORK SYNTHESIS & FILTER DESIGN	NPP	100	40	51	P C	14. SYSTEM PROGRA.& OPERATING SYS.	TW	50	20	27	P C	
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	21	P C	15. COMPUTER ORGANIZATION & ARCHITEC	PP	100	40	44	P C	
06. MICROCONTROLLERS & APPLICATION	PP	100	40	41	P C	16. INDUSTRIAL MANAGEMENT	PP	100	40	58	P C	
07. MICROCONTROLLERS & APPLICATION	PR	50	20	34	P C	17. WAVE THEORY & ANTENNA	PP	100	40	58	P C	
08. DIGITAL SIGNAL PROCESSING	PP	100	40	43	P C	18. WAVE THEORY & ANTENNA	PR	50	20	25	P C	
09. DIGITAL SIGNAL PROCESSING	OR	50	20	41	P C	19. MINI PROJECT & SEMINAR	OR	50	20	AA	F	
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	22	P C	20. TEST & MEASUREMENT TECHNIQUES	OR	50	20	21	P C	

GRAND TOTAL = 747/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

T80053114	PANMAND SUHAS DASHRATH			HIRA		, 71344366K				PICT		T80053114
01. CONTROL SYSTEMS	PP	100	40	68	P							
02. DIGITAL COMMUNICATION	PP	100	40	68	P							
03. DIGITAL COMMUNICATION	PR	50	20	41	P							
04. NETWORK SYNTHESIS & FILTER DESIGN	NPP	100	40	81	P							
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	47	P							
06. MICROCONTROLLERS & APPLICATION	PP	100	40	63	P							
07. MICROCONTROLLERS & APPLICATION	PR	50	20	39	P							
08. DIGITAL SIGNAL PROCESSING	PP	100	40	74	P							
09. DIGITAL SIGNAL PROCESSING	OR	50	20	38	P							
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	42	P							

FIRST TERM TOTAL = 561/750.

ORDN. 1 MARKS :

T80053115	PARAKH BHUSHAN RAJENDRA			PUSHPA		, 71344367H				PICT		T80053115
01. CONTROL SYSTEMS	PP	100	40	73	P							
02. DIGITAL COMMUNICATION	PP	100	40	61	P							
03. DIGITAL COMMUNICATION	PR	50	20	36	P							
04. NETWORK SYNTHESIS & FILTER DESIGN	NPP	100	40	77	P							
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	43	P							
06. MICROCONTROLLERS & APPLICATION	PP	100	40	62	P							
07. MICROCONTROLLERS & APPLICATION	PR	50	20	31	P							
08. DIGITAL SIGNAL PROCESSING	PP	100	40	66	P							
09. DIGITAL SIGNAL PROCESSING	OR	50	20	37	P							
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	38	P							

FIRST TERM TOTAL = 524/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80053116 PATEL KINNARY VIPIN VINAYA PATEL , 71344368F , PICT , T80053116

01. CONTROL SYSTEMS	PP	100	40	73	P
02. DIGITAL COMMUNICATION	PP	100	40	70	P
03. DIGITAL COMMUNICATION	PR	50	20	41	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	81	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	44	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	78	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	43	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	69	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	38	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	37	P

FIRST TERM TOTAL = 574/750.

ORDN. 1 MARKS :

T80053117 PATIL AMOL HIRAMAN LATA , 71100937G , T80053086 , PICT , T80053117

01. CONTROL SYSTEMS	PP	100	40	58	P C	11. SIGNAL CODING & ESTIMATION THEORY	PP	100	40	40	P C
02. DIGITAL COMMUNICATION	PP	100	40	44	P C	12. SIGNAL CODING & ESTIMATION THEORY	PR	50	20	37	P C
03. DIGITAL COMMUNICATION	PR	50	20	27	P C	13. SYSTEM PROGRA.& OPERATING SYS.	PP	100	40	55	P C
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	45	P C	14. SYSTEM PROGRA.& OPERATING SYS.	TW	50	20	39	P C
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	41	P C	15. COMPUTER ORGANIZATION & ARCHITEC	PP	100	40	60	P C
06. MICROCONTROLLERS & APPLICATION	PP	100	40	54	P C	16. INDUSTRIAL MANAGEMENT	PP	100	40	61	P C
07. MICROCONTROLLERS & APPLICATION	PR	50	20	32	P C	17. WAVE THEORY & ANTENNA	PP	100	40	61	P C
08. DIGITAL SIGNAL PROCESSING	PP	100	40	45	P C	18. WAVE THEORY & ANTENNA	PR	50	20	40	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	35	P C	19. MINI PROJECT & SEMINAR	OR	50	20	37	P C
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	29	P C	20. TEST & MEASUREMENT TECHNIQUES	OR	50	20	34	P C

GRAND TOTAL = 874/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

T80053118 PATIL CHINAR SAMPATRAO PATIL SUVARNA SAMPAT , 71344369D , PICT , T80053118

01. CONTROL SYSTEMS	PP	100	40	49	P
02. DIGITAL COMMUNICATION	PP	100	40	54	P
03. DIGITAL COMMUNICATION	PR	50	20	36	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	59	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	39	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	63	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	30	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	57	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	41	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	40	P

FIRST TERM TOTAL = 468/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80053119 PATIL SAYALI ABHAY NILIMA , 71201009C , PICT , T80053119

01. CONTROL SYSTEMS	PP	100	40	74	P
02. DIGITAL COMMUNICATION	PP	100	40	48	P
03. DIGITAL COMMUNICATION	PR	50	20	32	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	75	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	41	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	55	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	34	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	64	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	41	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	36	P

FIRST TERM TOTAL = 500/750.

ORDN. 1 MARKS :

T80053120 PATIL TUSHAR TUKARAM KAMAL , 71201010G , PICT , T80053120

01. CONTROL SYSTEMS	PP	100	40	69	P
02. DIGITAL COMMUNICATION	PP	100	40	55	P
03. DIGITAL COMMUNICATION	PR	50	20	42	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	63	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	45	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	42	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	33	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	70	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	37	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	38	P

FIRST TERM TOTAL = 494/750.

ORDN. 1 MARKS :

T80053121 PAVAN TENKALE SHIVKANTA , 71100946F , T80053091 , PICT , T80053121

01. CONTROL SYSTEMS	PP	100	40	56	P C	11. SIGNAL CODING & ESTIMATION THEORY	PP	100	40	49	P
02. DIGITAL COMMUNICATION	PP	100	40	44	P C	12. SIGNAL CODING & ESTIMATION THEORY	PR	50	20	27	P
03. DIGITAL COMMUNICATION	PR	50	20	20	P C	13. SYSTEM PROGRA.& OPERATING SYS.	PP	100	40	48	P C
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	51	P C	14. SYSTEM PROGRA.& OPERATING SYS.	TW	50	20	28	P C
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	25	P C	15. COMPUTER ORGANIZATION & ARCHITEC	PP	100	40	45	P C
06. MICROCONTROLLERS & APPLICATION	PP	100	40	40	P C	16. INDUSTRIAL MANAGEMENT	PP	100	40	54	P C
07. MICROCONTROLLERS & APPLICATION	PR	50	20	22	P C	17. WAVE THEORY & ANTENNA	PP	100	40	44	P C
08. DIGITAL SIGNAL PROCESSING	PP	100	40	58	P C	18. WAVE THEORY & ANTENNA	PR	50	20	36	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	20	P C	19. MINI PROJECT & SEMINAR	OR	50	20	23	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	20	P C	20. TEST & MEASUREMENT TECHNIQUES	OR	50	20	25	P C

GRAND TOTAL = 735/1500, RESULT: PASS CLASS

ORDN. 1 MARKS :



NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80053122 PAWAR GAURAV GURUNATH JYOTSNA , 71201012C , , PICT , T80053122

01. CONTROL SYSTEMS	PP	100	40	65	P
02. DIGITAL COMMUNICATION	PP	100	40	50	P
03. DIGITAL COMMUNICATION	PR	50	20	34	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	68	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	44	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	54	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	21	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	60	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	35	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	42	P

FIRST TERM TOTAL = 473/750.

ORDN. 1 MARKS :

T80053123 PHADKE ASHWINI SHEKHAR ANJALI , 71201013M , , PICT , T80053123

01. CONTROL SYSTEMS	PP	100	40	58	P
02. DIGITAL COMMUNICATION	PP	100	40	49	P
03. DIGITAL COMMUNICATION	PR	50	20	39	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	54	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	36	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	55	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	25	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	46	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	34	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	32	P

FIRST TERM TOTAL = 428/750.

ORDN. 1 MARKS :

T80053124 PRANITA PRAMOD GHOLE SANGITA , 71201020D , , PICT , T80053124

01. CONTROL SYSTEMS	PP	100	40	74	P
02. DIGITAL COMMUNICATION	PP	100	40	58	P
03. DIGITAL COMMUNICATION	PR	50	20	35	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	64	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	43	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	66	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	30	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	54	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	39	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	35	P

FIRST TERM TOTAL = 498/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80053125 PRASHANT KUMAR LALITA DEVI , 71201021B , , PICT , T80053125

01. CONTROL SYSTEMS	PP	100	40	46	P
02. DIGITAL COMMUNICATION	PP	100	40	40	P
03. DIGITAL COMMUNICATION	PR	50	20	09	F
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	50	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	36	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	65	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	27	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	51	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	32	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	41	P

FIRST TERM TOTAL = 397/750.

ORDN. 1 MARKS :

T80053126 PRIYANKA BALRAJ R MANORAMA , 71201024G , , PICT , T80053126

01. CONTROL SYSTEMS	PP	100	40	77	P
02. DIGITAL COMMUNICATION	PP	100	40	62	P
03. DIGITAL COMMUNICATION	PR	50	20	42	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	74	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	42	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	70	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	35	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	52	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	37	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	25	P

FIRST TERM TOTAL = 516/750.

ORDN. 1 MARKS :

T80053127 PULEKAR ADITYA CHANDRASHEKHAR MADHAVI , 71201025E , , PICT , T80053127

01. CONTROL SYSTEMS	PP	100	40	69	P
02. DIGITAL COMMUNICATION	PP	100	40	65	P
03. DIGITAL COMMUNICATION	PR	50	20	43	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	62	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	35	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	58	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	26	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	56	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	37	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	32	P

FIRST TERM TOTAL = 483/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80053128 R GOKUL KRISHNAN R RAJESWARI , 71201026C , , PICT , T80053128

01. CONTROL SYSTEMS	PP	100	40	66	P
02. DIGITAL COMMUNICATION	PP	100	40	53	P
03. DIGITAL COMMUNICATION	PR	50	20	42	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	58	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	42	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	65	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	35	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	53	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	39	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	30	P

FIRST TERM TOTAL = 483/750.

ORDN. 1 MARKS :

T80053129 RADHIKA RAJE VIDYA , 71201027M , , PICT , T80053129

01. CONTROL SYSTEMS	PP	100	40	74	P
02. DIGITAL COMMUNICATION	PP	100	40	69	P
03. DIGITAL COMMUNICATION	PR	50	20	39	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	48	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	41	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	71	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	37	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	54	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	37	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	28	P

FIRST TERM TOTAL = 498/750.

ORDN. 1 MARKS :

T80053130 RAHEGAONKAR RUSHIKESH VIJAY LALITA , 71201028K , , PICT , T80053130

01. CONTROL SYSTEMS	PP	100	40	75	P
02. DIGITAL COMMUNICATION	PP	100	40	47	P
03. DIGITAL COMMUNICATION	PR	50	20	40	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	73	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	38	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	60	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	33	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	58	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	36	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	29	P

FIRST TERM TOTAL = 489/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80053131 RAHUL MAHIPATI KALE SUVARNA , 71201030M , , PICT , T80053131

01. CONTROL SYSTEMS	PP	100	40	75	P
02. DIGITAL COMMUNICATION	PP	100	40	56	P
03. DIGITAL COMMUNICATION	PR	50	20	38	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	56	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	42	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	52	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	28	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	44	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	37	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	35	P

FIRST TERM TOTAL = 463/750.

ORDN. 1 MARKS :

T80053132 RATHOD ABHIJIT KALURAM ALKA , 71201034D , , PICT , T80053132

01. CONTROL SYSTEMS	PP	100	40	71	P
02. DIGITAL COMMUNICATION	PP	100	40	24	F
03. DIGITAL COMMUNICATION	PR	50	20	30	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	40	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	35	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	52	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	23	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	30	F
09. DIGITAL SIGNAL PROCESSING	OR	50	20	38	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	30	P

FIRST TERM TOTAL = 373/750.

ORDN. 1 MARKS :

T80053133 RATHOD PRIYANKA NEMICHAND SHOBHA , 71201036L , , PICT , T80053133

01. CONTROL SYSTEMS	PP	100	40	40	P
02. DIGITAL COMMUNICATION	PP	100	40	40	P
03. DIGITAL COMMUNICATION	PR	50	20	33	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	41	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	36	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	48	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	24	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	40	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	36	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	22	P

FIRST TERM TOTAL = 360/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80053134	RATHOD VIJAY HIRAMAN				KANTA		, 71236326C		T80053100		PICT		T80053134
01. CONTROL SYSTEMS	PP	100	40	60	P C	11. SIGNAL CODING & ESTIMATION THEORY	PP	100	40	40	P C		
02. DIGITAL COMMUNICATION	PP	100	40	57	P C	12. SIGNAL CODING & ESTIMATION THEORY	PR	50	20	30	P		
03. DIGITAL COMMUNICATION	PR	50	20	28	P C	13. SYSTEM PROGRA.& OPERATING SYS.	PP	100	40	54	P C		
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	52	P C	14. SYSTEM PROGRA.& OPERATING SYS.	TW	50	20	38	P C		
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	39	P C	15. COMPUTER ORGANIZATION & ARCHITEC	PP	100	40	59	P C		
06. MICROCONTROLLERS & APPLICATION	PP	100	40	61	P C	16. INDUSTRIAL MANAGEMENT	PP	100	40	55	P C		
07. MICROCONTROLLERS & APPLICATION	PR	50	20	32	P C	17. WAVE THEORY & ANTENNA	PP	100	40	52	P C		
08. DIGITAL SIGNAL PROCESSING	PP	100	40	42	P C	18. WAVE THEORY & ANTENNA	PR	50	20	25	P C		
09. DIGITAL SIGNAL PROCESSING	OR	50	20	34	P C	19. MINI PROJECT & SEMINAR	OR	50	20	38	P C		
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	36	P C	20. TEST & MEASUREMENT TECHNIQUES	OR	50	20	30	P C		

GRAND TOTAL = 862/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

T80053135	RITAPURE KIRAN KAMLAKAR				SUVARNA		, 71344370H				PICT		T80053135
01. CONTROL SYSTEMS	PP	100	40	59	P								
02. DIGITAL COMMUNICATION	PP	100	40	51	P								
03. DIGITAL COMMUNICATION	PR	50	20	38	P								
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	43	P								
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	38	P								
06. MICROCONTROLLERS & APPLICATION	PP	100	40	63	P								
07. MICROCONTROLLERS & APPLICATION	PR	50	20	40	P								
08. DIGITAL SIGNAL PROCESSING	PP	100	40	42	P								
09. DIGITAL SIGNAL PROCESSING	OR	50	20	40	P								
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	36	P								

FIRST TERM TOTAL = 450/750.

ORDN. 1 MARKS :

T80053136	ROHANKAR AKSHAY ANILRAO				REKHA		, 71344371F				PICT		T80053136
01. CONTROL SYSTEMS	PP	100	40	64	P								
02. DIGITAL COMMUNICATION	PP	100	40	33	F								
03. DIGITAL COMMUNICATION	PR	50	20	37	P								
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	46	P								
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	39	P								
06. MICROCONTROLLERS & APPLICATION	PP	100	40	56	P								
07. MICROCONTROLLERS & APPLICATION	PR	50	20	05	F								
08. DIGITAL SIGNAL PROCESSING	PP	100	40	40	P								
09. DIGITAL SIGNAL PROCESSING	OR	50	20	36	P								
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	36	P								

FIRST TERM TOTAL = 392/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80053137 ROHIT GUPTA GEETA , 71201042E , , PICT , T80053137

01. CONTROL SYSTEMS	PP	100	40	69	P
02. DIGITAL COMMUNICATION	PP	100	40	45	P
03. DIGITAL COMMUNICATION	PR	50	20	28	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	54	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	27	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	62	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	06	F
08. DIGITAL SIGNAL PROCESSING	PP	100	40	55	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	35	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	14	F

FIRST TERM TOTAL = 395/750.

ORDN. 1 MARKS :

T80053138 ROKADE SUYOG SUDARSHAN SUREKHA , 71201043C , , PICT , T80053138

01. CONTROL SYSTEMS	PP	100	40	54	P
02. DIGITAL COMMUNICATION	PP	100	40	10	F
03. DIGITAL COMMUNICATION	PR	50	20	34	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	21	F
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	34	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	AA	F
07. MICROCONTROLLERS & APPLICATION	PR	50	20	05	F
08. DIGITAL SIGNAL PROCESSING	PP	100	40	45	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	27	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	30	P

FIRST TERM TOTAL = 260/750.

ORDN. 1 MARKS :

T80053139 RUTUJA S JADHAV BHARATI , 71201045K , , PICT , T80053139

01. CONTROL SYSTEMS	PP	100	40	70	P
02. DIGITAL COMMUNICATION	PP	100	40	58	P
03. DIGITAL COMMUNICATION	PR	50	20	32	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	53	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	38	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	62	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	37	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	49	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	27	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	34	P

FIRST TERM TOTAL = 460/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80053140	SALUNKHE MITALI KISHOR				BHARATI					, 71236327M			T80053105		PICT			T80053140
01. CONTROL SYSTEMS	PP	100	40	63	P C	11. SIGNAL CODING & ESTIMATION THEORY	PP	100	40	45	P C							
02. DIGITAL COMMUNICATION	PP	100	40	40	P C	12. SIGNAL CODING & ESTIMATION THEORY	PR	50	20	37	P							
03. DIGITAL COMMUNICATION	PR	50	20	26	P C	13. SYSTEM PROGRA.& OPERATING SYS.	PP	100	40	56	P C							
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	55	P C	14. SYSTEM PROGRA.& OPERATING SYS.	TW	50	20	37	P C							
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	40	P C	15. COMPUTER ORGANIZATION & ARCHITEC	PP	100	40	60	P C							
06. MICROCONTROLLERS & APPLICATION	PP	100	40	63	P C	16. INDUSTRIAL MANAGEMENT	PP	100	40	66	P C							
07. MICROCONTROLLERS & APPLICATION	PR	50	20	40	P C	17. WAVE THEORY & ANTENNA	PP	100	40	65	P C							
08. DIGITAL SIGNAL PROCESSING	PP	100	40	46	P C	18. WAVE THEORY & ANTENNA	PR	50	20	33	P C							
09. DIGITAL SIGNAL PROCESSING	OR	50	20	34	P C	19. MINI PROJECT & SEMINAR	OR	50	20	37	P C							
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	22	P C	20. TEST & MEASUREMENT TECHNIQUES	OR	50	20	30	P C							
GRAND TOTAL = 895+05/1500, RESULT: FIRST CLASS [0.2]																		

ORDN. 1 MARKS :

T80053141	SALVE PRAJAKTA BHAUSAHEB				MANGAL					, 71201051D					PICT			T80053141
01. CONTROL SYSTEMS	PP	100	40	65	P													
02. DIGITAL COMMUNICATION	PP	100	40	44	P													
03. DIGITAL COMMUNICATION	PR	50	20	37	P													
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	50	P													
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	40	P													
06. MICROCONTROLLERS & APPLICATION	PP	100	40	65	P													
07. MICROCONTROLLERS & APPLICATION	PR	50	20	43	P													
08. DIGITAL SIGNAL PROCESSING	PP	100	40	48	P													
09. DIGITAL SIGNAL PROCESSING	OR	50	20	29	P													
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	36	P													
FIRST TERM TOTAL = 457/750.																		

ORDN. 1 MARKS :

T80053142	SARWAR ABHIJIT ASHOK				MEERA					, 71100988M			T80053107		PICT			T80053142
01. CONTROL SYSTEMS	PP	100	40	51	P C	11. SIGNAL CODING & ESTIMATION THEORY	PP	100	40	54	P							
02. DIGITAL COMMUNICATION	PP	100	40	40	P C	12. SIGNAL CODING & ESTIMATION THEORY	PR	50	20	34	P C							
03. DIGITAL COMMUNICATION	PR	50	20	30	P C	13. SYSTEM PROGRA.& OPERATING SYS.	PP	100	40	46	P C							
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	40	P C	14. SYSTEM PROGRA.& OPERATING SYS.	TW	50	20	37	P C							
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	36	P C	15. COMPUTER ORGANIZATION & ARCHITEC	PP	100	40	44	P C							
06. MICROCONTROLLERS & APPLICATION	PP	100	40	42	P C	16. INDUSTRIAL MANAGEMENT	PP	100	40	58	P C							
07. MICROCONTROLLERS & APPLICATION	PR	50	20	30	P C	17. WAVE THEORY & ANTENNA	PP	100	40	67	P C							
08. DIGITAL SIGNAL PROCESSING	PP	100	40	42	P C	18. WAVE THEORY & ANTENNA	PR	50	20	32	P C							
09. DIGITAL SIGNAL PROCESSING	OR	50	20	36	P C	19. MINI PROJECT & SEMINAR	OR	50	20	35	P C							
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	28	P C	20. TEST & MEASUREMENT TECHNIQUES	OR	50	20	31	P C							

GRAND TOTAL = 813/1500, RESULT: SECOND CLASS

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80053143 SATPUTE SHIVRAJ PRAKASH VIJAYA , 71201055G , , PICT , T80053143

01. CONTROL SYSTEMS	PP	100	40	66	P
02. DIGITAL COMMUNICATION	PP	100	40	51	P
03. DIGITAL COMMUNICATION	PR	50	20	43	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	64	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	38	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	63	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	32	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	51	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	38	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	32	P

FIRST TERM TOTAL = 478/750.

ORDN. 1 MARKS :

T80053144 SAURABH NANGLIA SANDHYA , 71201056E , , PICT , T80053144

01. CONTROL SYSTEMS	PP	100	40	63	P
02. DIGITAL COMMUNICATION	PP	100	40	43	P
03. DIGITAL COMMUNICATION	PR	50	20	39	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	55	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	30	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	56	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	10	F
08. DIGITAL SIGNAL PROCESSING	PP	100	40	52	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	33	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	22	P

FIRST TERM TOTAL = 403/750.

ORDN. 1 MARKS :

T80053145 SAURAV KUMAR RAUSHAN DEVI , 71201057C , , PICT , T80053145

01. CONTROL SYSTEMS	PP	100	40	70	P
02. DIGITAL COMMUNICATION	PP	100	40	40	P
03. DIGITAL COMMUNICATION	PR	50	20	42	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	40	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	41	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	56	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	23	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	48	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	39	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	35	P

FIRST TERM TOTAL = 434/750.

ORDN. 1 MARKS :



NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80053146 SHAH PARIN LITESH BHAVYA , 71201060C , , PICT , T80053146

01. CONTROL SYSTEMS	PP	100	40	81	P
02. DIGITAL COMMUNICATION	PP	100	40	43	P
03. DIGITAL COMMUNICATION	PR	50	20	32	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	66	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	32	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	65	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	30	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	56	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	33	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	33	P

FIRST TERM TOTAL = 471/750.

ORDN. 1 MARKS :

T80053147 SHAHA SANKET MANOJKUMAR NEETA , 71100997L , , PICT , T80053147

01. CONTROL SYSTEMS	PP	100	40	65	P
02. DIGITAL COMMUNICATION	PP	100	40	43	P
03. DIGITAL COMMUNICATION	PR	50	20	44	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	46	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	40	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	48	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	32	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	47	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	35	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	36	P

FIRST TERM TOTAL = 436/750.

ORDN. 1 MARKS :

T80053148 SHELAR RAHUL BAPU MEENA , 71201065D , , PICT , T80053148

01. CONTROL SYSTEMS	PP	100	40	66	P
02. DIGITAL COMMUNICATION	PP	100	40	53	P
03. DIGITAL COMMUNICATION	PR	50	20	38	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	55	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	37	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	66	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	33	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	48	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	36	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	36	P

FIRST TERM TOTAL = 468/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80053149 SHELAR SAGAR PRAKASH REKHA , 71201066B , , PICT , T80053149

01. CONTROL SYSTEMS	PP	100	40	65	P
02. DIGITAL COMMUNICATION	PP	100	40	51	P
03. DIGITAL COMMUNICATION	PR	50	20	37	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	62	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	41	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	48	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	32	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	61	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	41	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	34	P

FIRST TERM TOTAL = 472/750.

ORDN. 1 MARKS :

T80053150 SHEP VISHWAJIT CHANDRAKANT URMILA , 71201068J , , PICT , T80053150

01. CONTROL SYSTEMS	PP	100	40	46	P
02. DIGITAL COMMUNICATION	PP	100	40	27	F
03. DIGITAL COMMUNICATION	PR	50	20	20	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	40	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	29	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	46	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	22	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	46	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	28	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	28	P

FIRST TERM TOTAL = 332/750.

ORDN. 1 MARKS :

T80053151 SHINDE SANIKA HANMANT SANGEETA , 71201071J , , PICT , T80053151

01. CONTROL SYSTEMS	PP	100	40	56	P
02. DIGITAL COMMUNICATION	PP	100	40	40	P
03. DIGITAL COMMUNICATION	PR	50	20	32	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	52	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	39	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	60	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	30	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	61	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	40	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	30	P

FIRST TERM TOTAL = 440/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80053152 SHINDE SIDDESH BALASAHEB ASHA , 71201072G , PICT , T80053152

01. CONTROL SYSTEMS	PP	100	40	78	P
02. DIGITAL COMMUNICATION	PP	100	40	51	P
03. DIGITAL COMMUNICATION	PR	50	20	38	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	77	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	44	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	76	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	32	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	73	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	42	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	33	P

FIRST TERM TOTAL = 544/750.

ORDN. 1 MARKS :

T80053153 SHINDE VAISHALI GULAB SHOBHA , 71344373B , PICT , T80053153

01. CONTROL SYSTEMS	PP	100	40	84	P
02. DIGITAL COMMUNICATION	PP	100	40	58	P
03. DIGITAL COMMUNICATION	PR	50	20	33	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	58	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	41	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	65	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	38	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	47	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	38	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	22	P

FIRST TERM TOTAL = 484/750.

ORDN. 1 MARKS :

T80053154 SHITRE SOURABH DNYANESHWAR SNEHA , 71101011M , T80053111 , PICT , T80053154

01. CONTROL SYSTEMS	PP	100	40	47	P C	11. SIGNAL CODING & ESTIMATION THEORY	PP	100	40	52	P
02. DIGITAL COMMUNICATION	PP	100	40	40	P C	12. SIGNAL CODING & ESTIMATION THEORY	PR	50	20	39	P C
03. DIGITAL COMMUNICATION	PR	50	20	42	P C	13. SYSTEM PROGRA.& OPERATING SYS.	PP	100	40	56	P C
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	46	P C	14. SYSTEM PROGRA.& OPERATING SYS.	TW	50	20	42	P C
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	43	P C	15. COMPUTER ORGANIZATION & ARCHITEC	PP	100	40	59	P C
06. MICROCONTROLLERS & APPLICATION	PP	100	40	48	P C	16. INDUSTRIAL MANAGEMENT	PP	100	40	50	P C
07. MICROCONTROLLERS & APPLICATION	PR	50	20	40	P C	17. WAVE THEORY & ANTENNA	PP	100	40	40	P C
08. DIGITAL SIGNAL PROCESSING	PP	100	40	46	P C	18. WAVE THEORY & ANTENNA	PR	50	20	38	P C
09. DIGITAL SIGNAL PROCESSING	OR	50	20	35	P C	19. MINI PROJECT & SEMINAR	OR	50	20	44	P C
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	38	P C	20. TEST & MEASUREMENT TECHNIQUES	OR	50	20	36	P C

GRAND TOTAL = 881/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.  
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80053155	SIDDHANT KHURANA				SAROJ					, 71101016B			T80053114		PICT			T80053155
01. CONTROL SYSTEMS	PP	100	40	40	P C	11. SIGNAL CODING & ESTIMATION THEORY	PP	100	40	42	P							
02. DIGITAL COMMUNICATION	PP	100	40	23	F	12. SIGNAL CODING & ESTIMATION THEORY	PR	50	20	35	P							
03. DIGITAL COMMUNICATION	PR	50	20	20	P C	13. SYSTEM PROGRA.& OPERATING SYS.	PP	100	40	45	P C							
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	42	P C	14. SYSTEM PROGRA.& OPERATING SYS.	TW	50	20	34	P C							
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	31	P C	15. COMPUTER ORGANIZATION & ARCHITEC	PP	100	40	44	P C							
06. MICROCONTROLLERS & APPLICATION	PP	100	40	49	P C	16. INDUSTRIAL MANAGEMENT	PP	100	40	54	P C							
07. MICROCONTROLLERS & APPLICATION	PR	50	20	24	P C	17. WAVE THEORY & ANTENNA	PP	100	40	40	P C							
08. DIGITAL SIGNAL PROCESSING	PP	100	40	57	P C	18. WAVE THEORY & ANTENNA	PR	50	20	21	P C							
09. DIGITAL SIGNAL PROCESSING	OR	50	20	24	P C	19. MINI PROJECT & SEMINAR	OR	50	20	26	P C							
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	32	P C	20. TEST & MEASUREMENT TECHNIQUES	OR	50	20	35	P							

GRAND TOTAL = 718/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

T80053156	SONAWANE YASHWANT DEEPAK				SEEMA					, 71201079D					PICT			T80053156
01. CONTROL SYSTEMS	PP	100	40	54	P													
02. DIGITAL COMMUNICATION	PP	100	40	57	P													
03. DIGITAL COMMUNICATION	PR	50	20	38	P													
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	47	P													
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	27	P													
06. MICROCONTROLLERS & APPLICATION	PP	100	40	58	P													
07. MICROCONTROLLERS & APPLICATION	PR	50	20	26	P													
08. DIGITAL SIGNAL PROCESSING	PP	100	40	53	P													
09. DIGITAL SIGNAL PROCESSING	OR	50	20	35	P													
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	30	P													

FIRST TERM TOTAL = 425/750.

ORDN. 1 MARKS :

T80053157	SONU SINGH				USHA					, 71045633G			T80053160		PICT			T80053157
01. CONTROL SYSTEMS	PP	100	40	60	P C	11. SIGNAL CODING & ESTIMATION THEORY	PP	100	40	43	P							
02. DIGITAL COMMUNICATION	PP	100	40	33#	P	12. SIGNAL CODING & ESTIMATION THEORY	PR	50	20	20	P							
03. DIGITAL COMMUNICATION	PR	50	20	29	P C	13. SYSTEM PROGRA.& OPERATING SYS.	PP	100	40	40	P C							
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	40	P C	14. SYSTEM PROGRA.& OPERATING SYS.	TW	50	20	27	P C							
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	31	P C	15. COMPUTER ORGANIZATION & ARCHITEC	PP	100	40	40	P C							
06. MICROCONTROLLERS & APPLICATION	PP	100	40	40	P C	16. INDUSTRIAL MANAGEMENT	PP	100	40	55	P C							
07. MICROCONTROLLERS & APPLICATION	PR	50	20	20	P C	17. WAVE THEORY & ANTENNA	PP	100	40	54	P C							
08. DIGITAL SIGNAL PROCESSING	PP	100	40	41	P C	18. WAVE THEORY & ANTENNA	PR	50	20	35	P C							
09. DIGITAL SIGNAL PROCESSING	OR	50	20	35	P C	19. MINI PROJECT & SEMINAR	OR	50	20	35	P C							
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	25	P C	20. TEST & MEASUREMENT TECHNIQUES	OR	50	20	30	P C							

GRAND TOTAL = 733/1500, RESULT: PASS CLASS # [0.4]

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80053158 SORATE TEJASWI TANAJI NALINI , 71344374L , , PICT , T80053158

01. CONTROL SYSTEMS	PP	100	40	66	P
02. DIGITAL COMMUNICATION	PP	100	40	50	P
03. DIGITAL COMMUNICATION	PR	50	20	38	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	74	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	38	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	71	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	41	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	59	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	31	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	33	P

FIRST TERM TOTAL = 501/750.

ORDN. 1 MARKS :

T80053159 SRISHTI GANJOO NISHA , 71201080H , , PICT , T80053159

01. CONTROL SYSTEMS	PP	100	40	64	P
02. DIGITAL COMMUNICATION	PP	100	40	63	P
03. DIGITAL COMMUNICATION	PR	50	20	40	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	64	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	43	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	77	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	24	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	70	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	36	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	38	P

FIRST TERM TOTAL = 519/750.

ORDN. 1 MARKS :

T80053160 SUSHIL KUMAR SHAKUNTALA DEVI , 71201085J , , PICT , T80053160

01. CONTROL SYSTEMS	PP	100	40	60	P
02. DIGITAL COMMUNICATION	PP	100	40	32	F
03. DIGITAL COMMUNICATION	PR	50	20	20	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	69	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	36	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	43	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	02	F
08. DIGITAL SIGNAL PROCESSING	PP	100	40	46	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	34	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	22	P

FIRST TERM TOTAL = 364/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80053161 TAMBE MAITREYEE MILIND MRUDULA , 71201090E , PICT , T80053161

01. CONTROL SYSTEMS	PP	100	40	66	P
02. DIGITAL COMMUNICATION	PP	100	40	45	P
03. DIGITAL COMMUNICATION	PR	50	20	35	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	80	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	47	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	65	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	35	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	71	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	38	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	38	P

FIRST TERM TOTAL = 520/750.

ORDN. 1 MARKS :

T80053162 TANDEL PRASAD SADANAND SNEHA , 71201092M , PICT , T80053162

01. CONTROL SYSTEMS	PP	100	40	77	P
02. DIGITAL COMMUNICATION	PP	100	40	55	P
03. DIGITAL COMMUNICATION	PR	50	20	44	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	80	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	38	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	75	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	43	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	64	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	37	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	34	P

FIRST TERM TOTAL = 547/750.

ORDN. 1 MARKS :

T80053163 TANNA VATSAL AJAY VAISHALI , 71236329H , T80053120 , PICT , T80053163

01. CONTROL SYSTEMS	PP	100	40	62	P C	11. SIGNAL CODING & ESTIMATION THEORY	PP	100	40	AA	F
02. DIGITAL COMMUNICATION	PP	100	40	AA	F	12. SIGNAL CODING & ESTIMATION THEORY	PR	50	20	29	P C
03. DIGITAL COMMUNICATION	PR	50	20	30	P C	13. SYSTEM PROGRA.& OPERATING SYS.	PP	100	40	70	P C
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	43	P C	14. SYSTEM PROGRA.& OPERATING SYS.	TW	50	20	31	P C
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	34	P C	15. COMPUTER ORGANIZATION & ARCHITEC	PP	100	40	54	P C
06. MICROCONTROLLERS & APPLICATION	PP	100	40	43	P C	16. INDUSTRIAL MANAGEMENT	PP	100	40	65	P C
07. MICROCONTROLLERS & APPLICATION	PR	50	20	23	P C	17. WAVE THEORY & ANTENNA	PP	100	40	40	P C
08. DIGITAL SIGNAL PROCESSING	PP	100	40	40	P C	18. WAVE THEORY & ANTENNA	PR	50	20	35	P C
09. DIGITAL SIGNAL PROCESSING	OR	50	20	30	P C	19. MINI PROJECT & SEMINAR	OR	50	20	34	P C
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	29	P C	20. TEST & MEASUREMENT TECHNIQUES	OR	50	20	34	P C

GRAND TOTAL = 726/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80053164 TATHE SAKET NANDKISHOR MEENA , 71201093K , , PICT , T80053164

01. CONTROL SYSTEMS	PP	100	40	72	P
02. DIGITAL COMMUNICATION	PP	100	40	50	P
03. DIGITAL COMMUNICATION	PR	50	20	37	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	56	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	33	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	69	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	26	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	45	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	35	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	35	P

FIRST TERM TOTAL = 458/750.

ORDN. 1 MARKS :

T80053165 TERKAR PRASAD VITTHALRAO ASHA , 71101030H , , PICT , T80053165

01. CONTROL SYSTEMS	PP	100	40	53	P
02. DIGITAL COMMUNICATION	PP	100	40	55	P
03. DIGITAL COMMUNICATION	PR	50	20	07	F
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	47	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	33	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	42	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	08	F
08. DIGITAL SIGNAL PROCESSING	PP	100	40	43	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	34	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	14	F

FIRST TERM TOTAL = 336/750.

ORDN. 1 MARKS :

T80053166 THAKARE APURV VIJAY MEENA , 71101031F , , PICT , T80053166

01. CONTROL SYSTEMS	PP	100	40	58	P
02. DIGITAL COMMUNICATION	PP	100	40	33	F
03. DIGITAL COMMUNICATION	PR	50	20	07	F
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	42	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	31	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	52	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	05	F
08. DIGITAL SIGNAL PROCESSING	PP	100	40	50	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	30	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	28	P

FIRST TERM TOTAL = 336/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80053167 TIKHE SHIRIN ABHAY SWATI , 71201096D , PICT , T80053167

01. CONTROL SYSTEMS	PP	100	40	64	P
02. DIGITAL COMMUNICATION	PP	100	40	49	P
03. DIGITAL COMMUNICATION	PR	50	20	33	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	71	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	38	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	60	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	26	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	56	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	29	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	32	P

FIRST TERM TOTAL = 458/750.

ORDN. 1 MARKS :

T80053168 TIWARI VIPUL TARUN PADMA , 71101038C , T80053125 , PICT , T80053168

01. CONTROL SYSTEMS	PP	100	40	61	P C	11. SIGNAL CODING & ESTIMATION THEORY	PP	100	40	40	P C
02. DIGITAL COMMUNICATION	PP	100	40	50	P C	12. SIGNAL CODING & ESTIMATION THEORY	PR	50	20	22	P
03. DIGITAL COMMUNICATION	PR	50	20	20	P C	13. SYSTEM PROGRA.& OPERATING SYS.	PP	100	40	70	P C
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	40	P C	14. SYSTEM PROGRA.& OPERATING SYS.	TW	50	20	34	P C
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	31	P C	15. COMPUTER ORGANIZATION & ARCHITEC	PP	100	40	53	P C
06. MICROCONTROLLERS & APPLICATION	PP	100	40	55	P C	16. INDUSTRIAL MANAGEMENT	PP	100	40	67	P C
07. MICROCONTROLLERS & APPLICATION	PR	50	20	23	P C	17. WAVE THEORY & ANTENNA	PP	100	40	63	P C
08. DIGITAL SIGNAL PROCESSING	PP	100	40	52	P C	18. WAVE THEORY & ANTENNA	PR	50	20	28	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	20	P C	19. MINI PROJECT & SEMINAR	OR	50	20	22	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	20	P C	20. TEST & MEASUREMENT TECHNIQUES	OR	50	20	22	P C

GRAND TOTAL = 793/1500, RESULT: SECOND CLASS

ORDN. 1 MARKS :

T80053169 TUPE NITIN BABASAHEB SANJIVANI , 71201098L , PICT , T80053169

01. CONTROL SYSTEMS	PP	100	40	60	P
02. DIGITAL COMMUNICATION	PP	100	40	33	F
03. DIGITAL COMMUNICATION	PR	50	20	38	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	45	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	40	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	57	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	21	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	AA	F
09. DIGITAL SIGNAL PROCESSING	OR	50	20	30	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	35	P

FIRST TERM TOTAL = 359/750.

ORDN. 1 MARKS :



NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80053170 UNDE MADHURI BALASAHEB LAXMI , 71344375J , , PICT , T80053170

01. CONTROL SYSTEMS	PP	100	40	62	P
02. DIGITAL COMMUNICATION	PP	100	40	28	F
03. DIGITAL COMMUNICATION	PR	50	20	08	F
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	42	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	37	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	58	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	30	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	53	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	34	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	30	P

FIRST TERM TOTAL = 382/750.

ORDN. 1 MARKS :

T80053171 UPADHYE VEDA UDAY MANISHA , 71201101D , , PICT , T80053171

01. CONTROL SYSTEMS	PP	100	40	79	P
02. DIGITAL COMMUNICATION	PP	100	40	59	P
03. DIGITAL COMMUNICATION	PR	50	20	42	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	63	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	42	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	68	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	24	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	69	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	38	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	32	P

FIRST TERM TOTAL = 516/750.

ORDN. 1 MARKS :

T80053172 USTAD SABA ABDUL SHAKOOR ATIYA , 71201102B , , PICT , T80053172

01. CONTROL SYSTEMS	PP	100	40	76	P
02. DIGITAL COMMUNICATION	PP	100	40	65	P
03. DIGITAL COMMUNICATION	PR	50	20	43	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	78	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	39	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	77	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	35	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	66	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	38	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	35	P

FIRST TERM TOTAL = 552/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80053173	VAIDYA SANDIP MILIND	SANGEETA					, 71101044H	, T80053127	PICT	, T80053173			
01. CONTROL SYSTEMS	PP	100	40	65	P C	11. SIGNAL CODING & ESTIMATION THEORY	PP	100	40	47	P		
02. DIGITAL COMMUNICATION	PP	100	40	42	P C	12. SIGNAL CODING & ESTIMATION THEORY	PR	50	20	38	P C		
03. DIGITAL COMMUNICATION	PR	50	20	35	P C	13. SYSTEM PROGRA.& OPERATING SYS.	PP	100	40	56	P C		
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	56	P C	14. SYSTEM PROGRA.& OPERATING SYS.	TW	50	20	40	P C		
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	42	P C	15. COMPUTER ORGANIZATION & ARCHITEC	PP	100	40	52	P C		
06. MICROCONTROLLERS & APPLICATION	PP	100	40	53	P C	16. INDUSTRIAL MANAGEMENT	PP	100	40	58	P C		
07. MICROCONTROLLERS & APPLICATION	PR	50	20	41	P C	17. WAVE THEORY & ANTENNA	PP	100	40	67	P C		
08. DIGITAL SIGNAL PROCESSING	PP	100	40	40	P C	18. WAVE THEORY & ANTENNA	PR	50	20	35	P C		
09. DIGITAL SIGNAL PROCESSING	OR	50	20	35	P C	19. MINI PROJECT & SEMINAR	OR	50	20	43	P C		
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	37	P C	20. TEST & MEASUREMENT TECHNIQUES	OR	50	20	40	P C		

GRAND TOTAL = 922/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

T80053174	VARADE RUSHIKESH DILIP	CHHAYA					, 71101045F	, T80053128	PICT	, T80053174			
01. CONTROL SYSTEMS	PP	100	40	46	P C		11. SIGNAL CODING & ESTIMATION THEORY	PP	100	40	40	P C	
02. DIGITAL COMMUNICATION	PP	100	40	27	F		12. SIGNAL CODING & ESTIMATION THEORY	PR	50	20	36	P C	
03. DIGITAL COMMUNICATION	PR	50	20	23	P C		13. SYSTEM PROGRA.& OPERATING SYS.	PP	100	40	53	P C	
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	40	P C		14. SYSTEM PROGRA.& OPERATING SYS.	TW	50	20	30	P C	
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	27	P C		15. COMPUTER ORGANIZATION & ARCHITEC	PP	100	40	53	P C	
06. MICROCONTROLLERS & APPLICATION	PP	100	40	40	P C		16. INDUSTRIAL MANAGEMENT	PP	100	40	60	P C	
07. MICROCONTROLLERS & APPLICATION	PR	50	20	35	P C		17. WAVE THEORY & ANTENNA	PP	100	40	49	P C	
08. DIGITAL SIGNAL PROCESSING	PP	100	40	47	P C		18. WAVE THEORY & ANTENNA	PR	50	20	22	P C	
09. DIGITAL SIGNAL PROCESSING	OR	50	20	40	P C		19. MINI PROJECT & SEMINAR	OR	50	20	36	P C	
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	29	P C		20. TEST & MEASUREMENT TECHNIQUES	OR	50	20	28	P C	

GRAND TOTAL = 761/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

T80053175	VIDHATE TRUPTI SOPAN				VARSHA		, 71101048L		, T80053130		PICT		, T80053175
01. CONTROL SYSTEMS	PP	100	40	52	P C		11. SIGNAL CODING & ESTIMATION THEORY	PP	100	40	40	P C	
02. DIGITAL COMMUNICATION	PP	100	40	40	P		12. SIGNAL CODING & ESTIMATION THEORY	PR	50	20	33	P C	
03. DIGITAL COMMUNICATION	PR	50	20	36	P C		13. SYSTEM PROGRA.& OPERATING SYS.	PP	100	40	47	P C	
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	40	P C		14. SYSTEM PROGRA.& OPERATING SYS.	TW	50	20	35	P C	
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	38	P C		15. COMPUTER ORGANIZATION & ARCHITEC	PP	100	40	40	P C	
06. MICROCONTROLLERS & APPLICATION	PP	100	40	41	P C		16. INDUSTRIAL MANAGEMENT	PP	100	40	46	P C	
07. MICROCONTROLLERS & APPLICATION	PR	50	20	27	P C		17. WAVE THEORY & ANTENNA	PP	100	40	47	P C	
08. DIGITAL SIGNAL PROCESSING	PP	100	40	45	P C		18. WAVE THEORY & ANTENNA	PR	50	20	32	P C	
09. DIGITAL SIGNAL PROCESSING	OR	50	20	30	P C		19. MINI PROJECT & SEMINAR	OR	50	20	36	P C	
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	27	P C		20. TEST & MEASUREMENT TECHNIQUES	OR	50	20	33	P C	

GRAND TOTAL = 765/1500, RESULT: SECOND CLASS

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80053176 VIKRANT SINGH SUMITRA , 71201110C , , PICT , T80053176

01. CONTROL SYSTEMS	PP	100	40	AA	F
02. DIGITAL COMMUNICATION	PP	100	40	31	F
03. DIGITAL COMMUNICATION	PR	50	20	23	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	53	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	28	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	15	F
07. MICROCONTROLLERS & APPLICATION	PR	50	20	03	F
08. DIGITAL SIGNAL PROCESSING	PP	100	40	48	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	37	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	22	P

FIRST TERM TOTAL = 260/750.

ORDN. 1 MARKS :

T80053177 VISHAL VATSA ASHA , 71201111M , , PICT , T80053177

01. CONTROL SYSTEMS	PP	100	40	66	P
02. DIGITAL COMMUNICATION	PP	100	40	40	P
03. DIGITAL COMMUNICATION	PR	50	20	36	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	65	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	36	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	46	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	30	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	50	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	35	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	35	P

FIRST TERM TOTAL = 439/750.

ORDN. 1 MARKS :

T80053178 WADIKAR MUGDHA RAVINDRA MEENAL , 71201113H , , PICT , T80053178

01. CONTROL SYSTEMS	PP	100	40	72	P
02. DIGITAL COMMUNICATION	PP	100	40	43	P
03. DIGITAL COMMUNICATION	PR	50	20	41	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	58	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	37	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	68	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	30	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	55	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	35	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	37	P

FIRST TERM TOTAL = 476/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80053179 WARPE HRUSHEEKESH SUNIL SMITA , 71201119G , , PICT , T80053179

01. CONTROL SYSTEMS	PP	100	40	66	P
02. DIGITAL COMMUNICATION	PP	100	40	55	P
03. DIGITAL COMMUNICATION	PR	50	20	07	F
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	55	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	36	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	56	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	30	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	60	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	35	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	36	P

FIRST TERM TOTAL = 436/750.

ORDN. 1 MARKS :

T80053180 WAYKOS ABHIJIT JINCHANDRA NAYANA , 71201120L , , PICT , T80053180

01. CONTROL SYSTEMS	PP	100	40	71	P
02. DIGITAL COMMUNICATION	PP	100	40	67	P
03. DIGITAL COMMUNICATION	PR	50	20	42	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	70	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	36	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	72	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	10	F
08. DIGITAL SIGNAL PROCESSING	PP	100	40	59	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	36	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	35	P

FIRST TERM TOTAL = 498/750.

ORDN. 1 MARKS :

T80053181 YEMULA AKHIL MANJULA , 71201123E , , PICT , T80053181

01. CONTROL SYSTEMS	PP	100	40	62	P
02. DIGITAL COMMUNICATION	PP	100	40	53	P
03. DIGITAL COMMUNICATION	PR	50	20	35	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	61	P
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	34	P
06. MICROCONTROLLERS & APPLICATION	PP	100	40	63	P
07. MICROCONTROLLERS & APPLICATION	PR	50	20	32	P
08. DIGITAL SIGNAL PROCESSING	PP	100	40	71	P
09. DIGITAL SIGNAL PROCESSING	OR	50	20	35	P
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	33	P

FIRST TERM TOTAL = 479/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80053182 YEOLE PRAJAKT PRAMOD JYOTI , 71236332H , T80053134 , PICT , T80053182

01. CONTROL SYSTEMS	PP	100	40	AA	F
02. DIGITAL COMMUNICATION	PP	100	40	AA	F
03. DIGITAL COMMUNICATION	PR	50	20	32	P
04. NETWORK SYNTHESIS & FILTER DESIGN	PP	100	40	AA	F
05. NETWORK SYNTHESIS & FILTER DESIGN	TW	50	20	20	P C
06. MICROCONTROLLERS & APPLICATION	PP	100	40	AA	F
07. MICROCONTROLLERS & APPLICATION	PR	50	20	22	P C
08. DIGITAL SIGNAL PROCESSING	PP	100	40	AA	F
09. DIGITAL SIGNAL PROCESSING	OR	50	20	35	P C
10. ELECTRONIC DESIGN PRACTICE	OR	50	20	27	P C

FIRST TERM TOTAL = 136/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80054201 AADITYA RAMESH DINAZ , 71200745J , , PICT , T80054201

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	60	P
02. DATA COMMUNICATION	PP	100	40	69	P
03. MICROPROCESSORS & MICROCONTROLLERPP		100	40	58	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	57	P
05. THEORY OF COMPUTATION	PP	100	40	66	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	35	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	39	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	19	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	40	P
10. HARDWARE LABORATORY	TW	25	10	19	P
11. HARDWARE LABORATORY	PR	50	20	38	P

FIRST TERM TOTAL = 500/750.

ORDN. 1 MARKS :

T80054202 ABHINAV KAUL SUREKHA , 71200747E , , PICT , T80054202

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	50	P
02. DATA COMMUNICATION	PP	100	40	47	P
03. MICROPROCESSORS & MICROCONTROLLERPP		100	40	22	F
04. DIGITAL SIGNAL PROCESSING	PP	100	40	24	F
05. THEORY OF COMPUTATION	PP	100	40	40	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	36	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	31	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	18	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	30	P
10. HARDWARE LABORATORY	TW	25	10	15	P
11. HARDWARE LABORATORY	PR	50	20	30	P

FIRST TERM TOTAL = 343/750.

ORDN. 1 MARKS :

T80054203 ABHYANKAR RUCHA RHISHIKESH ARTI , 71200749M , , PICT , T80054203

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	60	P
02. DATA COMMUNICATION	PP	100	40	63	P
03. MICROPROCESSORS & MICROCONTROLLERPP		100	40	52	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	62	P
05. THEORY OF COMPUTATION	PP	100	40	64	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	40	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	43	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	21	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	42	P
10. HARDWARE LABORATORY	TW	25	10	20	P
11. HARDWARE LABORATORY	PR	50	20	40	P

FIRST TERM TOTAL = 507/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80054204 ABNAVE OMKAR RAJENDRA SHOBHA , 71045358C , T80054202 , PICT , T80054204

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	51	P	C
02. DATA COMMUNICATION	PP	100	40	48	P	C
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	AA	F	
04. DIGITAL SIGNAL PROCESSING	PP	100	40	AA	F	
05. THEORY OF COMPUTATION	PP	100	40	AA	F	
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	21	P	C
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	AA	F	
08. SIGNAL PROCESSING LABORATORY	TW	25	10	10	P	C
09. SIGNAL PROCESSING LABORATORY	OR	50	20	AA	F	
10. HARDWARE LABORATORY	TW	25	10	15	P	C
11. HARDWARE LABORATORY	PR	50	20	AA	F	

FIRST TERM TOTAL = 145/750.

ORDN. 1 MARKS :

T80054205 AJINKYA DNYANESHWAR RAJPUT MEENAKSHI , 71200753K , , PICT , T80054205

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	58	P	
02. DATA COMMUNICATION	PP	100	40	60	P	
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	53	P	
04. DIGITAL SIGNAL PROCESSING	PP	100	40	40	P	
05. THEORY OF COMPUTATION	PP	100	40	40	P	
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	36	P	
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	38	P	
08. SIGNAL PROCESSING LABORATORY	TW	25	10	16	P	
09. SIGNAL PROCESSING LABORATORY	OR	50	20	30	P	
10. HARDWARE LABORATORY	TW	25	10	14	P	
11. HARDWARE LABORATORY	PR	50	20	40	P	

FIRST TERM TOTAL = 425/750.

ORDN. 1 MARKS :

T80054206 AKASH MAHAPATRA SUJATA , 71200754H , , PICT , T80054206

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	55	P	
02. DATA COMMUNICATION	PP	100	40	56	P	
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	40	P	
04. DIGITAL SIGNAL PROCESSING	PP	100	40	47	P	
05. THEORY OF COMPUTATION	PP	100	40	50	P	
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	42	P	
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	41	P	
08. SIGNAL PROCESSING LABORATORY	TW	25	10	22	P	
09. SIGNAL PROCESSING LABORATORY	OR	50	20	27	P	
10. HARDWARE LABORATORY	TW	25	10	21	P	
11. HARDWARE LABORATORY	PR	50	20	34	P	

FIRST TERM TOTAL = 435/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80054207 AMAN RATHI GEETA , 71200759J , , PICT , T80054207

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	55	P
02. DATA COMMUNICATION	PP	100	40	60	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	46	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	20	F
05. THEORY OF COMPUTATION	PP	100	40	AA	F
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	39	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	38	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	19	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	22	P
10. HARDWARE LABORATORY	TW	25	10	20	P
11. HARDWARE LABORATORY	PR	50	20	20	P

FIRST TERM TOTAL = 339/750.

ORDN. 1 MARKS :

T80054208 AMAN SINGH KIRAN , 71200760B , , PICT , T80054208

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	53	P
02. DATA COMMUNICATION	PP	100	40	66	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	50	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	22	F
05. THEORY OF COMPUTATION	PP	100	40	17	F
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	24	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	30	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	13	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	28	P
10. HARDWARE LABORATORY	TW	25	10	13	P
11. HARDWARE LABORATORY	PR	50	20	40	P

FIRST TERM TOTAL = 356/750.

ORDN. 1 MARKS :

T80054209 ANIRUDH SUDARSHAN PADMAPRIYA , 71200762J , , PICT , T80054209

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	58	P
02. DATA COMMUNICATION	PP	100	40	64	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	53	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	69	P
05. THEORY OF COMPUTATION	PP	100	40	77	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	39	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	42	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	17	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	24	P
10. HARDWARE LABORATORY	TW	25	10	16	P
11. HARDWARE LABORATORY	PR	50	20	38	P

FIRST TERM TOTAL = 497/750.

ORDN. 1 MARKS :



NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80054210 ANTHONY ELIZABETH XAVIER ANJALI , 71347755F , , PICT , T80054210

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	46	P
02. DATA COMMUNICATION	PP	100	40	43	P
03. MICROPROCESSORS & MICROCONTROLLERPP	PP	100	40	47	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	47	P
05. THEORY OF COMPUTATION	PP	100	40	54	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	45	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	45	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	22	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	37	P
10. HARDWARE LABORATORY	TW	25	10	22	P
11. HARDWARE LABORATORY	PR	50	20	36	P

FIRST TERM TOTAL = 444/750.

ORDN. 1 MARKS :

T80054211 ANVEKAR ASHWINI KISHOR KANCHANA , 71200763G , , PICT , T80054211

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	60	P
02. DATA COMMUNICATION	PP	100	40	46	P
03. MICROPROCESSORS & MICROCONTROLLERPP	PP	100	40	47	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	57	P
05. THEORY OF COMPUTATION	PP	100	40	54	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	43	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	45	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	23	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	44	P
10. HARDWARE LABORATORY	TW	25	10	21	P
11. HARDWARE LABORATORY	PR	50	20	41	P

FIRST TERM TOTAL = 481/750.

ORDN. 1 MARKS :

T80054212 BAGDIYA TUSHAR VIJAYCHAND VEENA , 71200771H , , PICT , T80054212

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	51	P
02. DATA COMMUNICATION	PP	100	40	44	P
03. MICROPROCESSORS & MICROCONTROLLERPP	PP	100	40	58	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	61	P
05. THEORY OF COMPUTATION	PP	100	40	71	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	36	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	38	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	17	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	35	P
10. HARDWARE LABORATORY	TW	25	10	16	P
11. HARDWARE LABORATORY	PR	50	20	42	P

FIRST TERM TOTAL = 469/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80054213 BAGUL PALLAVI DEEPAK MADHAVI , 71200772F , , PICT , T80054213

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	52	P
02. DATA COMMUNICATION	PP	100	40	42	P
03. MICROPROCESSORS & MICROCONTROLLERPP		100	40	46	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	74	P
05. THEORY OF COMPUTATION	PP	100	40	52	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	39	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	32	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	21	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	30	P
10. HARDWARE LABORATORY	TW	25	10	22	P
11. HARDWARE LABORATORY	PR	50	20	16	F

FIRST TERM TOTAL = 426/750.

ORDN. 1 MARKS :

T80054214 BAHETI BHUSHAN RAJGOPAL KIRAN , 71200773D , , PICT , T80054214

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	49	P
02. DATA COMMUNICATION	PP	100	40	51	P
03. MICROPROCESSORS & MICROCONTROLLERPP		100	40	40	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	59	P
05. THEORY OF COMPUTATION	PP	100	40	54	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	36	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	40	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	19	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	41	P
10. HARDWARE LABORATORY	TW	25	10	19	P
11. HARDWARE LABORATORY	PR	50	20	42	P

FIRST TERM TOTAL = 450/750.

ORDN. 1 MARKS :

T80054215 BAJAJ KRISHNA ANJANIKUMAR VARSHA , 71200775L , , PICT , T80054215

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	62	P
02. DATA COMMUNICATION	PP	100	40	56	P
03. MICROPROCESSORS & MICROCONTROLLERPP		100	40	41	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	63	P
05. THEORY OF COMPUTATION	PP	100	40	60	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	39	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	38	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	21	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	43	P
10. HARDWARE LABORATORY	TW	25	10	20	P
11. HARDWARE LABORATORY	PR	50	20	37	P

FIRST TERM TOTAL = 480/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80054216 BALIWANT SOURABH SANJAY KALPANA , 71100741B , T80054206 , PICT , T80054216

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	43	P C	12. PRINCIPLES OF PROGRAMMING LANG.	PP	100	40	59	P C
02. DATA COMMUNICATION	PP	100	40	61	P C	13. COMPUTER NETWORKS	PP	100	40	43	P C
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	45	P C	14. FINANCE & MANAGEMENT INFORMA.SYS.	PP	100	40	55	P C
04. DIGITAL SIGNAL PROCESSING	PP	100	40	44	P C	15. SYSTEMS PROGRAMMING & OPERA.SYS.	PP	100	40	54	P C
05. THEORY OF COMPUTATION	PP	100	40	44	P C	16. SOFTWARE ENGINEERING	PP	100	40	54	P C
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	33	P C	17. SOFTWARE LABORATORY	TW	25	10	19	P C
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	44	P C	18. SOFTWARE LABORATORY	PR	50	20	28	P C
08. SIGNAL PROCESSING LABORATORY	TW	25	10	16	P C	19. COMPUTER NETWORK	TW	25	10	20	P C
09. SIGNAL PROCESSING LABORATORY	OR	50	20	22	P C	20. COMPUTER NETWORK	OR	50	20	37	P
10. HARDWARE LABORATORY	TW	25	10	18	P C	21. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	38	P C
11. HARDWARE LABORATORY	PR	50	20	30	P C	22. SEMINAR AND TECHNICAL COMMUNI.	TW	50	20	38	P C

GRAND TOTAL = 845/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

T80054217 BANG VIJAY SANTOSHKUMAR JYOTI , 71200780G , , PICT , T80054217

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	62	P
02. DATA COMMUNICATION	PP	100	40	57	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	48	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	64	P
05. THEORY OF COMPUTATION	PP	100	40	57	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	41	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	44	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	22	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	45	P
10. HARDWARE LABORATORY	TW	25	10	22	P
11. HARDWARE LABORATORY	PR	50	20	40	P

FIRST TERM TOTAL = 502/750.

ORDN. 1 MARKS :

T80054218 BANGAD ANKITA ISHWARPRASAD USHA , 71200781E , , PICT , T80054218

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	61	P
02. DATA COMMUNICATION	PP	100	40	52	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	46	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	73	P
05. THEORY OF COMPUTATION	PP	100	40	52	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	35	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	40	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	21	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	40	P
10. HARDWARE LABORATORY	TW	25	10	21	P
11. HARDWARE LABORATORY	PR	50	20	41	P

FIRST TERM TOTAL = 482/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80054219 BAT PANKAJ RAMSINGH PUSHPA , 71200786F , , PICT , T80054219

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	49	P
02. DATA COMMUNICATION	PP	100	40	41	P
03. MICROPROCESSORS & MICROCONTROLLERPP	PP	100	40	40	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	48	P
05. THEORY OF COMPUTATION	PP	100	40	46	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	38	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	40	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	20	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	40	P
10. HARDWARE LABORATORY	TW	25	10	19	P
11. HARDWARE LABORATORY	PR	50	20	36	P

FIRST TERM TOTAL = 417/750.

ORDN. 1 MARKS :

T80054220 BELE ANAND PURUSHOTTAM UJJWALA , 71347756D , , PICT , T80054220

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	52	P
02. DATA COMMUNICATION	PP	100	40	58	P
03. MICROPROCESSORS & MICROCONTROLLERPP	PP	100	40	47	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	62	P
05. THEORY OF COMPUTATION	PP	100	40	65	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	37	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	45	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	21	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	28	P
10. HARDWARE LABORATORY	TW	25	10	19	P
11. HARDWARE LABORATORY	PR	50	20	32	P

FIRST TERM TOTAL = 466/750.

ORDN. 1 MARKS :

T80054221 BHADKAMKAR SIDDHARTH CHARUDATTA SNEHA , 71200789L , , PICT , T80054221

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	58	P
02. DATA COMMUNICATION	PP	100	40	55	P
03. MICROPROCESSORS & MICROCONTROLLERPP	PP	100	40	40	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	75	P
05. THEORY OF COMPUTATION	PP	100	40	68	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	38	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	40	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	19	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	25	P
10. HARDWARE LABORATORY	TW	25	10	19	P
11. HARDWARE LABORATORY	PR	50	20	35	P

FIRST TERM TOTAL = 472/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.  
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80054222 BHALERAO SHRADDHA VINOD SUNITA , 71347757B , , PICT , T80054222

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	52	P
02. DATA COMMUNICATION	PP	100	40	40	P
03. MICROPROCESSORS & MICROCONTROLLERPP		100	40	29	F
04. DIGITAL SIGNAL PROCESSING	PP	100	40	42	P
05. THEORY OF COMPUTATION	PP	100	40	44	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	43	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	43	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	22	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	38	P
10. HARDWARE LABORATORY	TW	25	10	22	P
11. HARDWARE LABORATORY	PR	50	20	26	P

FIRST TERM TOTAL = 401/750.

ORDN. 1 MARKS :

T80054223 BHAWSAR SHOBHANA JAYVANT JAYSHREE , 71347758L , , PICT , T80054223

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	57	P
02. DATA COMMUNICATION	PP	100	40	50	P
03. MICROPROCESSORS & MICROCONTROLLERPP		100	40	51	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	66	P
05. THEORY OF COMPUTATION	PP	100	40	52	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	38	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	40	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	18	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	40	P
10. HARDWARE LABORATORY	TW	25	10	18	P
11. HARDWARE LABORATORY	PR	50	20	39	P

FIRST TERM TOTAL = 469/750.

ORDN. 1 MARKS :

T80054224 BHOIR RUPALI LAXMAN VANITA , 71045390G , , PICT , T80054224

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	20	F
02. DATA COMMUNICATION	PP	100	40	27	F
03. MICROPROCESSORS & MICROCONTROLLERPP		100	40	18	F
04. DIGITAL SIGNAL PROCESSING	PP	100	40	23	F
05. THEORY OF COMPUTATION	PP	100	40	16	F
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	32	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	40	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	18	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	08	F
10. HARDWARE LABORATORY	TW	25	10	18	P
11. HARDWARE LABORATORY	PR	50	20	10	F

FIRST TERM TOTAL = 230/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.  
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80054225 BHOMBE AMOL SONAJI KOKILA , 70701380B , T80054216 , PICT , T80054225

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	45	P	C	12. PRINCIPLES OF PROGRAMMING LANG.	PP	100	40	53	P	C
02. DATA COMMUNICATION	PP	100	40	55	P	C	13. COMPUTER NETWORKS	PP	100	40	54	P	C
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	52	P	C	14. FINANCE & MANAGEMENT INFORMA.SYS.	PP	100	40	59	P	C
04. DIGITAL SIGNAL PROCESSING	PP	100	40	45	P	C	15. SYSTEMS PROGRAMMING & OPERA.SYS.	PP	100	40	62	P	C
05. THEORY OF COMPUTATION	PP	100	40	42	P	C	16. SOFTWARE ENGINEERING	PP	100	40	59	P	C
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	21	P	C	17. SOFTWARE LABORATORY	TW	25	10	12	P	C
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	34	P	C	18. SOFTWARE LABORATORY	PR	50	20	23	P	C
08. SIGNAL PROCESSING LABORATORY	TW	25	10	13	P	C	19. COMPUTER NETWORK	TW	25	10	13	P	C
09. SIGNAL PROCESSING LABORATORY	OR	50	20	21	P	C	20. COMPUTER NETWORK	OR	50	20	35	P	
10. HARDWARE LABORATORY	TW	25	10	13	P	C	21. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	23	P	C
11. HARDWARE LABORATORY	PR	50	20	20	P	C	22. SEMINAR AND TECHNICAL COMMUNI.	TW	50	20	31	P	C

GRAND TOTAL = 785/1500, RESULT: SECOND CLASS

ORDN. 1 MARKS :

T80054226 BHOSALE SHITAL MANOHAR JAYA , 71132426D , T80054347 , PICT , T80054226

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	45	P	C	12. PRINCIPLES OF PROGRAMMING LANG.	PP	100	40	40	P	C
02. DATA COMMUNICATION	PP	100	40	40	P	C	13. COMPUTER NETWORKS	PP	100	40	47	P	C
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	40	P	C	14. FINANCE & MANAGEMENT INFORMA.SYS.	PP	100	40	46	P	C
04. DIGITAL SIGNAL PROCESSING	PP	100	40	AA	F		15. SYSTEMS PROGRAMMING & OPERA.SYS.	PP	100	40	40	P	C
05. THEORY OF COMPUTATION	PP	100	40	40	P	C	16. SOFTWARE ENGINEERING	PP	100	40	40	P	C
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	34	P	C	17. SOFTWARE LABORATORY	TW	25	10	15	P	C
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	22	P	C	18. SOFTWARE LABORATORY	PR	50	20	25	P	C
08. SIGNAL PROCESSING LABORATORY	TW	25	10	15	P	C	19. COMPUTER NETWORK	TW	25	10	18	P	C
09. SIGNAL PROCESSING LABORATORY	OR	50	20	22	P	C	20. COMPUTER NETWORK	OR	50	20	21	P	C
10. HARDWARE LABORATORY	TW	25	10	18	P	C	21. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	27	P	C
11. HARDWARE LABORATORY	PR	50	20	23	P	C	22. SEMINAR AND TECHNICAL COMMUNI.	TW	50	20	34	P	C

GRAND TOTAL = 652/1500, RESULT: FAILS A.T.K.T.

RESULT RESERVED FOR BKLG

ORDN. 1 MARKS :

T80054227 BHUTADA RUTUSHA KAMALKISHOR SUNITA , 71200798K , PICT , T80054227

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	59	P	
02. DATA COMMUNICATION	PP	100	40	60	P	
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	43	P	
04. DIGITAL SIGNAL PROCESSING	PP	100	40	80	P	
05. THEORY OF COMPUTATION	PP	100	40	72	P	
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	44	P	
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	43	P	
08. SIGNAL PROCESSING LABORATORY	TW	25	10	24	P	
09. SIGNAL PROCESSING LABORATORY	OR	50	20	41	P	
10. HARDWARE LABORATORY	TW	25	10	22	P	
11. HARDWARE LABORATORY	PR	50	20	40	P	

FIRST TERM TOTAL = 528/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80054228 BINO JOSEPH JASMINE , 71200800E , , PICT , T80054228

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	61	P
02. DATA COMMUNICATION	PP	100	40	63	P
03. MICROPROCESSORS & MICROCONTROLLERPP		100	40	52	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	79	P
05. THEORY OF COMPUTATION	PP	100	40	70	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	40	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	46	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	21	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	28	P
10. HARDWARE LABORATORY	TW	25	10	21	P
11. HARDWARE LABORATORY	PR	50	20	41	P

FIRST TERM TOTAL = 522/750.

ORDN. 1 MARKS :

T80054229 BORSE AKSHAY RAJENDRA SUREKHA , 71200804H , , PICT , T80054229

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	54	P
02. DATA COMMUNICATION	PP	100	40	44	P
03. MICROPROCESSORS & MICROCONTROLLERPP		100	40	42	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	74	P
05. THEORY OF COMPUTATION	PP	100	40	54	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	35	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	43	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	20	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	30	P
10. HARDWARE LABORATORY	TW	25	10	19	P
11. HARDWARE LABORATORY	PR	50	20	01	F

FIRST TERM TOTAL = 416/750.

ORDN. 1 MARKS :

T80054230 BUDHAWANT MAYUR SHIVAJI HIRABAI , 71200806D , , PICT , T80054230

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	59	P
02. DATA COMMUNICATION	PP	100	40	53	P
03. MICROPROCESSORS & MICROCONTROLLERPP		100	40	50	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	65	P
05. THEORY OF COMPUTATION	PP	100	40	61	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	42	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	42	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	21	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	42	P
10. HARDWARE LABORATORY	TW	25	10	21	P
11. HARDWARE LABORATORY	PR	50	20	40	P

FIRST TERM TOTAL = 496/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80054231 CHAUDHAR VISHAL SHESHRAO SAVITA , 71100762E , T80054218 , PICT , T80054231

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	52	P	C	12. PRINCIPLES OF PROGRAMMING LANG.	PP	100	40	52	P	C
02. DATA COMMUNICATION	PP	100	40	72	P	C	13. COMPUTER NETWORKS	PP	100	40	55	P	C
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	55	P	C	14. FINANCE & MANAGEMENT INFORMA.SYS.	PP	100	40	53	P	C
04. DIGITAL SIGNAL PROCESSING	PP	100	40	42	P	C	15. SYSTEMS PROGRAMMING & OPERA.SYS.	PP	100	40	66	P	C
05. THEORY OF COMPUTATION	PP	100	40	40	P	C	16. SOFTWARE ENGINEERING	PP	100	40	52	P	C
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	24	P	C	17. SOFTWARE LABORATORY	TW	25	10	16	P	C
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	34	P	C	18. SOFTWARE LABORATORY	PR	50	20	21	P	C
08. SIGNAL PROCESSING LABORATORY	TW	25	10	16	P	C	19. COMPUTER NETWORK	TW	25	10	18	P	C
09. SIGNAL PROCESSING LABORATORY	OR	50	20	30	P	C	20. COMPUTER NETWORK	OR	50	20	30	P	C
10. HARDWARE LABORATORY	TW	25	10	18	P	C	21. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	25	P	C
11. HARDWARE LABORATORY	PR	50	20	24	P	C	22. SEMINAR AND TECHNICAL COMMUNI.	TW	50	20	32	P	C

GRAND TOTAL = 827/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

T80054232 CHAUDHARI KALYANI BABASAHEB NANDA , 71347759J , PICT , T80054232

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	67	P
02. DATA COMMUNICATION	PP	100	40	61	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	59	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	77	P
05. THEORY OF COMPUTATION	PP	100	40	51	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	43	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	44	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	23	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	44	P
10. HARDWARE LABORATORY	TW	25	10	23	P
11. HARDWARE LABORATORY	PR	50	20	41	P

FIRST TERM TOTAL = 533/750.

ORDN. 1 MARKS :

T80054233 CHAUHAN KRUTI SANDEEP REETA , 71200813G , PICT , T80054233

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	49	P
02. DATA COMMUNICATION	PP	100	40	49	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	40	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	60	P
05. THEORY OF COMPUTATION	PP	100	40	58	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	37	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	40	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	20	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	28	P
10. HARDWARE LABORATORY	TW	25	10	19	P
11. HARDWARE LABORATORY	PR	50	20	35	P

FIRST TERM TOTAL = 435/750.

ORDN. 1 MARKS :



NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80054234 CHAVAN GAURAV SANDIP MEENAKSHI , 71200814E , , PICT , T80054234

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	51	P
02. DATA COMMUNICATION	PP	100	40	40	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	40	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	60	P
05. THEORY OF COMPUTATION	PP	100	40	55	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	43	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	40	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	21	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	41	P
10. HARDWARE LABORATORY	TW	25	10	21	P
11. HARDWARE LABORATORY	PR	50	20	33	P

FIRST TERM TOTAL = 445/750.

ORDN. 1 MARKS :

T80054235 CHAVAN PRACHI RAMKRISHNA SADHANA , 71200815C , , PICT , T80054235

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	51	P
02. DATA COMMUNICATION	PP	100	40	53	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	54	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	61	P
05. THEORY OF COMPUTATION	PP	100	40	61	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	42	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	44	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	23	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	42	P
10. HARDWARE LABORATORY	TW	25	10	22	P
11. HARDWARE LABORATORY	PR	50	20	42	P

FIRST TERM TOTAL = 495/750.

ORDN. 1 MARKS :

T80054236 CHAVHAN KUNAL RAVINDRA LALITA , 71200819F , , PICT , T80054236

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	45	P
02. DATA COMMUNICATION	PP	100	40	AA	F
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	23	F
04. DIGITAL SIGNAL PROCESSING	PP	100	40	42	P
05. THEORY OF COMPUTATION	PP	100	40	45	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	34	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	38	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	16	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	22	P
10. HARDWARE LABORATORY	TW	25	10	15	P
11. HARDWARE LABORATORY	PR	50	20	33	P

FIRST TERM TOTAL = 313/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.  
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80054237	DAMSE SAYALI LAXMAN				LATA				, 71100774J				T80054225		PICT		T80054237
01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	40	P	C		12.	PRINCIPLES OF PROGRAMMING LANG.	PP	100	40	54	P	C	
02.	DATA COMMUNICATION	PP	100	40	40	P	C		13.	COMPUTER NETWORKS	PP	100	40	47	P	C	
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	43	P	C		14.	FINANCE & MANAGEMENT INFORMA.SYS.	PP	100	40	47	P	C	
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	40	P	C		15.	SYSTEMS PROGRAMMING & OPERA.SYS.	PP	100	40	52	P	C	
05.	THEORY OF COMPUTATION	PP	100	40	40	P	C		16.	SOFTWARE ENGINEERING	PP	100	40	40	P	C	
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	31	P	C		17.	SOFTWARE LABORATORY	TW	25	10	20	P	C	
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	42	P	C		18.	SOFTWARE LABORATORY	PR	50	20	25	P		
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	18	P	C		19.	COMPUTER NETWORK	TW	25	10	22	P	C	
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	28	P	C		20.	COMPUTER NETWORK	OR	50	20	30	P		
10.	HARDWARE LABORATORY	TW	25	10	17	P	C		21.	SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	36	P	C	
11.	HARDWARE LABORATORY	PR	50	20	26	P	C		22.	SEMINAR AND TECHNICAL COMMUNI.	TW	50	20	38	P	C	

GRAND TOTAL = 776/1500, RESULT: SECOND CLASS

ORDN. 1 MARKS :

T80054238	DESAI SHAILEE MIHIR				PALLAVI				, 71200830G					PICT		T80054238
01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	55	P										
02.	DATA COMMUNICATION	PP	100	40	45	P										
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	42	P										
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	40	P										
05.	THEORY OF COMPUTATION	PP	100	40	49	P										
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	39	P										
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	32	P										
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	20	P										
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	24	P										
10.	HARDWARE LABORATORY	TW	25	10	21	P										
11.	HARDWARE LABORATORY	PR	50	20	39	P										

FIRST TERM TOTAL = 406/750.

ORDN. 1 MARKS :

T80054239	DESHMUKH POOJA DEEPAK				KANCHAN				, 71347760B					PICT		T80054239
01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	62	P										
02.	DATA COMMUNICATION	PP	100	40	47	P										
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	56	P										
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	59	P										
05.	THEORY OF COMPUTATION	PP	100	40	60	P										
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	43	P										
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	44	P										
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	22	P										
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	32	P										
10.	HARDWARE LABORATORY	TW	25	10	22	P										
11.	HARDWARE LABORATORY	PR	50	20	34	P										

FIRST TERM TOTAL = 481/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80054240 DHAGE SHRADDHA BHAUSAHEB SUMAN , 71200837D , PICT , T80054240

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	58	P
02. DATA COMMUNICATION	PP	100	40	55	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	49	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	50	P
05. THEORY OF COMPUTATION	PP	100	40	57	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	41	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	42	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	22	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	31	P
10. HARDWARE LABORATORY	TW	25	10	21	P
11. HARDWARE LABORATORY	PR	50	20	36	P

FIRST TERM TOTAL = 462/750.

ORDN. 1 MARKS :

T80054241 DHAPTE OMKAR HANUMANT MINAXI , 71239295F , T80054231 , PICT , T80054241

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	56	P C	12. PRINCIPLES OF PROGRAMMING LANG.	PP	100	40	64	P C
02. DATA COMMUNICATION	PP	100	40	62	P C	13. COMPUTER NETWORKS	PP	100	40	64	P C
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	61	P C	14. FINANCE & MANAGEMENT INFORMA.SYS.	PP	100	40	61	P C
04. DIGITAL SIGNAL PROCESSING	PP	100	40	53	P C	15. SYSTEMS PROGRAMMING & OPERA.SYS.	PP	100	40	71	P C
05. THEORY OF COMPUTATION	PP	100	40	40	P C	16. SOFTWARE ENGINEERING	PP	100	40	62	P C
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	33	P C	17. SOFTWARE LABORATORY	TW	25	10	12	P C
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	39	P C	18. SOFTWARE LABORATORY	PR	50	20	40	P C
08. SIGNAL PROCESSING LABORATORY	TW	25	10	15	P C	19. COMPUTER NETWORK	TW	25	10	15	P C
09. SIGNAL PROCESSING LABORATORY	OR	50	20	21	P C	20. COMPUTER NETWORK	OR	50	20	32	P
10. HARDWARE LABORATORY	TW	25	10	18	P C	21. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	25	P C
11. HARDWARE LABORATORY	PR	50	20	25	P C	22. SEMINAR AND TECHNICAL COMMUNI.	TW	50	20	31	P C

GRAND TOTAL = 900/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

T80054242 DHAVALIKAR RAHUL DEEPAK SMITA , 71200839L , PICT , T80054242

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	57	P
02. DATA COMMUNICATION	PP	100	40	44	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	55	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	53	P
05. THEORY OF COMPUTATION	PP	100	40	68	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	39	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	42	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	19	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	34	P
10. HARDWARE LABORATORY	TW	25	10	18	P
11. HARDWARE LABORATORY	PR	50	20	43	P

FIRST TERM TOTAL = 472/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80054243 DHEKALE AMRUTA RAMBHAU KANTABAI , 71200840D , , PICT , T80054243

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	72	P
02. DATA COMMUNICATION	PP	100	40	46	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	49	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	56	P
05. THEORY OF COMPUTATION	PP	100	40	67	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	35	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	32	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	18	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	22	P
10. HARDWARE LABORATORY	TW	25	10	18	P
11. HARDWARE LABORATORY	PR	50	20	28	P

FIRST TERM TOTAL = 443/750.

ORDN. 1 MARKS :

T80054244 DHOLE ANIKET RAJENDRA SUNITA , 71200841B , , PICT , T80054244

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	68	P
02. DATA COMMUNICATION	PP	100	40	54	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	50	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	66	P
05. THEORY OF COMPUTATION	PP	100	40	66	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	34	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	40	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	17	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	25	P
10. HARDWARE LABORATORY	TW	25	10	15	P
11. HARDWARE LABORATORY	PR	50	20	39	P

FIRST TERM TOTAL = 474/750.

ORDN. 1 MARKS :

T80054245 DINGANE RASHMI SUNIL CHITRA , 71200844G , , PICT , T80054245

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	75	P
02. DATA COMMUNICATION	PP	100	40	66	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	62	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	57	P
05. THEORY OF COMPUTATION	PP	100	40	55	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	40	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	41	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	20	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	40	P
10. HARDWARE LABORATORY	TW	25	10	20	P
11. HARDWARE LABORATORY	PR	50	20	39	P

FIRST TERM TOTAL = 515/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.  
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80054246 DOIPHODE NISHIGANDHA SANJAY MADHUBALA , 71100789G , T80054234 , PICT , T80054246

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	49	P C	12. PRINCIPLES OF PROGRAMMING LANG.	PP	100	40	58	P C
02. DATA COMMUNICATION	PP	100	40	44	P C	13. COMPUTER NETWORKS	PP	100	40	56	P C
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	45	P C	14. FINANCE & MANAGEMENT INFORMA.SYS.	PP	100	40	51	P C
04. DIGITAL SIGNAL PROCESSING	PP	100	40	41	P C	15. SYSTEMS PROGRAMMING & OPERA.SYS.	PP	100	40	58	P C
05. THEORY OF COMPUTATION	PP	100	40	40	P C	16. SOFTWARE ENGINEERING	PP	100	40	50	P C
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	36	P C	17. SOFTWARE LABORATORY	TW	25	10	19	P C
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	40	P C	18. SOFTWARE LABORATORY	PR	50	20	42	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	17	P C	19. COMPUTER NETWORK	TW	25	10	23	P C
09. SIGNAL PROCESSING LABORATORY	OR	50	20	32	P C	20. COMPUTER NETWORK	OR	50	20	30	P C
10. HARDWARE LABORATORY	TW	25	10	19	P C	21. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	39	P C
11. HARDWARE LABORATORY	PR	50	20	30	P C	22. SEMINAR AND TECHNICAL COMMUNI.	TW	50	20	40	P C

GRAND TOTAL = 859/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

T80054247 DURUGKAR SURAJ ANIL NIRMALA , 71045424E , T80054236 , PICT , T80054247

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	48	P
02. DATA COMMUNICATION	PP	100	40	44	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	30	F
04. DIGITAL SIGNAL PROCESSING	PP	100	40	23	F
05. THEORY OF COMPUTATION	PP	100	40	45	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	22	P C
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	36	P C
08. SIGNAL PROCESSING LABORATORY	TW	25	10	12	P C
09. SIGNAL PROCESSING LABORATORY	OR	50	20	21	P C
10. HARDWARE LABORATORY	TW	25	10	12	P C
11. HARDWARE LABORATORY	PR	50	20	20	P C

FIRST TERM TOTAL = 313/750.

ORDN. 1 MARKS :

T80054248 GABHALE VARSHA LIMBAJI JANABAI , 71045426M , T80054238 , PICT , T80054248

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	42	P C	12. PRINCIPLES OF PROGRAMMING LANG.	PP	100	40	46	P C
02. DATA COMMUNICATION	PP	100	40	48	P C	13. COMPUTER NETWORKS	PP	100	40	40	P C
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	40	P C	14. FINANCE & MANAGEMENT INFORMA.SYS.	PP	100	40	40	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	41	P C	15. SYSTEMS PROGRAMMING & OPERA.SYS.	PP	100	40	45	P C
05. THEORY OF COMPUTATION	PP	100	40	40	P C	16. SOFTWARE ENGINEERING	PP	100	40	40	P C
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	33	P C	17. SOFTWARE LABORATORY	TW	25	10	21	P C
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	22	P C	18. SOFTWARE LABORATORY	PR	50	20	25	P C
08. SIGNAL PROCESSING LABORATORY	TW	25	10	18	P C	19. COMPUTER NETWORK	TW	25	10	22	P C
09. SIGNAL PROCESSING LABORATORY	OR	50	20	32	P C	20. COMPUTER NETWORK	OR	50	20	26	P C
10. HARDWARE LABORATORY	TW	25	10	20	P C	21. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	40	P C
11. HARDWARE LABORATORY	PR	50	20	34	P C	22. SEMINAR AND TECHNICAL COMMUNI.	TW	50	20	40	P C

GRAND TOTAL = 755/1500, RESULT: SECOND CLASS

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80054250 GAIKWAD KAJAL PANDURANG SINDHU , 71200852H , PICT , T80054250

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	67	P
02. DATA COMMUNICATION	PP	100	40	66	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	53	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	55	P
05. THEORY OF COMPUTATION	PP	100	40	53	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	41	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	34	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	22	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	32	P
10. HARDWARE LABORATORY	TW	25	10	22	P
11. HARDWARE LABORATORY	PR	50	20	42	P

FIRST TERM TOTAL = 487/750.

ORDN. 1 MARKS :

T80054251 GAIKWAD MAHESH EKNATH LATA , 71045431H , T80054350 , PICT , T80054251

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	40	P C	12. PRINCIPLES OF PROGRAMMING LANG.	PP	100	40	40	P C
02. DATA COMMUNICATION	PP	100	40	42	P C	13. COMPUTER NETWORKS	PP	100	40	40	P C
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	42	P C	14. FINANCE & MANAGEMENT INFORMA.SYS.	PP	100	40	46	P C
04. DIGITAL SIGNAL PROCESSING	PP	100	40	45	P C	15. SYSTEMS PROGRAMMING & OPERA.SYS.	PP	100	40	40	P C
05. THEORY OF COMPUTATION	PP	100	40	41	P C	16. SOFTWARE ENGINEERING	PP	100	40	44	P C
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	34	P C	17. SOFTWARE LABORATORY	TW	25	10	10	P C
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	25	P C	18. SOFTWARE LABORATORY	PR	50	20	10	F
08. SIGNAL PROCESSING LABORATORY	TW	25	10	14	P C	19. COMPUTER NETWORK	TW	25	10	15	P C
09. SIGNAL PROCESSING LABORATORY	OR	50	20	20	P C	20. COMPUTER NETWORK	OR	50	20	35	P C
10. HARDWARE LABORATORY	TW	25	10	13	P C	21. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	20	P C
11. HARDWARE LABORATORY	PR	50	20	20	P C	22. SEMINAR AND TECHNICAL COMMUNI.	TW	50	20	27	P C

GRAND TOTAL = 663/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

T80054252 GAIKWAD POOJA ANIL SWATI , 71347761L , PICT , T80054252

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	68	P
02. DATA COMMUNICATION	PP	100	40	58	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	54	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	21	F
05. THEORY OF COMPUTATION	PP	100	40	52	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	38	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	40	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	21	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	21	P
10. HARDWARE LABORATORY	TW	25	10	21	P
11. HARDWARE LABORATORY	PR	50	20	32	P

FIRST TERM TOTAL = 426/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80054253 GAURAV MISHRA MAMATA , 71200858G , PICT , T80054253

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	66	P
02. DATA COMMUNICATION	PP	100	40	56	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	53	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	58	P
05. THEORY OF COMPUTATION	PP	100	40	63	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	41	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	42	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	19	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	35	P
10. HARDWARE LABORATORY	TW	25	10	20	P
11. HARDWARE LABORATORY	PR	50	20	33	P

FIRST TERM TOTAL = 486/750.

ORDN. 1 MARKS :

T80054254 GAURI SANDEEP DESHMUKH AARTI , 71200859E , PICT , T80054254

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	71	P
02. DATA COMMUNICATION	PP	100	40	69	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	46	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	48	P
05. THEORY OF COMPUTATION	PP	100	40	51	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	41	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	43	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	23	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	37	P
10. HARDWARE LABORATORY	TW	25	10	23	P
11. HARDWARE LABORATORY	PR	50	20	39	P

FIRST TERM TOTAL = 491/750.

ORDN. 1 MARKS :

T80054255 GAVALI TANUJA KASHINATH PARVATA , 71100799D , T80054241 , PICT , T80054255

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	52	P C	12. PRINCIPLES OF PROGRAMMING LANG.	PP	100	40	58	P C
02. DATA COMMUNICATION	PP	100	40	44	P C	13. COMPUTER NETWORKS	PP	100	40	53	P C
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	47	P C	14. FINANCE & MANAGEMENT INFORMA.SYS.	PP	100	40	50	P C
04. DIGITAL SIGNAL PROCESSING	PP	100	40	40	P C	15. SYSTEMS PROGRAMMING & OPERA.SYS.	PP	100	40	49	P C
05. THEORY OF COMPUTATION	PP	100	40	40	P C	16. SOFTWARE ENGINEERING	PP	100	40	53	P C
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	32	P C	17. SOFTWARE LABORATORY	TW	25	10	19	P C
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	25	P C	18. SOFTWARE LABORATORY	PR	50	20	27	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	16	P C	19. COMPUTER NETWORK	TW	25	10	19	P C
09. SIGNAL PROCESSING LABORATORY	OR	50	20	21	P C	20. COMPUTER NETWORK	OR	50	20	32	P C
10. HARDWARE LABORATORY	TW	25	10	15	P C	21. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	39	P C
11. HARDWARE LABORATORY	PR	50	20	22	P C	22. SEMINAR AND TECHNICAL COMMUNI.	TW	50	20	36	P C

GRAND TOTAL = 789/1500, RESULT: SECOND CLASS

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80054256 GAWALI PRAJAKTA PANDURANG SHOBHA , 71200861G , , PICT , T80054256

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	44	P
02. DATA COMMUNICATION	PP	100	40	45	P
03. MICROPROCESSORS & MICROCONTROLLERPP		100	40	22	F
04. DIGITAL SIGNAL PROCESSING	PP	100	40	27	F
05. THEORY OF COMPUTATION	PP	100	40	18	F
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	35	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	32	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	15	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	28	P
10. HARDWARE LABORATORY	TW	25	10	15	P
11. HARDWARE LABORATORY	PR	50	20	22	P

FIRST TERM TOTAL = 303/750.

ORDN. 1 MARKS :

T80054257 GAWANDE KETKI NANDKISHORE SUSHAMA , 71200862E , , PICT , T80054257

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	68	P
02. DATA COMMUNICATION	PP	100	40	52	P
03. MICROPROCESSORS & MICROCONTROLLERPP		100	40	43	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	46	P
05. THEORY OF COMPUTATION	PP	100	40	51	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	38	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	42	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	19	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	38	P
10. HARDWARE LABORATORY	TW	25	10	20	P
11. HARDWARE LABORATORY	PR	50	20	37	P

FIRST TERM TOTAL = 454/750.

ORDN. 1 MARKS :

T80054258 GOVIND BAPU CHAUDHARY SUMAN , 71200870F , , PICT , T80054258

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	74	P
02. DATA COMMUNICATION	PP	100	40	58	P
03. MICROPROCESSORS & MICROCONTROLLERPP		100	40	49	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	45	P
05. THEORY OF COMPUTATION	PP	100	40	62	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	45	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	45	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	20	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	33	P
10. HARDWARE LABORATORY	TW	25	10	21	P
11. HARDWARE LABORATORY	PR	50	20	39	P

FIRST TERM TOTAL = 491/750.

ORDN. 1 MARKS :



NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80054259 GUNJAL DIPALI BHANUDAS SUREKHA , 71100812E , , PICT , T80054259

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	55	P
02. DATA COMMUNICATION	PP	100	40	53	P
03. MICROPROCESSORS & MICROCONTROLLERPP	PP	100	40	29	F
04. DIGITAL SIGNAL PROCESSING	PP	100	40	27	F
05. THEORY OF COMPUTATION	PP	100	40	40	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	20	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	39	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	12	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	07	F
10. HARDWARE LABORATORY	TW	25	10	13	P
11. HARDWARE LABORATORY	PR	50	20	04	F

FIRST TERM TOTAL = 299/750.

ORDN. 1 MARKS :

T80054260 HALKARE PRAJAKTA DNYANDEO SANGITA , 71200876E , , PICT , T80054260

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	64	P
02. DATA COMMUNICATION	PP	100	40	68	P
03. MICROPROCESSORS & MICROCONTROLLERPP	PP	100	40	40	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	63	P
05. THEORY OF COMPUTATION	PP	100	40	59	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	44	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	41	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	22	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	35	P
10. HARDWARE LABORATORY	TW	25	10	21	P
11. HARDWARE LABORATORY	PR	50	20	42	P

FIRST TERM TOTAL = 499/750.

ORDN. 1 MARKS :

T80054261 HOOLI MAYURESH BASAVARAJ SHAILA , 71200882K , , PICT , T80054261

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	61	P
02. DATA COMMUNICATION	PP	100	40	57	P
03. MICROPROCESSORS & MICROCONTROLLERPP	PP	100	40	40	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	23	F
05. THEORY OF COMPUTATION	PP	100	40	55	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	37	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	38	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	19	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	22	P
10. HARDWARE LABORATORY	TW	25	10	19	P
11. HARDWARE LABORATORY	PR	50	20	32	P

FIRST TERM TOTAL = 403/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.  
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80054262 JADHAV AMOL JETA LATA , 71045453J , T80054252 , PICT , T80054262

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	60	P C
02. DATA COMMUNICATION	PP	100	40	60	P C
03. MICROPROCESSORS & MICROCONTROLLERPP		100	40	46	P C
04. DIGITAL SIGNAL PROCESSING	PP	100	40	45	P
05. THEORY OF COMPUTATION	PP	100	40	AA	F
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	21	P C
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	12	F
08. SIGNAL PROCESSING LABORATORY	TW	25	10	10	P C
09. SIGNAL PROCESSING LABORATORY	OR	50	20	05	F
10. HARDWARE LABORATORY	TW	25	10	10	P C
11. HARDWARE LABORATORY	PR	50	20	AA	F

FIRST TERM TOTAL = 269/750.

ORDN. 1 MARKS :

T80054263 JADHAV RUTUJA RAMESH NANDA , 71347762J , , PICT , T80054263

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	75	P
02. DATA COMMUNICATION	PP	100	40	72	P
03. MICROPROCESSORS & MICROCONTROLLERPP		100	40	65	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	43	P
05. THEORY OF COMPUTATION	PP	100	40	70	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	35	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	40	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	18	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	28	P
10. HARDWARE LABORATORY	TW	25	10	18	P
11. HARDWARE LABORATORY	PR	50	20	34	P

FIRST TERM TOTAL = 498/750.

ORDN. 1 MARKS :

T80054264 JAGTAP AMRUTA SATISH SANJIVANI , 71200885D , , PICT , T80054264

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	80	P
02. DATA COMMUNICATION	PP	100	40	76	P
03. MICROPROCESSORS & MICROCONTROLLERPP		100	40	56	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	54	P
05. THEORY OF COMPUTATION	PP	100	40	67	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	45	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	42	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	23	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	46	P
10. HARDWARE LABORATORY	TW	25	10	23	P
11. HARDWARE LABORATORY	PR	50	20	43	P

FIRST TERM TOTAL = 555/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80054265 JOSHI AKSHAY ANIL JYOTI , 71200892G , , PICT , T80054265

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	68	P
02. DATA COMMUNICATION	PP	100	40	63	P
03. MICROPROCESSORS & MICROCONTROLLERPP		100	40	54	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	51	P
05. THEORY OF COMPUTATION	PP	100	40	61	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	42	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	44	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	22	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	44	P
10. HARDWARE LABORATORY	TW	25	10	20	P
11. HARDWARE LABORATORY	PR	50	20	40	P

FIRST TERM TOTAL = 509/750.

ORDN. 1 MARKS :

T80054266 JOSHI AVANI AVINASH ASHWINI , 71200893E , , PICT , T80054266

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	61	P
02. DATA COMMUNICATION	PP	100	40	60	P
03. MICROPROCESSORS & MICROCONTROLLERPP		100	40	47	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	40	P
05. THEORY OF COMPUTATION	PP	100	40	42	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	32	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	15	F
08. SIGNAL PROCESSING LABORATORY	TW	25	10	19	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	40	P
10. HARDWARE LABORATORY	TW	25	10	19	P
11. HARDWARE LABORATORY	PR	50	20	02	F

FIRST TERM TOTAL = 377/750.

ORDN. 1 MARKS :

T80054267 JOSHIPURA RUJUTA SHISHIR MEGHAL , 71200897H , , PICT , T80054267

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	76	P
02. DATA COMMUNICATION	PP	100	40	75	P
03. MICROPROCESSORS & MICROCONTROLLERPP		100	40	64	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	50	P
05. THEORY OF COMPUTATION	PP	100	40	71	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	39	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	43	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	19	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	43	P
10. HARDWARE LABORATORY	TW	25	10	18	P
11. HARDWARE LABORATORY	PR	50	20	40	P

FIRST TERM TOTAL = 538/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80054268 KADAM VIJAY BALASAHEB SUREKHA , 71347763G , , PICT , T80054268

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	60	P
02. DATA COMMUNICATION	PP	100	40	64	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	54	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	45	P
05. THEORY OF COMPUTATION	PP	100	40	55	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	35	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	44	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	22	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	40	P
10. HARDWARE LABORATORY	TW	25	10	20	P
11. HARDWARE LABORATORY	PR	50	20	43	P

FIRST TERM TOTAL = 482/750.

ORDN. 1 MARKS :

T80054269 KALBHOR KOMAL JALINDAR KALBHOR RAJASHREE , 71347764E , , PICT , T80054269

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	64	P
02. DATA COMMUNICATION	PP	100	40	52	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	49	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	52	P
05. THEORY OF COMPUTATION	PP	100	40	54	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	43	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	45	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	22	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	35	P
10. HARDWARE LABORATORY	TW	25	10	22	P
11. HARDWARE LABORATORY	PR	50	20	35	P

FIRST TERM TOTAL = 473/750.

ORDN. 1 MARKS :

T80054270 KALBHOR SHILPA SOPAN KALBHOR ANJALI , 71347765C , , PICT , T80054270

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	68	P
02. DATA COMMUNICATION	PP	100	40	61	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	51	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	40	P
05. THEORY OF COMPUTATION	PP	100	40	65	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	44	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	45	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	22	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	36	P
10. HARDWARE LABORATORY	TW	25	10	22	P
11. HARDWARE LABORATORY	PR	50	20	39	P

FIRST TERM TOTAL = 493/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80054271 KALDATE SHASHANK RAMDAS JAYASHRI , 71200901K , , PICT , T80054271

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	57	P
02. DATA COMMUNICATION	PP	100	40	54	P
03. MICROPROCESSORS & MICROCONTROLLERPP		100	40	40	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	40	P
05. THEORY OF COMPUTATION	PP	100	40	55	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	32	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	39	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	18	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	02	F
10. HARDWARE LABORATORY	TW	25	10	17	P
11. HARDWARE LABORATORY	PR	50	20	37	P

FIRST TERM TOTAL = 391/750.

ORDN. 1 MARKS :

T80054272 KALUSKAR SANIYA VIKRAM PALLAVEE , 71200904D , , PICT , T80054272

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	63	P
02. DATA COMMUNICATION	PP	100	40	65	P
03. MICROPROCESSORS & MICROCONTROLLERPP		100	40	52	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	52	P
05. THEORY OF COMPUTATION	PP	100	40	60	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	30	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	39	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	17	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	35	P
10. HARDWARE LABORATORY	TW	25	10	18	P
11. HARDWARE LABORATORY	PR	50	20	26	P

FIRST TERM TOTAL = 457/750.

ORDN. 1 MARKS :

T80054273 KAMBALE NILESH VIJAY JAYMALA , 71347766M , , PICT , T80054273

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	66	P
02. DATA COMMUNICATION	PP	100	40	53	P
03. MICROPROCESSORS & MICROCONTROLLERPP		100	40	32	F
04. DIGITAL SIGNAL PROCESSING	PP	100	40	17	F
05. THEORY OF COMPUTATION	PP	100	40	21	F
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	35	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	36	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	20	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	09	F
10. HARDWARE LABORATORY	TW	25	10	20	P
11. HARDWARE LABORATORY	PR	50	20	10	F

FIRST TERM TOTAL = 319/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.  
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80054274 KAMBLE ASHAY UTTAM LATA , 71200905B , PICT , T80054274

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	64	P
02. DATA COMMUNICATION	PP	100	40	45	P
03. MICROPROCESSORS & MICROCONTROLLERPP		100	40	40	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	40	P
05. THEORY OF COMPUTATION	PP	100	40	52	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	24	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	38	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	15	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	24	P
10. HARDWARE LABORATORY	TW	25	10	16	P
11. HARDWARE LABORATORY	PR	50	20	30	P

FIRST TERM TOTAL = 388/750.

ORDN. 1 MARKS :

T80054275 KANTALE SACHIN WAMANRAO PRAMILA , 71100848F , T80054262 , PICT , T80054275

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	46	P
02. DATA COMMUNICATION	PP	100	40	40	P
03. MICROPROCESSORS & MICROCONTROLLERPP		100	40	28	F
04. DIGITAL SIGNAL PROCESSING	PP	100	40	09	F
05. THEORY OF COMPUTATION	PP	100	40	30	F
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	24	P C
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	23	P C
08. SIGNAL PROCESSING LABORATORY	TW	25	10	11	P C
09. SIGNAL PROCESSING LABORATORY	OR	50	20	22	P
10. HARDWARE LABORATORY	TW	25	10	13	P C
11. HARDWARE LABORATORY	PR	50	20	04	F

FIRST TERM TOTAL = 250/750.

ORDN. 1 MARKS :

T80054276 KAPILESHWAR KAUSTUBH DATTATRAYA KETAKI , 71200910J , PICT , T80054276

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	68	P
02. DATA COMMUNICATION	PP	100	40	46	P
03. MICROPROCESSORS & MICROCONTROLLERPP		100	40	50	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	51	P
05. THEORY OF COMPUTATION	PP	100	40	68	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	43	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	45	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	23	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	42	P
10. HARDWARE LABORATORY	TW	25	10	21	P
11. HARDWARE LABORATORY	PR	50	20	38	P

FIRST TERM TOTAL = 495/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80054277 KARANDE RAHUL MADAN SUNITA , 71045472E , T80054264 , PICT , T80054277

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	40	P C	12. PRINCIPLES OF PROGRAMMING LANG.	PP	100	40	49	P C
02. DATA COMMUNICATION	PP	100	40	49	P C	13. COMPUTER NETWORKS	PP	100	40	40	P C
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	40	P C	14. FINANCE & MANAGEMENT INFORMA.SYS.	PP	100	40	41	P C
04. DIGITAL SIGNAL PROCESSING	PP	100	40	AA	F	15. SYSTEMS PROGRAMMING & OPERA.SYS.	PP	100	40	50	P C
05. THEORY OF COMPUTATION	PP	100	40	33	F	16. SOFTWARE ENGINEERING	PP	100	40	52	P C
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	23	P C	17. SOFTWARE LABORATORY	TW	25	10	12	P C
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	25	P C	18. SOFTWARE LABORATORY	PR	50	20	22	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	11	P C	19. COMPUTER NETWORK	TW	25	10	13	P C
09. SIGNAL PROCESSING LABORATORY	OR	50	20	22	P C	20. COMPUTER NETWORK	OR	50	20	30	P
10. HARDWARE LABORATORY	TW	25	10	10	P C	21. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	24	P C
11. HARDWARE LABORATORY	PR	50	20	26	P C	22. SEMINAR AND TECHNICAL COMMUNI.	TW	50	20	22	P C

GRAND TOTAL = 634/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

T80054278 KARPE AKASH TUKARAM ASHA , 71200912E , , PICT , T80054278

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	72	P
02. DATA COMMUNICATION	PP	100	40	61	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	46	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	49	P
05. THEORY OF COMPUTATION	PP	100	40	51	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	35	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	40	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	19	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	23	P
10. HARDWARE LABORATORY	TW	25	10	19	P
11. HARDWARE LABORATORY	PR	50	20	35	P

FIRST TERM TOTAL = 450/750.

ORDN. 1 MARKS :

T80054279 KARTIK ANIL REDDY NAMRATA , 71200913C , , PICT , T80054279

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	60	P
02. DATA COMMUNICATION	PP	100	40	51	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	45	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	40	P
05. THEORY OF COMPUTATION	PP	100	40	55	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	39	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	45	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	18	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	35	P
10. HARDWARE LABORATORY	TW	25	10	18	P
11. HARDWARE LABORATORY	PR	50	20	30	P

FIRST TERM TOTAL = 436/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80054280 KASAT SARVESH BALKISAN SWARUPA , 71200914M , , PICT , T80054280

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	72	P
02. DATA COMMUNICATION	PP	100	40	72	P
03. MICROPROCESSORS & MICROCONTROLLERPP		100	40	63	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	74	P
05. THEORY OF COMPUTATION	PP	100	40	69	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	42	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	45	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	23	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	44	P
10. HARDWARE LABORATORY	TW	25	10	22	P
11. HARDWARE LABORATORY	PR	50	20	40	P

FIRST TERM TOTAL = 566/750.

ORDN. 1 MARKS :

T80054281 KEDARE SWAPNIL PANDIT NIRMALA , 71045479B , , PICT , T80054281

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	43	P
02. DATA COMMUNICATION	PP	100	40	41	P
03. MICROPROCESSORS & MICROCONTROLLERPP		100	40	11	F
04. DIGITAL SIGNAL PROCESSING	PP	100	40	11	F
05. THEORY OF COMPUTATION	PP	100	40	09	F
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	20	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	AA	F
08. SIGNAL PROCESSING LABORATORY	TW	25	10	12	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	05	F
10. HARDWARE LABORATORY	TW	25	10	11	P
11. HARDWARE LABORATORY	PR	50	20	AA	F

FIRST TERM TOTAL = 163/750.

ORDN. 1 MARKS :

T80054282 KHADE SOURABH DURYODHAN SANGITA , 71200919B , , PICT , T80054282

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	40	P
02. DATA COMMUNICATION	PP	100	40	49	P
03. MICROPROCESSORS & MICROCONTROLLERPP		100	40	33	F
04. DIGITAL SIGNAL PROCESSING	PP	100	40	41	P
05. THEORY OF COMPUTATION	PP	100	40	41	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	31	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	40	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	15	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	28	P
10. HARDWARE LABORATORY	TW	25	10	14	P
11. HARDWARE LABORATORY	PR	50	20	35	P

FIRST TERM TOTAL = 367/750.

ORDN. 1 MARKS :



NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80054283 KHADKE SRUSHTI SANJEEV USHA , 71200920F , , PICT , T80054283

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	56	P
02. DATA COMMUNICATION	PP	100	40	49	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	42	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	44	P
05. THEORY OF COMPUTATION	PP	100	40	43	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	39	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	40	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	19	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	32	P
10. HARDWARE LABORATORY	TW	25	10	21	P
11. HARDWARE LABORATORY	PR	50	20	35	P

FIRST TERM TOTAL = 420/750.

ORDN. 1 MARKS :

T80054284 KHALID RAZA KHAN AYESHA , 70925473D , T80054269 , PICT , T80054284

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	40	P C
02. DATA COMMUNICATION	PP	100	40	AA	F
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	27	F
04. DIGITAL SIGNAL PROCESSING	PP	100	40	AA	F
05. THEORY OF COMPUTATION	PP	100	40	26	F
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	20	P C
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	20	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	10	P C
09. SIGNAL PROCESSING LABORATORY	OR	50	20	AA	F
10. HARDWARE LABORATORY	TW	25	10	10	P C
11. HARDWARE LABORATORY	PR	50	20	AA	F

FIRST TERM TOTAL = 153/750.

ORDN. 1 MARKS :

T80054285 KHANDAVE MAYUR NARAYANRAO PUSHPA , 71200923L , , PICT , T80054285

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	52	P
02. DATA COMMUNICATION	PP	100	40	47	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	59	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	61	P
05. THEORY OF COMPUTATION	PP	100	40	67	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	35	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	42	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	16	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	37	P
10. HARDWARE LABORATORY	TW	25	10	17	P
11. HARDWARE LABORATORY	PR	50	20	38	P

FIRST TERM TOTAL = 471/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.  
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80054286 KOTHARI APARNA ASHOK ARUNA , 71200931M , , PICT , T80054286

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	54	P
02. DATA COMMUNICATION	PP	100	40	51	P
03. MICROPROCESSORS & MICROCONTROLLERPP		100	40	59	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	72	P
05. THEORY OF COMPUTATION	PP	100	40	65	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	37	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	42	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	21	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	45	P
10. HARDWARE LABORATORY	TW	25	10	20	P
11. HARDWARE LABORATORY	PR	50	20	40	P

FIRST TERM TOTAL = 506/750.

ORDN. 1 MARKS :

T80054287 KULKARNI JANHAVI SUHASCHANDRA SAVANI , 71200934F , , PICT , T80054287

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	59	P
02. DATA COMMUNICATION	PP	100	40	52	P
03. MICROPROCESSORS & MICROCONTROLLERPP		100	40	60	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	56	P
05. THEORY OF COMPUTATION	PP	100	40	72	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	35	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	43	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	18	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	43	P
10. HARDWARE LABORATORY	TW	25	10	19	P
11. HARDWARE LABORATORY	PR	50	20	42	P

FIRST TERM TOTAL = 499/750.

ORDN. 1 MARKS :

T80054288 KULKARNI MALHAR SUNIL VYJAYANTHI , 71200935D , , PICT , T80054288

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	61	P
02. DATA COMMUNICATION	PP	100	40	56	P
03. MICROPROCESSORS & MICROCONTROLLERPP		100	40	61	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	58	P
05. THEORY OF COMPUTATION	PP	100	40	67	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	38	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	45	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	21	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	45	P
10. HARDWARE LABORATORY	TW	25	10	21	P
11. HARDWARE LABORATORY	PR	50	20	42	P

FIRST TERM TOTAL = 515/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.  
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80054289 KULKARNI VARAD SURESH TILOTTAMA , 70818756M , T80054279 , PICT , T80054289

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	55	P C	12. PRINCIPLES OF PROGRAMMING LANG.	PP	100	40	60	P C
02. DATA COMMUNICATION	PP	100	40	40	P C	13. COMPUTER NETWORKS	PP	100	40	45	P C
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	40	P C	14. FINANCE & MANAGEMENT INFORMA.SYS.	PP	100	40	50	P C
04. DIGITAL SIGNAL PROCESSING	PP	100	40	40	P	15. SYSTEMS PROGRAMMING & OPERA.SYS.	PP	100	40	48	P C
05. THEORY OF COMPUTATION	PP	100	40	40	P C	16. SOFTWARE ENGINEERING	PP	100	40	40	P C
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	39	P C	17. SOFTWARE LABORATORY	TW	25	10	16	P C
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	43	P C	18. SOFTWARE LABORATORY	PR	50	20	39	P C
08. SIGNAL PROCESSING LABORATORY	TW	25	10	19	P C	19. COMPUTER NETWORK	TW	25	10	16	P C
09. SIGNAL PROCESSING LABORATORY	OR	50	20	33	P C	20. COMPUTER NETWORK	OR	50	20	38	P C
10. HARDWARE LABORATORY	TW	25	10	17	P C	21. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	35	P C
11. HARDWARE LABORATORY	PR	50	20	38	P C	22. SEMINAR AND TECHNICAL COMMUNI.	TW	50	20	40	P C

GRAND TOTAL = 831/1500, RESULT: HIGHER SECOND CLASS RESULT RESERVED FOR BKLG

ORDN. 1 MARKS :

T80054290 LANKE PRASAD PRAVIN SUNANDA , 71200943E , PICT , T80054290

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	71	P
02. DATA COMMUNICATION	PP	100	40	73	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	71	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	85	P
05. THEORY OF COMPUTATION	PP	100	40	89	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	48	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	47	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	24	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	46	P
10. HARDWARE LABORATORY	TW	25	10	23	P
11. HARDWARE LABORATORY	PR	50	20	45	P

FIRST TERM TOTAL = 622/750.

ORDN. 1 MARKS :

T80054291 MAHAJAN ABHIJEET SHIVDAS PRABHAVATI , 71100887G , T80054284 , PICT , T80054291

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	63	P C	12. PRINCIPLES OF PROGRAMMING LANG.	PP	100	40	50	P C
02. DATA COMMUNICATION	PP	100	40	65	P C	13. COMPUTER NETWORKS	PP	100	40	48	P C
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	48	P C	14. FINANCE & MANAGEMENT INFORMA.SYS.	PP	100	40	53	P C
04. DIGITAL SIGNAL PROCESSING	PP	100	40	51	P C	15. SYSTEMS PROGRAMMING & OPERA.SYS.	PP	100	40	51	P C
05. THEORY OF COMPUTATION	PP	100	40	45	P C	16. SOFTWARE ENGINEERING	PP	100	40	42	P C
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	27	P C	17. SOFTWARE LABORATORY	TW	25	10	16	P C
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	42	P C	18. SOFTWARE LABORATORY	PR	50	20	30	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	15	P C	19. COMPUTER NETWORK	TW	25	10	16	P C
09. SIGNAL PROCESSING LABORATORY	OR	50	20	35	P C	20. COMPUTER NETWORK	OR	50	20	22	P C
10. HARDWARE LABORATORY	TW	25	10	16	P C	21. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	31	P C
11. HARDWARE LABORATORY	PR	50	20	30	P C	22. SEMINAR AND TECHNICAL COMMUNI.	TW	50	20	33	P C

GRAND TOTAL = 829/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80054292 MAHAJAN ABOLI VIKRANT ARCHANA , 71200946K , , PICT , T80054292

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	53	P
02. DATA COMMUNICATION	PP	100	40	56	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	60	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	76	P
05. THEORY OF COMPUTATION	PP	100	40	72	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	44	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	45	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	24	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	47	P
10. HARDWARE LABORATORY	TW	25	10	23	P
11. HARDWARE LABORATORY	PR	50	20	43	P

FIRST TERM TOTAL = 543/750.

ORDN. 1 MARKS :

T80054293 MAHALE DHANSHRI RAJENDRA MANGALA , 71200948F , , PICT , T80054293

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	55	P
02. DATA COMMUNICATION	PP	100	40	52	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	51	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	69	P
05. THEORY OF COMPUTATION	PP	100	40	76	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	38	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	42	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	19	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	41	P
10. HARDWARE LABORATORY	TW	25	10	18	P
11. HARDWARE LABORATORY	PR	50	20	36	P

FIRST TERM TOTAL = 497/750.

ORDN. 1 MARKS :

T80054294 MAITHILI TAKALE MEENA , 71200950H , , PICT , T80054294

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	60	P
02. DATA COMMUNICATION	PP	100	40	60	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	56	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	47	P
05. THEORY OF COMPUTATION	PP	100	40	47	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	41	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	41	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	21	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	35	P
10. HARDWARE LABORATORY	TW	25	10	21	P
11. HARDWARE LABORATORY	PR	50	20	34	P

FIRST TERM TOTAL = 463/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80054295 MALI PARAG SHRIKRISHNA SUNANDA , 71200951F , , PICT , T80054295

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	68	P
02. DATA COMMUNICATION	PP	100	40	70	P
03. MICROPROCESSORS & MICROCONTROLLERPP		100	40	63	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	77	P
05. THEORY OF COMPUTATION	PP	100	40	77	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	45	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	46	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	24	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	41	P
10. HARDWARE LABORATORY	TW	25	10	22	P
11. HARDWARE LABORATORY	PR	50	20	40	P

FIRST TERM TOTAL = 573/750.

ORDN. 1 MARKS :

T80054296 MANE SAGAR ASHOK SANGITA , 71200955J , , PICT , T80054296

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	58	P
02. DATA COMMUNICATION	PP	100	40	53	P
03. MICROPROCESSORS & MICROCONTROLLERPP		100	40	43	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	52	P
05. THEORY OF COMPUTATION	PP	100	40	13	F
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	36	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	41	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	18	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	24	P
10. HARDWARE LABORATORY	TW	25	10	18	P
11. HARDWARE LABORATORY	PR	50	20	30	P

FIRST TERM TOTAL = 386/750.

ORDN. 1 MARKS :

T80054297 MANTRI SHARAYU SHARAD PRIYA , 71200957E , , PICT , T80054297

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	62	P
02. DATA COMMUNICATION	PP	100	40	64	P
03. MICROPROCESSORS & MICROCONTROLLERPP		100	40	57	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	76	P
05. THEORY OF COMPUTATION	PP	100	40	61	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	35	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	36	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	20	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	39	P
10. HARDWARE LABORATORY	TW	25	10	20	P
11. HARDWARE LABORATORY	PR	50	20	39	P

FIRST TERM TOTAL = 509/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80054298 MHASKE POOJA DAGDU MANISHA , 71347767K , , PICT , T80054298

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	64	P
02. DATA COMMUNICATION	PP	100	40	53	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	67	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	67	P
05. THEORY OF COMPUTATION	PP	100	40	51	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	36	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	37	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	18	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	30	P
10. HARDWARE LABORATORY	TW	25	10	18	P
11. HARDWARE LABORATORY	PR	50	20	38	P

FIRST TERM TOTAL = 479/750.

ORDN. 1 MARKS :

T80054299 MHASKULE YOGESH DATTATRAYA LATA , 71347768H , , PICT , T80054299

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	62	P
02. DATA COMMUNICATION	PP	100	40	56	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	64	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	60	P
05. THEORY OF COMPUTATION	PP	100	40	45	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	40	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	40	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	21	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	26	P
10. HARDWARE LABORATORY	TW	25	10	20	P
11. HARDWARE LABORATORY	PR	50	20	26	P

FIRST TERM TOTAL = 460/750.

ORDN. 1 MARKS :

T80054300 MISAL AKSHAYA ARJUN SANGITA , 71200962M , , PICT , T80054300

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	66	P
02. DATA COMMUNICATION	PP	100	40	60	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	68	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	66	P
05. THEORY OF COMPUTATION	PP	100	40	69	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	39	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	37	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	17	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	23	P
10. HARDWARE LABORATORY	TW	25	10	18	P
11. HARDWARE LABORATORY	PR	50	20	29	P

FIRST TERM TOTAL = 492/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.  
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80054301 MOHIT RAJVARDHAN MEENA KUMARI , 71045671K , T80054289 , PICT , T80054301

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	44	P C	12. PRINCIPLES OF PROGRAMMING LANG.	PP	100	40	47	P C
02. DATA COMMUNICATION	PP	100	40	40	P C	13. COMPUTER NETWORKS	PP	100	40	40	P C
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	49	P C	14. FINANCE & MANAGEMENT INFORMA.SYS.	PP	100	40	40	P C
04. DIGITAL SIGNAL PROCESSING	PP	100	40	40	P	15. SYSTEMS PROGRAMMING & OPERA.SYS.	PP	100	40	40	P C
05. THEORY OF COMPUTATION	PP	100	40	41	P C	16. SOFTWARE ENGINEERING	PP	100	40	40	P C
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	32	P C	17. SOFTWARE LABORATORY	TW	25	10	18	P C
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	38	P C	18. SOFTWARE LABORATORY	PR	50	20	35	P C
08. SIGNAL PROCESSING LABORATORY	TW	25	10	16	P C	19. COMPUTER NETWORK	TW	25	10	18	P C
09. SIGNAL PROCESSING LABORATORY	OR	50	20	20	P C	20. COMPUTER NETWORK	OR	50	20	39	P
10. HARDWARE LABORATORY	TW	25	10	15	P C	21. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	37	P C
11. HARDWARE LABORATORY	PR	50	20	36	P C	22. SEMINAR AND TECHNICAL COMMUNI.	TW	50	20	41	P C

GRAND TOTAL = 766/1500, RESULT: SECOND CLASS

ORDN. 1 MARKS :

T80054302 MORDANI NAVIN BHAGWAN SUPARNA , 71200965F , , PICT , T80054302

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	58	P
02. DATA COMMUNICATION	PP	100	40	48	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	59	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	67	P
05. THEORY OF COMPUTATION	PP	100	40	56	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	41	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	42	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	21	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	39	P
10. HARDWARE LABORATORY	TW	25	10	22	P
11. HARDWARE LABORATORY	PR	50	20	40	P

FIRST TERM TOTAL = 493/750.

ORDN. 1 MARKS :

T80054303 MORE SAYALI SHANKAR MORE ANITA SHANKAR , 71347769F , , PICT , T80054303

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	65	P
02. DATA COMMUNICATION	PP	100	40	68	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	67	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	52	P
05. THEORY OF COMPUTATION	PP	100	40	60	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	38	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	42	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	18	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	35	P
10. HARDWARE LABORATORY	TW	25	10	18	P
11. HARDWARE LABORATORY	PR	50	20	36	P

FIRST TERM TOTAL = 499/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.  
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80054304 MORE SOHAM SAMEER HARSHA , 71200966D , , PICT , T80054304

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	53	P
02. DATA COMMUNICATION	PP	100	40	51	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	44	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	40	P
05. THEORY OF COMPUTATION	PP	100	40	44	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	21	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	38	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	13	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	15	F
10. HARDWARE LABORATORY	TW	25	10	12	P
11. HARDWARE LABORATORY	PR	50	20	36	P

FIRST TERM TOTAL = 367/750.

ORDN. 1 MARKS :

T80054305 MUDASSIR KHAN MASOOD KHAN AMENA KHANUM , 71200967B , , PICT , T80054305

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	58	P
02. DATA COMMUNICATION	PP	100	40	52	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	58	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	67	P
05. THEORY OF COMPUTATION	PP	100	40	55	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	43	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	44	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	19	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	32	P
10. HARDWARE LABORATORY	TW	25	10	18	P
11. HARDWARE LABORATORY	PR	50	20	34	P

FIRST TERM TOTAL = 480/750.

ORDN. 1 MARKS :

T80054306 MUNMOON GHOSH REKHA , 71100902D , T80054291 , PICT , T80054306

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	59	P C	12. PRINCIPLES OF PROGRAMMING LANG.	PP	100	40	69	P C
02. DATA COMMUNICATION	PP	100	40	53	P C	13. COMPUTER NETWORKS	PP	100	40	55	P C
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	62	P C	14. FINANCE & MANAGEMENT INFORMA.SYS.	PP	100	40	55	P C
04. DIGITAL SIGNAL PROCESSING	PP	100	40	62	P C	15. SYSTEMS PROGRAMMING & OPERA.SYS.	PP	100	40	67	P C
05. THEORY OF COMPUTATION	PP	100	40	41	P C	16. SOFTWARE ENGINEERING	PP	100	40	49	P C
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	29	P C	17. SOFTWARE LABORATORY	TW	25	10	11	P C
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	37	P C	18. SOFTWARE LABORATORY	PR	50	20	22	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	15	P C	19. COMPUTER NETWORK	TW	25	10	13	P C
09. SIGNAL PROCESSING LABORATORY	OR	50	20	34	P C	20. COMPUTER NETWORK	OR	50	20	38	P
10. HARDWARE LABORATORY	TW	25	10	14	P C	21. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	20	P C
11. HARDWARE LABORATORY	PR	50	20	25	P C	22. SEMINAR AND TECHNICAL COMMUNI.	TW	50	20	35	P C

GRAND TOTAL = 865/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :



NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80054307 MUNOT BHAVESH RAJENDRA MANORAMA , 71200971L , , PICT , T80054307

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	50	P
02. DATA COMMUNICATION	PP	100	40	54	P
03. MICROPROCESSORS & MICROCONTROLLERPP		100	40	58	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	69	P
05. THEORY OF COMPUTATION	PP	100	40	64	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	40	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	43	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	19	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	29	P
10. HARDWARE LABORATORY	TW	25	10	22	P
11. HARDWARE LABORATORY	PR	50	20	40	P

FIRST TERM TOTAL = 488/750.

ORDN. 1 MARKS :

T80054308 MURARKA UTKARSH VINAY HEMLATA , 71200972J , , PICT , T80054308

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	48	P
02. DATA COMMUNICATION	PP	100	40	56	P
03. MICROPROCESSORS & MICROCONTROLLERPP		100	40	62	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	63	P
05. THEORY OF COMPUTATION	PP	100	40	40	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	26	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	36	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	14	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	21	P
10. HARDWARE LABORATORY	TW	25	10	12	P
11. HARDWARE LABORATORY	PR	50	20	16	F

FIRST TERM TOTAL = 394/750.

ORDN. 1 MARKS :

T80054309 MUZUMDAR VARSHA JAYANT SUNITA , 71200973G , , PICT , T80054309

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	72	P
02. DATA COMMUNICATION	PP	100	40	63	P
03. MICROPROCESSORS & MICROCONTROLLERPP		100	40	65	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	72	P
05. THEORY OF COMPUTATION	PP	100	40	58	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	42	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	35	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	19	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	38	P
10. HARDWARE LABORATORY	TW	25	10	19	P
11. HARDWARE LABORATORY	PR	50	20	42	P

FIRST TERM TOTAL = 525/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80054310 NADE APPURWA ANIL ARUNA , 71200974E , , PICT , T80054310

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	40	P
02. DATA COMMUNICATION	PP	100	40	44	P
03. MICROPROCESSORS & MICROCONTROLLERPP	PP	100	40	40	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	40	P
05. THEORY OF COMPUTATION	PP	100	40	48	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	37	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	39	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	17	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	20	P
10. HARDWARE LABORATORY	TW	25	10	17	P
11. HARDWARE LABORATORY	PR	50	20	41	P

FIRST TERM TOTAL = 383/750.

ORDN. 1 MARKS :

T80054311 NAHAR AKSHAY VIJAY MANGAL , 71200976M , , PICT , T80054311

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	60	P
02. DATA COMMUNICATION	PP	100	40	62	P
03. MICROPROCESSORS & MICROCONTROLLERPP	PP	100	40	53	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	74	P
05. THEORY OF COMPUTATION	PP	100	40	57	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	38	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	34	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	18	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	36	P
10. HARDWARE LABORATORY	TW	25	10	19	P
11. HARDWARE LABORATORY	PR	50	20	40	P

FIRST TERM TOTAL = 491/750.

ORDN. 1 MARKS :

T80054312 NAIK GANESH NILKANTH SUREKHA , 71347770K , , PICT , T80054312

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	64	P
02. DATA COMMUNICATION	PP	100	40	57	P
03. MICROPROCESSORS & MICROCONTROLLERPP	PP	100	40	48	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	59	P
05. THEORY OF COMPUTATION	PP	100	40	51	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	42	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	42	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	20	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	20	P
10. HARDWARE LABORATORY	TW	25	10	19	P
11. HARDWARE LABORATORY	PR	50	20	32	P

FIRST TERM TOTAL = 454/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.  
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80054313 NAIK SANDESH TARSING KAMAL , 71100908C , T80054293 , PICT , T80054313

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	63	P C	12. PRINCIPLES OF PROGRAMMING LANG.	PP	100	40	53	P C
02. DATA COMMUNICATION	PP	100	40	66	P C	13. COMPUTER NETWORKS	PP	100	40	43	P C
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	48	P C	14. FINANCE & MANAGEMENT INFORMA.SYS.	PP	100	40	59	P C
04. DIGITAL SIGNAL PROCESSING	PP	100	40	48	P C	15. SYSTEMS PROGRAMMING & OPERA.SYS.	PP	100	40	40	P C
05. THEORY OF COMPUTATION	PP	100	40	51	P C	16. SOFTWARE ENGINEERING	PP	100	40	48	P C
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	24	P C	17. SOFTWARE LABORATORY	TW	25	10	10	P C
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	25	P C	18. SOFTWARE LABORATORY	PR	50	20	35	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	10	P C	19. COMPUTER NETWORK	TW	25	10	10	P C
09. SIGNAL PROCESSING LABORATORY	OR	50	20	32	P C	20. COMPUTER NETWORK	OR	50	20	32	P
10. HARDWARE LABORATORY	TW	25	10	13	P C	21. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	20	P C
11. HARDWARE LABORATORY	PR	50	20	26	P C	22. SEMINAR AND TECHNICAL COMMUNI.	TW	50	20	20	P C

GRAND TOTAL = 776/1500, RESULT: SECOND CLASS

ORDN. 1 MARKS :

T80054314 NAINA BALANA KUSUM , 71200979F , , PICT , T80054314

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	62	P
02. DATA COMMUNICATION	PP	100	40	67	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	66	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	65	P
05. THEORY OF COMPUTATION	PP	100	40	69	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	33	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	38	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	15	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	22	P
10. HARDWARE LABORATORY	TW	25	10	15	P
11. HARDWARE LABORATORY	PR	50	20	36	P

FIRST TERM TOTAL = 488/750.

ORDN. 1 MARKS :

T80054315 NAVEEN KUMAR GUPTA ANITA , 71100915F , T80054295 , PICT , T80054315

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	75	P C	12. PRINCIPLES OF PROGRAMMING LANG.	PP	100	40	69	P C
02. DATA COMMUNICATION	PP	100	40	63	P C	13. COMPUTER NETWORKS	PP	100	40	61	P C
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	57	P C	14. FINANCE & MANAGEMENT INFORMA.SYS.	PP	100	40	63	P C
04. DIGITAL SIGNAL PROCESSING	PP	100	40	62	P C	15. SYSTEMS PROGRAMMING & OPERA.SYS.	PP	100	40	65	P C
05. THEORY OF COMPUTATION	PP	100	40	56	P C	16. SOFTWARE ENGINEERING	PP	100	40	55	P C
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	25	P C	17. SOFTWARE LABORATORY	TW	25	10	15	P C
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	38	P C	18. SOFTWARE LABORATORY	PR	50	20	35	P C
08. SIGNAL PROCESSING LABORATORY	TW	25	10	14	P C	19. COMPUTER NETWORK	TW	25	10	16	P C
09. SIGNAL PROCESSING LABORATORY	OR	50	20	35	P C	20. COMPUTER NETWORK	OR	50	20	30	P
10. HARDWARE LABORATORY	TW	25	10	15	P C	21. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	27	P C
11. HARDWARE LABORATORY	PR	50	20	30	P C	22. SEMINAR AND TECHNICAL COMMUNI.	TW	50	20	41	P C

GRAND TOTAL = 947/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80054316 NAZARKAR SHWETA SHRIPAD SMITA , 71200983D , , PICT , T80054316

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	51	P
02. DATA COMMUNICATION	PP	100	40	52	P
03. MICROPROCESSORS & MICROCONTROLLERPP		100	40	61	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	73	P
05. THEORY OF COMPUTATION	PP	100	40	63	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	39	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	37	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	19	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	25	P
10. HARDWARE LABORATORY	TW	25	10	19	P
11. HARDWARE LABORATORY	PR	50	20	35	P

FIRST TERM TOTAL = 474/750.

ORDN. 1 MARKS :

T80054317 NIKHIL DWIVEDI KUSUM , 71200988E , , PICT , T80054317

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	51	P
02. DATA COMMUNICATION	PP	100	40	46	P
03. MICROPROCESSORS & MICROCONTROLLERPP		100	40	52	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	69	P
05. THEORY OF COMPUTATION	PP	100	40	58	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	44	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	43	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	23	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	44	P
10. HARDWARE LABORATORY	TW	25	10	22	P
11. HARDWARE LABORATORY	PR	50	20	42	P

FIRST TERM TOTAL = 494/750.

ORDN. 1 MARKS :

T80054318 NIMKAR HRUSHIKESH CHARUDATTA SHARMILA , 71200989C , , PICT , T80054318

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	53	P
02. DATA COMMUNICATION	PP	100	40	51	P
03. MICROPROCESSORS & MICROCONTROLLERPP		100	40	56	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	74	P
05. THEORY OF COMPUTATION	PP	100	40	57	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	40	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	41	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	23	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	40	P
10. HARDWARE LABORATORY	TW	25	10	20	P
11. HARDWARE LABORATORY	PR	50	20	42	P

FIRST TERM TOTAL = 497/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80054319 NISHTHA KALRA SANGEETA , 71200990G , , PICT , T80054319

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	48	P
02. DATA COMMUNICATION	PP	100	40	51	P
03. MICROPROCESSORS & MICROCONTROLLERPP	PP	100	40	48	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	54	P
05. THEORY OF COMPUTATION	PP	100	40	52	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	35	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	37	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	15	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	24	P
10. HARDWARE LABORATORY	TW	25	10	15	P
11. HARDWARE LABORATORY	PR	50	20	02	F

FIRST TERM TOTAL = 381/750.

ORDN. 1 MARKS :

T80054320 PAGAR PRITESH DILIP PAGAR PALLAVI DILIP , 71347771H , , PICT , T80054320

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	63	P
02. DATA COMMUNICATION	PP	100	40	50	P
03. MICROPROCESSORS & MICROCONTROLLERPP	PP	100	40	57	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	62	P
05. THEORY OF COMPUTATION	PP	100	40	58	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	42	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	43	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	16	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	34	P
10. HARDWARE LABORATORY	TW	25	10	17	P
11. HARDWARE LABORATORY	PR	50	20	30	P

FIRST TERM TOTAL = 472/750.

ORDN. 1 MARKS :

T80054321 PAITHANKAR PRANETA VISHWANATH UTKARSHA , 71200995H , , PICT , T80054321

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	63	P
02. DATA COMMUNICATION	PP	100	40	59	P
03. MICROPROCESSORS & MICROCONTROLLERPP	PP	100	40	62	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	63	P
05. THEORY OF COMPUTATION	PP	100	40	54	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	40	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	38	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	19	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	35	P
10. HARDWARE LABORATORY	TW	25	10	20	P
11. HARDWARE LABORATORY	PR	50	20	41	P

FIRST TERM TOTAL = 494/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.  
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80054322 PANDE SWAPNIL SURESH SADHANA , 71100926M , T80054297 , PICT , T80054322

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	64	P C	12. PRINCIPLES OF PROGRAMMING LANG.	PP	100	40	68	P C
02. DATA COMMUNICATION	PP	100	40	69	P C	13. COMPUTER NETWORKS	PP	100	40	57	P C
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	56	P C	14. FINANCE & MANAGEMENT INFORMA.SYS.	PP	100	40	59	P C
04. DIGITAL SIGNAL PROCESSING	PP	100	40	62	P C	15. SYSTEMS PROGRAMMING & OPERA.SYS.	PP	100	40	56	P C
05. THEORY OF COMPUTATION	PP	100	40	48	P C	16. SOFTWARE ENGINEERING	PP	100	40	42	P C
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	38	P C	17. SOFTWARE LABORATORY	TW	25	10	17	P C
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	28	P C	18. SOFTWARE LABORATORY	PR	50	20	38	P C
08. SIGNAL PROCESSING LABORATORY	TW	25	10	16	P C	19. COMPUTER NETWORK	TW	25	10	18	P C
09. SIGNAL PROCESSING LABORATORY	OR	50	20	32	P C	20. COMPUTER NETWORK	OR	50	20	38	P
10. HARDWARE LABORATORY	TW	25	10	19	P C	21. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	37	P C
11. HARDWARE LABORATORY	PR	50	20	28	P C	22. SEMINAR AND TECHNICAL COMMUNI.	TW	50	20	41	P C

GRAND TOTAL = 931/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

T80054323 PANDHARE PRIYANKA RAVINDRA RAJANI , 71200998B , , PICT , T80054323

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	47	P
02. DATA COMMUNICATION	PP	100	40	40	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	47	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	42	P
05. THEORY OF COMPUTATION	PP	100	40	40	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	34	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	32	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	17	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	20	P
10. HARDWARE LABORATORY	TW	25	10	17	P
11. HARDWARE LABORATORY	PR	50	20	05	F

FIRST TERM TOTAL = 341/750.

ORDN. 1 MARKS :

T80054324 PARAG MAHALA PRAGYAN , 71201000K , , PICT , T80054324

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	42	P
02. DATA COMMUNICATION	PP	100	40	46	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	46	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	53	P
05. THEORY OF COMPUTATION	PP	100	40	43	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	38	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	28	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	17	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	20	P
10. HARDWARE LABORATORY	TW	25	10	17	P
11. HARDWARE LABORATORY	PR	50	20	35	P

FIRST TERM TOTAL = 385/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80054325 PARAKH RUSHABH GIRISH JYOTI , 71201002F , , PICT , T80054325

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	61	P
02. DATA COMMUNICATION	PP	100	40	55	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	47	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	63	P
05. THEORY OF COMPUTATION	PP	100	40	55	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	35	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	40	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	16	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	20	P
10. HARDWARE LABORATORY	TW	25	10	16	P
11. HARDWARE LABORATORY	PR	50	20	40	P

FIRST TERM TOTAL = 448/750.

ORDN. 1 MARKS :

T80054326 PATIL ABHILASHA VIJAY SANDHYA , 71201004B , , PICT , T80054326

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	72	P
02. DATA COMMUNICATION	PP	100	40	64	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	40	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	77	P
05. THEORY OF COMPUTATION	PP	100	40	71	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	38	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	35	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	18	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	32	P
10. HARDWARE LABORATORY	TW	25	10	20	P
11. HARDWARE LABORATORY	PR	50	20	41	P

FIRST TERM TOTAL = 508/750.

ORDN. 1 MARKS :

T80054327 PATIL PRATIK PRALHAD PRATIBHA , 71201008E , , PICT , T80054327

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	65	P
02. DATA COMMUNICATION	PP	100	40	58	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	43	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	59	P
05. THEORY OF COMPUTATION	PP	100	40	54	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	37	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	41	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	16	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	12	F
10. HARDWARE LABORATORY	TW	25	10	16	P
11. HARDWARE LABORATORY	PR	50	20	42	P

FIRST TERM TOTAL = 443/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.  
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80054328 PATIL VINEET JAYANT PRAJAKTA , 70601438D , T80054369 , PICT , T80054328

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	46	P C	12. PRINCIPLES OF PROGRAMMING LANG.	PP	100	40	45	P C
02. DATA COMMUNICATION	PP	100	40	AA	F	13. COMPUTER NETWORKS	PP	100	40	44	P C
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	AA	F	14. FINANCE & MANAGEMENT INFORMA.SYS.	PP	100	40	46	P C
04. DIGITAL SIGNAL PROCESSING	PP	100	40	41	P C	15. SYSTEMS PROGRAMMING & OPERA.SYS.	PP	100	40	62	P C
05. THEORY OF COMPUTATION	PP	100	40	44	P C	16. SOFTWARE ENGINEERING	PP	100	40	40	P C
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	20	P C	17. SOFTWARE LABORATORY	TW	25	10	10	P C
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	20	P	18. SOFTWARE LABORATORY	PR	50	20	36	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	11	P C	19. COMPUTER NETWORK	TW	25	10	12	P C
09. SIGNAL PROCESSING LABORATORY	OR	50	20	22	P C	20. COMPUTER NETWORK	OR	50	20	21	P C
10. HARDWARE LABORATORY	TW	25	10	10	P C	21. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	21	P C
11. HARDWARE LABORATORY	PR	50	20	27	P C	22. SEMINAR AND TECHNICAL COMMUNI.	TW	50	20	21	P C

GRAND TOTAL = 599/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

T80054329 PATTHE KAUSTUBH TEJRAM SHOBHA , 71347772F , PICT , T80054329

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	44	P
02. DATA COMMUNICATION	PP	100	40	53	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	33	F
04. DIGITAL SIGNAL PROCESSING	PP	100	40	42	P
05. THEORY OF COMPUTATION	PP	100	40	40	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	32	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	27	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	20	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	27	P
10. HARDWARE LABORATORY	TW	25	10	20	P
11. HARDWARE LABORATORY	PR	50	20	20	P

FIRST TERM TOTAL = 358/750.

ORDN. 1 MARKS :

T80054330 PAWAR NAYAN SUNIL NALINI , 71239304J , T80054305 , PICT , T80054330

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	66	P C	12. PRINCIPLES OF PROGRAMMING LANG.	PP	100	40	66	P C
02. DATA COMMUNICATION	PP	100	40	62	P C	13. COMPUTER NETWORKS	PP	100	40	59	P C
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	65	P C	14. FINANCE & MANAGEMENT INFORMA.SYS.	PP	100	40	65	P C
04. DIGITAL SIGNAL PROCESSING	PP	100	40	56	P C	15. SYSTEMS PROGRAMMING & OPERA.SYS.	PP	100	40	66	P C
05. THEORY OF COMPUTATION	PP	100	40	57	P C	16. SOFTWARE ENGINEERING	PP	100	40	66	P C
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	37	P C	17. SOFTWARE LABORATORY	TW	25	10	22	P C
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	33	P C	18. SOFTWARE LABORATORY	PR	50	20	40	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	19	P C	19. COMPUTER NETWORK	TW	25	10	20	P C
09. SIGNAL PROCESSING LABORATORY	OR	50	20	20	P C	20. COMPUTER NETWORK	OR	50	20	22	P C
10. HARDWARE LABORATORY	TW	25	10	21	P C	21. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	42	P C
11. HARDWARE LABORATORY	PR	50	20	34	P C	22. SEMINAR AND TECHNICAL COMMUNI.	TW	50	20	41	P C

GRAND TOTAL = 979/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :



NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.  
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80054331 PHARANDE ABHISHEK VIJAY RUKHMINEE , 71201015H , , PICT , T80054331

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	57	P
02. DATA COMMUNICATION	PP	100	40	62	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	44	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	66	P
05. THEORY OF COMPUTATION	PP	100	40	67	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	38	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	40	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	19	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	36	P
10. HARDWARE LABORATORY	TW	25	10	18	P
11. HARDWARE LABORATORY	PR	50	20	36	P

FIRST TERM TOTAL = 483/750.

ORDN. 1 MARKS :

T80054332 RAJEEV SEBASTIAN THRESA , 71201031K , , PICT , T80054332

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	62	P
02. DATA COMMUNICATION	PP	100	40	73	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	41	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	80	P
05. THEORY OF COMPUTATION	PP	100	40	71	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	42	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	43	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	23	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	38	P
10. HARDWARE LABORATORY	TW	25	10	22	P
11. HARDWARE LABORATORY	PR	50	20	40	P

FIRST TERM TOTAL = 535/750.

ORDN. 1 MARKS :

T80054333 RATHI ADITYA PRADIPKUMAR SNEHAL , 71201032H , , PICT , T80054333

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	40	P
02. DATA COMMUNICATION	PP	100	40	52	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	40	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	44	P
05. THEORY OF COMPUTATION	PP	100	40	47	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	35	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	30	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	18	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	20	P
10. HARDWARE LABORATORY	TW	25	10	17	P
11. HARDWARE LABORATORY	PR	50	20	25	P

FIRST TERM TOTAL = 368/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80054334 RATHI ROSHANI DIPAK JYOTI , 71100969E , T80054311 , PICT , T80054334

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	40	P C	12. PRINCIPLES OF PROGRAMMING LANG.	PP	100	40	57	P C
02. DATA COMMUNICATION	PP	100	40	55	P C	13. COMPUTER NETWORKS	PP	100	40	52	P C
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	50	P C	14. FINANCE & MANAGEMENT INFORMA.SYS.	PP	100	40	42	P C
04. DIGITAL SIGNAL PROCESSING	PP	100	40	45	P C	15. SYSTEMS PROGRAMMING & OPERA.SYS.	PP	100	40	56	P C
05. THEORY OF COMPUTATION	PP	100	40	67	P C	16. SOFTWARE ENGINEERING	PP	100	40	54	P C
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	29	P C	17. SOFTWARE LABORATORY	TW	25	10	20	P C
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	38	P C	18. SOFTWARE LABORATORY	PR	50	20	32	P C
08. SIGNAL PROCESSING LABORATORY	TW	25	10	18	P C	19. COMPUTER NETWORK	TW	25	10	19	P C
09. SIGNAL PROCESSING LABORATORY	OR	50	20	28	P C	20. COMPUTER NETWORK	OR	50	20	30	P
10. HARDWARE LABORATORY	TW	25	10	19	P C	21. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	32	P C
11. HARDWARE LABORATORY	PR	50	20	28	P C	22. SEMINAR AND TECHNICAL COMMUNI.	TW	50	20	33	P C

GRAND TOTAL = 844/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

T80054335 RATHORE DHRUVESH JAYESH JYOTI , 71201037J , , PICT , T80054335

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	68	P
02. DATA COMMUNICATION	PP	100	40	76	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	56	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	81	P
05. THEORY OF COMPUTATION	PP	100	40	76	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	44	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	44	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	24	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	43	P
10. HARDWARE LABORATORY	TW	25	10	23	P
11. HARDWARE LABORATORY	PR	50	20	41	P

FIRST TERM TOTAL = 576/750.

ORDN. 1 MARKS :

T80054336 RATHORE VIPULKUMAR RAMESH ANJANA , 71201038G , , PICT , T80054336

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	74	P
02. DATA COMMUNICATION	PP	100	40	63	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	45	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	76	P
05. THEORY OF COMPUTATION	PP	100	40	76	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	42	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	42	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	21	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	38	P
10. HARDWARE LABORATORY	TW	25	10	21	P
11. HARDWARE LABORATORY	PR	50	20	42	P

FIRST TERM TOTAL = 540/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.  
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80054337	RAUT VAIBHAV SHARAD				RUKMINI					, 71100973C			T80054312		PICT		T80054337
01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	54	P	C			12.	PRINCIPLES OF PROGRAMMING LANG.	PP	100	40	47	P	C
02.	DATA COMMUNICATION	PP	100	40	66	P	C			13.	COMPUTER NETWORKS	PP	100	40	51	P	C
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	54	P	C			14.	FINANCE & MANAGEMENT INFORMA.SYS.	PP	100	40	51	P	C
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	73	P	C			15.	SYSTEMS PROGRAMMING & OPERA.SYS.	PP	100	40	52	P	C
05.	THEORY OF COMPUTATION	PP	100	40	65	P	C			16.	SOFTWARE ENGINEERING	PP	100	40	49	P	C
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	23	P	C			17.	SOFTWARE LABORATORY	TW	25	10	15	P	C
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	39	P	C			18.	SOFTWARE LABORATORY	PR	50	20	34	P	C
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	10	P	C			19.	COMPUTER NETWORK	TW	25	10	15	P	C
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	35	P	C			20.	COMPUTER NETWORK	OR	50	20	28	P	
10.	HARDWARE LABORATORY	TW	25	10	13	P	C			21.	SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	31	P	C
11.	HARDWARE LABORATORY	PR	50	20	33	P	C			22.	SEMINAR AND TECHNICAL COMMUNI.	TW	50	20	31	P	C

GRAND TOTAL = 869/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

T80054338	RENUKE JAGDISH SHIVAJIRAO				VANAMALA					, 71100974M			T80054313		PICT		T80054338
01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	40	P	C			12.	PRINCIPLES OF PROGRAMMING LANG.	PP	100	40	52	P	C
02.	DATA COMMUNICATION	PP	100	40	48	P	C			13.	COMPUTER NETWORKS	PP	100	40	56	P	C
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	50	P	C			14.	FINANCE & MANAGEMENT INFORMA.SYS.	PP	100	40	55	P	C
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	43	P	C			15.	SYSTEMS PROGRAMMING & OPERA.SYS.	PP	100	40	47	P	C
05.	THEORY OF COMPUTATION	PP	100	40	50	P	C			16.	SOFTWARE ENGINEERING	PP	100	40	49	P	C
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	33	P	C			17.	SOFTWARE LABORATORY	TW	25	10	18	P	C
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	22	P	C			18.	SOFTWARE LABORATORY	PR	50	20	22	P	
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	16	P	C			19.	COMPUTER NETWORK	TW	25	10	18	P	C
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	20	P	C			20.	COMPUTER NETWORK	OR	50	20	28	P	
10.	HARDWARE LABORATORY	TW	25	10	19	P	C			21.	SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	30	P	C
11.	HARDWARE LABORATORY	PR	50	20	23	P	C			22.	SEMINAR AND TECHNICAL COMMUNI.	TW	50	20	33	P	C

GRAND TOTAL = 772/1500, RESULT: SECOND CLASS

ORDN. 1 MARKS :

T80054339	RISHI AGRAWAL				MANJU					, 71201039E					PICT		T80054339
01.	DATABASE MANAGEMENT SYSTEMS	PP	100	40	54	P											
02.	DATA COMMUNICATION	PP	100	40	57	P											
03.	MICROPROCESSORS & MICROCONTROLLER	PP	100	40	46	P											
04.	DIGITAL SIGNAL PROCESSING	PP	100	40	64	P											
05.	THEORY OF COMPUTATION	PP	100	40	57	P											
06.	RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	37	P											
07.	RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	38	P											
08.	SIGNAL PROCESSING LABORATORY	TW	25	10	16	P											
09.	SIGNAL PROCESSING LABORATORY	OR	50	20	22	P											
10.	HARDWARE LABORATORY	TW	25	10	19	P											
11.	HARDWARE LABORATORY	PR	50	20	35	P											

FIRST TERM TOTAL = 445/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80054340 ROHERA SHWETA VINOD VEENA , 71201041G , PICT , T80054340

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	50	P
02. DATA COMMUNICATION	PP	100	40	59	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	30	F
04. DIGITAL SIGNAL PROCESSING	PP	100	40	41	P
05. THEORY OF COMPUTATION	PP	100	40	54	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	31	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	26	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	15	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	20	P
10. HARDWARE LABORATORY	TW	25	10	15	P
11. HARDWARE LABORATORY	PR	50	20	36	P

FIRST TERM TOTAL = 377/750.

ORDN. 1 MARKS :

T80054341 RUIKAR AMEY SUHAS ARATI , 71201044M , PICT , T80054341

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	67	P
02. DATA COMMUNICATION	PP	100	40	68	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	53	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	71	P
05. THEORY OF COMPUTATION	PP	100	40	74	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	45	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	40	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	23	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	39	P
10. HARDWARE LABORATORY	TW	25	10	21	P
11. HARDWARE LABORATORY	PR	50	20	38	P

FIRST TERM TOTAL = 539/750.

ORDN. 1 MARKS :

T80054342 RUPNER GEETA MADHAV GOKULA , 71239305G , T80054314 , PICT , T80054342

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	60	P C	12. PRINCIPLES OF PROGRAMMING LANG.	PP	100	40	60	P C
02. DATA COMMUNICATION	PP	100	40	67	P C	13. COMPUTER NETWORKS	PP	100	40	63	P C
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	65	P C	14. FINANCE & MANAGEMENT INFORMA.SYS.	PP	100	40	72	P C
04. DIGITAL SIGNAL PROCESSING	PP	100	40	46	P C	15. SYSTEMS PROGRAMMING & OPERA.SYS.	PP	100	40	55	P C
05. THEORY OF COMPUTATION	PP	100	40	59	P C	16. SOFTWARE ENGINEERING	PP	100	40	59	P C
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	40	P C	17. SOFTWARE LABORATORY	TW	25	10	23	P C
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	45	P C	18. SOFTWARE LABORATORY	PR	50	20	39	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	23	P C	19. COMPUTER NETWORK	TW	25	10	23	P C
09. SIGNAL PROCESSING LABORATORY	OR	50	20	29	P C	20. COMPUTER NETWORK	OR	50	20	22	P C
10. HARDWARE LABORATORY	TW	25	10	23	P C	21. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	44	P C
11. HARDWARE LABORATORY	PR	50	20	35	P C	22. SEMINAR AND TECHNICAL COMMUNI.	TW	50	20	42	P C

GRAND TOTAL = 994/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80054343 SAISH SURESH SALI MEENA , 71201047F , PICT , T80054343

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	62	P
02. DATA COMMUNICATION	PP	100	40	62	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	51	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	75	P
05. THEORY OF COMPUTATION	PP	100	40	71	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	44	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	38	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	23	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	38	P
10. HARDWARE LABORATORY	TW	25	10	22	P
11. HARDWARE LABORATORY	PR	50	20	41	P

FIRST TERM TOTAL = 527/750.

ORDN. 1 MARKS :

T80054344 SALECHA DIVYA SANJAY NIRMALA , 71100983L , T80054315 , PICT , T80054344

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	46	P C	12. PRINCIPLES OF PROGRAMMING LANG.	PP	100	40	42	P C
02. DATA COMMUNICATION	PP	100	40	53	P C	13. COMPUTER NETWORKS	PP	100	40	41	P C
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	43	P C	14. FINANCE & MANAGEMENT INFORMA.SYS.	PP	100	40	40	P C
04. DIGITAL SIGNAL PROCESSING	PP	100	40	55	P C	15. SYSTEMS PROGRAMMING & OPERA.SYS.	PP	100	40	43	P C
05. THEORY OF COMPUTATION	PP	100	40	62	P C	16. SOFTWARE ENGINEERING	PP	100	40	40	P C
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	32	P C	17. SOFTWARE LABORATORY	TW	25	10	20	P C
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	37	P C	18. SOFTWARE LABORATORY	PR	50	20	12	F
08. SIGNAL PROCESSING LABORATORY	TW	25	10	18	P C	19. COMPUTER NETWORK	TW	25	10	20	P C
09. SIGNAL PROCESSING LABORATORY	OR	50	20	36	P C	20. COMPUTER NETWORK	OR	50	20	30	P C
10. HARDWARE LABORATORY	TW	25	10	18	P C	21. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	40	P C
11. HARDWARE LABORATORY	PR	50	20	32	P C	22. SEMINAR AND TECHNICAL COMMUNI.	TW	50	20	39	P C

GRAND TOTAL = 799/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

T80054346 SALVE HONEY SUNIL SABINA , 71201050F , PICT , T80054346

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	54	P
02. DATA COMMUNICATION	PP	100	40	47	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	41	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	61	P
05. THEORY OF COMPUTATION	PP	100	40	59	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	35	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	39	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	22	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	35	P
10. HARDWARE LABORATORY	TW	25	10	20	P
11. HARDWARE LABORATORY	PR	50	20	10	F

FIRST TERM TOTAL = 423/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80054347 SANAP VISHAL PRABHAKAR SUMATI , 71347773D , , PICT , T80054347

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	57	P
02. DATA COMMUNICATION	PP	100	40	64	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	49	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	60	P
05. THEORY OF COMPUTATION	PP	100	40	65	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	39	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	42	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	22	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	22	P
10. HARDWARE LABORATORY	TW	25	10	20	P
11. HARDWARE LABORATORY	PR	50	20	31	P

FIRST TERM TOTAL = 471/750.

ORDN. 1 MARKS :

T80054348 SANDEEP AGARWAL RITA , 70925587L , T80054375 , PICT , T80054348

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	47	P C	12. PRINCIPLES OF PROGRAMMING LANG.	PP	100	40	40	P C
02. DATA COMMUNICATION	PP	100	40	40	P C	13. COMPUTER NETWORKS	PP	100	40	41	P C
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	AA	F	14. FINANCE & MANAGEMENT INFORMA.SYS.	PP	100	40	40	P C
04. DIGITAL SIGNAL PROCESSING	PP	100	40	50	P C	15. SYSTEMS PROGRAMMING & OPERA.SYS.	PP	100	40	40	P
05. THEORY OF COMPUTATION	PP	100	40	50	P C	16. SOFTWARE ENGINEERING	PP	100	40	44	P C
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	34	P C	17. SOFTWARE LABORATORY	TW	25	10	10	P C
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	20	P C	18. SOFTWARE LABORATORY	PR	50	20	22	P C
08. SIGNAL PROCESSING LABORATORY	TW	25	10	15	P C	19. COMPUTER NETWORK	TW	25	10	11	P C
09. SIGNAL PROCESSING LABORATORY	OR	50	20	22	P C	20. COMPUTER NETWORK	OR	50	20	21	P C
10. HARDWARE LABORATORY	TW	25	10	17	P C	21. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	20	P C
11. HARDWARE LABORATORY	PR	50	20	30	P C	22. SEMINAR AND TECHNICAL COMMUNI.	TW	50	20	24	P C

GRAND TOTAL = 638/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

T80054349 SANDEEP JEEVRAJ PARMAR PUSHPA PARMAR , 71347774B , , PICT , T80054349

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	57	P
02. DATA COMMUNICATION	PP	100	40	60	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	40	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	54	P
05. THEORY OF COMPUTATION	PP	100	40	50	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	44	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	41	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	22	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	23	P
10. HARDWARE LABORATORY	TW	25	10	21	P
11. HARDWARE LABORATORY	PR	50	20	40	P

FIRST TERM TOTAL = 452/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.  
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80054350 SATHE VYANKTESHPRASAD GANESH BHAGYASHRI , 71201054J , PICT , T80054350

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	57	P
02. DATA COMMUNICATION	PP	100	40	64	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	42	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	55	P
05. THEORY OF COMPUTATION	PP	100	40	65	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	24	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	34	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	12	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	20	P
10. HARDWARE LABORATORY	TW	25	10	11	P
11. HARDWARE LABORATORY	PR	50	20	34	P

FIRST TERM TOTAL = 418/750.

ORDN. 1 MARKS :

T80054352 SAVAJI ADITYA JAYANT SHRUTI , 71100991M , T80054320 , PICT , T80054352

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	47	P C	12. PRINCIPLES OF PROGRAMMING LANG.	PP	100	40	48	P C
02. DATA COMMUNICATION	PP	100	40	72	P C	13. COMPUTER NETWORKS	PP	100	40	47	P C
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	44	P C	14. FINANCE & MANAGEMENT INFORMA.SYS.	PP	100	40	57	P C
04. DIGITAL SIGNAL PROCESSING	PP	100	40	54	P C	15. SYSTEMS PROGRAMMING & OPERA.SYS.	PP	100	40	49	P C
05. THEORY OF COMPUTATION	PP	100	40	40	P C	16. SOFTWARE ENGINEERING	PP	100	40	48	P C
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	35	P C	17. SOFTWARE LABORATORY	TW	25	10	21	P C
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	39	P C	18. SOFTWARE LABORATORY	PR	50	20	30	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	20	P C	19. COMPUTER NETWORK	TW	25	10	21	P C
09. SIGNAL PROCESSING LABORATORY	OR	50	20	38	P C	20. COMPUTER NETWORK	OR	50	20	25	P C
10. HARDWARE LABORATORY	TW	25	10	18	P C	21. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	38	P C
11. HARDWARE LABORATORY	PR	50	20	26	P C	22. SEMINAR AND TECHNICAL COMMUNI.	TW	50	20	38	P C

GRAND TOTAL = 855/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

T80054353 SAWANT TEJASWINI NANASAHEB SUNITA , 71100992K , T80054321 , PICT , T80054353

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	48	P C	12. PRINCIPLES OF PROGRAMMING LANG.	PP	100	40	56	P C
02. DATA COMMUNICATION	PP	100	40	58	P C	13. COMPUTER NETWORKS	PP	100	40	43	P C
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	47	P C	14. FINANCE & MANAGEMENT INFORMA.SYS.	PP	100	40	53	P C
04. DIGITAL SIGNAL PROCESSING	PP	100	40	40	P C	15. SYSTEMS PROGRAMMING & OPERA.SYS.	PP	100	40	46	P C
05. THEORY OF COMPUTATION	PP	100	40	47	P C	16. SOFTWARE ENGINEERING	PP	100	40	46	P C
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	32	P C	17. SOFTWARE LABORATORY	TW	25	10	19	P C
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	30	P C	18. SOFTWARE LABORATORY	PR	50	20	29	P C
08. SIGNAL PROCESSING LABORATORY	TW	25	10	16	P C	19. COMPUTER NETWORK	TW	25	10	21	P C
09. SIGNAL PROCESSING LABORATORY	OR	50	20	34	P C	20. COMPUTER NETWORK	OR	50	20	34	P
10. HARDWARE LABORATORY	TW	25	10	20	P C	21. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	36	P C
11. HARDWARE LABORATORY	PR	50	20	21	P C	22. SEMINAR AND TECHNICAL COMMUNI.	TW	50	20	36	P C

GRAND TOTAL = 812/1500, RESULT: SECOND CLASS

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80054354 SHAHANE TANMAY SHIVKUMAR VASUNDHARA , 71201062K , , PICT , T80054354

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	45	P
02. DATA COMMUNICATION	PP	100	40	40	P
03. MICROPROCESSORS & MICROCONTROLLERPP	PP	100	40	40	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	40	P
05. THEORY OF COMPUTATION	PP	100	40	44	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	20	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	36	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	15	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	13	F
10. HARDWARE LABORATORY	TW	25	10	13	P
11. HARDWARE LABORATORY	PR	50	20	31	P

FIRST TERM TOTAL = 337/750.

ORDN. 1 MARKS :

T80054355 SHELKANDE AKASH DAGADU SEETA , 71101000F , , PICT , T80054355

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	42	P
02. DATA COMMUNICATION	PP	100	40	32	F
03. MICROPROCESSORS & MICROCONTROLLERPP	PP	100	40	19	F
04. DIGITAL SIGNAL PROCESSING	PP	100	40	26	F
05. THEORY OF COMPUTATION	PP	100	40	32	F
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	22	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	30	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	15	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	20	P
10. HARDWARE LABORATORY	TW	25	10	15	P
11. HARDWARE LABORATORY	PR	50	20	32	P

FIRST TERM TOTAL = 285/750.

ORDN. 1 MARKS :

T80054356 SHENDKAR NISHIGANDHA NARSING SANGEETA , 71201067L , , PICT , T80054356

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	51	P
02. DATA COMMUNICATION	PP	100	40	57	P
03. MICROPROCESSORS & MICROCONTROLLERPP	PP	100	40	48	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	66	P
05. THEORY OF COMPUTATION	PP	100	40	65	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	41	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	42	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	22	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	20	P
10. HARDWARE LABORATORY	TW	25	10	21	P
11. HARDWARE LABORATORY	PR	50	20	40	P

FIRST TERM TOTAL = 473/750.

ORDN. 1 MARKS :



NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80054357 SHETE PRASHANT SAYAJI MEERA , 71201069G , , PICT , T80054357

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	66	P
02. DATA COMMUNICATION	PP	100	40	56	P
03. MICROPROCESSORS & MICROCONTROLLERPP		100	40	57	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	57	P
05. THEORY OF COMPUTATION	PP	100	40	72	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	35	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	37	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	21	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	23	P
10. HARDWARE LABORATORY	TW	25	10	20	P
11. HARDWARE LABORATORY	PR	50	20	28	P

FIRST TERM TOTAL = 472/750.

ORDN. 1 MARKS :

T80054358 SHETTY VIPLUV GANESH PUSHPA , 71201070L , , PICT , T80054358

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	49	P
02. DATA COMMUNICATION	PP	100	40	69	P
03. MICROPROCESSORS & MICROCONTROLLERPP		100	40	43	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	54	P
05. THEORY OF COMPUTATION	PP	100	40	68	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	40	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	40	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	21	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	36	P
10. HARDWARE LABORATORY	TW	25	10	20	P
11. HARDWARE LABORATORY	PR	50	20	45	P

FIRST TERM TOTAL = 485/750.

ORDN. 1 MARKS :

T80054359 SHEWALE NEHA SHAMRAO SHEWALE VAISHALI , 71347775L , , PICT , T80054359

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	45	P
02. DATA COMMUNICATION	PP	100	40	59	P
03. MICROPROCESSORS & MICROCONTROLLERPP		100	40	32	F
04. DIGITAL SIGNAL PROCESSING	PP	100	40	46	P
05. THEORY OF COMPUTATION	PP	100	40	60	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	40	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	36	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	21	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	21	P
10. HARDWARE LABORATORY	TW	25	10	18	P
11. HARDWARE LABORATORY	PR	50	20	30	P

FIRST TERM TOTAL = 408/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80054360 SHIKHA AGARWAL RITA , 71101003L , , PICT , T80054360

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	61	P
02. DATA COMMUNICATION	PP	100	40	63	P
03. MICROPROCESSORS & MICROCONTROLLERPP		100	40	40	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	52	P
05. THEORY OF COMPUTATION	PP	100	40	52	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	38	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	34	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	20	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	21	P
10. HARDWARE LABORATORY	TW	25	10	19	P
11. HARDWARE LABORATORY	PR	50	20	36	P

FIRST TERM TOTAL = 436/750.

ORDN. 1 MARKS :

T80054361 SHUBHAM SINGH JAISHREE , 70601498H , T80054379 , PICT , T80054361

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	AA	F
02. DATA COMMUNICATION	PP	100	40	AA	F
03. MICROPROCESSORS & MICROCONTROLLERPP		100	40	AA	F
04. DIGITAL SIGNAL PROCESSING	PP	100	40	AA	F
05. THEORY OF COMPUTATION	PP	100	40	AA	F
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	20	P C
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	AA	F
08. SIGNAL PROCESSING LABORATORY	TW	25	10	11	P C
09. SIGNAL PROCESSING LABORATORY	OR	50	20	20	P C
10. HARDWARE LABORATORY	TW	25	10	10	P C
11. HARDWARE LABORATORY	PR	50	20	20	P C

FIRST TERM TOTAL = 81/750.

ORDN. 1 MARKS :

T80054362 SIDDHANT SURENDRA GADRE VIDULA , 71201074C , , PICT , T80054362

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	57	P
02. DATA COMMUNICATION	PP	100	40	63	P
03. MICROPROCESSORS & MICROCONTROLLERPP		100	40	41	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	51	P
05. THEORY OF COMPUTATION	PP	100	40	59	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	35	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	40	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	20	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	28	P
10. HARDWARE LABORATORY	TW	25	10	17	P
11. HARDWARE LABORATORY	PR	50	20	29	P

FIRST TERM TOTAL = 440/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80054363 SIDHANT AGGARWAL ALPANA , 71201076K , , PICT , T80054363

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	58	P
02. DATA COMMUNICATION	PP	100	40	58	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	40	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	53	P
05. THEORY OF COMPUTATION	PP	100	40	51	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	41	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	41	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	21	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	24	P
10. HARDWARE LABORATORY	TW	25	10	20	P
11. HARDWARE LABORATORY	PR	50	20	36	P

FIRST TERM TOTAL = 443/750.

ORDN. 1 MARKS :

T80054364 SOMDEEP DEY JHUMA , 71201078F , , PICT , T80054364

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	67	P
02. DATA COMMUNICATION	PP	100	40	65	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	46	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	56	P
05. THEORY OF COMPUTATION	PP	100	40	73	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	40	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	40	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	22	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	39	P
10. HARDWARE LABORATORY	TW	25	10	22	P
11. HARDWARE LABORATORY	PR	50	20	36	P

FIRST TERM TOTAL = 506/750.

ORDN. 1 MARKS :

T80054365 SUKET SHARMA SEEMA , 71201081F , , PICT , T80054365

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	68	P
02. DATA COMMUNICATION	PP	100	40	68	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	45	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	74	P
05. THEORY OF COMPUTATION	PP	100	40	78	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	46	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	46	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	24	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	22	P
10. HARDWARE LABORATORY	TW	25	10	23	P
11. HARDWARE LABORATORY	PR	50	20	45	P

FIRST TERM TOTAL = 539/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.  
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80054366 SURADKAR VIVEK PRAKASH DEEPALI , 71201083B , , PICT , T80054366

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	57	P
02. DATA COMMUNICATION	PP	100	40	64	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	49	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	59	P
05. THEORY OF COMPUTATION	PP	100	40	61	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	35	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	42	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	20	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	20	P
10. HARDWARE LABORATORY	TW	25	10	19	P
11. HARDWARE LABORATORY	PR	50	20	21	P

FIRST TERM TOTAL = 447/750.

ORDN. 1 MARKS :

T80054367 SURUCHI SINGH RENU , 71201084L , , PICT , T80054367

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	58	P
02. DATA COMMUNICATION	PP	100	40	54	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	40	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	51	P
05. THEORY OF COMPUTATION	PP	100	40	29	F
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	36	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	37	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	19	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	20	P
10. HARDWARE LABORATORY	TW	25	10	18	P
11. HARDWARE LABORATORY	PR	50	20	21	P

FIRST TERM TOTAL = 383/750.

ORDN. 1 MARKS :

T80054368 SUTAR ASHUTOSH SANJAY SUTAR MANISHA SANJAY , 71347776J , , PICT , T80054368

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	48	P
02. DATA COMMUNICATION	PP	100	40	AA	F
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	45	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	44	P
05. THEORY OF COMPUTATION	PP	100	40	50	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	35	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	36	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	18	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	20	P
10. HARDWARE LABORATORY	TW	25	10	17	P
11. HARDWARE LABORATORY	PR	50	20	36	P

FIRST TERM TOTAL = 349/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80054369 SUYASH PRAMOD MULBAGAL SHAILAJA , 71201086G , , PICT , T80054369

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	53	P
02. DATA COMMUNICATION	PP	100	40	41	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	40	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	46	P
05. THEORY OF COMPUTATION	PP	100	40	31	F
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	35	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	34	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	18	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	30	P
10. HARDWARE LABORATORY	TW	25	10	18	P
11. HARDWARE LABORATORY	PR	50	20	28	P

FIRST TERM TOTAL = 374/750.

ORDN. 1 MARKS :

T80054370 SWAPNIL KUMAR LANKE SUNITA , 71201087E , , PICT , T80054370

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	56	P
02. DATA COMMUNICATION	PP	100	40	55	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	43	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	42	P
05. THEORY OF COMPUTATION	PP	100	40	43	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	20	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	30	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	12	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	10	F
10. HARDWARE LABORATORY	TW	25	10	12	P
11. HARDWARE LABORATORY	PR	50	20	29	P

FIRST TERM TOTAL = 352/750.

ORDN. 1 MARKS :

T80054371 TAMHANE NEIL AJIT VAIBHAVI , 71201091C , , PICT , T80054371

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	43	P
02. DATA COMMUNICATION	PP	100	40	41	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	33	F
04. DIGITAL SIGNAL PROCESSING	PP	100	40	49	P
05. THEORY OF COMPUTATION	PP	100	40	25	F
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	38	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	30	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	19	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	12	F
10. HARDWARE LABORATORY	TW	25	10	19	P
11. HARDWARE LABORATORY	PR	50	20	36	P

FIRST TERM TOTAL = 345/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.  
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80054372 TAYADE SUSHANT RAMESH NANDINI , 71201094H , PICT , T80054372

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	AA	F
02. DATA COMMUNICATION	PP	100	40	32	F
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	AA	F
04. DIGITAL SIGNAL PROCESSING	PP	100	40	AA	F
05. THEORY OF COMPUTATION	PP	100	40	AA	F
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	20	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	31	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	12	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	10	F
10. HARDWARE LABORATORY	TW	25	10	12	P
11. HARDWARE LABORATORY	PR	50	20	AA	F

FIRST TERM TOTAL = 117/750.

ORDN. 1 MARKS :

T80054373 THAKARE SAGAR SATISH JAYASHREE , 70925632K , T80054381 , PICT , T80054373

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	42	P C	12. PRINCIPLES OF PROGRAMMING LANG.	PP	100	40	45	P C
02. DATA COMMUNICATION	PP	100	40	47	P C	13. COMPUTER NETWORKS	PP	100	40	40	P C
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	40	P C	14. FINANCE & MANAGEMENT INFORMA.SYS.	PP	100	40	48	P C
04. DIGITAL SIGNAL PROCESSING	PP	100	40	40	P	15. SYSTEMS PROGRAMMING & OPERA.SYS.	PP	100	40	59	P C
05. THEORY OF COMPUTATION	PP	100	40	40	P C	16. SOFTWARE ENGINEERING	PP	100	40	47	P C
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	30	P C	17. SOFTWARE LABORATORY	TW	25	10	10	P C
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	37	P C	18. SOFTWARE LABORATORY	PR	50	20	20\$	P C
08. SIGNAL PROCESSING LABORATORY	TW	25	10	12	P C	19. COMPUTER NETWORK	TW	25	10	11	P C
09. SIGNAL PROCESSING LABORATORY	OR	50	20	25	P C	20. COMPUTER NETWORK	OR	50	20	21	P C
10. HARDWARE LABORATORY	TW	25	10	15	P C	21. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	20	P C
11. HARDWARE LABORATORY	PR	50	20	21	P C	22. SEMINAR AND TECHNICAL COMMUNI.	TW	50	20	33	P C

GRAND TOTAL = 703/1500, RESULT: PASS CLASS [\$ 0.1]

ORDN. 1 MARKS : (18)2,

T80054374 THAKUR DHIRAJ SING UDAYSING SAROJADEVI , 71045648E , T80054335 , PICT , T80054374

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	44	P	12. PRINCIPLES OF PROGRAMMING LANG.	PP	100	40	41	P C
02. DATA COMMUNICATION	PP	100	40	28	F	13. COMPUTER NETWORKS	PP	100	40	40	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	40	P C	14. FINANCE & MANAGEMENT INFORMA.SYS.	PP	100	40	42	P C
04. DIGITAL SIGNAL PROCESSING	PP	100	40	AA	F	15. SYSTEMS PROGRAMMING & OPERA.SYS.	PP	100	40	11	F
05. THEORY OF COMPUTATION	PP	100	40	40	P C	16. SOFTWARE ENGINEERING	PP	100	40	41	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	20	P C	17. SOFTWARE LABORATORY	TW	25	10	12	P C
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	27	P C	18. SOFTWARE LABORATORY	PR	50	20	36	P C
08. SIGNAL PROCESSING LABORATORY	TW	25	10	10	P C	19. COMPUTER NETWORK	TW	25	10	12	P C
09. SIGNAL PROCESSING LABORATORY	OR	50	20	22	P C	20. COMPUTER NETWORK	OR	50	20	35	P
10. HARDWARE LABORATORY	TW	25	10	10	P C	21. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	24	P C
11. HARDWARE LABORATORY	PR	50	20	29	P C	22. SEMINAR AND TECHNICAL COMMUNI.	TW	50	20	20	P C

GRAND TOTAL = 584/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.  
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80054375 TIPARE ROHAN RAVINDRA KALPANA , 71201097B , , PICT , T80054375

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	54	P
02. DATA COMMUNICATION	PP	100	40	57	P
03. MICROPROCESSORS & MICROCONTROLLERPP		100	40	50	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	68	P
05. THEORY OF COMPUTATION	PP	100	40	58	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	42	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	37	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	21	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	10	F
10. HARDWARE LABORATORY	TW	25	10	21	P
11. HARDWARE LABORATORY	PR	50	20	39	P

FIRST TERM TOTAL = 457/750.

ORDN. 1 MARKS :

T80054376 UDATE PIYUSH NAGANATH NAINA , 71201099J , , PICT , T80054376

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	60	P
02. DATA COMMUNICATION	PP	100	40	61	P
03. MICROPROCESSORS & MICROCONTROLLERPP		100	40	53	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	75	P
05. THEORY OF COMPUTATION	PP	100	40	55	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	39	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	38	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	22	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	36	P
10. HARDWARE LABORATORY	TW	25	10	19	P
11. HARDWARE LABORATORY	PR	50	20	39	P

FIRST TERM TOTAL = 497/750.

ORDN. 1 MARKS :

T80054377 UNAVANE RUTUJA RAJESH ANJALI , 71201100F , , PICT , T80054377

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	72	P
02. DATA COMMUNICATION	PP	100	40	61	P
03. MICROPROCESSORS & MICROCONTROLLERPP		100	40	58	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	77	P
05. THEORY OF COMPUTATION	PP	100	40	53	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	43	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	44	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	23	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	20	P
10. HARDWARE LABORATORY	TW	25	10	23	P
11. HARDWARE LABORATORY	PR	50	20	40	P

FIRST TERM TOTAL = 514/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80054378 VALLARI ANAND RACHANA , 71201104J , , PICT , T80054378

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	46	P
02. DATA COMMUNICATION	PP	100	40	40	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	26	F
04. DIGITAL SIGNAL PROCESSING	PP	100	40	40	P
05. THEORY OF COMPUTATION	PP	100	40	28	F
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	34	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	37	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	17	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	30	P
10. HARDWARE LABORATORY	TW	25	10	16	P
11. HARDWARE LABORATORY	PR	50	20	10	F

FIRST TERM TOTAL = 324/750.

ORDN. 1 MARKS :

T80054379 VARADE POOJA VIJAY CHANDA , 71201105G , , PICT , T80054379

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	78	P
02. DATA COMMUNICATION	PP	100	40	50	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	53	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	77	P
05. THEORY OF COMPUTATION	PP	100	40	64	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	38	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	36	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	21	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	39	P
10. HARDWARE LABORATORY	TW	25	10	21	P
11. HARDWARE LABORATORY	PR	50	20	38	P

FIRST TERM TOTAL = 515/750.

ORDN. 1 MARKS :

T80054380 VARMA RASHMI RAVI LEELA , 71201106E , , PICT , T80054380

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	53	P
02. DATA COMMUNICATION	PP	100	40	53	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	43	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	40	P
05. THEORY OF COMPUTATION	PP	100	40	40	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	28	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	25	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	17	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	20	P
10. HARDWARE LABORATORY	TW	25	10	18	P
11. HARDWARE LABORATORY	PR	50	20	36	P

FIRST TERM TOTAL = 373/750.

ORDN. 1 MARKS :



NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80054381 VASAVE SUSMITA MANUVEL INDU , 71201107C , , PICT , T80054381

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	61	P
02. DATA COMMUNICATION	PP	100	40	49	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	48	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	69	P
05. THEORY OF COMPUTATION	PP	100	40	54	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	38	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	30	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	20	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	13	F
10. HARDWARE LABORATORY	TW	25	10	18	P
11. HARDWARE LABORATORY	PR	50	20	38	P

FIRST TERM TOTAL = 438/750.

ORDN. 1 MARKS :

T80054382 VAZIRABADKAR KETAN MADHAVRAO ANITA , 71201108M , , PICT , T80054382

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	63	P
02. DATA COMMUNICATION	PP	100	40	55	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	49	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	63	P
05. THEORY OF COMPUTATION	PP	100	40	43	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	35	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	30	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	20	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	20	P
10. HARDWARE LABORATORY	TW	25	10	19	P
11. HARDWARE LABORATORY	PR	50	20	32	P

FIRST TERM TOTAL = 429/750.

ORDN. 1 MARKS :

T80054383 VIKRAM SUNIL PATIL JAYMALA , 71201109K , , PICT , T80054383

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	61	P
02. DATA COMMUNICATION	PP	100	40	52	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	55	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	55	P
05. THEORY OF COMPUTATION	PP	100	40	43	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	39	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	41	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	23	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	36	P
10. HARDWARE LABORATORY	TW	25	10	19	P
11. HARDWARE LABORATORY	PR	50	20	35	P

FIRST TERM TOTAL = 459/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80054384 VISPUTE PRIYANKA SUNIL ANITA , 71201112K , , PICT , T80054384

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	59	P
02. DATA COMMUNICATION	PP	100	40	45	P
03. MICROPROCESSORS & MICROCONTROLLERPP		100	40	33	F
04. DIGITAL SIGNAL PROCESSING	PP	100	40	57	P
05. THEORY OF COMPUTATION	PP	100	40	40	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	35	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	37	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	21	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	38	P
10. HARDWARE LABORATORY	TW	25	10	20	P
11. HARDWARE LABORATORY	PR	50	20	22	P

FIRST TERM TOTAL = 407/750.

ORDN. 1 MARKS :

T80054385 WAGH SHARAD DADASAHEB DAYABAI , 71347778E , , PICT , T80054385

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	71	P
02. DATA COMMUNICATION	PP	100	40	58	P
03. MICROPROCESSORS & MICROCONTROLLERPP		100	40	49	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	40	P
05. THEORY OF COMPUTATION	PP	100	40	31	F
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	34	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	23	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	15	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	11	F
10. HARDWARE LABORATORY	TW	25	10	17	P
11. HARDWARE LABORATORY	PR	50	20	05	F

FIRST TERM TOTAL = 354/750.

ORDN. 1 MARKS :

T80054386 WALVE SUWAS NANDAKUMAR SAMPADA , 71201116B , , PICT , T80054386

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	47	P
02. DATA COMMUNICATION	PP	100	40	54	P
03. MICROPROCESSORS & MICROCONTROLLERPP		100	40	52	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	69	P
05. THEORY OF COMPUTATION	PP	100	40	46	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	34	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	34	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	20	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	32	P
10. HARDWARE LABORATORY	TW	25	10	16	P
11. HARDWARE LABORATORY	PR	50	20	32	P

FIRST TERM TOTAL = 436/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80054387 WALVEKAR PARITOSH MANDAR APARNA , 71201117L , , PICT , T80054387

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	71	P
02. DATA COMMUNICATION	PP	100	40	55	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	51	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	48	P
05. THEORY OF COMPUTATION	PP	100	40	40	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	35	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	38	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	17	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	25	P
10. HARDWARE LABORATORY	TW	25	10	17	P
11. HARDWARE LABORATORY	PR	50	20	31	P

FIRST TERM TOTAL = 428/750.

ORDN. 1 MARKS :

T80054388 YADAV PRAKHAR SUNILKUMAR CHHAYA , 71101059F , , PICT , T80054388

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	AA	F
02. DATA COMMUNICATION	PP	100	40	AA	F
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	AA	F
04. DIGITAL SIGNAL PROCESSING	PP	100	40	AA	F
05. THEORY OF COMPUTATION	PP	100	40	30	F
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	25	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	24	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	15	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	22	P
10. HARDWARE LABORATORY	TW	25	10	15	P
11. HARDWARE LABORATORY	PR	50	20	26	P

FIRST TERM TOTAL = 157/750.

ORDN. 1 MARKS :

T80054389 YAWALKAR VIBHAV VIRENDRA SHARAYU , 71201122G , , PICT , T80054389

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	49	P
02. DATA COMMUNICATION	PP	100	40	47	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	53	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	62	P
05. THEORY OF COMPUTATION	PP	100	40	43	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	39	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	26	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	21	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	40	P
10. HARDWARE LABORATORY	TW	25	10	17	P
11. HARDWARE LABORATORY	PR	50	20	30	P

FIRST TERM TOTAL = 427/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80054390 YEREKAR KARISHMA TUKARAM RANJANA , 71101064B , T80054342 , PICT , T80054390

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	44	P C	12. PRINCIPLES OF PROGRAMMING LANG.	PP	100	40	57	P C
02. DATA COMMUNICATION	PP	100	40	67	P C	13. COMPUTER NETWORKS	PP	100	40	60	P C
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	58	P C	14. FINANCE & MANAGEMENT INFORMA.SYS.	PP	100	40	60	P C
04. DIGITAL SIGNAL PROCESSING	PP	100	40	53	P C	15. SYSTEMS PROGRAMMING & OPERA.SYS.	PP	100	40	54	P C
05. THEORY OF COMPUTATION	PP	100	40	52	P C	16. SOFTWARE ENGINEERING	PP	100	40	56	P C
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	34	P C	17. SOFTWARE LABORATORY	TW	25	10	20	P C
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	32	P C	18. SOFTWARE LABORATORY	PR	50	20	43	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	18	P C	19. COMPUTER NETWORK	TW	25	10	21	P C
09. SIGNAL PROCESSING LABORATORY	OR	50	20	30	P C	20. COMPUTER NETWORK	OR	50	20	25	P C
10. HARDWARE LABORATORY	TW	25	10	21	P C	21. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	35	P C
11. HARDWARE LABORATORY	PR	50	20	30	P C	22. SEMINAR AND TECHNICAL COMMUNI.	TW	50	20	36	P C

GRAND TOTAL = 906/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

T80054391 YEWALE SHUBHANGI KAILAS MANISHA , 71101065L , , PICT , T80054391

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	52	P
02. DATA COMMUNICATION	PP	100	40	44	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	40	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	49	P
05. THEORY OF COMPUTATION	PP	100	40	26	F
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	30	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	30	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	18	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	10	F
10. HARDWARE LABORATORY	TW	25	10	18	P
11. HARDWARE LABORATORY	PR	50	20	25	P

FIRST TERM TOTAL = 342/750.

ORDN. 1 MARKS :

T80054392 ZAGADE VISHAL RAJU SARALA , 71201124C , , PICT , T80054392

01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	55	P
02. DATA COMMUNICATION	PP	100	40	45	P
03. MICROPROCESSORS & MICROCONTROLLER	PP	100	40	49	P
04. DIGITAL SIGNAL PROCESSING	PP	100	40	54	P
05. THEORY OF COMPUTATION	PP	100	40	40	P
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	30	P
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	34	P
08. SIGNAL PROCESSING LABORATORY	TW	25	10	17	P
09. SIGNAL PROCESSING LABORATORY	OR	50	20	20	P
10. HARDWARE LABORATORY	TW	25	10	16	P
11. HARDWARE LABORATORY	PR	50	20	36	P

FIRST TERM TOTAL = 396/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.  
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80058501 AAYUSH FUSKELAY SHASHI , 71100711L , T80058501 , PICT , T80058501

01. OPERATING SYSTEM	PP	100	40	72	P C	13. SYSTEM SOFTWARE PROGRAMMING	PP	100	40	52	P C
02. THEORY OF COMPUTATION	PP	100	40	54	P C	14. MANAGEMENT INFORMATION SYSTEMS	PP	100	40	54	P C
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	57	P C	15. PROGRAMMING PARADIGMS	PP	100	40	57	P C
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	71	P C	16. DESIGN & ANALYSIS OF ALGORITHMS	PP	100	40	63	P C
05. SOFTWARE ENGINEERING	PP	100	40	53	P C	17. HUMAN COMPU.INTERACTION & USABI.	PP	100	40	57	P C
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	21	P C	18. SOFTWARE DESIGN LABORATORY	TW	50	20	21	P C
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	35	P C	19. SOFTWARE DESIGN LABORATORY	PR	50	20	36	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	18	P C	20. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	30	P C
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	38	P C	21. SOFTWARE DEVELOPMENT TOOLS LAB.	OR	50	20	41	P C
10. NETWORK LABORATORY	TW	25	10	18	P C	22. SEMINAR AND TECHNICAL COMMUN.	TW	50	20	36	P C
11. NETWORK LABORATORY	OR	50	20	39	P C						
12. SOFT SKILLS LABORATORY	TW	25	10	18	P C						

GRAND TOTAL = 941/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

T80058502 ABHALE SAURABH BHANUDAS SUNITA , 71200746G , , PICT , T80058502

01. OPERATING SYSTEM	PP	100	40	51	P						
02. THEORY OF COMPUTATION	PP	100	40	64	P						
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	71	P						
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	60	P						
05. SOFTWARE ENGINEERING	PP	100	40	68	P						
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	21	P						
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	42	P						
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	22	P						
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	44	P						
10. NETWORK LABORATORY	TW	25	10	22	P						
11. NETWORK LABORATORY	OR	50	20	36	P						
12. SOFT SKILLS LABORATORY	TW	25	10	21	P						

FIRST TERM TOTAL = 522/750.

ORDN. 1 MARKS :

T80058503 ADARSH KUMAR KIRAN , 71200750E , , PICT , T80058503

01. OPERATING SYSTEM	PP	100	40	12	F						
02. THEORY OF COMPUTATION	PP	100	40	AA	F						
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	64	P						
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	47	P						
05. SOFTWARE ENGINEERING	PP	100	40	AA	F						
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	10	P						
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	AA	F						
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	10	P						
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	AA	F						
10. NETWORK LABORATORY	TW	25	10	10	P						
11. NETWORK LABORATORY	OR	50	20	AA	F						
12. SOFT SKILLS LABORATORY	TW	25	10	10	P						

FIRST TERM TOTAL = 163/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80058504 ADITYA SINGH SOLANKI DIMPLE , 71100719F , T80058504 , PICT , T80058504

01. OPERATING SYSTEM	PP	100	40	40	P C	13. SYSTEM SOFTWARE PROGRAMMING	PP	100	40	40	P C
02. THEORY OF COMPUTATION	PP	100	40	42	P	14. MANAGEMENT INFORMATION SYSTEMS	PP	100	40	40	P C
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	46	P C	15. PROGRAMMING PARADIGMS	PP	100	40	45	P C
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	40	P C	16. DESIGN & ANALYSIS OF ALGORITHMS	PP	100	40	40	P C
05. SOFTWARE ENGINEERING	PP	100	40	43	P C	17. HUMAN COMPU.INTERACTION & USABI.	PP	100	40	47	P C
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	15	P C	18. SOFTWARE DESIGN LABORATORY	TW	50	20	20	P C
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	28	P C	19. SOFTWARE DESIGN LABORATORY	PR	50	20	32	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	13	P C	20. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	28	P C
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	20	P C	21. SOFTWARE DEVELOPMENT TOOLS LAB.	OR	50	20	26	P
10. NETWORK LABORATORY	TW	25	10	15	P C	22. SEMINAR AND TECHNICAL COMMUN.	TW	50	20	26	P C
11. NETWORK LABORATORY	OR	50	20	20	P						
12. SOFT SKILLS LABORATORY	TW	25	10	14	P C						

GRAND TOTAL = 680/1500, RESULT: PASS CLASS

ORDN. 1 MARKS :

T80058505 AHIRE AKSHATA RAJENDRA MEENAKSHI , 71200752M , , PICT , T80058505

01. OPERATING SYSTEM	PP	100	40	56	P
02. THEORY OF COMPUTATION	PP	100	40	59	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	50	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	49	P
05. SOFTWARE ENGINEERING	PP	100	40	45	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	22	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	38	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	20	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	20	P
10. NETWORK LABORATORY	TW	25	10	22	P
11. NETWORK LABORATORY	OR	50	20	25	P
12. SOFT SKILLS LABORATORY	TW	25	10	22	P

FIRST TERM TOTAL = 428/750.

ORDN. 1 MARKS :

T80058506 AKSHAY A ARLIKATTI SHANTHI , 71200755F , , PICT , T80058506

01. OPERATING SYSTEM	PP	100	40	52	P
02. THEORY OF COMPUTATION	PP	100	40	45	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	58	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	54	P
05. SOFTWARE ENGINEERING	PP	100	40	44	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	20	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	40	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	20	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	45	P
10. NETWORK LABORATORY	TW	25	10	20	P
11. NETWORK LABORATORY	OR	50	20	35	P
12. SOFT SKILLS LABORATORY	TW	25	10	16	P

FIRST TERM TOTAL = 449/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80058507 AMAN KUMAR NIGAM ROOPALI NIGAM , 71200758L , , PICT , T80058507

01. OPERATING SYSTEM	PP	100	40	51	P
02. THEORY OF COMPUTATION	PP	100	40	48	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	56	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	52	P
05. SOFTWARE ENGINEERING	PP	100	40	55	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	22	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	45	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	20	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	39	P
10. NETWORK LABORATORY	TW	25	10	20	P
11. NETWORK LABORATORY	OR	50	20	32	P
12. SOFT SKILLS LABORATORY	TW	25	10	17	P

FIRST TERM TOTAL = 457/750.

ORDN. 1 MARKS :

T80058508 ANKIT V BANSAL URMILA , 71045371L , , PICT , T80058508

01. OPERATING SYSTEM	PP	100	40	16	F
02. THEORY OF COMPUTATION	PP	100	40	22	F
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	46	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	43	P
05. SOFTWARE ENGINEERING	PP	100	40	50	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	18	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	AA	F
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	13	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	03	F
10. NETWORK LABORATORY	TW	25	10	13	P
11. NETWORK LABORATORY	OR	50	20	25	P
12. SOFT SKILLS LABORATORY	TW	25	10	12	P

FIRST TERM TOTAL = 261/750.

ORDN. 1 MARKS :

T80058509 ANUSHKA GHOGALE NIVEDITA , 70925337M , T80058512 , PICT , T80058509

01. OPERATING SYSTEM	PP	100	40	55	P
02. THEORY OF COMPUTATION	PP	100	40	58	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	69	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	59	P
05. SOFTWARE ENGINEERING	PP	100	40	59	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	20	P C
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	20	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	11	P C
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	35	P
10. NETWORK LABORATORY	TW	25	10	17	P C
11. NETWORK LABORATORY	OR	50	20	28	P C
12. SOFT SKILLS LABORATORY	TW	25	10	12	P C

FIRST TERM TOTAL = 443/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80058510 ARIJIT PANDE ANNAPURNA , 71200764E , PICT , T80058510

01. OPERATING SYSTEM	PP	100	40	56	P
02. THEORY OF COMPUTATION	PP	100	40	45	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	63	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	63	P
05. SOFTWARE ENGINEERING	PP	100	40	61	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	23	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	40	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	23	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	41	P
10. NETWORK LABORATORY	TW	25	10	23	P
11. NETWORK LABORATORY	OR	50	20	40	P
12. SOFT SKILLS LABORATORY	TW	25	10	22	P

FIRST TERM TOTAL = 500/750.

ORDN. 1 MARKS :

T80058511 ASHISH GUPTA MEENA , 71200765C , PICT , T80058511

01. OPERATING SYSTEM	PP	100	40	54	P
02. THEORY OF COMPUTATION	PP	100	40	71	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	70	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	59	P
05. SOFTWARE ENGINEERING	PP	100	40	61	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	20	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	40	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	20	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	42	P
10. NETWORK LABORATORY	TW	25	10	21	P
11. NETWORK LABORATORY	OR	50	20	44	P
12. SOFT SKILLS LABORATORY	TW	25	10	16	P

FIRST TERM TOTAL = 518/750.

ORDN. 1 MARKS :

T80058512 AUTADE SAGAR ASHOK INDIRA , 71045376M , T80058515 , PICT , T80058512

01. OPERATING SYSTEM	PP	100	40	45	P C	13. SYSTEM SOFTWARE PROGRAMMING	PP	100	40	40	P C
02. THEORY OF COMPUTATION	PP	100	40	40	P C	14. MANAGEMENT INFORMATION SYSTEMS	PP	100	40	40	P C
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	46	P C	15. PROGRAMMING PARADIGMS	PP	100	40	40	P C
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	40	P C	16. DESIGN & ANALYSIS OF ALGORITHMS	PP	100	40	60	P
05. SOFTWARE ENGINEERING	PP	100	40	42	P C	17. HUMAN COMPU.INTERACTION & USABI.	PP	100	40	42	P C
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	21	P C	18. SOFTWARE DESIGN LABORATORY	TW	50	20	29	P C
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	39	P C	19. SOFTWARE DESIGN LABORATORY	PR	50	20	32	P C
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	22	P C	20. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	33	P C
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	41	P C	21. SOFTWARE DEVELOPMENT TOOLS LAB.	OR	50	20	30	P
10. NETWORK LABORATORY	TW	25	10	21	P C	22. SEMINAR AND TECHNICAL COMMUN.	TW	50	20	31	P C
11. NETWORK LABORATORY	OR	50	20	28	P C						
12. SOFT SKILLS LABORATORY	TW	25	10	22	P C						

GRAND TOTAL = 784/1500, RESULT: SECOND CLASS

ORDN. 1 MARKS :



NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80058513 BALDAWA KOMAL DWARKADAS USHA , 71200778E , , PICT , T80058513

01. OPERATING SYSTEM	PP	100	40	40	P
02. THEORY OF COMPUTATION	PP	100	40	60	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	43	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	60	P
05. SOFTWARE ENGINEERING	PP	100	40	42	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	17	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	25	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	14	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	08	F
10. NETWORK LABORATORY	TW	25	10	17	P
11. NETWORK LABORATORY	OR	50	20	38	P
12. SOFT SKILLS LABORATORY	TW	25	10	17	P

FIRST TERM TOTAL = 381/750.

ORDN. 1 MARKS :

T80058514 BANSODE ASHLESHA ANKUSH SUREKHA , 71200782C , , PICT , T80058514

01. OPERATING SYSTEM	PP	100	40	51	P
02. THEORY OF COMPUTATION	PP	100	40	56	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	59	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	59	P
05. SOFTWARE ENGINEERING	PP	100	40	61	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	21	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	35	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	21	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	35	P
10. NETWORK LABORATORY	TW	25	10	22	P
11. NETWORK LABORATORY	OR	50	20	37	P
12. SOFT SKILLS LABORATORY	TW	25	10	23	P

FIRST TERM TOTAL = 480/750.

ORDN. 1 MARKS :

T80058515 BARASKAR ADITI NARENDRA LEENA , 71200783M , , PICT , T80058515

01. OPERATING SYSTEM	PP	100	40	43	P
02. THEORY OF COMPUTATION	PP	100	40	50	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	69	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	69	P
05. SOFTWARE ENGINEERING	PP	100	40	65	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	16	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	30	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	19	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	44	P
10. NETWORK LABORATORY	TW	25	10	21	P
11. NETWORK LABORATORY	OR	50	20	43	P
12. SOFT SKILLS LABORATORY	TW	25	10	16	P

FIRST TERM TOTAL = 485/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80058516 BARDE REEMA ANIL JYOTI , 71200784K , , PICT , T80058516

01. OPERATING SYSTEM	PP	100	40	22	F
02. THEORY OF COMPUTATION	PP	100	40	46	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	49	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	52	P
05. SOFTWARE ENGINEERING	PP	100	40	46	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	23	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	30	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	22	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	43	P
10. NETWORK LABORATORY	TW	25	10	22	P
11. NETWORK LABORATORY	OR	50	20	34	P
12. SOFT SKILLS LABORATORY	TW	25	10	23	P

FIRST TERM TOTAL = 412/750.

ORDN. 1 MARKS :

T80058517 BENDHALE AJAY MARUTI KALPANA , 71350916D , , PICT , T80058517

01. OPERATING SYSTEM	PP	100	40	49	P
02. THEORY OF COMPUTATION	PP	100	40	59	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	77	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	58	P
05. SOFTWARE ENGINEERING	PP	100	40	56	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	24	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	38	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	24	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	33	P
10. NETWORK LABORATORY	TW	25	10	24	P
11. NETWORK LABORATORY	OR	50	20	32	P
12. SOFT SKILLS LABORATORY	TW	25	10	24	P

FIRST TERM TOTAL = 498/750.

ORDN. 1 MARKS :

T80058518 BHALERAO DHAIRYASHEEL RATNAKAR RENUKA , 71200790D , , PICT , T80058518

01. OPERATING SYSTEM	PP	100	40	14	F
02. THEORY OF COMPUTATION	PP	100	40	40	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	49	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	51	P
05. SOFTWARE ENGINEERING	PP	100	40	56	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	20	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	07	F
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	20	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	35	P
10. NETWORK LABORATORY	TW	25	10	19	P
11. NETWORK LABORATORY	OR	50	20	32	P
12. SOFT SKILLS LABORATORY	TW	25	10	17	P

FIRST TERM TOTAL = 360/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80058519 BHALERAO MANGESH GANESH NANDA , 70925361D , T80058642 , PICT , T80058519

01. OPERATING SYSTEM	PP	100	40	40	P C	13. SYSTEM SOFTWARE PROGRAMMING	PP	100	40	53	P C
02. THEORY OF COMPUTATION	PP	100	40	40	P C	14. MANAGEMENT INFORMATION SYSTEMS	PP	100	40	46	P C
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	40	P C	15. PROGRAMMING PARADIGMS	PP	100	40	40	P C
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	40	P C	16. DESIGN & ANALYSIS OF ALGORITHMS	PP	100	40	48	P
05. SOFTWARE ENGINEERING	PP	100	40	40	P C	17. HUMAN COMPU.INTERACTION & USABI.	PP	100	40	44	P C
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	15	P C	18. SOFTWARE DESIGN LABORATORY	TW	50	20	20	P C
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	24	P C	19. SOFTWARE DESIGN LABORATORY	PR	50	20	23	P C
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	15	P C	20. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	30	P C
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	41	P C	21. SOFTWARE DEVELOPMENT TOOLS LAB.	OR	50	20	37	P
10. NETWORK LABORATORY	TW	25	10	16	P C	22. SEMINAR AND TECHNICAL COMMUN.	TW	50	20	37	P C
11. NETWORK LABORATORY	OR	50	20	32	P C						
12. SOFT SKILLS LABORATORY	TW	25	10	21	P C						

GRAND TOTAL = 742+08/1500, RESULT: SECOND CLASS [0.2]

ORDN. 1 MARKS :

T80058520 BHANDARKUMTHE MADHUR MADHAVRAO UMA , 71200792L , , PICT , T80058520

01. OPERATING SYSTEM	PP	100	40	55	P
02. THEORY OF COMPUTATION	PP	100	40	60	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	66	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	64	P
05. SOFTWARE ENGINEERING	PP	100	40	65	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	24	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	40	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	23	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	38	P
10. NETWORK LABORATORY	TW	25	10	24	P
11. NETWORK LABORATORY	OR	50	20	42	P
12. SOFT SKILLS LABORATORY	TW	25	10	24	P

FIRST TERM TOTAL = 525/750.

ORDN. 1 MARKS :

T80058521 BHANDE KRISHNA SHRIDHAR SHILABAI , 71200793J , , PICT , T80058521

01. OPERATING SYSTEM	PP	100	40	49	P
02. THEORY OF COMPUTATION	PP	100	40	65	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	64	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	58	P
05. SOFTWARE ENGINEERING	PP	100	40	64	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	20	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	21	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	19	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	38	P
10. NETWORK LABORATORY	TW	25	10	20	P
11. NETWORK LABORATORY	OR	50	20	32	P
12. SOFT SKILLS LABORATORY	TW	25	10	17	P

FIRST TERM TOTAL = 467/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80058522 BHOIR KALPESH SHIVRAM SOMABAI , 71200796C , , PICT , T80058522

01. OPERATING SYSTEM	PP	100	40	14	F
02. THEORY OF COMPUTATION	PP	100	40	40	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	40	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	45	P
05. SOFTWARE ENGINEERING	PP	100	40	44	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	23	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	18	F
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	23	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	30	P
10. NETWORK LABORATORY	TW	25	10	23	P
11. NETWORK LABORATORY	OR	50	20	25	P
12. SOFT SKILLS LABORATORY	TW	25	10	23	P

FIRST TERM TOTAL = 348/750.

ORDN. 1 MARKS :

T80058523 BHOSURE YOGESH KASHINATH SHEVANTA , 71350918L , , PICT , T80058523

01. OPERATING SYSTEM	PP	100	40	61	P
02. THEORY OF COMPUTATION	PP	100	40	51	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	62	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	63	P
05. SOFTWARE ENGINEERING	PP	100	40	56	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	22	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	35	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	21	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	34	P
10. NETWORK LABORATORY	TW	25	10	22	P
11. NETWORK LABORATORY	OR	50	20	37	P
12. SOFT SKILLS LABORATORY	TW	25	10	19	P

FIRST TERM TOTAL = 483/750.

ORDN. 1 MARKS :

T80058524 BIRHADE NIKETAN SHASHIKANT VANDANA , 71200801C , , PICT , T80058524

01. OPERATING SYSTEM	PP	100	40	44	P
02. THEORY OF COMPUTATION	PP	100	40	40	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	47	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	47	P
05. SOFTWARE ENGINEERING	PP	100	40	40	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	18	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	35	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	20	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	41	P
10. NETWORK LABORATORY	TW	25	10	21	P
11. NETWORK LABORATORY	OR	50	20	40	P
12. SOFT SKILLS LABORATORY	TW	25	10	19	P

FIRST TERM TOTAL = 412/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80058525 BORATE SHRADDHA RAJARAM KAMAL , 71350919J , , PICT , T80058525

01. OPERATING SYSTEM	PP	100	40	56	P
02. THEORY OF COMPUTATION	PP	100	40	57	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	68	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	55	P
05. SOFTWARE ENGINEERING	PP	100	40	63	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	22	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	35	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	21	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	15	F
10. NETWORK LABORATORY	TW	25	10	23	P
11. NETWORK LABORATORY	OR	50	20	35	P
12. SOFT SKILLS LABORATORY	TW	25	10	20	P

FIRST TERM TOTAL = 470/750.

ORDN. 1 MARKS :

T80058526 CHANDRATRE ABHISHEK SURENDRA ARCHANA , 71200807B , , PICT , T80058526

01. OPERATING SYSTEM	PP	100	40	53	P
02. THEORY OF COMPUTATION	PP	100	40	51	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	62	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	60	P
05. SOFTWARE ENGINEERING	PP	100	40	52	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	24	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	43	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	24	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	43	P
10. NETWORK LABORATORY	TW	25	10	24	P
11. NETWORK LABORATORY	OR	50	20	45	P
12. SOFT SKILLS LABORATORY	TW	25	10	24	P

FIRST TERM TOTAL = 505/750.

ORDN. 1 MARKS :

T80058527 CHANDRIKA PARIMOO ANITA , 71200808L , , PICT , T80058527

01. OPERATING SYSTEM	PP	100	40	66	P
02. THEORY OF COMPUTATION	PP	100	40	73	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	72	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	69	P
05. SOFTWARE ENGINEERING	PP	100	40	65	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	22	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	44	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	22	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	47	P
10. NETWORK LABORATORY	TW	25	10	22	P
11. NETWORK LABORATORY	OR	50	20	40	P
12. SOFT SKILLS LABORATORY	TW	25	10	23	P

FIRST TERM TOTAL = 565/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80058528 CHARKHA BHUSHAN HEMANT SEEMA , 71200809J , , PICT , T80058528

01. OPERATING SYSTEM	PP	100	40	30	F
02. THEORY OF COMPUTATION	PP	100	40	40	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	66	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	59	P
05. SOFTWARE ENGINEERING	PP	100	40	46	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	16	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	30	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	16	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	40	P
10. NETWORK LABORATORY	TW	25	10	10	P
11. NETWORK LABORATORY	OR	50	20	29	P
12. SOFT SKILLS LABORATORY	TW	25	10	13	P

FIRST TERM TOTAL = 395/750.

ORDN. 1 MARKS :

T80058529 CHASKAR VIPUL VINAYAK POOJA , 71200811L , , PICT , T80058529

01. OPERATING SYSTEM	PP	100	40	58	P
02. THEORY OF COMPUTATION	PP	100	40	74	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	74	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	57	P
05. SOFTWARE ENGINEERING	PP	100	40	56	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	24	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	40	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	24	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	42	P
10. NETWORK LABORATORY	TW	25	10	24	P
11. NETWORK LABORATORY	OR	50	20	42	P
12. SOFT SKILLS LABORATORY	TW	25	10	23	P

FIRST TERM TOTAL = 538/750.

ORDN. 1 MARKS :

T80058530 CHATTERJEE SUDIPTO ANJAN MADHUCHHANDA , 71200812J , , PICT , T80058530

01. OPERATING SYSTEM	PP	100	40	46	P
02. THEORY OF COMPUTATION	PP	100	40	40	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	61	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	40	P
05. SOFTWARE ENGINEERING	PP	100	40	51	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	21	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	39	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	20	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	41	P
10. NETWORK LABORATORY	TW	25	10	23	P
11. NETWORK LABORATORY	OR	50	20	43	P
12. SOFT SKILLS LABORATORY	TW	25	10	17	P

FIRST TERM TOTAL = 442/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80058531 CHAUDHARI PRANITA RAVISHANKAR MEENA , 71100764M , T80058527 , PICT , T80058531

01. OPERATING SYSTEM	PP	100	40	40	P C	13. SYSTEM SOFTWARE PROGRAMMING	PP	100	40	40	P C
02. THEORY OF COMPUTATION	PP	100	40	40	P C	14. MANAGEMENT INFORMATION SYSTEMS	PP	100	40	AA	F
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	40	P C	15. PROGRAMMING PARADIGMS	PP	100	40	40	P C
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	AA	F	16. DESIGN & ANALYSIS OF ALGORITHMS	PP	100	40	61	P
05. SOFTWARE ENGINEERING	PP	100	40	43	P	17. HUMAN COMPU.INTERACTION & USABI.	PP	100	40	44	P C
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	21	P C	18. SOFTWARE DESIGN LABORATORY	TW	50	20	22	P C
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	30	P C	19. SOFTWARE DESIGN LABORATORY	PR	50	20	35	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	18	P C	20. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	38	P C
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	33	P C	21. SOFTWARE DEVELOPMENT TOOLS LAB.	OR	50	20	30	P C
10. NETWORK LABORATORY	TW	25	10	18	P C	22. SEMINAR AND TECHNICAL COMMUN.	TW	50	20	23	P C
11. NETWORK LABORATORY	OR	50	20	28	P C						
12. SOFT SKILLS LABORATORY	TW	25	10	19	P C						

GRAND TOTAL = 663/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

T80058532 CHAVAN KAVITA DHANAJI SHOBHA , 71350920B , , PICT , T80058532

01. OPERATING SYSTEM	PP	100	40	41	P
02. THEORY OF COMPUTATION	PP	100	40	40	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	48	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	54	P
05. SOFTWARE ENGINEERING	PP	100	40	49	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	21	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	20	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	22	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	22	P
10. NETWORK LABORATORY	TW	25	10	22	P
11. NETWORK LABORATORY	OR	50	20	38	P
12. SOFT SKILLS LABORATORY	TW	25	10	22	P

FIRST TERM TOTAL = 399/750.

ORDN. 1 MARKS :

T80058533 CHAVAN SONAL NILKANTH SAROJ , 71200817K , , PICT , T80058533

01. OPERATING SYSTEM	PP	100	40	51	P
02. THEORY OF COMPUTATION	PP	100	40	53	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	66	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	59	P
05. SOFTWARE ENGINEERING	PP	100	40	57	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	18	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	30	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	22	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	38	P
10. NETWORK LABORATORY	TW	25	10	22	P
11. NETWORK LABORATORY	OR	50	20	40	P
12. SOFT SKILLS LABORATORY	TW	25	10	20	P

FIRST TERM TOTAL = 476/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80058534 CHAWARE PURUSHOTTAM GAJANAN MANJUSHA , 71200820K , , PICT , T80058534

01. OPERATING SYSTEM	PP	100	40	45	P
02. THEORY OF COMPUTATION	PP	100	40	55	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	71	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	64	P
05. SOFTWARE ENGINEERING	PP	100	40	72	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	23	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	40	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	22	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	36	P
10. NETWORK LABORATORY	TW	25	10	22	P
11. NETWORK LABORATORY	OR	50	20	42	P
12. SOFT SKILLS LABORATORY	TW	25	10	24	P

FIRST TERM TOTAL = 516/750.

ORDN. 1 MARKS :

T80058535 CHOUDHARI PRIYANKA SANJAY POONAM , 71200822F , , PICT , T80058535

01. OPERATING SYSTEM	PP	100	40	31	F
02. THEORY OF COMPUTATION	PP	100	40	26	F
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	48	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	40	P
05. SOFTWARE ENGINEERING	PP	100	40	40	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	19	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	22	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	18	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	14	F
10. NETWORK LABORATORY	TW	25	10	20	P
11. NETWORK LABORATORY	OR	50	20	22	P
12. SOFT SKILLS LABORATORY	TW	25	10	16	P

FIRST TERM TOTAL = 316/750.

ORDN. 1 MARKS :

T80058536 D CUNHA JOANNE JOHN ANNIE , 71200824B , , PICT , T80058536

01. OPERATING SYSTEM	PP	100	40	40	P
02. THEORY OF COMPUTATION	PP	100	40	51	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	58	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	46	P
05. SOFTWARE ENGINEERING	PP	100	40	52	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	19	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	22	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	18	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	28	P
10. NETWORK LABORATORY	TW	25	10	22	P
11. NETWORK LABORATORY	OR	50	20	38	P
12. SOFT SKILLS LABORATORY	TW	25	10	20	P

FIRST TERM TOTAL = 414/750.

ORDN. 1 MARKS :



NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.  
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80058537 DABARE DEEP RAJESH RASHMI , 71200825L , , PICT , T80058537

01. OPERATING SYSTEM	PP	100	40	42	P
02. THEORY OF COMPUTATION	PP	100	40	53	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	67	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	52	P
05. SOFTWARE ENGINEERING	PP	100	40	58	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	18	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	33	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	17	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	33	P
10. NETWORK LABORATORY	TW	25	10	19	P
11. NETWORK LABORATORY	OR	50	20	37	P
12. SOFT SKILLS LABORATORY	TW	25	10	14	P

FIRST TERM TOTAL = 443/750.

ORDN. 1 MARKS :

T80058538 DAHAWAD SUNIL NAMDEO VANITA , 71241757F , T80058529 , PICT , T80058538

01. OPERATING SYSTEM	PP	100	40	43	P C
02. THEORY OF COMPUTATION	PP	100	40	46	P C
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	57	P C
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	40	P C
05. SOFTWARE ENGINEERING	PP	100	40	40	P C
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	18	P C
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	29	P C
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	18	P C
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	25	P C
10. NETWORK LABORATORY	TW	25	10	17	P C
11. NETWORK LABORATORY	OR	50	20	AA	F
12. SOFT SKILLS LABORATORY	TW	25	10	19	P C

FIRST TERM TOTAL = 352/750.

ORDN. 1 MARKS :

T80058539 DARAK SHREYA TEJRAJ JYOTI , 71200827G , , PICT , T80058539

01. OPERATING SYSTEM	PP	100	40	59	P
02. THEORY OF COMPUTATION	PP	100	40	62	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	65	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	53	P
05. SOFTWARE ENGINEERING	PP	100	40	58	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	22	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	34	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	22	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	40	P
10. NETWORK LABORATORY	TW	25	10	22	P
11. NETWORK LABORATORY	OR	50	20	39	P
12. SOFT SKILLS LABORATORY	TW	25	10	22	P

FIRST TERM TOTAL = 498/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80058540 DARDA MAYOOR MONISH KALPANA , 71045410E , T80058530 , PICT , T80058540

01. OPERATING SYSTEM	PP	100	40	52	P C	13. SYSTEM SOFTWARE PROGRAMMING	PP	100	40	43	P C
02. THEORY OF COMPUTATION	PP	100	40	AA	F	14. MANAGEMENT INFORMATION SYSTEMS	PP	100	40	41	P C
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	60	P C	15. PROGRAMMING PARADIGMS	PP	100	40	55	P C
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	40	P C	16. DESIGN & ANALYSIS OF ALGORITHMS	PP	100	40	AA	F
05. SOFTWARE ENGINEERING	PP	100	40	53	P C	17. HUMAN COMPU.INTERACTION & USABI.	PP	100	40	52	P C
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	10	P C	18. SOFTWARE DESIGN LABORATORY	TW	50	20	20	P C
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	AA	F	19. SOFTWARE DESIGN LABORATORY	PR	50	20	AA	F
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	11	P C	20. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	20	P C
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	30	P C	21. SOFTWARE DEVELOPMENT TOOLS LAB.	OR	50	20	AA	F
10. NETWORK LABORATORY	TW	25	10	13	P C	22. SEMINAR AND TECHNICAL COMMUN.	TW	50	20	20	P C
11. NETWORK LABORATORY	OR	50	20	AA	F						
12. SOFT SKILLS LABORATORY	TW	25	10	13	P C						

GRAND TOTAL = 533/1500, RESULT: FAILS

ORDN. 1 MARKS :

T80058541 DEOGHATKAR BRAMHESH NAMDEO MAYALI , 71200829C , PICT , T80058541

01. OPERATING SYSTEM	PP	100	40	43	P
02. THEORY OF COMPUTATION	PP	100	40	47	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	64	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	52	P
05. SOFTWARE ENGINEERING	PP	100	40	48	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	17	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	38	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	18	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	39	P
10. NETWORK LABORATORY	TW	25	10	18	P
11. NETWORK LABORATORY	OR	50	20	35	P
12. SOFT SKILLS LABORATORY	TW	25	10	14	P

FIRST TERM TOTAL = 433/750.

ORDN. 1 MARKS :

T80058542 DESHMUKH MONIKA JAYANT JYOTI , 71200831E , PICT , T80058542

01. OPERATING SYSTEM	PP	100	40	60	P
02. THEORY OF COMPUTATION	PP	100	40	61	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	71	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	60	P
05. SOFTWARE ENGINEERING	PP	100	40	64	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	19	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	37	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	21	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	35	P
10. NETWORK LABORATORY	TW	25	10	22	P
11. NETWORK LABORATORY	OR	50	20	37	P
12. SOFT SKILLS LABORATORY	TW	25	10	19	P

FIRST TERM TOTAL = 506/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80058543 DEVASHISH SINGH SUSHMA , 71200836F , , PICT , T80058543

01. OPERATING SYSTEM	PP	100	40	41	P
02. THEORY OF COMPUTATION	PP	100	40	67	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	56	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	58	P
05. SOFTWARE ENGINEERING	PP	100	40	58	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	20	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	41	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	17	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	33	P
10. NETWORK LABORATORY	TW	25	10	20	P
11. NETWORK LABORATORY	OR	50	20	35	P
12. SOFT SKILLS LABORATORY	TW	25	10	21	P

FIRST TERM TOTAL = 467/750.

ORDN. 1 MARKS :

T80058544 DHAMANE AKANKSHA ARUN KANCHAN , 71200838B , , PICT , T80058544

01. OPERATING SYSTEM	PP	100	40	48	P
02. THEORY OF COMPUTATION	PP	100	40	53	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	51	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	53	P
05. SOFTWARE ENGINEERING	PP	100	40	57	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	18	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	35	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	21	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	33	P
10. NETWORK LABORATORY	TW	25	10	21	P
11. NETWORK LABORATORY	OR	50	20	30	P
12. SOFT SKILLS LABORATORY	TW	25	10	19	P

FIRST TERM TOTAL = 439/750.

ORDN. 1 MARKS :

T80058545 DHANAVE PRATIK BALASAHEB UJJWALLA , 71100783H , T80058531 , PICT , T80058545

01. OPERATING SYSTEM	PP	100	40	42	P C
02. THEORY OF COMPUTATION	PP	100	40	AA	F
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	51	P C
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	43	P C
05. SOFTWARE ENGINEERING	PP	100	40	48	P C
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	12	P C
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	22	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	16	P C
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	35	P C
10. NETWORK LABORATORY	TW	25	10	13	P C
11. NETWORK LABORATORY	OR	50	20	40	P
12. SOFT SKILLS LABORATORY	TW	25	10	13	P C

FIRST TERM TOTAL = 335/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80058546 DISALE GITA BIBHISHAN RADHA , 71200845E , , PICT , T80058546

01. OPERATING SYSTEM	PP	100	40	40	P
02. THEORY OF COMPUTATION	PP	100	40	43	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	49	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	45	P
05. SOFTWARE ENGINEERING	PP	100	40	43	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	21	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	33	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	21	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	22	P
10. NETWORK LABORATORY	TW	25	10	21	P
11. NETWORK LABORATORY	OR	50	20	32	P
12. SOFT SKILLS LABORATORY	TW	25	10	22	P

FIRST TERM TOTAL = 392/750.

ORDN. 1 MARKS :

T80058547 DIXIT PRANAV SUDHIR SHUBHANGI , 71200846C , , PICT , T80058547

01. OPERATING SYSTEM	PP	100	40	40	P
02. THEORY OF COMPUTATION	PP	100	40	62	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	62	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	50	P
05. SOFTWARE ENGINEERING	PP	100	40	59	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	19	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	40	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	19	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	33	P
10. NETWORK LABORATORY	TW	25	10	21	P
11. NETWORK LABORATORY	OR	50	20	39	P
12. SOFT SKILLS LABORATORY	TW	25	10	19	P

FIRST TERM TOTAL = 463/750.

ORDN. 1 MARKS :

T80058548 DURGE PRAPTI ANIL BABITA , 71200850M , , PICT , T80058548

01. OPERATING SYSTEM	PP	100	40	26	F
02. THEORY OF COMPUTATION	PP	100	40	40	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	50	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	52	P
05. SOFTWARE ENGINEERING	PP	100	40	43	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	18	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	18	F
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	23	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	35	P
10. NETWORK LABORATORY	TW	25	10	23	P
11. NETWORK LABORATORY	OR	50	20	30	P
12. SOFT SKILLS LABORATORY	TW	25	10	22	P

FIRST TERM TOTAL = 380/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80058549 GAIKWAD PIYUSH GHANASHYAM VASUDHA , 71100793E , T80058535 , PICT , T80058549

01. OPERATING SYSTEM	PP	100	40	40	P C	13. SYSTEM SOFTWARE PROGRAMMING	PP	100	40	AA	F
02. THEORY OF COMPUTATION	PP	100	40	40	P C	14. MANAGEMENT INFORMATION SYSTEMS	PP	100	40	48	P C
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	41	P C	15. PROGRAMMING PARADIGMS	PP	100	40	43	P C
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	40	P C	16. DESIGN & ANALYSIS OF ALGORITHMS	PP	100	40	45	P C
05. SOFTWARE ENGINEERING	PP	100	40	48	P C	17. HUMAN COMPU.INTERACTION & USABI.	PP	100	40	47	P C
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	10	P C	18. SOFTWARE DESIGN LABORATORY	TW	50	20	20	P C
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	31	P C	19. SOFTWARE DESIGN LABORATORY	PR	50	20	30	P C
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	15	P C	20. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	20	P C
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	20	P C	21. SOFTWARE DEVELOPMENT TOOLS LAB.	OR	50	20	31	P
10. NETWORK LABORATORY	TW	25	10	14	P C	22. SEMINAR AND TECHNICAL COMMUN.	TW	50	20	30	P C
11. NETWORK LABORATORY	OR	50	20	30	P C						
12. SOFT SKILLS LABORATORY	TW	25	10	16	P C						

GRAND TOTAL = 659/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

T80058550 GAIKWAD PRINCE VIJAYKUMAR PRABHA , 71200853F , , PICT , T80058550

01. OPERATING SYSTEM	PP	100	40	27	F
02. THEORY OF COMPUTATION	PP	100	40	40	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	57	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	33	F
05. SOFTWARE ENGINEERING	PP	100	40	50	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	16	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	34	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	18	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	20	P
10. NETWORK LABORATORY	TW	25	10	19	P
11. NETWORK LABORATORY	OR	50	20	28	P
12. SOFT SKILLS LABORATORY	TW	25	10	17	P

FIRST TERM TOTAL = 359/750.

ORDN. 1 MARKS :

T80058551 GAME AJAY BALASAHEB ANITA , 71200855B , , PICT , T80058551

01. OPERATING SYSTEM	PP	100	40	66	P
02. THEORY OF COMPUTATION	PP	100	40	74	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	70	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	63	P
05. SOFTWARE ENGINEERING	PP	100	40	69	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	20	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	38	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	20	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	40	P
10. NETWORK LABORATORY	TW	25	10	23	P
11. NETWORK LABORATORY	OR	50	20	38	P
12. SOFT SKILLS LABORATORY	TW	25	10	21	P

FIRST TERM TOTAL = 542/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80058552 GANDHI KAMLESH ISHWARLAL SUVARNA , 71200856L , , PICT , T80058552

01. OPERATING SYSTEM	PP	100	40	57	P
02. THEORY OF COMPUTATION	PP	100	40	62	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	54	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	56	P
05. SOFTWARE ENGINEERING	PP	100	40	63	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	21	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	40	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	24	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	37	P
10. NETWORK LABORATORY	TW	25	10	24	P
11. NETWORK LABORATORY	OR	50	20	36	P
12. SOFT SKILLS LABORATORY	TW	25	10	24	P

FIRST TERM TOTAL = 498/750.

ORDN. 1 MARKS :

T80058553 GAVALI ANKITA SUNIL SUDHA , 71200860J , , PICT , T80058553

01. OPERATING SYSTEM	PP	100	40	40	P
02. THEORY OF COMPUTATION	PP	100	40	20	F
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	40	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	42	P
05. SOFTWARE ENGINEERING	PP	100	40	47	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	14	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	25	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	16	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	22	P
10. NETWORK LABORATORY	TW	25	10	17	P
11. NETWORK LABORATORY	OR	50	20	22	P
12. SOFT SKILLS LABORATORY	TW	25	10	15	P

FIRST TERM TOTAL = 320/750.

ORDN. 1 MARKS :

T80058554 GHATE ABHIJIT SURESHRAO KUNDA , 71200864M , , PICT , T80058554

01. OPERATING SYSTEM	PP	100	40	47	P
02. THEORY OF COMPUTATION	PP	100	40	40	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	53	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	51	P
05. SOFTWARE ENGINEERING	PP	100	40	56	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	17	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	37	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	18	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	33	P
10. NETWORK LABORATORY	TW	25	10	16	P
11. NETWORK LABORATORY	OR	50	20	29	P
12. SOFT SKILLS LABORATORY	TW	25	10	18	P

FIRST TERM TOTAL = 415/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.  
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80058555	GHODAKE PRAJAKTA DHANANJAY				SWATI				, 71100802H				, T80058538		PICT			, T80058555
01. OPERATING SYSTEM	PP	100	40	51	P C	13. SYSTEM SOFTWARE PROGRAMMING	PP	100	40	45	P C							
02. THEORY OF COMPUTATION	PP	100	40	47	P C	14. MANAGEMENT INFORMATION SYSTEMS	PP	100	40	48	P							
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	40	P C	15. PROGRAMMING PARADIGMS	PP	100	40	40	P C							
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	40	P C	16. DESIGN & ANALYSIS OF ALGORITHMS	PP	100	40	49	P C							
05. SOFTWARE ENGINEERING	PP	100	40	42	P C	17. HUMAN COMPU.INTERACTION & USABI.	PP	100	40	40	P C							
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	18	P C	18. SOFTWARE DESIGN LABORATORY	TW	50	20	30	P C							
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	38	P C	19. SOFTWARE DESIGN LABORATORY	PR	50	20	33	P C							
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	18	P C	20. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	30	P C							
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	24	P C	21. SOFTWARE DEVELOPMENT TOOLS LAB.	OR	50	20	30	P							
10. NETWORK LABORATORY	TW	25	10	16	P C	22. SEMINAR AND TECHNICAL COMMUN.	TW	50	20	35	P C							
11. NETWORK LABORATORY	OR	50	20	20	P C													
12. SOFT SKILLS LABORATORY	TW	25	10	18	P C													

GRAND TOTAL = 752/1500, RESULT: SECOND CLASS

ORDN. 1 MARKS :

T80058556	GHODE AMAR PRADEEPKUMAR				SINDHU				, 71045436J				, T80058539		PICT			, T80058556
01. OPERATING SYSTEM	PP	100	40	53	P C	13. SYSTEM SOFTWARE PROGRAMMING	PP	100	40	46	P C							
02. THEORY OF COMPUTATION	PP	100	40	40	P C	14. MANAGEMENT INFORMATION SYSTEMS	PP	100	40	AA	F							
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	48	P C	15. PROGRAMMING PARADIGMS	PP	100	40	50	P C							
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	40	P C	16. DESIGN & ANALYSIS OF ALGORITHMS	PP	100	40	AA	F							
05. SOFTWARE ENGINEERING	PP	100	40	52	P C	17. HUMAN COMPU.INTERACTION & USABI.	PP	100	40	42	P C							
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	17	P C	18. SOFTWARE DESIGN LABORATORY	TW	50	20	20	P C							
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	AA	F	19. SOFTWARE DESIGN LABORATORY	PR	50	20	AA	F							
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	17	P C	20. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	20	P C							
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	22	P C	21. SOFTWARE DEVELOPMENT TOOLS LAB.	OR	50	20	AA	F							
10. NETWORK LABORATORY	TW	25	10	12	P C	22. SEMINAR AND TECHNICAL COMMUN.	TW	50	20	21	P C							
11. NETWORK LABORATORY	OR	50	20	28	P C													
12. SOFT SKILLS LABORATORY	TW	25	10	15	P C													

GRAND TOTAL = 543/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

T80058557	GHODKE NIRANJAN DATTATRAYA				PRADNYA				, 71200865K						PICT			, T80058557
01. OPERATING SYSTEM	PP	100	40	62	P													
02. THEORY OF COMPUTATION	PP	100	40	62	P													
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	56	P													
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	61	P													
05. SOFTWARE ENGINEERING	PP	100	40	59	P													
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	21	P													
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	38	P													
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	21	P													
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	42	P													
10. NETWORK LABORATORY	TW	25	10	22	P													
11. NETWORK LABORATORY	OR	50	20	38	P													
12. SOFT SKILLS LABORATORY	TW	25	10	21	P													

FIRST TERM TOTAL = 503/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80058558 GOHOKAR KIRAN DADAJI REKHA , 71350921L , , PICT , T80058558

01. OPERATING SYSTEM	PP	100	40	47	P
02. THEORY OF COMPUTATION	PP	100	40	48	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	56	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	48	P
05. SOFTWARE ENGINEERING	PP	100	40	52	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	18	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	20	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	21	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	29	P
10. NETWORK LABORATORY	TW	25	10	21	P
11. NETWORK LABORATORY	OR	50	20	38	P
12. SOFT SKILLS LABORATORY	TW	25	10	23	P

FIRST TERM TOTAL = 421/750.

ORDN. 1 MARKS :

T80058559 GOKHALE VALLARI SHRIKANT PADMAJA , 71200867F , , PICT , T80058559

01. OPERATING SYSTEM	PP	100	40	56	P
02. THEORY OF COMPUTATION	PP	100	40	64	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	62	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	54	P
05. SOFTWARE ENGINEERING	PP	100	40	60	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	20	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	30	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	22	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	30	P
10. NETWORK LABORATORY	TW	25	10	22	P
11. NETWORK LABORATORY	OR	50	20	33	P
12. SOFT SKILLS LABORATORY	TW	25	10	23	P

FIRST TERM TOTAL = 476/750.

ORDN. 1 MARKS :

T80058560 GOLAPKAR PIYUSH ANIL APARNA , 71200868D , , PICT , T80058560

01. OPERATING SYSTEM	PP	100	40	41	P
02. THEORY OF COMPUTATION	PP	100	40	53	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	57	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	51	P
05. SOFTWARE ENGINEERING	PP	100	40	61	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	17	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	42	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	19	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	43	P
10. NETWORK LABORATORY	TW	25	10	19	P
11. NETWORK LABORATORY	OR	50	20	34	P
12. SOFT SKILLS LABORATORY	TW	25	10	17	P

FIRST TERM TOTAL = 454/750.

ORDN. 1 MARKS :



NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80058562 GOSAVI KALYANI RAJGIR KAMINI , 71200869B , , PICT , T80058562

01. OPERATING SYSTEM	PP	100	40	54	P
02. THEORY OF COMPUTATION	PP	100	40	59	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	62	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	62	P
05. SOFTWARE ENGINEERING	PP	100	40	45	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	19	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	35	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	22	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	33	P
10. NETWORK LABORATORY	TW	25	10	22	P
11. NETWORK LABORATORY	OR	50	20	33	P
12. SOFT SKILLS LABORATORY	TW	25	10	23	P

FIRST TERM TOTAL = 469/750.

ORDN. 1 MARKS :

T80058563 GUPTA SHRADHA RAVINDRA AMITA , 71200873L , , PICT , T80058563

01. OPERATING SYSTEM	PP	100	40	71	P
02. THEORY OF COMPUTATION	PP	100	40	81	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	61	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	53	P
05. SOFTWARE ENGINEERING	PP	100	40	57	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	24	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	42	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	24	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	38	P
10. NETWORK LABORATORY	TW	25	10	23	P
11. NETWORK LABORATORY	OR	50	20	40	P
12. SOFT SKILLS LABORATORY	TW	25	10	23	P

FIRST TERM TOTAL = 537/750.

ORDN. 1 MARKS :

T80058564 HADKE ANUP BANDA MANIK , 71200875G , , PICT , T80058564

01. OPERATING SYSTEM	PP	100	40	40	P
02. THEORY OF COMPUTATION	PP	100	40	40	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	42	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	32	F
05. SOFTWARE ENGINEERING	PP	100	40	58	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	22	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	22	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	20	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	39	P
10. NETWORK LABORATORY	TW	25	10	23	P
11. NETWORK LABORATORY	OR	50	20	37	P
12. SOFT SKILLS LABORATORY	TW	25	10	21	P

FIRST TERM TOTAL = 396/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80058566 HARSH BAHETI ANJANA , 71200877C , , PICT , T80058566

01. OPERATING SYSTEM	PP	100	40	40	P
02. THEORY OF COMPUTATION	PP	100	40	60	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	50	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	56	P
05. SOFTWARE ENGINEERING	PP	100	40	41	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	10	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	20	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	17	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	33	P
10. NETWORK LABORATORY	TW	25	10	18	P
11. NETWORK LABORATORY	OR	50	20	38	P
12. SOFT SKILLS LABORATORY	TW	25	10	16	P

FIRST TERM TOTAL = 399/750.

ORDN. 1 MARKS :

T80058567 HATOLKAR ABHIRAM PRASANNA MANJUSHA , 71200878M , , PICT , T80058567

01. OPERATING SYSTEM	PP	100	40	54	P
02. THEORY OF COMPUTATION	PP	100	40	45	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	61	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	57	P
05. SOFTWARE ENGINEERING	PP	100	40	54	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	17	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	44	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	20	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	43	P
10. NETWORK LABORATORY	TW	25	10	22	P
11. NETWORK LABORATORY	OR	50	20	40	P
12. SOFT SKILLS LABORATORY	TW	25	10	18	P

FIRST TERM TOTAL = 475/750.

ORDN. 1 MARKS :

T80058568 IYER PRIYANKA SUBRAMANIAN USHA , 71200884F , , PICT , T80058568

01. OPERATING SYSTEM	PP	100	40	47	P
02. THEORY OF COMPUTATION	PP	100	40	60	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	62	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	62	P
05. SOFTWARE ENGINEERING	PP	100	40	57	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	22	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	38	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	23	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	37	P
10. NETWORK LABORATORY	TW	25	10	24	P
11. NETWORK LABORATORY	OR	50	20	42	P
12. SOFT SKILLS LABORATORY	TW	25	10	21	P

FIRST TERM TOTAL = 495/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80058569 JAGTAP MANISH KISHOR BHARTI , 71350923G , PICT , T80058569

01. OPERATING SYSTEM	PP	100	40	52	P
02. THEORY OF COMPUTATION	PP	100	40	44	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	56	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	45	P
05. SOFTWARE ENGINEERING	PP	100	40	54	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	23	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	46	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	23	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	43	P
10. NETWORK LABORATORY	TW	25	10	24	P
11. NETWORK LABORATORY	OR	50	20	39	P
12. SOFT SKILLS LABORATORY	TW	25	10	21	P

FIRST TERM TOTAL = 470/750.

ORDN. 1 MARKS :

T80058570 JAIPURIA ROHIT SHARADKUMAR AMITA , 71100828M , T80058548 , PICT , T80058570

01. OPERATING SYSTEM	PP	100	40	40	P C	13. SYSTEM SOFTWARE PROGRAMMING	PP	100	40	41	P C
02. THEORY OF COMPUTATION	PP	100	40	41	P C	14. MANAGEMENT INFORMATION SYSTEMS	PP	100	40	40	P C
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	45	P C	15. PROGRAMMING PARADIGMS	PP	100	40	40	P C
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	43	P C	16. DESIGN & ANALYSIS OF ALGORITHMS	PP	100	40	42	P C
05. SOFTWARE ENGINEERING	PP	100	40	42	P C	17. HUMAN COMPU.INTERACTION & USABI.	PP	100	40	40	P C
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	19	P C	18. SOFTWARE DESIGN LABORATORY	TW	50	20	35	P C
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	20	\$ P	19. SOFTWARE DESIGN LABORATORY	PR	50	20	22	P C
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	18	P C	20. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	35	P C
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	30	P C	21. SOFTWARE DEVELOPMENT TOOLS LAB.	OR	50	20	35	P C
10. NETWORK LABORATORY	TW	25	10	19	P C	22. SEMINAR AND TECHNICAL COMMUN.	TW	50	20	37	P C
11. NETWORK LABORATORY	OR	50	20	28	P C						
12. SOFT SKILLS LABORATORY	TW	25	10	20	P C						

GRAND TOTAL = 732/1500, RESULT: PASS CLASS [\$ 0.1]

ORDN. 1 MARKS : (07)2,

T80058571 JALAMKAR ABHILASH RAJENDRA SUNITA , 71200889G , PICT , T80058571

01. OPERATING SYSTEM	PP	100	40	49	P
02. THEORY OF COMPUTATION	PP	100	40	46	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	58	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	53	P
05. SOFTWARE ENGINEERING	PP	100	40	45	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	15	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	30	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	19	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	35	P
10. NETWORK LABORATORY	TW	25	10	20	P
11. NETWORK LABORATORY	OR	50	20	37	P
12. SOFT SKILLS LABORATORY	TW	25	10	16	P

FIRST TERM TOTAL = 423/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80058573 JATAB NIKHIL HARIDWARILAL YASHODA , 71350924E , , PICT , T80058573

01. OPERATING SYSTEM	PP	100	40	67	P
02. THEORY OF COMPUTATION	PP	100	40	48	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	56	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	64	P
05. SOFTWARE ENGINEERING	PP	100	40	61	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	20	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	37	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	22	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	44	P
10. NETWORK LABORATORY	TW	25	10	24	P
11. NETWORK LABORATORY	OR	50	20	40	P
12. SOFT SKILLS LABORATORY	TW	25	10	21	P

FIRST TERM TOTAL = 504/750.

ORDN. 1 MARKS :

T80058574 JEEVJYOT SUKHDEV SINGH CHHABDA SANGEET KAUR CHHABDA , 71350925C , , PICT , T80058574

01. OPERATING SYSTEM	PP	100	40	47	P
02. THEORY OF COMPUTATION	PP	100	40	40	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	46	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	63	P
05. SOFTWARE ENGINEERING	PP	100	40	54	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	21	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	42	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	23	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	45	P
10. NETWORK LABORATORY	TW	25	10	23	P
11. NETWORK LABORATORY	OR	50	20	40	P
12. SOFT SKILLS LABORATORY	TW	25	10	20	P

FIRST TERM TOTAL = 464/750.

ORDN. 1 MARKS :

T80058575 JOSHI SWANAND ARVIND ARUNA , 71200896K , , PICT , T80058575

01. OPERATING SYSTEM	PP	100	40	67	P
02. THEORY OF COMPUTATION	PP	100	40	68	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	62	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	66	P
05. SOFTWARE ENGINEERING	PP	100	40	55	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	22	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	42	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	22	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	42	P
10. NETWORK LABORATORY	TW	25	10	21	P
11. NETWORK LABORATORY	OR	50	20	42	P
12. SOFT SKILLS LABORATORY	TW	25	10	19	P

FIRST TERM TOTAL = 528/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80058576 KADAM YASHASWINI VISHNU ALKA , 71200899D , , PICT , T80058576

01. OPERATING SYSTEM	PP	100	40	49	P
02. THEORY OF COMPUTATION	PP	100	40	49	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	58	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	65	P
05. SOFTWARE ENGINEERING	PP	100	40	66	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	17	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	12	F
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	20	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	42	P
10. NETWORK LABORATORY	TW	25	10	20	P
11. NETWORK LABORATORY	OR	50	20	35	P
12. SOFT SKILLS LABORATORY	TW	25	10	18	P

FIRST TERM TOTAL = 451/750.

ORDN. 1 MARKS :

T80058577 KADU SHRADDHA AJIT MANJIRI , 71200900M , , PICT , T80058577

01. OPERATING SYSTEM	PP	100	40	56	P
02. THEORY OF COMPUTATION	PP	100	40	72	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	64	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	58	P
05. SOFTWARE ENGINEERING	PP	100	40	63	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	22	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	35	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	22	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	37	P
10. NETWORK LABORATORY	TW	25	10	24	P
11. NETWORK LABORATORY	OR	50	20	42	P
12. SOFT SKILLS LABORATORY	TW	25	10	22	P

FIRST TERM TOTAL = 517/750.

ORDN. 1 MARKS :

T80058578 KAILAS JAHANGIR PAWARA THAWALI , 71100842G , , PICT , T80058578

01. OPERATING SYSTEM	PP	100	40	33	F
02. THEORY OF COMPUTATION	PP	100	40	26	F
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	40	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	AA	F
05. SOFTWARE ENGINEERING	PP	100	40	AA	F
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	13	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	32	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	17	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	35	P
10. NETWORK LABORATORY	TW	25	10	16	P
11. NETWORK LABORATORY	OR	50	20	22	P
12. SOFT SKILLS LABORATORY	TW	25	10	15	P

FIRST TERM TOTAL = 249/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80058579 KAMBLE NIKITA MAHADEV VANDANA , 71200907J , , PICT , T80058579

01. OPERATING SYSTEM	PP	100	40	46	P
02. THEORY OF COMPUTATION	PP	100	40	46	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	62	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	50	P
05. SOFTWARE ENGINEERING	PP	100	40	56	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	22	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	33	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	23	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	44	P
10. NETWORK LABORATORY	TW	25	10	23	P
11. NETWORK LABORATORY	OR	50	20	30	P
12. SOFT SKILLS LABORATORY	TW	25	10	23	P

FIRST TERM TOTAL = 458/750.

ORDN. 1 MARKS :

T80058580 KATKE AMRUTA SURESH MANISHA , 71200916H , , PICT , T80058580

01. OPERATING SYSTEM	PP	100	40	46	P
02. THEORY OF COMPUTATION	PP	100	40	47	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	54	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	57	P
05. SOFTWARE ENGINEERING	PP	100	40	57	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	20	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	36	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	20	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	34	P
10. NETWORK LABORATORY	TW	25	10	24	P
11. NETWORK LABORATORY	OR	50	20	34	P
12. SOFT SKILLS LABORATORY	TW	25	10	21	P

FIRST TERM TOTAL = 450/750.

ORDN. 1 MARKS :

T80058581 KAVADE PRIYANKA DHANANJAY RAJASHREE , 71200917F , , PICT , T80058581

01. OPERATING SYSTEM	PP	100	40	58	P
02. THEORY OF COMPUTATION	PP	100	40	64	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	49	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	61	P
05. SOFTWARE ENGINEERING	PP	100	40	60	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	21	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	38	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	22	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	35	P
10. NETWORK LABORATORY	TW	25	10	22	P
11. NETWORK LABORATORY	OR	50	20	31	P
12. SOFT SKILLS LABORATORY	TW	25	10	23	P

FIRST TERM TOTAL = 484/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80058582 KAVITKAR JAYASHREE GOPAL ANITA , 71200918D , , PICT , T80058582

01. OPERATING SYSTEM	PP	100	40	51	P
02. THEORY OF COMPUTATION	PP	100	40	64	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	49	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	54	P
05. SOFTWARE ENGINEERING	PP	100	40	42	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	20	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	38	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	20	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	37	P
10. NETWORK LABORATORY	TW	25	10	23	P
11. NETWORK LABORATORY	OR	50	20	35	P
12. SOFT SKILLS LABORATORY	TW	25	10	20	P

FIRST TERM TOTAL = 453/750.

ORDN. 1 MARKS :

T80058583 KHABIYA ASHWINI ASHOK SUREKHA , 71350926M , , PICT , T80058583

01. OPERATING SYSTEM	PP	100	40	45	P
02. THEORY OF COMPUTATION	PP	100	40	60	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	62	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	63	P
05. SOFTWARE ENGINEERING	PP	100	40	65	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	21	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	25	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	22	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	31	P
10. NETWORK LABORATORY	TW	25	10	22	P
11. NETWORK LABORATORY	OR	50	20	36	P
12. SOFT SKILLS LABORATORY	TW	25	10	23	P

FIRST TERM TOTAL = 475/750.

ORDN. 1 MARKS :

T80058584 KHAN SHAHBAZ AHMAD FATIMA , 71200922B , , PICT , T80058584

01. OPERATING SYSTEM	PP	100	40	49	P
02. THEORY OF COMPUTATION	PP	100	40	50	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	57	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	62	P
05. SOFTWARE ENGINEERING	PP	100	40	48	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	19	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	12	F
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	20	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	15	F
10. NETWORK LABORATORY	TW	25	10	20	P
11. NETWORK LABORATORY	OR	50	20	42	P
12. SOFT SKILLS LABORATORY	TW	25	10	21	P

FIRST TERM TOTAL = 415/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80058585 KHANDELWAL ABHISHEK GOURISHANKAR SANTOSHI , 71200924J , , PICT , T80058585

01. OPERATING SYSTEM	PP	100	40	61	P
02. THEORY OF COMPUTATION	PP	100	40	64	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	63	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	67	P
05. SOFTWARE ENGINEERING	PP	100	40	67	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	23	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	45	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	23	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	42	P
10. NETWORK LABORATORY	TW	25	10	23	P
11. NETWORK LABORATORY	OR	50	20	39	P
12. SOFT SKILLS LABORATORY	TW	25	10	23	P

FIRST TERM TOTAL = 540/750.

ORDN. 1 MARKS :

T80058586 KHANDELWAL ARPIT NARENDRA MAMTA , 71200925G , , PICT , T80058586

01. OPERATING SYSTEM	PP	100	40	24	F
02. THEORY OF COMPUTATION	PP	100	40	41	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	29	F
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	42	P
05. SOFTWARE ENGINEERING	PP	100	40	41	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	12	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	24	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	19	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	40	P
10. NETWORK LABORATORY	TW	25	10	19	P
11. NETWORK LABORATORY	OR	50	20	36	P
12. SOFT SKILLS LABORATORY	TW	25	10	17	P

FIRST TERM TOTAL = 344/750.

ORDN. 1 MARKS :

T80058587 KHAPLI TEJAS RAVINDRA RANJANA , 71200926E , , PICT , T80058587

01. OPERATING SYSTEM	PP	100	40	32	F
02. THEORY OF COMPUTATION	PP	100	40	53	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	53	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	51	P
05. SOFTWARE ENGINEERING	PP	100	40	50	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	20	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	30	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	20	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	40	P
10. NETWORK LABORATORY	TW	25	10	22	P
11. NETWORK LABORATORY	OR	50	20	38	P
12. SOFT SKILLS LABORATORY	TW	25	10	18	P

FIRST TERM TOTAL = 427/750.

ORDN. 1 MARKS :



NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80058588 KHARAT GANESH MAHADEO SHARDHA , 71045482B , , PICT , T80058588

01. OPERATING SYSTEM	PP	100	40	24	F
02. THEORY OF COMPUTATION	PP	100	40	40	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	30	F
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	40	P
05. SOFTWARE ENGINEERING	PP	100	40	43	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	17	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	31	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	18	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	35	P
10. NETWORK LABORATORY	TW	25	10	21	P
11. NETWORK LABORATORY	OR	50	20	10	F
12. SOFT SKILLS LABORATORY	TW	25	10	19	P

FIRST TERM TOTAL = 328/750.

ORDN. 1 MARKS :

T80058589 KHARCHE KAUSTUBH PRAMOD NEHA , 71200927C , , PICT , T80058589

01. OPERATING SYSTEM	PP	100	40	55	P
02. THEORY OF COMPUTATION	PP	100	40	53	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	62	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	50	P
05. SOFTWARE ENGINEERING	PP	100	40	55	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	21	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	42	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	24	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	38	P
10. NETWORK LABORATORY	TW	25	10	23	P
11. NETWORK LABORATORY	OR	50	20	38	P
12. SOFT SKILLS LABORATORY	TW	25	10	23	P

FIRST TERM TOTAL = 484/750.

ORDN. 1 MARKS :

T80058590 KHEDKAR NITIN AJINATH SUNITA , 71200928M , , PICT , T80058590

01. OPERATING SYSTEM	PP	100	40	31	F
02. THEORY OF COMPUTATION	PP	100	40	40	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	49	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	50	P
05. SOFTWARE ENGINEERING	PP	100	40	40	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	18	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	28	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	18	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	43	P
10. NETWORK LABORATORY	TW	25	10	20	P
11. NETWORK LABORATORY	OR	50	20	26	P
12. SOFT SKILLS LABORATORY	TW	25	10	17	P

FIRST TERM TOTAL = 380/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80058591 KOSHTI ANUJA RAMESH MANGAL , 71045485G , T80058559 , PICT , T80058591

01. OPERATING SYSTEM	PP	100	40	46	P C	13. SYSTEM SOFTWARE PROGRAMMING	PP	100	40	61	P C
02. THEORY OF COMPUTATION	PP	100	40	40	P C	14. MANAGEMENT INFORMATION SYSTEMS	PP	100	40	54	P C
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	45	P C	15. PROGRAMMING PARADIGMS	PP	100	40	61	P C
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	43	P C	16. DESIGN & ANALYSIS OF ALGORITHMS	PP	100	40	55	P C
05. SOFTWARE ENGINEERING	PP	100	40	52	P C	17. HUMAN COMPU.INTERACTION & USABI.	PP	100	40	50	P C
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	20	P C	18. SOFTWARE DESIGN LABORATORY	TW	50	20	34	P C
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	08	F	19. SOFTWARE DESIGN LABORATORY	PR	50	20	20	P C
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	18	P C	20. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	31	P C
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	32	P C	21. SOFTWARE DEVELOPMENT TOOLS LAB.	OR	50	20	30	P C
10. NETWORK LABORATORY	TW	25	10	20	P C	22. SEMINAR AND TECHNICAL COMMUN.	TW	50	20	32	P C
11. NETWORK LABORATORY	OR	50	20	30	P C						
12. SOFT SKILLS LABORATORY	TW	25	10	20	P C						

GRAND TOTAL = 802/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

T80058592 KOTHADIA RAMANI BHUPENDRA ANAGHA , 71200930C , , PICT , T80058592

01. OPERATING SYSTEM	PP	100	40	43	P						
02. THEORY OF COMPUTATION	PP	100	40	40	P						
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	40	P						
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	62	P						
05. SOFTWARE ENGINEERING	PP	100	40	50	P						
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	18	P						
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	36	P						
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	21	P						
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	25	P						
10. NETWORK LABORATORY	TW	25	10	20	P						
11. NETWORK LABORATORY	OR	50	20	42	P						
12. SOFT SKILLS LABORATORY	TW	25	10	19	P						

FIRST TERM TOTAL = 416/750.

ORDN. 1 MARKS :

T80058593 KULKARNI PALLAVI RAJENDRA REKHA , 71350927K , , PICT , T80058593

01. OPERATING SYSTEM	PP	100	40	63	P						
02. THEORY OF COMPUTATION	PP	100	40	50	P						
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	64	P						
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	63	P						
05. SOFTWARE ENGINEERING	PP	100	40	54	P						
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	20	P						
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	42	P						
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	23	P						
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	33	P						
10. NETWORK LABORATORY	TW	25	10	23	P						
11. NETWORK LABORATORY	OR	50	20	32	P						
12. SOFT SKILLS LABORATORY	TW	25	10	22	P						

FIRST TERM TOTAL = 489/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80058594 KULKARNI PRANIT KRISHNA LATA , 71200937L , , PICT , T80058594

01. OPERATING SYSTEM	PP	100	40	46	P
02. THEORY OF COMPUTATION	PP	100	40	41	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	49	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	62	P
05. SOFTWARE ENGINEERING	PP	100	40	44	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	19	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	10	F
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	21	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	40	P
10. NETWORK LABORATORY	TW	25	10	20	P
11. NETWORK LABORATORY	OR	50	20	21	P
12. SOFT SKILLS LABORATORY	TW	25	10	20	P

FIRST TERM TOTAL = 393/750.

ORDN. 1 MARKS :

T80058595 KULKARNI TANMAY VIJAY ALKA , 71200938J , , PICT , T80058595

01. OPERATING SYSTEM	PP	100	40	41	P
02. THEORY OF COMPUTATION	PP	100	40	54	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	54	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	62	P
05. SOFTWARE ENGINEERING	PP	100	40	55	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	17	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	25	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	19	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	42	P
10. NETWORK LABORATORY	TW	25	10	20	P
11. NETWORK LABORATORY	OR	50	20	27	P
12. SOFT SKILLS LABORATORY	TW	25	10	18	P

FIRST TERM TOTAL = 434/750.

ORDN. 1 MARKS :

T80058596 KUMARGAURAV SINGH MAMATA , 71200940L , , PICT , T80058596

01. OPERATING SYSTEM	PP	100	40	54	P
02. THEORY OF COMPUTATION	PP	100	40	50	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	41	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	51	P
05. SOFTWARE ENGINEERING	PP	100	40	55	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	23	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	20	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	23	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	40	P
10. NETWORK LABORATORY	TW	25	10	23	P
11. NETWORK LABORATORY	OR	50	20	31	P
12. SOFT SKILLS LABORATORY	TW	25	10	24	P

FIRST TERM TOTAL = 435/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80058597 KUNJI RAHUL SRINIVASAN USHA , 71200941J , , PICT , T80058597

01. OPERATING SYSTEM	PP	100	40	73	P
02. THEORY OF COMPUTATION	PP	100	40	77	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	71	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	64	P
05. SOFTWARE ENGINEERING	PP	100	40	70	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	24	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	45	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	24	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	44	P
10. NETWORK LABORATORY	TW	25	10	24	P
11. NETWORK LABORATORY	OR	50	20	44	P
12. SOFT SKILLS LABORATORY	TW	25	10	24	P

FIRST TERM TOTAL = 584/750.

ORDN. 1 MARKS :

T80058598 LUNKAD JAYESH PRAKASH UJJWALA , 71350928H , , PICT , T80058598

01. OPERATING SYSTEM	PP	100	40	67	P
02. THEORY OF COMPUTATION	PP	100	40	71	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	78	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	68	P
05. SOFTWARE ENGINEERING	PP	100	40	70	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	24	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	45	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	24	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	47	P
10. NETWORK LABORATORY	TW	25	10	24	P
11. NETWORK LABORATORY	OR	50	20	46	P
12. SOFT SKILLS LABORATORY	TW	25	10	24	P

FIRST TERM TOTAL = 588/750.

ORDN. 1 MARKS :

T80058599 MAHENDRA TANVEERSINGH TEJENDRASINGH SURINDERKAUR , 71200949D , , PICT , T80058599

01. OPERATING SYSTEM	PP	100	40	31	F
02. THEORY OF COMPUTATION	PP	100	40	49	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	64	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	52	P
05. SOFTWARE ENGINEERING	PP	100	40	47	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	20	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	38	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	20	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	44	P
10. NETWORK LABORATORY	TW	25	10	18	P
11. NETWORK LABORATORY	OR	50	20	30	P
12. SOFT SKILLS LABORATORY	TW	25	10	17	P

FIRST TERM TOTAL = 430/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80058600 MANALI DESAI MONA , 71200952D , , PICT , T80058600

01. OPERATING SYSTEM	PP	100	40	49	P
02. THEORY OF COMPUTATION	PP	100	40	40	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	54	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	40	P
05. SOFTWARE ENGINEERING	PP	100	40	40	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	14	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	32	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	18	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	37	P
10. NETWORK LABORATORY	TW	25	10	15	P
11. NETWORK LABORATORY	OR	50	20	36	P
12. SOFT SKILLS LABORATORY	TW	25	10	12	P

FIRST TERM TOTAL = 387/750.

ORDN. 1 MARKS :

T80058601 MANDGE SHIVSHANKAR SHIVKUMAR USHA , 71200953B , , PICT , T80058601

01. OPERATING SYSTEM	PP	100	40	31	F
02. THEORY OF COMPUTATION	PP	100	40	41	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	60	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	46	P
05. SOFTWARE ENGINEERING	PP	100	40	40	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	22	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	31	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	24	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	38	P
10. NETWORK LABORATORY	TW	25	10	23	P
11. NETWORK LABORATORY	OR	50	20	21	P
12. SOFT SKILLS LABORATORY	TW	25	10	23	P

FIRST TERM TOTAL = 400/750.

ORDN. 1 MARKS :

T80058602 MANIK AGARWAL REENA , 71200956G , , PICT , T80058602

01. OPERATING SYSTEM	PP	100	40	40	P
02. THEORY OF COMPUTATION	PP	100	40	40	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	70	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	46	P
05. SOFTWARE ENGINEERING	PP	100	40	51	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	17	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	25	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	17	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	35	P
10. NETWORK LABORATORY	TW	25	10	18	P
11. NETWORK LABORATORY	OR	50	20	30	P
12. SOFT SKILLS LABORATORY	TW	25	10	18	P

FIRST TERM TOTAL = 407/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80058603 MARADKAR VISHAL PRABHAKAR SUMATI , 71200959M , , PICT , T80058603

01. OPERATING SYSTEM	PP	100	40	60	P
02. THEORY OF COMPUTATION	PP	100	40	56	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	59	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	65	P
05. SOFTWARE ENGINEERING	PP	100	40	48	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	20	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	38	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	19	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	30	P
10. NETWORK LABORATORY	TW	25	10	21	P
11. NETWORK LABORATORY	OR	50	20	25	P
12. SOFT SKILLS LABORATORY	TW	25	10	21	P

FIRST TERM TOTAL = 462/750.

ORDN. 1 MARKS :

T80058604 MOGRA ISHITA HEMANT SUNITA , 71200963K , , PICT , T80058604

01. OPERATING SYSTEM	PP	100	40	55	P
02. THEORY OF COMPUTATION	PP	100	40	66	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	55	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	60	P
05. SOFTWARE ENGINEERING	PP	100	40	47	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	18	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	42	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	18	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	39	P
10. NETWORK LABORATORY	TW	25	10	21	P
11. NETWORK LABORATORY	OR	50	20	22	P
12. SOFT SKILLS LABORATORY	TW	25	10	18	P

FIRST TERM TOTAL = 461/750.

ORDN. 1 MARKS :

T80058605 MOHAMMED AAQUIB ANSARI MOHAMMED YUSUF SUFIYA , 71045526H , T8058574 , PICT , T80058605

01. OPERATING SYSTEM	PP	100	40	45	P C
02. THEORY OF COMPUTATION	PP	100	40	40	P C
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	45	P C
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	40	P C
05. SOFTWARE ENGINEERING	PP	100	40	40	P C
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	10	P C
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	20	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	10	P C
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	AA	F
10. NETWORK LABORATORY	TW	25	10	10	P C
11. NETWORK LABORATORY	OR	50	20	11	F
12. SOFT SKILLS LABORATORY	TW	25	10	12	P C

FIRST TERM TOTAL = 283/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80058606 MOKASHI SUPRIYA MANIK SHARADA , 71200964H , PICT , T80058606

01. OPERATING SYSTEM	PP	100	40	41	P
02. THEORY OF COMPUTATION	PP	100	40	54	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	57	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	53	P
05. SOFTWARE ENGINEERING	PP	100	40	48	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	18	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	05	F
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	22	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	38	P
10. NETWORK LABORATORY	TW	25	10	21	P
11. NETWORK LABORATORY	OR	50	20	12	F
12. SOFT SKILLS LABORATORY	TW	25	10	18	P

FIRST TERM TOTAL = 387/750.

ORDN. 1 MARKS :

T80058607 MULAY AMIT YESHWANT SNEHAL , 71045530F , T80058654 , PICT , T80058607

01. OPERATING SYSTEM	PP	100	40	40	P C	13. SYSTEM SOFTWARE PROGRAMMING	PP	100	40	40	P C
02. THEORY OF COMPUTATION	PP	100	40	52	P C	14. MANAGEMENT INFORMATION SYSTEMS	PP	100	40	67	P C
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	58	P C	15. PROGRAMMING PARADIGMS	PP	100	40	50	P C
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	58	P C	16. DESIGN & ANALYSIS OF ALGORITHMS	PP	100	40	50	P C
05. SOFTWARE ENGINEERING	PP	100	40	46	P C	17. HUMAN COMPU.INTERACTION & USABI.	PP	100	40	46	P C
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	15	P C	18. SOFTWARE DESIGN LABORATORY	TW	50	20	20	P C
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	23	P C	19. SOFTWARE DESIGN LABORATORY	PR	50	20	AA	F
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	15	P C	20. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	21	P C
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	20	P C	21. SOFTWARE DEVELOPMENT TOOLS LAB.	OR	50	20	20	P C
10. NETWORK LABORATORY	TW	25	10	14	P C	22. SEMINAR AND TECHNICAL COMMUN.	TW	50	20	20	P C
11. NETWORK LABORATORY	OR	50	20	23	P C						
12. SOFT SKILLS LABORATORY	TW	25	10	10	P C						

GRAND TOTAL = 708/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

T80058608 MULE MANJUSHA ASHOK SUVARNA , 71200968L , PICT , T80058608

01. OPERATING SYSTEM	PP	100	40	56	P
02. THEORY OF COMPUTATION	PP	100	40	67	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	72	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	63	P
05. SOFTWARE ENGINEERING	PP	100	40	50	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	20	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	38	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	23	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	30	P
10. NETWORK LABORATORY	TW	25	10	22	P
11. NETWORK LABORATORY	OR	50	20	39	P
12. SOFT SKILLS LABORATORY	TW	25	10	20	P

FIRST TERM TOTAL = 500/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80058609 MUNOT SHRENIK SUMTILAL SADHANA , 71100903B , , PICT , T80058609

01. OPERATING SYSTEM	PP	100	40	43	P
02. THEORY OF COMPUTATION	PP	100	40	26	F
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	44	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	41	P
05. SOFTWARE ENGINEERING	PP	100	40	24	F
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	21	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	25	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	22	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	42	P
10. NETWORK LABORATORY	TW	25	10	22	P
11. NETWORK LABORATORY	OR	50	20	25	P
12. SOFT SKILLS LABORATORY	TW	25	10	20	P

FIRST TERM TOTAL = 355/750.

ORDN. 1 MARKS :

T80058610 NAIK PRIYANKA MANOJ KAVITA , 71200977K , , PICT , T80058610

01. OPERATING SYSTEM	PP	100	40	55	P
02. THEORY OF COMPUTATION	PP	100	40	54	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	62	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	60	P
05. SOFTWARE ENGINEERING	PP	100	40	49	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	22	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	43	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	23	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	30	P
10. NETWORK LABORATORY	TW	25	10	24	P
11. NETWORK LABORATORY	OR	50	20	44	P
12. SOFT SKILLS LABORATORY	TW	25	10	23	P

FIRST TERM TOTAL = 489/750.

ORDN. 1 MARKS :

T80058611 NAIK SHIVANI SHRIKANT RADHIKA , 71200978H , , PICT , T80058611

01. OPERATING SYSTEM	PP	100	40	47	P
02. THEORY OF COMPUTATION	PP	100	40	77	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	68	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	62	P
05. SOFTWARE ENGINEERING	PP	100	40	53	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	18	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	42	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	19	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	44	P
10. NETWORK LABORATORY	TW	25	10	20	P
11. NETWORK LABORATORY	OR	50	20	42	P
12. SOFT SKILLS LABORATORY	TW	25	10	16	P

FIRST TERM TOTAL = 508/750.

ORDN. 1 MARKS :



NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80058612 NATU SWAPNIL RAVINDRA ASHA , 71200980K , , PICT , T80058612

01. OPERATING SYSTEM	PP	100	40	60	P
02. THEORY OF COMPUTATION	PP	100	40	65	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	78	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	68	P
05. SOFTWARE ENGINEERING	PP	100	40	52	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	24	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	44	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	23	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	45	P
10. NETWORK LABORATORY	TW	25	10	22	P
11. NETWORK LABORATORY	OR	50	20	24	P
12. SOFT SKILLS LABORATORY	TW	25	10	23	P

FIRST TERM TOTAL = 528/750.

ORDN. 1 MARKS :

T80058613 NAVGIRE SAGAR PRADEEP SUNETRA , 71200981H , , PICT , T80058613

01. OPERATING SYSTEM	PP	100	40	65	P
02. THEORY OF COMPUTATION	PP	100	40	43	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	79	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	58	P
05. SOFTWARE ENGINEERING	PP	100	40	50	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	19	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	40	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	20	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	42	P
10. NETWORK LABORATORY	TW	25	10	21	P
11. NETWORK LABORATORY	OR	50	20	36	P
12. SOFT SKILLS LABORATORY	TW	25	10	20	P

FIRST TERM TOTAL = 493/750.

ORDN. 1 MARKS :

T80058614 NEHE ABHISHEK ASHOK SUNITA , 71350929F , , PICT , T80058614

01. OPERATING SYSTEM	PP	100	40	70	P
02. THEORY OF COMPUTATION	PP	100	40	56	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	87	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	68	P
05. SOFTWARE ENGINEERING	PP	100	40	43	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	23	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	28	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	24	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	44	P
10. NETWORK LABORATORY	TW	25	10	24	P
11. NETWORK LABORATORY	OR	50	20	39	P
12. SOFT SKILLS LABORATORY	TW	25	10	22	P

FIRST TERM TOTAL = 528/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80058615 NEWALKAR REVA VIVEK SMITA , 71200986J , , PICT , T80058615

01. OPERATING SYSTEM	PP	100	40	53	P
02. THEORY OF COMPUTATION	PP	100	40	54	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	70	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	70	P
05. SOFTWARE ENGINEERING	PP	100	40	53	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	22	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	40	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	22	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	25	P
10. NETWORK LABORATORY	TW	25	10	22	P
11. NETWORK LABORATORY	OR	50	20	23	P
12. SOFT SKILLS LABORATORY	TW	25	10	23	P

FIRST TERM TOTAL = 477/750.

ORDN. 1 MARKS :

T80058616 OSWAL SAHIT SURESH SUMITRA , 71200992C , , PICT , T80058616

01. OPERATING SYSTEM	PP	100	40	61	P
02. THEORY OF COMPUTATION	PP	100	40	57	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	85	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	65	P
05. SOFTWARE ENGINEERING	PP	100	40	57	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	21	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	38	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	22	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	41	P
10. NETWORK LABORATORY	TW	25	10	23	P
11. NETWORK LABORATORY	OR	50	20	25	P
12. SOFT SKILLS LABORATORY	TW	25	10	21	P

FIRST TERM TOTAL = 516/750.

ORDN. 1 MARKS :

T80058617 PABALKAR SARANG SHIVNATH VIDYA , 71200993M , , PICT , T80058617

01. OPERATING SYSTEM	PP	100	40	40	P
02. THEORY OF COMPUTATION	PP	100	40	50	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	65	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	57	P
05. SOFTWARE ENGINEERING	PP	100	40	40	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	20	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	38	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	21	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	36	P
10. NETWORK LABORATORY	TW	25	10	21	P
11. NETWORK LABORATORY	OR	50	20	28	P
12. SOFT SKILLS LABORATORY	TW	25	10	20	P

FIRST TERM TOTAL = 436/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80058618 PADIA SARVESH LALIT SEEMA , 71200994K , , PICT , T80058618

01. OPERATING SYSTEM	PP	100	40	46	P
02. THEORY OF COMPUTATION	PP	100	40	68	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	83	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	60	P
05. SOFTWARE ENGINEERING	PP	100	40	53	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	22	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	45	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	22	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	45	P
10. NETWORK LABORATORY	TW	25	10	23	P
11. NETWORK LABORATORY	OR	50	20	32	P
12. SOFT SKILLS LABORATORY	TW	25	10	22	P

FIRST TERM TOTAL = 521/750.

ORDN. 1 MARKS :

T80058619 PALLOD HRISHIKESH RAMNARAYAN VANDANA , 71200996F , , PICT , T80058619

01. OPERATING SYSTEM	PP	100	40	59	P
02. THEORY OF COMPUTATION	PP	100	40	71	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	84	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	68	P
05. SOFTWARE ENGINEERING	PP	100	40	53	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	22	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	45	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	24	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	47	P
10. NETWORK LABORATORY	TW	25	10	24	P
11. NETWORK LABORATORY	OR	50	20	40	P
12. SOFT SKILLS LABORATORY	TW	25	10	22	P

FIRST TERM TOTAL = 559/750.

ORDN. 1 MARKS :

T80058620 PANCHARIYA ANIKET SANJAY LEENA , 71200997D , , PICT , T80058620

01. OPERATING SYSTEM	PP	100	40	61	P
02. THEORY OF COMPUTATION	PP	100	40	62	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	86	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	62	P
05. SOFTWARE ENGINEERING	PP	100	40	61	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	19	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	43	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	20	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	44	P
10. NETWORK LABORATORY	TW	25	10	20	P
11. NETWORK LABORATORY	OR	50	20	34	P
12. SOFT SKILLS LABORATORY	TW	25	10	18	P

FIRST TERM TOTAL = 530/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80058621 PARAKH NAMAN RAJENDRA SANGEETA , 71201001H , , PICT , T80058621

01. OPERATING SYSTEM	PP	100	40	52	P
02. THEORY OF COMPUTATION	PP	100	40	56	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	54	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	56	P
05. SOFTWARE ENGINEERING	PP	100	40	67	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	20	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	38	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	22	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	43	P
10. NETWORK LABORATORY	TW	25	10	21	P
11. NETWORK LABORATORY	OR	50	20	33	P
12. SOFT SKILLS LABORATORY	TW	25	10	20	P

FIRST TERM TOTAL = 482/750.

ORDN. 1 MARKS :

T80058622 PARINITA MATHARU RENU , 71201003D , , PICT , T80058622

01. OPERATING SYSTEM	PP	100	40	55	P
02. THEORY OF COMPUTATION	PP	100	40	55	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	58	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	54	P
05. SOFTWARE ENGINEERING	PP	100	40	49	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	16	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	28	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	18	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	38	P
10. NETWORK LABORATORY	TW	25	10	16	P
11. NETWORK LABORATORY	OR	50	20	33	P
12. SOFT SKILLS LABORATORY	TW	25	10	16	P

FIRST TERM TOTAL = 436/750.

ORDN. 1 MARKS :

T80058623 PATADE PRATIK RAMDAS SEEMA , 71350930K , , PICT , T80058623

01. OPERATING SYSTEM	PP	100	40	62	P
02. THEORY OF COMPUTATION	PP	100	40	47	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	66	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	59	P
05. SOFTWARE ENGINEERING	PP	100	40	50	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	18	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	32	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	21	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	35	P
10. NETWORK LABORATORY	TW	25	10	21	P
11. NETWORK LABORATORY	OR	50	20	27	P
12. SOFT SKILLS LABORATORY	TW	25	10	19	P

FIRST TERM TOTAL = 457/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80058624 PATIL AMOL MADHUKAR JYOTI , 71100938E , T80058588 , PICT , T80058624

01. OPERATING SYSTEM	PP	100	40	54	P C	13. SYSTEM SOFTWARE PROGRAMMING	PP	100	40	51	P C
02. THEORY OF COMPUTATION	PP	100	40	44	P C	14. MANAGEMENT INFORMATION SYSTEMS	PP	100	40	43	P C
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	45	P C	15. PROGRAMMING PARADIGMS	PP	100	40	42	P C
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	45	P C	16. DESIGN & ANALYSIS OF ALGORITHMS	PP	100	40	44	P C
05. SOFTWARE ENGINEERING	PP	100	40	52	P C	17. HUMAN COMPU.INTERACTION & USABI.	PP	100	40	52	P C
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	14	P C	18. SOFTWARE DESIGN LABORATORY	TW	50	20	29	P C
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	32	P C	19. SOFTWARE DESIGN LABORATORY	PR	50	20	36	P C
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	18	P C	20. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	30	P C
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	38	P C	21. SOFTWARE DEVELOPMENT TOOLS LAB.	OR	50	20	32	P
10. NETWORK LABORATORY	TW	25	10	12	P C	22. SEMINAR AND TECHNICAL COMMUN.	TW	50	20	23	P C
11. NETWORK LABORATORY	OR	50	20	29	P C						
12. SOFT SKILLS LABORATORY	TW	25	10	12	P C						

GRAND TOTAL = 777/1500, RESULT: SECOND CLASS

ORDN. 1 MARKS :

T80058625 PATIL DEEPAK RAMESH MINAKSHEE , 71201005L , PICT , T80058625

01. OPERATING SYSTEM	PP	100	40	49	P
02. THEORY OF COMPUTATION	PP	100	40	54	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	65	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	67	P
05. SOFTWARE ENGINEERING	PP	100	40	46	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	21	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	40	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	22	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	42	P
10. NETWORK LABORATORY	TW	25	10	22	P
11. NETWORK LABORATORY	OR	50	20	32	P
12. SOFT SKILLS LABORATORY	TW	25	10	23	P

FIRST TERM TOTAL = 483/750.

ORDN. 1 MARKS :

T80058626 PATIL INDRAJEET ANIL PRIYA , 71201006J , PICT , T80058626

01. OPERATING SYSTEM	PP	100	40	52	P
02. THEORY OF COMPUTATION	PP	100	40	40	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	49	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	49	P
05. SOFTWARE ENGINEERING	PP	100	40	46	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	21	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	20	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	23	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	45	P
10. NETWORK LABORATORY	TW	25	10	22	P
11. NETWORK LABORATORY	OR	50	20	41	P
12. SOFT SKILLS LABORATORY	TW	25	10	21	P

FIRST TERM TOTAL = 429/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80058627 PATIL MAYUR PAVANKUMAR MANDAKINI , 71350931H , PICT , T80058627

01. OPERATING SYSTEM	PP	100	40	60	P
02. THEORY OF COMPUTATION	PP	100	40	52	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	69	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	61	P
05. SOFTWARE ENGINEERING	PP	100	40	53	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	16	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	35	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	19	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	42	P
10. NETWORK LABORATORY	TW	25	10	18	P
11. NETWORK LABORATORY	OR	50	20	37	P
12. SOFT SKILLS LABORATORY	TW	25	10	16	P

FIRST TERM TOTAL = 478/750.

ORDN. 1 MARKS :

T80058628 PATKE SWATI SANJAY SUNITA , 71201011E , PICT , T80058628

01. OPERATING SYSTEM	PP	100	40	40	P
02. THEORY OF COMPUTATION	PP	100	40	40	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	45	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	51	P
05. SOFTWARE ENGINEERING	PP	100	40	40	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	19	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	42	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	19	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	37	P
10. NETWORK LABORATORY	TW	25	10	21	P
11. NETWORK LABORATORY	OR	50	20	34	P
12. SOFT SKILLS LABORATORY	TW	25	10	19	P

FIRST TERM TOTAL = 407/750.

ORDN. 1 MARKS :

T80058629 PAWAR CHANDAN DILIP REKHA , 70925555B , T80058592 , PICT , T80058629

01. OPERATING SYSTEM	PP	100	40	72	P C	13. SYSTEM SOFTWARE PROGRAMMING	PP	100	40	68	P C
02. THEORY OF COMPUTATION	PP	100	40	43	P C	14. MANAGEMENT INFORMATION SYSTEMS	PP	100	40	63	P C
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	61	P C	15. PROGRAMMING PARADIGMS	PP	100	40	57	P C
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	52	P C	16. DESIGN & ANALYSIS OF ALGORITHMS	PP	100	40	54	P C
05. SOFTWARE ENGINEERING	PP	100	40	57	P C	17. HUMAN COMPU.INTERACTION & USABI.	PP	100	40	69	P C
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	16	P C	18. SOFTWARE DESIGN LABORATORY	TW	50	20	25	P C
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	25	P C	19. SOFTWARE DESIGN LABORATORY	PR	50	20	36	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	17	P C	20. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	35	P C
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	25	P C	21. SOFTWARE DEVELOPMENT TOOLS LAB.	OR	50	20	39	P
10. NETWORK LABORATORY	TW	25	10	14	P C	22. SEMINAR AND TECHNICAL COMMUN.	TW	50	20	27	P C
11. NETWORK LABORATORY	OR	50	20	37	P C						
12. SOFT SKILLS LABORATORY	TW	25	10	13	P C						

GRAND TOTAL = 905/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80058630 PAWAR KAVITA SANDIPAN SHEVANTA , 71350932F , PICT , T80058630

01. OPERATING SYSTEM	PP	100	40	31	F
02. THEORY OF COMPUTATION	PP	100	40	AA	F
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	40	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	41	P
05. SOFTWARE ENGINEERING	PP	100	40	33	F
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	15	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	05	F
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	17	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	12	F
10. NETWORK LABORATORY	TW	25	10	17	P
11. NETWORK LABORATORY	OR	50	20	28	P
12. SOFT SKILLS LABORATORY	TW	25	10	13	P

FIRST TERM TOTAL = 252/750.

ORDN. 1 MARKS :

T80058631 PAWAR PRASAD GORAKSH MEENAKSHI , 71100951B , T80058593 , PICT , T80058631

01. OPERATING SYSTEM	PP	100	40	42	P C	13. SYSTEM SOFTWARE PROGRAMMING	PP	100	40	40	P C
02. THEORY OF COMPUTATION	PP	100	40	40	P C	14. MANAGEMENT INFORMATION SYSTEMS	PP	100	40	42	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	40	P C	15. PROGRAMMING PARADIGMS	PP	100	40	47	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	40	P C	16. DESIGN & ANALYSIS OF ALGORITHMS	PP	100	40	47	P C
05. SOFTWARE ENGINEERING	PP	100	40	40	P C	17. HUMAN COMPU.INTERACTION & USABI.	PP	100	40	40	P C
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	18	P C	18. SOFTWARE DESIGN LABORATORY	TW	50	20	27	P C
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	33	P C	19. SOFTWARE DESIGN LABORATORY	PR	50	20	25	P C
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	15	P C	20. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	37	P C
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	37	P C	21. SOFTWARE DEVELOPMENT TOOLS LAB.	OR	50	20	28	P C
10. NETWORK LABORATORY	TW	25	10	12	P C	22. SEMINAR AND TECHNICAL COMMUN.	TW	50	20	36	P C
11. NETWORK LABORATORY	OR	50	20	38	P C						
12. SOFT SKILLS LABORATORY	TW	25	10	12	P C						

GRAND TOTAL = 736/1500, RESULT: PASS CLASS

ORDN. 1 MARKS :

T80058632 PHADNIS SHARWARI SADANAND SUPRIYA , 71201014K , PICT , T80058632

01. OPERATING SYSTEM	PP	100	40	48	P
02. THEORY OF COMPUTATION	PP	100	40	60	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	72	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	53	P
05. SOFTWARE ENGINEERING	PP	100	40	56	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	19	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	30	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	20	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	35	P
10. NETWORK LABORATORY	TW	25	10	21	P
11. NETWORK LABORATORY	OR	50	20	38	P
12. SOFT SKILLS LABORATORY	TW	25	10	19	P

FIRST TERM TOTAL = 471/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80058633 PILAJI EKTA BALAJI ANUJA , 71201016F , , PICT , T80058633

01. OPERATING SYSTEM	PP	100	40	44	P
02. THEORY OF COMPUTATION	PP	100	40	59	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	53	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	53	P
05. SOFTWARE ENGINEERING	PP	100	40	50	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	21	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	40	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	21	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	30	P
10. NETWORK LABORATORY	TW	25	10	23	P
11. NETWORK LABORATORY	OR	50	20	38	P
12. SOFT SKILLS LABORATORY	TW	25	10	22	P

FIRST TERM TOTAL = 454/750.

ORDN. 1 MARKS :

T80058634 POHANKAR RASIKA AVINASH ANJALI , 71201017D , , PICT , T80058634

01. OPERATING SYSTEM	PP	100	40	63	P
02. THEORY OF COMPUTATION	PP	100	40	65	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	71	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	71	P
05. SOFTWARE ENGINEERING	PP	100	40	67	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	24	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	45	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	24	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	42	P
10. NETWORK LABORATORY	TW	25	10	24	P
11. NETWORK LABORATORY	OR	50	20	47	P
12. SOFT SKILLS LABORATORY	TW	25	10	24	P

FIRST TERM TOTAL = 567/750.

ORDN. 1 MARKS :

T80058635 POTDUKHE VAISHNAVI NILKANTH SANGEETA , 71201018B , , PICT , T80058635

01. OPERATING SYSTEM	PP	100	40	45	P
02. THEORY OF COMPUTATION	PP	100	40	40	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	40	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	40	P
05. SOFTWARE ENGINEERING	PP	100	40	32	F
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	22	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	32	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	22	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	34	P
10. NETWORK LABORATORY	TW	25	10	22	P
11. NETWORK LABORATORY	OR	50	20	21	P
12. SOFT SKILLS LABORATORY	TW	25	10	23	P

FIRST TERM TOTAL = 373/750.

ORDN. 1 MARKS :



NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80058636 POTNIS ISHA RAHUL SUVARNA , 71201019L , , PICT , T80058636

01. OPERATING SYSTEM	PP	100	40	59	P
02. THEORY OF COMPUTATION	PP	100	40	70	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	64	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	63	P
05. SOFTWARE ENGINEERING	PP	100	40	58	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	22	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	40	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	23	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	41	P
10. NETWORK LABORATORY	TW	25	10	24	P
11. NETWORK LABORATORY	OR	50	20	41	P
12. SOFT SKILLS LABORATORY	TW	25	10	23	P

FIRST TERM TOTAL = 528/750.

ORDN. 1 MARKS :

T80058637 PRASHUL SINGH ARCHANA , 71201022L , , PICT , T80058637

01. OPERATING SYSTEM	PP	100	40	56	P
02. THEORY OF COMPUTATION	PP	100	40	58	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	63	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	52	P
05. SOFTWARE ENGINEERING	PP	100	40	46	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	21	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	42	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	21	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	32	P
10. NETWORK LABORATORY	TW	25	10	23	P
11. NETWORK LABORATORY	OR	50	20	41	P
12. SOFT SKILLS LABORATORY	TW	25	10	22	P

FIRST TERM TOTAL = 477/750.

ORDN. 1 MARKS :

T80058638 PRERIT VILAS AUTI INDU , 71201023J , , PICT , T80058638

01. OPERATING SYSTEM	PP	100	40	46	P
02. THEORY OF COMPUTATION	PP	100	40	62	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	63	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	68	P
05. SOFTWARE ENGINEERING	PP	100	40	55	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	20	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	32	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	21	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	38	P
10. NETWORK LABORATORY	TW	25	10	23	P
11. NETWORK LABORATORY	OR	50	20	35	P
12. SOFT SKILLS LABORATORY	TW	25	10	19	P

FIRST TERM TOTAL = 482/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80058639 RAHUL KUMAR USHA , 71201029H , PICT , T80058639

01. OPERATING SYSTEM	PP	100	40	44	P
02. THEORY OF COMPUTATION	PP	100	40	40	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	60	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	53	P
05. SOFTWARE ENGINEERING	PP	100	40	29	F
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	19	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	35	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	20	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	32	P
10. NETWORK LABORATORY	TW	25	10	20	P
11. NETWORK LABORATORY	OR	50	20	13	F
12. SOFT SKILLS LABORATORY	TW	25	10	21	P

FIRST TERM TOTAL = 386/750.

ORDN. 1 MARKS :

T80058640 RAHUL RANJAN KUMKUM , 70701594E , T80058678 , PICT , T80058640

01. OPERATING SYSTEM	PP	100	40	46	P C	13. SYSTEM SOFTWARE PROGRAMMING	PP	100	40	40	P C
02. THEORY OF COMPUTATION	PP	100	40	40	P C	14. MANAGEMENT INFORMATION SYSTEMS	PP	100	40	AA	F
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	42	P C	15. PROGRAMMING PARADIGMS	PP	100	40	40	P C
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	41	P C	16. DESIGN & ANALYSIS OF ALGORITHMS	PP	100	40	40	P C
05. SOFTWARE ENGINEERING	PP	100	40	AA	F	17. HUMAN COMPU.INTERACTION & USABI.	PP	100	40	44	P C
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	10	P C	18. SOFTWARE DESIGN LABORATORY	TW	50	20	20	P C
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	29	P C	19. SOFTWARE DESIGN LABORATORY	PR	50	20	22	P C
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	10	P C	20. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	20	P C
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	23	P C	21. SOFTWARE DEVELOPMENT TOOLS LAB.	OR	50	20	20	P C
10. NETWORK LABORATORY	TW	25	10	10	P C	22. SEMINAR AND TECHNICAL COMMUN.	TW	50	20	20	P C
11. NETWORK LABORATORY	OR	50	20	20	P C						
12. SOFT SKILLS LABORATORY	TW	25	10	10	P C						

GRAND TOTAL = 547/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

T80058641 RAHUL SHARMA PROMILA , 71100963F , T80058599 , PICT , T80058641

01. OPERATING SYSTEM	PP	100	40	59	P C	13. SYSTEM SOFTWARE PROGRAMMING	PP	100	40	57	P C
02. THEORY OF COMPUTATION	PP	100	40	66	P C	14. MANAGEMENT INFORMATION SYSTEMS	PP	100	40	62	P C
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	46	P C	15. PROGRAMMING PARADIGMS	PP	100	40	49	P C
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	51	P C	16. DESIGN & ANALYSIS OF ALGORITHMS	PP	100	40	50	P C
05. SOFTWARE ENGINEERING	PP	100	40	43	P C	17. HUMAN COMPU.INTERACTION & USABI.	PP	100	40	60	P C
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	12	P C	18. SOFTWARE DESIGN LABORATORY	TW	50	20	20	P C
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	36	P C	19. SOFTWARE DESIGN LABORATORY	PR	50	20	25	P C
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	11	P C	20. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	31	P C
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	28	P C	21. SOFTWARE DEVELOPMENT TOOLS LAB.	OR	50	20	22	P C
10. NETWORK LABORATORY	TW	25	10	13	P C	22. SEMINAR AND TECHNICAL COMMUN.	TW	50	20	22	P C
11. NETWORK LABORATORY	OR	50	20	14	F						
12. SOFT SKILLS LABORATORY	TW	25	10	12	P C						

GRAND TOTAL = 789/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80058642 RANSHEVRE ROHIT YASHWANT USHA , 71350933D , , PICT , T80058642

01. OPERATING SYSTEM	PP	100	40	40	P
02. THEORY OF COMPUTATION	PP	100	40	40	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	57	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	63	P
05. SOFTWARE ENGINEERING	PP	100	40	44	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	16	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	AA	F
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	11	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	39	P
10. NETWORK LABORATORY	TW	25	10	20	P
11. NETWORK LABORATORY	OR	50	20	30	P
12. SOFT SKILLS LABORATORY	TW	25	10	15	P

FIRST TERM TOTAL = 375/750.

ORDN. 1 MARKS :

T80058643 RASKAR SUNITA ASHOK SUSHILA , 71350934B , , PICT , T80058643

01. OPERATING SYSTEM	PP	100	40	49	P
02. THEORY OF COMPUTATION	PP	100	40	47	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	49	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	53	P
05. SOFTWARE ENGINEERING	PP	100	40	49	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	21	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	30	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	22	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	38	P
10. NETWORK LABORATORY	TW	25	10	22	P
11. NETWORK LABORATORY	OR	50	20	42	P
12. SOFT SKILLS LABORATORY	TW	25	10	18	P

FIRST TERM TOTAL = 440/750.

ORDN. 1 MARKS :

T80058644 RATHI TUSHAR SANDEEP SANGITA , 71201033F , , PICT , T80058644

01. OPERATING SYSTEM	PP	100	40	50	P
02. THEORY OF COMPUTATION	PP	100	40	46	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	50	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	47	P
05. SOFTWARE ENGINEERING	PP	100	40	40	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	18	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	38	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	17	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	33	P
10. NETWORK LABORATORY	TW	25	10	23	P
11. NETWORK LABORATORY	OR	50	20	28	P
12. SOFT SKILLS LABORATORY	TW	25	10	17	P

FIRST TERM TOTAL = 407/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80058645 RATHOD NEHA SANJAY NALINI , 71201035B , , PICT , T80058645

01. OPERATING SYSTEM	PP	100	40	54	P
02. THEORY OF COMPUTATION	PP	100	40	48	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	63	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	51	P
05. SOFTWARE ENGINEERING	PP	100	40	48	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	21	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	30	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	23	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	32	P
10. NETWORK LABORATORY	TW	25	10	23	P
11. NETWORK LABORATORY	OR	50	20	38	P
12. SOFT SKILLS LABORATORY	TW	25	10	22	P

FIRST TERM TOTAL = 453/750.

ORDN. 1 MARKS :

T80058646 RITESH RAJIV POREY AMRUTA , 71201040J , , PICT , T80058646

01. OPERATING SYSTEM	PP	100	40	57	P
02. THEORY OF COMPUTATION	PP	100	40	61	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	68	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	58	P
05. SOFTWARE ENGINEERING	PP	100	40	57	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	19	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	40	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	22	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	44	P
10. NETWORK LABORATORY	TW	25	10	21	P
11. NETWORK LABORATORY	OR	50	20	28	P
12. SOFT SKILLS LABORATORY	TW	25	10	19	P

FIRST TERM TOTAL = 494/750.

ORDN. 1 MARKS :

T80058647 SAKSHI SIRPAL ANUPAMA , 71201048D , , PICT , T80058647

01. OPERATING SYSTEM	PP	100	40	59	P
02. THEORY OF COMPUTATION	PP	100	40	68	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	67	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	65	P
05. SOFTWARE ENGINEERING	PP	100	40	45	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	20	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	30	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	21	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	32	P
10. NETWORK LABORATORY	TW	25	10	23	P
11. NETWORK LABORATORY	OR	50	20	29	P
12. SOFT SKILLS LABORATORY	TW	25	10	21	P

FIRST TERM TOTAL = 480/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80058648 SALUNKE GAURI RANGNATH ANITA , 71201049B , , PICT , T80058648

01. OPERATING SYSTEM	PP	100	40	68	P
02. THEORY OF COMPUTATION	PP	100	40	59	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	68	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	68	P
05. SOFTWARE ENGINEERING	PP	100	40	55	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	24	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	45	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	23	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	42	P
10. NETWORK LABORATORY	TW	25	10	22	P
11. NETWORK LABORATORY	OR	50	20	32	P
12. SOFT SKILLS LABORATORY	TW	25	10	24	P

FIRST TERM TOTAL = 530/750.

ORDN. 1 MARKS :

T80058649 SAPKALE SNEHAL SURESH USHA , 71201052B , , PICT , T80058649

01. OPERATING SYSTEM	PP	100	40	40	P
02. THEORY OF COMPUTATION	PP	100	40	22	F
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	49	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	51	P
05. SOFTWARE ENGINEERING	PP	100	40	16	F
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	15	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	05	F
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	15	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	25	P
10. NETWORK LABORATORY	TW	25	10	16	P
11. NETWORK LABORATORY	OR	50	20	28	P
12. SOFT SKILLS LABORATORY	TW	25	10	12	P

FIRST TERM TOTAL = 294/750.

ORDN. 1 MARKS :

T80058650 SARODE ASHWINI RAJENDRA SANGITA , 71201053L , , PICT , T80058650

01. OPERATING SYSTEM	PP	100	40	65	P
02. THEORY OF COMPUTATION	PP	100	40	61	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	75	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	61	P
05. SOFTWARE ENGINEERING	PP	100	40	59	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	24	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	41	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	23	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	36	P
10. NETWORK LABORATORY	TW	25	10	24	P
11. NETWORK LABORATORY	OR	50	20	26	P
12. SOFT SKILLS LABORATORY	TW	25	10	23	P

FIRST TERM TOTAL = 518/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80058651 SARTHAK MAJITHIA POOJA , 71045598E , T80058666 , PICT , T80058651

01. OPERATING SYSTEM	PP	100	40	40	P C	13. SYSTEM SOFTWARE PROGRAMMING	PP	100	40	AA	F
02. THEORY OF COMPUTATION	PP	100	40	45	P C	14. MANAGEMENT INFORMATION SYSTEMS	PP	100	40	61	P C
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	40	P C	15. PROGRAMMING PARADIGMS	PP	100	40	47	P C
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	51	P C	16. DESIGN & ANALYSIS OF ALGORITHMS	PP	100	40	55	P C
05. SOFTWARE ENGINEERING	PP	100	40	40	P	17. HUMAN COMPU.INTERACTION & USABI.	PP	100	40	50	P C
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	11	P C	18. SOFTWARE DESIGN LABORATORY	TW	50	20	20	P C
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	22	P C	19. SOFTWARE DESIGN LABORATORY	PR	50	20	AA	F
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	12	P C	20. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	22	P C
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	25	P	21. SOFTWARE DEVELOPMENT TOOLS LAB.	OR	50	20	32	P C
10. NETWORK LABORATORY	TW	25	10	10	P C	22. SEMINAR AND TECHNICAL COMMUN.	TW	50	20	22	P C
11. NETWORK LABORATORY	OR	50	20	24	P C						
12. SOFT SKILLS LABORATORY	TW	25	10	18	P C						

GRAND TOTAL = 647/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

T80058652 SAWANT ARUNDHATI ULHAS MADHAVI , 71201058M , , PICT , T80058652

01. OPERATING SYSTEM	PP	100	40	51	P
02. THEORY OF COMPUTATION	PP	100	40	40	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	63	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	70	P
05. SOFTWARE ENGINEERING	PP	100	40	40	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	19	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	38	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	21	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	42	P
10. NETWORK LABORATORY	TW	25	10	20	P
11. NETWORK LABORATORY	OR	50	20	30	P
12. SOFT SKILLS LABORATORY	TW	25	10	19	P

FIRST TERM TOTAL = 453/750.

ORDN. 1 MARKS :

T80058653 SHAH DISHANT UMESH SHILPA , 71201059K , , PICT , T80058653

01. OPERATING SYSTEM	PP	100	40	56	P
02. THEORY OF COMPUTATION	PP	100	40	59	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	65	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	51	P
05. SOFTWARE ENGINEERING	PP	100	40	59	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	10	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	20	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	15	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	30	P
10. NETWORK LABORATORY	TW	25	10	21	P
11. NETWORK LABORATORY	OR	50	20	14	F
12. SOFT SKILLS LABORATORY	TW	25	10	16	P

FIRST TERM TOTAL = 416/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80058654 SHAH RUTUJA RAJESH BEENA , 71201061M , , PICT , T80058654

01. OPERATING SYSTEM	PP	100	40	57	P
02. THEORY OF COMPUTATION	PP	100	40	59	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	65	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	78	P
05. SOFTWARE ENGINEERING	PP	100	40	62	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	23	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	43	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	24	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	45	P
10. NETWORK LABORATORY	TW	25	10	24	P
11. NETWORK LABORATORY	OR	50	20	43	P
12. SOFT SKILLS LABORATORY	TW	25	10	23	P

FIRST TERM TOTAL = 546/750.

ORDN. 1 MARKS :

T80058655 SHARIKA KHURANA RASHMI , 71201064F , , PICT , T80058655

01. OPERATING SYSTEM	PP	100	40	56	P
02. THEORY OF COMPUTATION	PP	100	40	57	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	53	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	59	P
05. SOFTWARE ENGINEERING	PP	100	40	58	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	18	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	06	F
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	18	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	39	P
10. NETWORK LABORATORY	TW	25	10	21	P
11. NETWORK LABORATORY	OR	50	20	23	P
12. SOFT SKILLS LABORATORY	TW	25	10	17	P

FIRST TERM TOTAL = 425/750.

ORDN. 1 MARKS :

T80058656 SHINDE ANIKET NAVNATH BABYNANDA , 71350935L , , PICT , T80058656

01. OPERATING SYSTEM	PP	100	40	66	P
02. THEORY OF COMPUTATION	PP	100	40	67	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	73	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	72	P
05. SOFTWARE ENGINEERING	PP	100	40	55	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	18	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	30	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	17	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	42	P
10. NETWORK LABORATORY	TW	25	10	20	P
11. NETWORK LABORATORY	OR	50	20	40	P
12. SOFT SKILLS LABORATORY	TW	25	10	16	P

FIRST TERM TOTAL = 516/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.  
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80058657 SHIRBHATE ABHILASH DNYANESHWAR RAJSHRI , 71101010C , T80058617 , PICT , T80058657

01. OPERATING SYSTEM	PP	100	40	46	P C	13. SYSTEM SOFTWARE PROGRAMMING	PP	100	40	49	P C
02. THEORY OF COMPUTATION	PP	100	40	43	P C	14. MANAGEMENT INFORMATION SYSTEMS	PP	100	40	45	P C
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	62	P C	15. PROGRAMMING PARADIGMS	PP	100	40	43	P C
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	45	P C	16. DESIGN & ANALYSIS OF ALGORITHMS	PP	100	40	51	P
05. SOFTWARE ENGINEERING	PP	100	40	49	P C	17. HUMAN COMPU.INTERACTION & USABI.	PP	100	40	48	P C
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	17	P C	18. SOFTWARE DESIGN LABORATORY	TW	50	20	21	P C
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	21	P C	19. SOFTWARE DESIGN LABORATORY	PR	50	20	35	P C
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	15	P C	20. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	32	P C
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	39	P C	21. SOFTWARE DEVELOPMENT TOOLS LAB.	OR	50	20	38	P
10. NETWORK LABORATORY	TW	25	10	12	P C	22. SEMINAR AND TECHNICAL COMMUN.	TW	50	20	32	P C
11. NETWORK LABORATORY	OR	50	20	32	P C						
12. SOFT SKILLS LABORATORY	TW	25	10	14	P C						

GRAND TOTAL = 789/1500, RESULT: SECOND CLASS

ORDN. 1 MARKS :

T80058658 SHUBHAM BOHRA MONA , 71201073E , PICT , T80058658

01. OPERATING SYSTEM	PP	100	40	29	F						
02. THEORY OF COMPUTATION	PP	100	40	23	F						
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	40	P						
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	20	F						
05. SOFTWARE ENGINEERING	PP	100	40	40	P						
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	15	P						
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	30	P						
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	15	P						
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	42	P						
10. NETWORK LABORATORY	TW	25	10	22	P						
11. NETWORK LABORATORY	OR	50	20	12	F						
12. SOFT SKILLS LABORATORY	TW	25	10	16	P						

FIRST TERM TOTAL = 304/750.

ORDN. 1 MARKS :

T80058659 SHUKLA MANISH NITYANAND MADHURI , 71045622M , T80058618 , PICT , T80058659

01. OPERATING SYSTEM	PP	100	40	63	P C	13. SYSTEM SOFTWARE PROGRAMMING	PP	100	40	40	P C
02. THEORY OF COMPUTATION	PP	100	40	50	P C	14. MANAGEMENT INFORMATION SYSTEMS	PP	100	40	56	P C
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	63	P C	15. PROGRAMMING PARADIGMS	PP	100	40	58	P C
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	51	P C	16. DESIGN & ANALYSIS OF ALGORITHMS	PP	100	40	AA	F
05. SOFTWARE ENGINEERING	PP	100	40	55	P C	17. HUMAN COMPU.INTERACTION & USABI.	PP	100	40	44	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	17	P C	18. SOFTWARE DESIGN LABORATORY	TW	50	20	27	P C
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	29	P C	19. SOFTWARE DESIGN LABORATORY	PR	50	20	38	P C
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	16	P C	20. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	30	P C
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	41	P C	21. SOFTWARE DEVELOPMENT TOOLS LAB.	OR	50	20	21	P C
10. NETWORK LABORATORY	TW	25	10	14	P C	22. SEMINAR AND TECHNICAL COMMUN.	TW	50	20	20	P C
11. NETWORK LABORATORY	OR	50	20	29	P C						
12. SOFT SKILLS LABORATORY	TW	25	10	17	P C						

GRAND TOTAL = 779/1500, RESULT: FAILS A.T.K.T.

RESULT RESERVED FOR BKLG

ORDN. 1 MARKS :



NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80058660 SIDDHARTH DALAL REKHA , 71201075M , PICT , T80058660

01. OPERATING SYSTEM	PP	100	40	40	P
02. THEORY OF COMPUTATION	PP	100	40	50	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	59	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	60	P
05. SOFTWARE ENGINEERING	PP	100	40	57	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	21	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	38	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	23	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	36	P
10. NETWORK LABORATORY	TW	25	10	23	P
11. NETWORK LABORATORY	OR	50	20	40	P
12. SOFT SKILLS LABORATORY	TW	25	10	22	P

FIRST TERM TOTAL = 469/750.

ORDN. 1 MARKS :

T80058661 SIDHESH BADRINARAYAN KAUSALYA , 71201077H , PICT , T80058661

01. OPERATING SYSTEM	PP	100	40	49	P
02. THEORY OF COMPUTATION	PP	100	40	63	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	72	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	70	P
05. SOFTWARE ENGINEERING	PP	100	40	60	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	20	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	42	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	18	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	41	P
10. NETWORK LABORATORY	TW	25	10	21	P
11. NETWORK LABORATORY	OR	50	20	40	P
12. SOFT SKILLS LABORATORY	TW	25	10	16	P

FIRST TERM TOTAL = 512/750.

ORDN. 1 MARKS :

T80058662 SOLANKE BHUSHAN NARENDRA REKHA , 71101017L , T80058620 , PICT , T80058662

01. OPERATING SYSTEM	PP	100	40	42	P C	13. SYSTEM SOFTWARE PROGRAMMING	PP	100	40	40	P C
02. THEORY OF COMPUTATION	PP	100	40	48	P C	14. MANAGEMENT INFORMATION SYSTEMS	PP	100	40	42	P C
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	42	P C	15. PROGRAMMING PARADIGMS	PP	100	40	40	P C
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	40	P C	16. DESIGN & ANALYSIS OF ALGORITHMS	PP	100	40	AA	F
05. SOFTWARE ENGINEERING	PP	100	40	56	P C	17. HUMAN COMPU.INTERACTION & USABI.	PP	100	40	45	P C
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	22	P C	18. SOFTWARE DESIGN LABORATORY	TW	50	20	29	P C
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	27	P C	19. SOFTWARE DESIGN LABORATORY	PR	50	20	36	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	16	P C	20. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	34	P C
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	27	P C	21. SOFTWARE DEVELOPMENT TOOLS LAB.	OR	50	20	AA	F
10. NETWORK LABORATORY	TW	25	10	15	P C	22. SEMINAR AND TECHNICAL COMMUN.	TW	50	20	29	P C
11. NETWORK LABORATORY	OR	50	20	26	P C						
12. SOFT SKILLS LABORATORY	TW	25	10	16	P C						

GRAND TOTAL = 672/1500, RESULT: FAILS A.T.K.T.

RESULT RESERVED FOR BKL

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80058663 SUYASH PANDEY SHAILA , 70925625G , T80058670 , PICT , T80058663

01. OPERATING SYSTEM	PP	100	40	55	P C	13. SYSTEM SOFTWARE PROGRAMMING	PP	100	40	46	P C
02. THEORY OF COMPUTATION	PP	100	40	40	P C	14. MANAGEMENT INFORMATION SYSTEMS	PP	100	40	47	P C
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	46	P C	15. PROGRAMMING PARADIGMS	PP	100	40	44	P C
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	40	P C	16. DESIGN & ANALYSIS OF ALGORITHMS	PP	100	40	40	P
05. SOFTWARE ENGINEERING	PP	100	40	40	P C	17. HUMAN COMPU.INTERACTION & USABI.	PP	100	40	40	P C
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	11	P C	18. SOFTWARE DESIGN LABORATORY	TW	50	20	25	P C
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	30	P C	19. SOFTWARE DESIGN LABORATORY	PR	50	20	35	P C
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	14	P C	20. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	25	P C
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	28	P C	21. SOFTWARE DEVELOPMENT TOOLS LAB.	OR	50	20	24	P C
10. NETWORK LABORATORY	TW	25	10	10	P C	22. SEMINAR AND TECHNICAL COMMUN.	TW	50	20	30	P C
11. NETWORK LABORATORY	OR	50	20	32	P C						
12. SOFT SKILLS LABORATORY	TW	25	10	15	P C						

GRAND TOTAL = 717/1500, RESULT: PASS CLASS

ORDN. 1 MARKS :

T80058665 TAKALKAR TANMAYEE SUHAS JYOTI , 71201089M , , PICT , T80058665

01. OPERATING SYSTEM	PP	100	40	49	P
02. THEORY OF COMPUTATION	PP	100	40	61	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	66	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	70	P
05. SOFTWARE ENGINEERING	PP	100	40	67	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	19	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	40	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	19	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	38	P
10. NETWORK LABORATORY	TW	25	10	23	P
11. NETWORK LABORATORY	OR	50	20	30	P
12. SOFT SKILLS LABORATORY	TW	25	10	20	P

FIRST TERM TOTAL = 502/750.

ORDN. 1 MARKS :

T80058666 TEJASH KUMAR RITA , 71045642F , , PICT , T80058666

01. OPERATING SYSTEM	PP	100	40	22	F
02. THEORY OF COMPUTATION	PP	100	40	AA	F
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	44	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	40	P
05. SOFTWARE ENGINEERING	PP	100	40	AA	F
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	17	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	AA	F
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	10	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	08	F
10. NETWORK LABORATORY	TW	25	10	10	P
11. NETWORK LABORATORY	OR	50	20	AA	F
12. SOFT SKILLS LABORATORY	TW	25	10	10	P

FIRST TERM TOTAL = 161/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.  
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80058667 TEKE AMRUTA PRAKASH SHALINEE , 71101028F , T80058624 , PICT , T80058667

01. OPERATING SYSTEM	PP	100	40	46	P C	13. SYSTEM SOFTWARE PROGRAMMING	PP	100	40	55	P C
02. THEORY OF COMPUTATION	PP	100	40	40	P C	14. MANAGEMENT INFORMATION SYSTEMS	PP	100	40	41	P C
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	66	P C	15. PROGRAMMING PARADIGMS	PP	100	40	50	P C
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	60	P C	16. DESIGN & ANALYSIS OF ALGORITHMS	PP	100	40	55	P
05. SOFTWARE ENGINEERING	PP	100	40	61	P C	17. HUMAN COMPU.INTERACTION & USABI.	PP	100	40	52	P C
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	19	P C	18. SOFTWARE DESIGN LABORATORY	TW	50	20	27	P C
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	21	P C	19. SOFTWARE DESIGN LABORATORY	PR	50	20	36	P C
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	19	P C	20. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	34	P C
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	38	P C	21. SOFTWARE DEVELOPMENT TOOLS LAB.	OR	50	20	22	P C
10. NETWORK LABORATORY	TW	25	10	16	P C	22. SEMINAR AND TECHNICAL COMMUN.	TW	50	20	30	P C
11. NETWORK LABORATORY	OR	50	20	35	P C						
12. SOFT SKILLS LABORATORY	TW	25	10	19	P C						

GRAND TOTAL = 842/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

T80058668 THORAT SAYALI SURESH SULABHA , 71201095F , PICT , T80058668

01. OPERATING SYSTEM	PP	100	40	10	F
02. THEORY OF COMPUTATION	PP	100	40	40	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	40	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	33	F
05. SOFTWARE ENGINEERING	PP	100	40	32	F
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	19	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	25	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	21	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	33	P
10. NETWORK LABORATORY	TW	25	10	21	P
11. NETWORK LABORATORY	OR	50	20	30	P
12. SOFT SKILLS LABORATORY	TW	25	10	15	P

FIRST TERM TOTAL = 319/750.

ORDN. 1 MARKS :

T80058669 TIKONE SNEHAL SHAM SUNITA , 71350936J , PICT , T80058669

01. OPERATING SYSTEM	PP	100	40	33	F
02. THEORY OF COMPUTATION	PP	100	40	40	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	59	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	56	P
05. SOFTWARE ENGINEERING	PP	100	40	68	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	21	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	25	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	22	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	35	P
10. NETWORK LABORATORY	TW	25	10	22	P
11. NETWORK LABORATORY	OR	50	20	38	P
12. SOFT SKILLS LABORATORY	TW	25	10	20	P

FIRST TERM TOTAL = 439/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.  
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80058670	TOSHNIWAL VIVEK ANILKUMAR				SANJAYA					, 71101039M			T80058627		PICT			T80058670
01. OPERATING SYSTEM	PP	100	40	59	P C	13. SYSTEM SOFTWARE PROGRAMMING	PP	100	40	63	P C							
02. THEORY OF COMPUTATION	PP	100	40	55	P C	14. MANAGEMENT INFORMATION SYSTEMS	PP	100	40	48	P C							
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	62	P C	15. PROGRAMMING PARADIGMS	PP	100	40	61	P C							
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	40	P C	16. DESIGN & ANALYSIS OF ALGORITHMS	PP	100	40	54	P							
05. SOFTWARE ENGINEERING	PP	100	40	46	P C	17. HUMAN COMPU.INTERACTION & USABI.	PP	100	40	47	P C							
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	19	P C	18. SOFTWARE DESIGN LABORATORY	TW	50	20	38	P C							
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	23	P C	19. SOFTWARE DESIGN LABORATORY	PR	50	20	38	P C							
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	20	P C	20. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	42	P C							
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	43	P C	21. SOFTWARE DEVELOPMENT TOOLS LAB.	OR	50	20	33	P C							
10. NETWORK LABORATORY	TW	25	10	18	P C	22. SEMINAR AND TECHNICAL COMMUN.	TW	50	20	38	P C							
11. NETWORK LABORATORY	OR	50	20	29	P C													
12. SOFT SKILLS LABORATORY	TW	25	10	18	P C													

GRAND TOTAL = 894+06/1500, RESULT: FIRST CLASS [0.2]

ORDN. 1 MARKS :

T80058671	VARAT KAUSTUBH SOMKANT				TILOTTAMA					, 71101046D			T80058630		PICT			T80058671
01. OPERATING SYSTEM	PP	100	40	40	P C	13. SYSTEM SOFTWARE PROGRAMMING	PP	100	40	57	P C							
02. THEORY OF COMPUTATION	PP	100	40	45	P C	14. MANAGEMENT INFORMATION SYSTEMS	PP	100	40	49	P C							
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	40	P C	15. PROGRAMMING PARADIGMS	PP	100	40	47	P C							
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	50	P C	16. DESIGN & ANALYSIS OF ALGORITHMS	PP	100	40	49	P							
05. SOFTWARE ENGINEERING	PP	100	40	45	P C	17. HUMAN COMPU.INTERACTION & USABI.	PP	100	40	42	P C							
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	10	P C	18. SOFTWARE DESIGN LABORATORY	TW	50	20	20	P C							
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	AA	F	19. SOFTWARE DESIGN LABORATORY	PR	50	20	20	P C							
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	12	P C	20. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	25	P C							
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	AA	F	21. SOFTWARE DEVELOPMENT TOOLS LAB.	OR	50	20	27	P C							
10. NETWORK LABORATORY	TW	25	10	10	P C	22. SEMINAR AND TECHNICAL COMMUN.	TW	50	20	20	P C							
11. NETWORK LABORATORY	OR	50	20	AA	F													
12. SOFT SKILLS LABORATORY	TW	25	10	12	P C													

GRAND TOTAL = 620/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

T80058672	VICHARE GAURAV GIRISH				VINA					, 71101047B			T80058631		PICT			T80058672
01. OPERATING SYSTEM	PP	100	40	43	P C	13. SYSTEM SOFTWARE PROGRAMMING	PP	100	40	49	P C							
02. THEORY OF COMPUTATION	PP	100	40	44	P C	14. MANAGEMENT INFORMATION SYSTEMS	PP	100	40	40	P							
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	52	P C	15. PROGRAMMING PARADIGMS	PP	100	40	47	P C							
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	40	P C	16. DESIGN & ANALYSIS OF ALGORITHMS	PP	100	40	52	P							
05. SOFTWARE ENGINEERING	PP	100	40	40	P C	17. HUMAN COMPU.INTERACTION & USABI.	PP	100	40	40	P C							
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	21	P C	18. SOFTWARE DESIGN LABORATORY	TW	50	20	37	P C							
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	23	P C	19. SOFTWARE DESIGN LABORATORY	PR	50	20	36	P C							
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	24	P C	20. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	37	P C							
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	41	P C	21. SOFTWARE DEVELOPMENT TOOLS LAB.	OR	50	20	22	P C							
10. NETWORK LABORATORY	TW	25	10	20	P C	22. SEMINAR AND TECHNICAL COMMUN.	TW	50	20	40	P C							
11. NETWORK LABORATORY	OR	50	20	31	P C													
12. SOFT SKILLS LABORATORY	TW	25	10	21	P C													

GRAND TOTAL = 800/1500, RESULT: SECOND CLASS

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80058673 VIRWANI SUNNY JAMANLAL KIRTI , 71101051L , T80058633 , PICT , T80058673

01. OPERATING SYSTEM	PP	100	40	44	P C
02. THEORY OF COMPUTATION	PP	100	40	40	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	51	P C
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	40	P C
05. SOFTWARE ENGINEERING	PP	100	40	40	P C
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	15	P C
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	25	P C
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	15	P C
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	37	P C
10. NETWORK LABORATORY	TW	25	10	15	P C
11. NETWORK LABORATORY	OR	50	20	43	P C
12. SOFT SKILLS LABORATORY	TW	25	10	15	P C

FIRST TERM TOTAL = 380/750.

ORDN. 1 MARKS :

T80058674 WADILE NITIN ADHAR SUMAN , 71101052J , T80058634 , PICT , T80058674

01. OPERATING SYSTEM	PP	100	40	40	P C	13. SYSTEM SOFTWARE PROGRAMMING	PP	100	40	40	P C
02. THEORY OF COMPUTATION	PP	100	40	43	P C	14. MANAGEMENT INFORMATION SYSTEMS	PP	100	40	40	P C
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	48	P C	15. PROGRAMMING PARADIGMS	PP	100	40	40	P C
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	40	P C	16. DESIGN & ANALYSIS OF ALGORITHMS	PP	100	40	47	P
05. SOFTWARE ENGINEERING	PP	100	40	51	P C	17. HUMAN COMPU.INTERACTION & USABI.	PP	100	40	43	P C
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	10	P C	18. SOFTWARE DESIGN LABORATORY	TW	50	20	21	P C
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	27	P C	19. SOFTWARE DESIGN LABORATORY	PR	50	20	30	P C
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	12	P C	20. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	25	P C
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	20	P C	21. SOFTWARE DEVELOPMENT TOOLS LAB.	OR	50	20	27	P
10. NETWORK LABORATORY	TW	25	10	10	P C	22. SEMINAR AND TECHNICAL COMMUN.	TW	50	20	22	P C
11. NETWORK LABORATORY	OR	50	20	AA	F						
12. SOFT SKILLS LABORATORY	TW	25	10	13	P C						

GRAND TOTAL = 649/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

T80058676 WALZADE ROHAN SUNIL MANISHA , 71201118J , , PICT , T80058676

01. OPERATING SYSTEM	PP	100	40	40	P
02. THEORY OF COMPUTATION	PP	100	40	50	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	49	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	57	P
05. SOFTWARE ENGINEERING	PP	100	40	56	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	17	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	25	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	16	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	32	P
10. NETWORK LABORATORY	TW	25	10	23	P
11. NETWORK LABORATORY	OR	50	20	23	P
12. SOFT SKILLS LABORATORY	TW	25	10	18	P

FIRST TERM TOTAL = 406/750.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T80058677 YADAV AKASH VINOD VIJAYA , 71350937G , PICT , T80058677

01. OPERATING SYSTEM	PP	100	40	32	F
02. THEORY OF COMPUTATION	PP	100	40	42	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	52	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	32	F
05. SOFTWARE ENGINEERING	PP	100	40	64	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	20	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	20	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	22	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	38	P
10. NETWORK LABORATORY	TW	25	10	22	P
11. NETWORK LABORATORY	OR	50	20	35	P
12. SOFT SKILLS LABORATORY	TW	25	10	16	P

FIRST TERM TOTAL = 395/750.

ORDN. 1 MARKS :

T80058678 YADAV GAURAV RAJENDRA RASHMI , 71201121J , PICT , T80058678

01. OPERATING SYSTEM	PP	100	40	40	P
02. THEORY OF COMPUTATION	PP	100	40	53	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	64	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	62	P
05. SOFTWARE ENGINEERING	PP	100	40	60	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	21	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	35	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	24	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	44	P
10. NETWORK LABORATORY	TW	25	10	24	P
11. NETWORK LABORATORY	OR	50	20	32	P
12. SOFT SKILLS LABORATORY	TW	25	10	20	P

FIRST TERM TOTAL = 479/750.

ORDN. 1 MARKS :

T80058679 ZANJARE SHRIDATTA GOVIND PRAMILA , 71201125M , PICT , T80058679

01. OPERATING SYSTEM	PP	100	40	27	F
02. THEORY OF COMPUTATION	PP	100	40	40	P
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	51	P
04. DATABASE MANAGEMENT SYSTEMS	PP	100	40	50	P
05. SOFTWARE ENGINEERING	PP	100	40	71	P
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	22	P
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	22	P
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	17	P
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	41	P
10. NETWORK LABORATORY	TW	25	10	24	P
11. NETWORK LABORATORY	OR	50	20	23	P
12. SOFT SKILLS LABORATORY	TW	25	10	21	P

FIRST TERM TOTAL = 409/750.

ORDN. 1 MARKS :