DATE: 27 JULY 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 01 (395)

NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER NIRMAL KUMARI , 71100712J , S80053001 , PICT , S80053001 S80053001 ABHIMANYU SINGH PP 100 40 40 P C 11. ENGINEERING MATHEMATICS III PP 100 40 40 P 01. SIGNAL AND SYSTEMS 02. SIGNAL AND SYSTEMS OR 50 20 23 P 25 10 16 P 12. ENGINEERING MATHEMATICS III TW 100 40 AA F 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 AA F 03. SOLID STATES DEVICES AND CIRCUITSPP 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 27 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 27 P 05. NETWORK ANALYSIS 100 40 AA F 15. ELECTROMAGNETIC 100 40 AA F PP 100 40 AA F 25 10 19 P 06. DIGITAL LOGIC DESIGN 16. ELECTROMAGNETIC TW 50 20 35 P C PP 100 40 AA F 07. DIGITAL LOGIC DESIGN PR 17. DATA STRUCTURES 08. POWER DEVICES AND MACHINES PP 100 40 50 20 AA F AA F 18. DATA STRUCTURES 09. NETWORK AND POWER LAB. TW 50 20 33 P C PP 100 40 AA F 19. COMMUNICATION THEORY 50 20 29 P C 50 20 36 P 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 20. COMMUNICATION THEORY 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 34 P GRAND TOTAL = 359/1500, RESULT: FAILS RESULT RESERVED FOR BKLG ORDN. 1 MARKS: SINDHU S80053002 ABHISHEK ANAND , 71200748C , S80053002 , PICT , S80053002 11. ENGINEERING MATHEMATICS III PP 100 40 51 P PP 100 40 57 P C 01. SIGNAL AND SYSTEMS 02. SIGNAL AND SYSTEMS OR 50 20 31 P C 25 10 13 P 12. ENGINEERING MATHEMATICS III TW 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 45 P C 52 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 38 P C 50 20 35 P 14. INTEGRATED CIRCUITS APPLICATIONS PR 100 40 40 P C 100 40 05. NETWORK ANALYSIS 15. ELECTROMAGNETIC 63 P 25 10 100 40 16. ELECTROMAGNETIC 15 P 06. DIGITAL LOGIC DESIGN PP 65 P TW 50 20 100 40 07. DIGITAL LOGIC DESIGN PR 34 P C 17. DATA STRUCTURES PP 63 P 100 40 43 P C 50 20 08. POWER DEVICES AND MACHINES PP 18. DATA STRUCTURES 25 P 09. NETWORK AND POWER LAB. TW 50 20 31 P C 19. COMMUNICATION THEORY 100 40 62 P 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 34 P C 50 20 35 P 20. COMMUNICATION THEORY 50 20 20 P 21. CIRCUIT SIMULATION AND TOOLS TW GRAND TOTAL = 852/1500, RESULT: HIGHER SECOND CLASS ORDN. 1 MARKS: , 71200751C , S80053003 , PICT , S80053003 S80053003 AGRAWAL MUKUL RAVINDRA ASHA 01. SIGNAL AND SYSTEMS PP 100 40 70 P C 11. ENGINEERING MATHEMATICS III PP 100 40 80 P OR 02. SIGNAL AND SYSTEMS 12. ENGINEERING MATHEMATICS III TW 50 20 37 P C 25 10 20 P 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 65 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 64 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 35 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 34 P 05. NETWORK ANALYSIS PP 100 40 52 P C 100 40 68 P 15. ELECTROMAGNETIC PP 100 40 62 P C 06. DIGITAL LOGIC DESIGN TW 25 10 21 P 16. ELECTROMAGNETIC 07. DIGITAL LOGIC DESIGN 50 20 36 P C PP 100 40 79 P PR 17. DATA STRUCTURES 08. POWER DEVICES AND MACHINES PP 100 40 62 P C 18. DATA STRUCTURES PR 50 20 40 P 09. NETWORK AND POWER LAB. TW 50 20 41 P C 100 40 81 P 19. COMMUNICATION THEORY 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 38 P C 50 20 39 P 20. COMMUNICATION THEORY OR 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 40 P GRAND TOTAL = 1064/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER LAXMIBAI , 71344345G , S80053004 , PICT , S80053004 S80053004 AHIRE BHUSHAN SURESH 11. ENGINEERING MATHEMATICS III PP 100 40 42 P PP 100 40 44 P C 01. SIGNAL AND SYSTEMS OR 50 20 25 P C 25 10 21 P 02. SIGNAL AND SYSTEMS 12. ENGINEERING MATHEMATICS III TW 100 40 40 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 58 P 03. SOLID STATES DEVICES AND CIRCUITSPP 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 27 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 25 P 05. NETWORK ANALYSIS 100 40 42 P 15. ELECTROMAGNETIC 100 40 49 P 100 40 61 P 25 10 20 P 06. DIGITAL LOGIC DESIGN PP 16. ELECTROMAGNETIC TW 25 P 07. DIGITAL LOGIC DESIGN 50 20 PP 100 40 51 P PR 17. DATA STRUCTURES 08. POWER DEVICES AND MACHINES PP 100 40 49 P C 50 20 18. DATA STRUCTURES 36 P 09. NETWORK AND POWER LAB. TW 50 20 36 P C 100 40 63 P 19. COMMUNICATION THEORY 50 20 35 P C 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 32 P 20. COMMUNICATION THEORY 50 20 37 P 21. CIRCUIT SIMULATION AND TOOLS TW GRAND TOTAL = 818+07/1500, RESULT: HIGHER SECOND CLASS [0.2] ORDN. 1 MARKS: , 71200757B , S80053005 , PICT , S80053005 S80053005 ALGUR GAURAV ISHWAR VASANTI 11. ENGINEERING MATHEMATICS III PP 100 40 54 P PP 100 40 43 P C 01. SIGNAL AND SYSTEMS 50 20 23 P C 25 10 15 P 02. SIGNAL AND SYSTEMS OR 12. ENGINEERING MATHEMATICS III TW AA F 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 18 F 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 22 P C 50 20 20 P 14. INTEGRATED CIRCUITS APPLICATIONS PR 100 40 40 P PP 100 40 05. NETWORK ANALYSIS 15. ELECTROMAGNETIC 40 P 100 40 40 P C 16. ELECTROMAGNETIC 25 10 14 P 06. DIGITAL LOGIC DESIGN PP TW 50 20 20 P C 100 40 07. DIGITAL LOGIC DESIGN PR 17. DATA STRUCTURES PP 40 P 100 40 27 F 08. POWER DEVICES AND MACHINES PP 18. DATA STRUCTURES 50 20 22 P 09. NETWORK AND POWER LAB. TW 50 20 25 P C 19. COMMUNICATION THEORY 100 40 51 P 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 29 P C 50 20 34 P 20. COMMUNICATION THEORY 50 20 25 P 21. CIRCUIT SIMULATION AND TOOLS TW GRAND TOTAL = 602/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS: , 71200761L , S80053006 , PICT , S80053006 S80053006 ANIRUDDHA SINGH CHOUHAN RENUKA 01. SIGNAL AND SYSTEMS PP 100 40 40 P C 11. ENGINEERING MATHEMATICS III PP 100 40 64 P OR 02. SIGNAL AND SYSTEMS 12. ENGINEERING MATHEMATICS III TW 50 20 30 P C 25 10 16 P 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 40 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 32 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 20 P 05. NETWORK ANALYSIS PP 100 40 40 P C 100 40 51 P 15. ELECTROMAGNETIC PP 100 40 49 P C TW 25 10 17 P 06. DIGITAL LOGIC DESIGN 16. ELECTROMAGNETIC 64 P 07. DIGITAL LOGIC DESIGN 50 20 34 P C PP 100 40 PR 17. DATA STRUCTURES 08. POWER DEVICES AND MACHINES PP 100 40 40 P 18. DATA STRUCTURES PR 50 20 30 P 09. NETWORK AND POWER LAB. TW 50 20 35 P C 100 40 44 P 19. COMMUNICATION THEORY 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 34 P C 20. COMMUNICATION THEORY 50 20 33 P OR 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 31 P GRAND TOTAL = 784/1500, RESULT: SECOND CLASS ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER SHAILA PATIL , 71344346E , S80053007 , PICT , S80053007 S80053007 ASHISH VILAS PATIL 11. ENGINEERING MATHEMATICS III PP 100 40 86 P PP 100 40 72 P C 01. SIGNAL AND SYSTEMS O2. SIGNAL AND SYSTEMS OR 50 20 30 P C 25 10 21 P 12. ENGINEERING MATHEMATICS III TW 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 59 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 72 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 40 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 37 P 05. NETWORK ANALYSIS PP 100 40 63 P C 15. ELECTROMAGNETIC 100 40 63 P PP 100 40 57 P.C 25 10 06. DIGITAL LOGIC DESIGN 16. ELECTROMAGNETIC TW 21 P 07. DIGITAL LOGIC DESIGN 50 20 41 P C PP 100 40 71 P PR 17. DATA STRUCTURES 08. POWER DEVICES AND MACHINES PP 100 40 63 P C 50 20 41 P 18. DATA STRUCTURES 09. NETWORK AND POWER LAB. TW 50 20 43 P C 100 40 77 P 19. COMMUNICATION THEORY 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 41 P C 50 20 43 P 20. COMMUNICATION THEORY 50 20 42 P 21. CIRCUIT SIMULATION AND TOOLS TW GRAND TOTAL = 1083/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS: , 71212091C , S80053008 , PICT , S80053008 S80053008 ATHAWALE SHRADDHA MAHADEV MAYA 11. ENGINEERING MATHEMATICS III PP 100 40 64 P PP 100 40 46 P C 01. SIGNAL AND SYSTEMS 50 20 32 P C 25 10 19 P 02. SIGNAL AND SYSTEMS OR 12. ENGINEERING MATHEMATICS III TW 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 40 P C 46 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 36 P C 50 20 14. INTEGRATED CIRCUITS APPLICATIONS PR 28 P 100 40 43 P C PP 100 40 05. NETWORK ANALYSIS 15. ELECTROMAGNETIC 61 P 25 10 PP 100 40 40 P C 16. ELECTROMAGNETIC 20 P 06. DIGITAL LOGIC DESIGN TW 50 20 22 P C 100 40 55 P 07. DIGITAL LOGIC DESIGN PR 17. DATA STRUCTURES PP 100 40 47 P 50 20 08. POWER DEVICES AND MACHINES PP 18. DATA STRUCTURES 43 P 09. NETWORK AND POWER LAB. TW 50 20 39 P C 19. COMMUNICATION THEORY 100 40 45 P 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 39 P C 50 20 29 P 20. COMMUNICATION THEORY 50 20 39 P 21. CIRCUIT SIMULATION AND TOOLS TW GRAND TOTAL = 833/1500, RESULT: HIGHER SECOND CLASS ORDN. 1 MARKS: , 71200767K , S80053010 , PICT , S80053010 S80053010 AWACHAR MOHAN UDDHAO KAVERI 01. SIGNAL AND SYSTEMS PP 100 40 71 P C 11. ENGINEERING MATHEMATICS III PP 100 40 65 P OR 02. SIGNAL AND SYSTEMS 12. ENGINEERING MATHEMATICS III TW 50 20 35 P C 25 10 19 P 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 40 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 47 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 39 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 36 P 05. NETWORK ANALYSIS PP 100 40 55 P C 100 40 73 P 15. ELECTROMAGNETIC PP 100 40 19 P 06. DIGITAL LOGIC DESIGN 49 P C TW 25 10 16. ELECTROMAGNETIC 07. DIGITAL LOGIC DESIGN 50 20 20 P C PP 100 40 62 P PR 17. DATA STRUCTURES 08. POWER DEVICES AND MACHINES PP 100 40 47 P C 18. DATA STRUCTURES PR 50 20 38 P 09. NETWORK AND POWER LAB. TW 50 20 43 P C 100 40 61 P 19. COMMUNICATION THEORY 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 39 P C 50 20 31 P 20. COMMUNICATION THEORY OR 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 36 P GRAND TOTAL = 925/1500, RESULT: FIRST CLASS ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER LAKSHMI PRIYA , 71200768H , S80053011 , PICT , S80053011 S80053011 B KUMAR ANUPAM PP 100 40 40 P C 11. ENGINEERING MATHEMATICS III PP 100 40 22 F 01. SIGNAL AND SYSTEMS O2. SIGNAL AND SYSTEMS OR 50 20 25 P C 25 10 14 P 12. ENGINEERING MATHEMATICS III TW 100 40 48 P 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 28 F 03. SOLID STATES DEVICES AND CIRCUITSPP 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 39 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 25 P 05. NETWORK ANALYSIS PP 100 40 40 P C 15. ELECTROMAGNETIC 100 40 43 P PP 100 40 40 P 25 10 16 P 06. DIGITAL LOGIC DESIGN 16. ELECTROMAGNETIC TW 50 20 27 P C 100 40 07. DIGITAL LOGIC DESIGN PP 40 P PR 17. DATA STRUCTURES 08. POWER DEVICES AND MACHINES PP 100 40 40 P C 50 20 18. DATA STRUCTURES 36 P 09. NETWORK AND POWER LAB. TW 50 20 35 P C 100 40 46 P 19. COMMUNICATION THEORY 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 38 P C 50 20 32 P 20. COMMUNICATION THEORY 50 20 32 P 21. CIRCUIT SIMULATION AND TOOLS TW GRAND TOTAL = 706/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS: S80053012 BADE ABHIJIT ARJUN ASHA , 71200769F , S80053012 , PICT , S80053012 11. ENGINEERING MATHEMATICS III PP 100 40 57 P PP 100 40 69 P C 01. SIGNAL AND SYSTEMS 02. SIGNAL AND SYSTEMS OR 50 20 31 P C 25 10 21 P 12. ENGINEERING MATHEMATICS III TW 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 P 40 P C 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 38 P C 50 20 33 P 14. INTEGRATED CIRCUITS APPLICATIONS PR 100 40 40 P C PP 100 40 52 P 05. NETWORK ANALYSIS 15. ELECTROMAGNETIC 60 P 25 10 PP 100 40 16. ELECTROMAGNETIC 20 P 06. DIGITAL LOGIC DESIGN TW 50 20 28 P C 100 40 70 P 07. DIGITAL LOGIC DESIGN PR 17. DATA STRUCTURES PP 100 40 44 P C 50 20 35 P 08. POWER DEVICES AND MACHINES PP 18. DATA STRUCTURES 09. NETWORK AND POWER LAB. TW 50 20 41 P C 19. COMMUNICATION THEORY 100 40 47 P 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 38 P C 50 20 31 P 20. COMMUNICATION THEORY 50 20 39 P 21. CIRCUIT SIMULATION AND TOOLS TW GRAND TOTAL = 874/1500, RESULT: HIGHER SECOND CLASS ORDN. 1 MARKS: , 71200770к , S80053013 , РІСТ , S80053013 S80053013 BAGDE PRAJAKTA ASHOK MANISHA 01. SIGNAL AND SYSTEMS PP 100 40 75 P C 11. ENGINEERING MATHEMATICS III PP 100 40 92 P OR 02. SIGNAL AND SYSTEMS 50 20 34 P C 12. ENGINEERING MATHEMATICS III TW 25 10 20 P 40 P C 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 45 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 37 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 33 P 05. NETWORK ANALYSIS PP 100 40 62 P PP 100 40 63 P 15. ELECTROMAGNETIC 100 40 20 P 06. DIGITAL LOGIC DESIGN 41 P C TW 25 10 16. ELECTROMAGNETIC 07. DIGITAL LOGIC DESIGN 50 20 32 P C PP 100 40 66 P PR 17. DATA STRUCTURES 08. POWER DEVICES AND MACHINES PP 100 40 51 P C 18. DATA STRUCTURES PR 50 20 33 P 09. NETWORK AND POWER LAB. TW 50 20 41 P C 100 40 68 P 19. COMMUNICATION THEORY 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 42 P C 50 20 30 P 20. COMMUNICATION THEORY OR 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 36 P GRAND TOTAL = 961/1500, RESULT: FIRST CLASS ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER SULOCHANA , 71200774В , S80053014 , РІСТ , S80053014 S80053014 BAIRAGI SURAJ KACHARDAS PP 100 40 82 P C 11. ENGINEERING MATHEMATICS III PP 100 40 88 P 01. SIGNAL AND SYSTEMS O2. SIGNAL AND SYSTEMS OR 50 20 38 P C 25 10 23 P 12. ENGINEERING MATHEMATICS III TW 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 55 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 55 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 43 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 44 P 05. NETWORK ANALYSIS PP 100 40 59 P C 15. ELECTROMAGNETIC 100 40 82 P PP 100 40 48 P.C 25 10 22 P 06. DIGITAL LOGIC DESIGN 16. ELECTROMAGNETIC TW 50 20 39 P C 100 40 70 P 07. DIGITAL LOGIC DESIGN PP PR 17. DATA STRUCTURES 08. POWER DEVICES AND MACHINES PP 100 40 63 P C 50 20 27 P 18. DATA STRUCTURES 09. NETWORK AND POWER LAB. TW 50 20 43 P C 100 40 78 P 19. COMMUNICATION THEORY 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 42 P C 50 20 40 P 20. COMMUNICATION THEORY 50 20 43 P 21. CIRCUIT SIMULATION AND TOOLS TW GRAND TOTAL = 1084/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS: S80053015 BAJAJ POOJA BALAPRASAD SAVITA , 71200776J , S80053015 , PICT , S80053015 11. ENGINEERING MATHEMATICS III PP 100 40 67 P PP 100 40 58 P C 01. SIGNAL AND SYSTEMS OR 50 20 34 P C 25 10 17 P 02. SIGNAL AND SYSTEMS 12. ENGINEERING MATHEMATICS III TW 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 55 P C 29 F 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 35 P C 50 20 14. INTEGRATED CIRCUITS APPLICATIONS PR 30 P 100 40 41 P C PP 100 40 05. NETWORK ANALYSIS 15. ELECTROMAGNETIC 40 P 25 10 100 40 57 P 16. ELECTROMAGNETIC 06. DIGITAL LOGIC DESIGN PP TW 18 P 50 20 30 P C PP 100 40 53 P 07. DIGITAL LOGIC DESIGN PR 17. DATA STRUCTURES 08. POWER DEVICES AND MACHINES PP 100 40 41 P C 50 20 18. DATA STRUCTURES 43 P 09. NETWORK AND POWER LAB. TW 50 20 36 P C 19. COMMUNICATION THEORY 100 40 56 P 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 39 P C 50 20 30 P 20. COMMUNICATION THEORY 50 20 36 P 21. CIRCUIT SIMULATION AND TOOLS TW GRAND TOTAL = 845/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS: , 71200777G , S80053016 , PICT , S80053016 S80053016 BAKRE ANUJ RAMESH JAYASHREE 01. SIGNAL AND SYSTEMS PP 100 40 71 P C 11. ENGINEERING MATHEMATICS III PP 100 40 94 P OR 02. SIGNAL AND SYSTEMS 12. ENGINEERING MATHEMATICS III TW 50 20 38 P C 25 10 23 P 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 53 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 72 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 41 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 45 P 05. NETWORK ANALYSIS PP 100 40 67 P C PP 100 40 82 P 15. ELECTROMAGNETIC 100 40 06. DIGITAL LOGIC DESIGN 61 P C TW 25 10 21 P 16. ELECTROMAGNETIC 07. DIGITAL LOGIC DESIGN 50 20 44 P C PP 100 40 76 P PR 17. DATA STRUCTURES 08. POWER DEVICES AND MACHINES PP 100 40 55 P C 18. DATA STRUCTURES PR 50 20 45 P 09. NETWORK AND POWER LAB. TW 100 40 79 P 50 20 44 P C 19. COMMUNICATION THEORY 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 43 P C 50 20 35 P 20. COMMUNICATION THEORY OR 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 44 P GRAND TOTAL = 1133/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71344347C , S80053017 , PICT , S80053017 S80053017 BARKADE VIJAYA VASANT JAYASHRI 11. ENGINEERING MATHEMATICS III PP 100 40 77 P PP 100 40 64 P C 01. SIGNAL AND SYSTEMS OR 50 20 25 P C 25 10 22 P 02. SIGNAL AND SYSTEMS 12. ENGINEERING MATHEMATICS III TW 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 54 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 58 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 30 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 32 P 05. NETWORK ANALYSIS 100 40 49 P C 15. ELECTROMAGNETIC 100 40 62 P PP 100 40 40 P C 25 10 06. DIGITAL LOGIC DESIGN 16. ELECTROMAGNETIC TW 21 p 50 20 26 P C 100 40 07. DIGITAL LOGIC DESIGN PP 63 P PR 17. DATA STRUCTURES 08. POWER DEVICES AND MACHINES PP 100 40 61 P C 50 20 18. DATA STRUCTURES 38 P 09. NETWORK AND POWER LAB. TW 50 20 40 P C 100 40 73 P 19. COMMUNICATION THEORY 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 37 P C 50 20 34 P 20. COMMUNICATION THEORY 50 20 42 P 21. CIRCUIT SIMULATION AND TOOLS TW GRAND TOTAL = 948/1500, RESULT: FIRST CLASS ORDN. 1 MARKS: ANUPAMA , 71200785H , S80053018 , PICT , S80053018 S80053018 BASHIRABADKAR SAURABH MOHANKUMAR 11. ENGINEERING MATHEMATICS III PP 100 40 67 P PP 100 40 58 P C 01. SIGNAL AND SYSTEMS 50 20 30 P C 25 10 18 P 02. SIGNAL AND SYSTEMS 12. ENGINEERING MATHEMATICS III TW 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 49 P 40 P C 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 28 P C 50 20 36 P 14. INTEGRATED CIRCUITS APPLICATIONS PR 100 40 43 P C PP 100 40 05. NETWORK ANALYSIS 15. ELECTROMAGNETIC 49 P 25 10 100 40 45 P C 16. ELECTROMAGNETIC 06. DIGITAL LOGIC DESIGN PP TW 18 P 50 20 32 P C 100 40 07. DIGITAL LOGIC DESIGN PR 17. DATA STRUCTURES PP 69 P 100 40 50 20 08. POWER DEVICES AND MACHINES PP 43 P C 18. DATA STRUCTURES 33 P 09. NETWORK AND POWER LAB. TW 50 20 41 P C 19. COMMUNICATION THEORY 100 40 66 P 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 39 P C 50 20 34 P 20. COMMUNICATION THEORY 50 20 33 P 21. CIRCUIT SIMULATION AND TOOLS TW GRAND TOTAL = 871/1500, RESULT: HIGHER SECOND CLASS ORDN. 1 MARKS: , 71200787D , S80053019 , PICT , S80053019 S80053019 BAVKAR INDRANEEL NANDKUMAR RADHIKA 01. SIGNAL AND SYSTEMS PP 100 40 66 P C 11. ENGINEERING MATHEMATICS III PP 100 40 92 P OR 02. SIGNAL AND SYSTEMS 12. ENGINEERING MATHEMATICS III TW 50 20 32 P C 25 10 21 P 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 47 P.C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 40 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 36 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 30 P 05. NETWORK ANALYSIS PP 100 40 49 P C 100 40 62 P 15. ELECTROMAGNETIC PP 100 40 47 P C TW 25 10 21 P 06. DIGITAL LOGIC DESIGN 16. ELECTROMAGNETIC 07. DIGITAL LOGIC DESIGN 50 20 30 P C PP 100 40 65 P PR 17. DATA STRUCTURES 08. POWER DEVICES AND MACHINES PP 100 40 43 P C 18. DATA STRUCTURES PR 50 20 37 P 09. NETWORK AND POWER LAB. TW 50 20 42 P C 100 40 50 P 19. COMMUNICATION THEORY 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 43 P C 50 20 32 P 20. COMMUNICATION THEORY OR 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 42 P GRAND TOTAL = 927/1500, RESULT: FIRST CLASS ORDN. 1 MARKS:

DATE: 27 JULY 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 07 (401)

NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER URMILA , 71344348M , S80053020 , PICT , S80053020 S80053020 BAYAS DIPALI SUNILSING 11. ENGINEERING MATHEMATICS III PP 100 40 72 P PP 100 40 57 P C 01. SIGNAL AND SYSTEMS 50 20 28 P C 25 10 19 P 02. SIGNAL AND SYSTEMS 12. ENGINEERING MATHEMATICS III TW 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 50 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 67 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 30 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 33 P 05. NETWORK ANALYSIS PP 100 40 53 P C 15. ELECTROMAGNETIC 100 40 46 P PP 100 40 43 P C 25 10 20 P 06. DIGITAL LOGIC DESIGN 16. ELECTROMAGNETIC TW 100 40 07. DIGITAL LOGIC DESIGN 50 20 35 P C PP 65 P PR 17. DATA STRUCTURES 08. POWER DEVICES AND MACHINES PP 100 40 46 P C 50 20 25 P 18. DATA STRUCTURES 09. NETWORK AND POWER LAB. TW 50 20 40 P C 100 40 79 P 19. COMMUNICATION THEORY 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 39 P C 50 20 40 P 20. COMMUNICATION THEORY 50 20 39 P 21. CIRCUIT SIMULATION AND TOOLS TW GRAND TOTAL = 926/1500, RESULT: FIRST CLASS ORDN. 1 MARKS: S80053021 BERAD SUPRIYA LAXMIKANT SUNANDA , 71200788B , S80053021 , PICT , S80053021 11. ENGINEERING MATHEMATICS III PP 100 40 93 P PP 100 40 75 P C 01. SIGNAL AND SYSTEMS 02. SIGNAL AND SYSTEMS OR 50 20 40 P C 25 10 23 P 12. ENGINEERING MATHEMATICS III TW 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 49 P 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 53 P C 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 40 P C 50 20 37 P 14. INTEGRATED CIRCUITS APPLICATIONS PR 100 40 50 P C PP 100 40 76 P 05. NETWORK ANALYSIS 15. ELECTROMAGNETIC 25 10 PP 100 40 55 P C 16. ELECTROMAGNETIC 22 P 06. DIGITAL LOGIC DESIGN TW 50 20 43 P C PP 100 40 07. DIGITAL LOGIC DESIGN PR 17. DATA STRUCTURES 86 P 08. POWER DEVICES AND MACHINES PP 100 40 50 20 39 P 65 P C 18. DATA STRUCTURES 09. NETWORK AND POWER LAB. TW 50 20 43 P C 19. COMMUNICATION THEORY 100 40 72 P 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 40 P C 50 20 35 P 20. COMMUNICATION THEORY 50 20 41 P 21. CIRCUIT SIMULATION AND TOOLS TW GRAND TOTAL = 1077/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS: , 71344349K , S80053022 , PICT , S80053022 S80053022 BHALERAO MAHESH GOPAL SANGITA 01. SIGNAL AND SYSTEMS PP 100 40 52 P C 11. ENGINEERING MATHEMATICS III PP 100 40 44 P OR 02. SIGNAL AND SYSTEMS 12. ENGINEERING MATHEMATICS III TW 50 20 23 P C 25 10 18 P 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 48 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 60 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 32 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 38 P 05. NETWORK ANALYSIS PP 100 40 40 P C PP 100 40 54 P 15. ELECTROMAGNETIC 100 40 20 P 06. DIGITAL LOGIC DESIGN 53 P TW 25 10 16. ELECTROMAGNETIC 07. DIGITAL LOGIC DESIGN 50 20 22 P C PP 100 40 70 P PR 17. DATA STRUCTURES 08. POWER DEVICES AND MACHINES PP 100 40 54 P C 18. DATA STRUCTURES PR 50 20 25 P 09. NETWORK AND POWER LAB. TW 50 20 39 P C 100 40 68 P 19. COMMUNICATION THEORY 50 20 36 P C 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 30 P 20. COMMUNICATION THEORY OR 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 38 P GRAND TOTAL = 864/1500, RESULT: HIGHER SECOND CLASS ORDN. 1 MARKS:

DATE: 27 JULY 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 08 (402)

NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER SANGITA , 71200791B , S80053023 , PICT , S80053023 S80053023 BHANDARI AISHWARYA ABHAY 11. ENGINEERING MATHEMATICS III PP 100 40 84 P 01. SIGNAL AND SYSTEMS PP 100 40 80 P C 50 20 43 P C 25 10 22 P 02. SIGNAL AND SYSTEMS 12. ENGINEERING MATHEMATICS III TW 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 50 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 59 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 44 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 39 P 05. NETWORK ANALYSIS PP 100 40 57 P C 15. ELECTROMAGNETIC 100 40 70 P PP 100 40 48 P.C 25 10 22 P 06. DIGITAL LOGIC DESIGN 16. ELECTROMAGNETIC TW 50 20 45 P C 100 40 07. DIGITAL LOGIC DESIGN PP 63 P PR 17. DATA STRUCTURES 08. POWER DEVICES AND MACHINES PP 100 40 53 P C 50 20 18. DATA STRUCTURES 46 P 09. NETWORK AND POWER LAB. TW 50 20 44 P C 100 40 63 P 19. COMMUNICATION THEORY 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 43 P C 50 20 42 P 20. COMMUNICATION THEORY 50 20 45 P 21. CIRCUIT SIMULATION AND TOOLS TW GRAND TOTAL = 1062/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS: SUSHMA , 71200794G , S80053024 , PICT , S80053024 S80053024 BHARGANDE SAURABH VIJAYKUMAR 11. ENGINEERING MATHEMATICS III PP 100 40 40 P PP 100 40 40 P C 01. SIGNAL AND SYSTEMS 50 20 35 P C 25 10 20 P 02. SIGNAL AND SYSTEMS OR 12. ENGINEERING MATHEMATICS III TW 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 44 P 49 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 30 P C 50 20 30 P 14. INTEGRATED CIRCUITS APPLICATIONS PR 100 40 40 P PP 100 40 05. NETWORK ANALYSIS 15. ELECTROMAGNETIC 42 P 25 10 100 40 46 P 16. ELECTROMAGNETIC 19 P 06. DIGITAL LOGIC DESIGN PP TW 50 20 36 P C 100 40 07. DIGITAL LOGIC DESIGN PR 17. DATA STRUCTURES PP 48 P 100 40 42 P 50 20 08. POWER DEVICES AND MACHINES PP 18. DATA STRUCTURES 40 P 09. NETWORK AND POWER LAB. TW 50 20 37 P C 19. COMMUNICATION THEORY 100 40 43 P 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 36 P C 50 20 24 P 20. COMMUNICATION THEORY 50 20 36 P 21. CIRCUIT SIMULATION AND TOOLS TW GRAND TOTAL = 777/1500, RESULT: SECOND CLASS ORDN. 1 MARKS: , 71344350C , S80053025 , PICT , S80053025 SAVITA S80053025 BHAVSAR RUCHIKA KISHOR 01. SIGNAL AND SYSTEMS PP 100 40 51 P C 11. ENGINEERING MATHEMATICS III PP 100 40 71 P OR 02. SIGNAL AND SYSTEMS 50 20 29 P C 12. ENGINEERING MATHEMATICS III TW 25 10 24 P 41 P C 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 67 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 38 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 34 P 05. NETWORK ANALYSIS PP 100 40 50 P C 100 40 75 P 15. ELECTROMAGNETIC PP 100 40 23 P 53 P C TW 25 10 06. DIGITAL LOGIC DESIGN 16. ELECTROMAGNETIC 07. DIGITAL LOGIC DESIGN 50 20 38 P C PP 100 40 58 P PR 17. DATA STRUCTURES 08. POWER DEVICES AND MACHINES PP 100 40 48 P C 18. DATA STRUCTURES PR 50 20 40 P 09. NETWORK AND POWER LAB. TW 100 40 76 P 50 20 41 P C 19. COMMUNICATION THEORY 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 42 P C 50 20 39 P 20. COMMUNICATION THEORY OR 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 44 P GRAND TOTAL = 982/1500, RESULT: FIRST CLASS ORDN. 1 MARKS:

DATE: 27 JULY 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 09 ( 403)

NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER RUPALI , 71200795E , S80053026 , PICT , S80053026 S80053026 BHINGE ABHIJEET SOMESHWAR PP 100 40 82 P C 11. ENGINEERING MATHEMATICS III PP 100 40 87 P 01. SIGNAL AND SYSTEMS 50 20 43 P C 25 10 23 P 02. SIGNAL AND SYSTEMS 12. ENGINEERING MATHEMATICS III TW 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 60 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 64 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 44 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 45 P 05. NETWORK ANALYSIS PP 100 40 45 P C 15. ELECTROMAGNETIC 100 40 83 P PP 100 40 42 P.C 25 10 23 P 06. DIGITAL LOGIC DESIGN 16. ELECTROMAGNETIC TW 50 20 100 40 64 P 07. DIGITAL LOGIC DESIGN 48 P C PP PR 17. DATA STRUCTURES 08. POWER DEVICES AND MACHINES PP 100 40 65 P C 50 20 18. DATA STRUCTURES 44 P 09. NETWORK AND POWER LAB. TW 50 20 45 P C 100 40 81 P 19. COMMUNICATION THEORY 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 45 P C 50 20 49 P 20. COMMUNICATION THEORY 50 20 44 P 21. CIRCUIT SIMULATION AND TOOLS TW GRAND TOTAL = 1126/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS: S80053027 BHORDE MONALI MARUTI , 71344351M , S80053027 , PICT , S80053027 SUNANDA 11. ENGINEERING MATHEMATICS III PP 100 40 80 P PP 100 40 65 P C 01. SIGNAL AND SYSTEMS 02. SIGNAL AND SYSTEMS OR 50 20 30 P C 25 10 22 P 12. ENGINEERING MATHEMATICS III TW 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 40 P C 59 p 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 36 P C 50 20 36 P 14. INTEGRATED CIRCUITS APPLICATIONS PR 100 40 48 P C PP 100 40 05. NETWORK ANALYSIS 15. ELECTROMAGNETIC 59 P 25 10 100 40 40 P C 16. ELECTROMAGNETIC 22 P 06. DIGITAL LOGIC DESIGN PP TW 50 20 32 P C 100 40 75 P 07. DIGITAL LOGIC DESIGN PR 17. DATA STRUCTURES PP 08. POWER DEVICES AND MACHINES PP 100 40 54 P C 50 20 18. DATA STRUCTURES 44 P 09. NETWORK AND POWER LAB. TW 50 20 38 P C 19. COMMUNICATION THEORY 100 40 71 P 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 36 P C 50 20 30 P 20. COMMUNICATION THEORY 50 20 42 P 21. CIRCUIT SIMULATION AND TOOLS TW GRAND TOTAL = 959/1500, RESULT: FIRST CLASS ORDN. 1 MARKS: , 71200797M , S80053028 , PICT , S80053028 S80053028 BHOSALE JUILEE UMESH ANITA 01. SIGNAL AND SYSTEMS PP 100 40 76 P C 11. ENGINEERING MATHEMATICS III PP 100 40 93 P OR 02. SIGNAL AND SYSTEMS 12. ENGINEERING MATHEMATICS III TW 50 20 42 P C 25 10 24 P 100 40 53 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 50 P 03. SOLID STATES DEVICES AND CIRCUITSPP 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 36 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 40 P 05. NETWORK ANALYSIS PP 100 40 68 P C 100 40 73 P 15. ELECTROMAGNETIC PP 100 40 23 P 06. DIGITAL LOGIC DESIGN 50 P C TW 25 10 16. ELECTROMAGNETIC 07. DIGITAL LOGIC DESIGN 50 20 32 P C PP 100 40 82 P PR 17. DATA STRUCTURES 08. POWER DEVICES AND MACHINES PP 100 40 58 P C 18. DATA STRUCTURES PR 50 20 45 P 09. NETWORK AND POWER LAB. TW 50 20 43 P C 100 40 80 P 19. COMMUNICATION THEORY 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 40 P C 50 20 41 P 20. COMMUNICATION THEORY OR 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 45 P GRAND TOTAL = 1094/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS:

DATE: 27 JULY 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 10 (404)

NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71200799H , S80053029 , РІСТ , S80053029 S80053029 BIDE DADA VITHOBA JIJABAI 11. ENGINEERING MATHEMATICS III PP 100 40 50 P 01. SIGNAL AND SYSTEMS PP 100 40 58 P C OR 50 20 20 P C 25 10 18 P 02. SIGNAL AND SYSTEMS 12. ENGINEERING MATHEMATICS III TW 100 40 55 P 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 51 P 03. SOLID STATES DEVICES AND CIRCUITSPP 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 32 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 20 P 05. NETWORK ANALYSIS 100 40 40 P 15. ELECTROMAGNETIC 100 40 61 P PP 100 40 59 P 25 10 15 P 06. DIGITAL LOGIC DESIGN 16. ELECTROMAGNETIC TW 50 20 28 P 100 40 07. DIGITAL LOGIC DESIGN PP 42 P PR 17. DATA STRUCTURES 08. POWER DEVICES AND MACHINES PP 100 40 51 P 50 20 32 P 18. DATA STRUCTURES 09. NETWORK AND POWER LAB. TW 50 20 32 P C 100 40 52 P 19. COMMUNICATION THEORY 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 36 P C 50 20 22 P 20. COMMUNICATION THEORY 50 20 33 P 21. CIRCUIT SIMULATION AND TOOLS TW GRAND TOTAL = 807/1500, RESULT: SECOND CLASS ORDN. 1 MARKS: S80053030 BOBADE AJINKYA CHANDRAKANT CHHAYA , 71200802M , S80053030 , PICT , S80053030 PP 100 40 44 P C 11. ENGINEERING MATHEMATICS III PP 100 40 55 P 01. SIGNAL AND SYSTEMS 50 20 25 P C 25 10 19 P 02. SIGNAL AND SYSTEMS OR 12. ENGINEERING MATHEMATICS III TW 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 40 P C 29 F 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 35 P C 50 20 29 P 14. INTEGRATED CIRCUITS APPLICATIONS PR 100 40 40 P PP 100 40 05. NETWORK ANALYSIS 15. ELECTROMAGNETIC 46 P 25 10 100 40 40 P 16. ELECTROMAGNETIC 20 P 06. DIGITAL LOGIC DESIGN PP TW 50 20 27 P C 100 40 07. DIGITAL LOGIC DESIGN PR 17. DATA STRUCTURES PP 50 P 100 40 40 P C 50 20 08. POWER DEVICES AND MACHINES PP 18. DATA STRUCTURES 34 P 09. NETWORK AND POWER LAB. TW 50 20 38 P C 19. COMMUNICATION THEORY 100 40 45 P 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 39 P C 50 20 31 P 20. COMMUNICATION THEORY 50 20 37 P 21. CIRCUIT SIMULATION AND TOOLS TW GRAND TOTAL = 763/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS: , 71344352к , S80053031 , РІСТ , S80053031 S80053031 BONDE KAJAL SURESH KALPANA 01. SIGNAL AND SYSTEMS PP 100 40 48 P C 11. ENGINEERING MATHEMATICS III PP 100 40 P OR 02. SIGNAL AND SYSTEMS 12. ENGINEERING MATHEMATICS III TW 50 20 25 P C 25 10 20 P 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 42 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 58 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 33 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 32 P 05. NETWORK ANALYSIS PP 100 40 40 P C PP 100 40 53 P 15. ELECTROMAGNETIC 100 40 62 P 20 P 06. DIGITAL LOGIC DESIGN TW 25 10 16. ELECTROMAGNETIC 07. DIGITAL LOGIC DESIGN 50 20 35 P C PP 100 40 49 P PR 17. DATA STRUCTURES 08. POWER DEVICES AND MACHINES PP 100 40 45 P C 18. DATA STRUCTURES PR 50 20 26 P 09. NETWORK AND POWER LAB. TW 50 20 38 P C 100 40 60 P 19. COMMUNICATION THEORY 50 20 36 P C 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 38 P 20. COMMUNICATION THEORY OR 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 39 P GRAND TOTAL = 839/1500, RESULT: HIGHER SECOND CLASS ORDN. 1 MARKS:

DATE: 27 JULY 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 11 (405)

DATE . 27 JULY 2013	CENT	KC . P	UNE	TNSIT	. TOTE C	T COMPUTE	R TECHNOLOGY, PUNE.	PAGE	. NO.	тт	( 4	103)
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S80053032 BOOB SURABHI RAMDHAN	DD	100	40		NGEETA		, 71200803K , S80053032 ,			-	S8005	
	PP	100			P C		ENGINEERING MATHEMATICS III	PP Tw	100		67 10	
02. SIGNAL AND SYSTEMS		50 100	20		P C		ENGINEERING MATHEMATICS III			10	19	
03. SOLID STATES DEVICES AND CIRCU		100			P C	_	INTEGRATED CIRCUITS APPLICATIONS			40	62	
04. SOLID STATES DEVICES AND CIRCU		50 100	-		P C		INTEGRATED CIRCUITS APPLICATIONS		50		36 72	
05. NETWORK ANALYSIS	PP	100	40		PC		ELECTROMAGNETIC	PP	100	_	73	
06. DIGITAL LOGIC DESIGN		100	40		PC		ELECTROMAGNETIC	TW	25	10	19	
07. DIGITAL LOGIC DESIGN		50	20		P C		DATA STRUCTURES	PP	100	40	83	
	PP	100	40		P C		DATA STRUCTURES	PR	50		41	
09. NETWORK AND POWER LAB.		50	20		PC		COMMUNICATION THEORY	PP		40	67	-
10. ELECTRONIC INSTRUMENTS AND TOO	LS IW	50	20	42	РС		COMMUNICATION THEORY	OR	50		40	
1056/1500						21.	CIRCUIT SIMULATION AND TOOLS	TW	50	20	35	Р
GRAND TOTAL = 1056/1500, RESULT: FIR	ST CLAS	S WITH	DIS	IINCI	ION							
ORDN. 1 MARKS :												
COOFFEE CHARLES DEPART CHARLES												
S80053033 CHASKAR DIPALI SHRIDHA		100	40		.KA	11	, 71200810B , S80053033 ,				S8005	
01. SIGNAL AND SYSTEMS		100			P C		ENGINEERING MATHEMATICS III		100		68	
02. SIGNAL AND SYSTEMS	OR	50	20		P C		ENGINEERING MATHEMATICS III	TW		10	21	
03. SOLID STATES DEVICES AND CIRCU		100	40	64		_	INTEGRATED CIRCUITS APPLICATIONS			40	40	
04. SOLID STATES DEVICES AND CIRCU		50	_		PC		INTEGRATED CIRCUITS APPLICATIONS		50		20	
05. NETWORK ANALYSIS	PP	100	40		P C		ELECTROMAGNETIC	PP	100		57	
06. DIGITAL LOGIC DESIGN	PP 	100	40	54			ELECTROMAGNETIC	TW	25	_	22	
07. DIGITAL LOGIC DESIGN	PR	50	20		P C		DATA STRUCTURES	PP	100		53	
701 701 101 101 101 101 101 101 101 101	PP	100	40	56	P C		DATA STRUCTURES	PR		20	25	P -
09. NETWORK AND POWER LAB.	TW	50	20	39			COMMUNICATION THEORY	PP	100	40	54	P
10. ELECTRONIC INSTRUMENTS AND TOO	LS IW	50	20	38	PC		COMMUNICATION THEORY	OR		20	28	
CDAND TOTAL 070 /1500 DECULT: UTC						21.	CIRCUIT SIMULATION AND TOOLS	TW	50	20	40	Р
GRAND TOTAL = 870/1500, RESULT: HIGH	HER SEC	OND CL	.ASS									
ORDN. 1 MARKS :												
CONCERNAL CHAVAN POUTT LITTAM							71200916M 590052024					
S80053034 CHAVAN ROHIT UTTAM	DD	100	40		ENA	11	, 71200816M , S80053034 ,			-	S8005	
01. SIGNAL AND SYSTEMS	PP	100			P C		ENGINEERING MATHEMATICS III	PP	100		78	
02. SIGNAL AND SYSTEMS	OR	50	20		P C		ENGINEERING MATHEMATICS III			10	23	
03. SOLID STATES DEVICES AND CIRCU		100			P C		INTEGRATED CIRCUITS APPLICATIONS		100		63	
04. SOLID STATES DEVICES AND CIRCU		50			P C		INTEGRATED CIRCUITS APPLICATIONS			20	36	
05. NETWORK ANALYSIS	PP	100			PC		ELECTROMAGNETIC	PP	100		84	
06. DIGITAL LOGIC DESIGN	PP	100			PC		ELECTROMAGNETIC	TW		10	22	
07. DIGITAL LOGIC DESIGN	PR		20		P C		DATA STRUCTURES	PP	100		68	
	PP — .		40		PC		DATA STRUCTURES	PR		20	45	
	TW		20		P C		COMMUNICATION THEORY	PP	100		74	
10. ELECTRONIC INSTRUMENTS AND TOO	LS TW	50	20	45	РС		COMMUNICATION THEORY	OR		20	41	
CD 110 TOTAL 1107/1500	o <del>-</del>	a==				21.	CIRCUIT SIMULATION AND TOOLS	TW	50	20	45	Р
GRAND TOTAL = 1107/1500, RESULT: FIR	ST CLAS	S WITH	DIS	I INCT	TON							
ORDN. 1 MARKS :												

DATE: 27 JULY 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 12 ( 406)

NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71200821H , S80053035 , PICT , S80053035 S80053035 CHIRANIYA ANKITA GOPAL RAKHI 11. ENGINEERING MATHEMATICS III PP 100 40 54 P PP 100 40 51 P C 01. SIGNAL AND SYSTEMS OR 50 20 20 P 25 10 16 P 02. SIGNAL AND SYSTEMS 12. ENGINEERING MATHEMATICS III TW 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 63 P 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 42 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 27 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 15# P 05. NETWORK ANALYSIS 100 40 43 P 15. ELECTROMAGNETIC 100 40 43 P 100 40 49 P 25 10 17 P 06. DIGITAL LOGIC DESIGN PP 16. ELECTROMAGNETIC TW 50 20 31 P C 100 40 07. DIGITAL LOGIC DESIGN PP 62 P PR 17. DATA STRUCTURES 08. POWER DEVICES AND MACHINES PP 100 40 40 P C 50 20 27 P 18. DATA STRUCTURES 09. NETWORK AND POWER LAB. TW 50 20 34 P C 100 40 48 P 19. COMMUNICATION THEORY 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 32 P C 50 20 30 P 20. COMMUNICATION THEORY 50 20 30 P 21. CIRCUIT SIMULATION AND TOOLS TW GRAND TOTAL = 774/1500, RESULT: SECOND CLASS # [0.4] ORDN. 1 MARKS: S80053036 CHOWDHARY NOOPUR YUVRAJKUMAR HARSHA , 71200823D , S80053036 , PICT , S80053036 PP 100 40 40 P C 11. ENGINEERING MATHEMATICS III PP 100 40 43 P 01. SIGNAL AND SYSTEMS 50 20 33 P C 25 10 18 P 02. SIGNAL AND SYSTEMS OR 12. ENGINEERING MATHEMATICS III TW 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 55 P 65 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 35 P C 50 20 25 P 14. INTEGRATED CIRCUITS APPLICATIONS PR 100 40 48 P PP 100 40 05. NETWORK ANALYSIS 15. ELECTROMAGNETIC 53 P 25 10 100 40 51 P 16. ELECTROMAGNETIC 18 P 06. DIGITAL LOGIC DESIGN PP TW 50 20 32 P C 100 40 07. DIGITAL LOGIC DESIGN PR 17. DATA STRUCTURES PP 49 P 100 40 51 P C 50 20 30 P 08. POWER DEVICES AND MACHINES PP 18. DATA STRUCTURES 09. NETWORK AND POWER LAB. TW 50 20 31 P C 19. COMMUNICATION THEORY 100 40 51 P 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 33 P C 50 20 33 P 20. COMMUNICATION THEORY 50 20 33 P 21. CIRCUIT SIMULATION AND TOOLS TW GRAND TOTAL = 827/1500, RESULT: HIGHER SECOND CLASS ORDN. 1 MARKS: , 71200826J , S80053037 , PICT , S80053037 ARUNA S80053037 DAKE ABOLI BALASAHEB 01. SIGNAL AND SYSTEMS PP 100 40 53 P C 11. ENGINEERING MATHEMATICS III PP 100 40 P OR 02. SIGNAL AND SYSTEMS 12. ENGINEERING MATHEMATICS III TW 50 20 30 P C 25 10 17 P 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 40 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 36 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 35 P 05. NETWORK ANALYSIS PP 100 40 40 P 100 40 62 P 15. ELECTROMAGNETIC PP 100 40 06. DIGITAL LOGIC DESIGN 40 P C TW 25 10 17 P 16. ELECTROMAGNETIC 07. DIGITAL LOGIC DESIGN 50 20 35 P C PP 100 40 43 P PR 17. DATA STRUCTURES 08. POWER DEVICES AND MACHINES PP 100 40 45 P C 18. DATA STRUCTURES PR 50 20 35 P 09. NETWORK AND POWER LAB. TW 50 20 35 P C 100 40 41 P 19. COMMUNICATION THEORY 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 31 P C 20. COMMUNICATION THEORY 50 20 27 P OR 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 31 P GRAND TOTAL = 773/1500, RESULT: SECOND CLASS ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER S80053038 DESHPANDE CHAITANYA DHANANJAY SWATI , 71200832C , S80053038 , PICT , S80053038 11. ENGINEERING MATHEMATICS III PP 100 40 90 P 01. SIGNAL AND SYSTEMS PP 100 40 74 P C 50 20 38 P C 25 10 24 P 02. SIGNAL AND SYSTEMS 12. ENGINEERING MATHEMATICS III TW 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 71 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 71 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 38 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 42 P 05. NETWORK ANALYSIS 100 40 63 P C 15. ELECTROMAGNETIC 100 40 77 P 100 40 56 P C 25 10 23 P 06. DIGITAL LOGIC DESIGN PP 16. ELECTROMAGNETIC TW 50 20 40 P C 100 40 07. DIGITAL LOGIC DESIGN PP 80 P PR 17. DATA STRUCTURES 08. POWER DEVICES AND MACHINES PP 100 40 72 P C 50 20 18. DATA STRUCTURES 46 P 09. NETWORK AND POWER LAB. TW 50 20 43 P C 100 40 71 P 19. COMMUNICATION THEORY 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 43 P C 50 20 40 P 20. COMMUNICATION THEORY 50 20 46 P 21. CIRCUIT SIMULATION AND TOOLS TW GRAND TOTAL = 1148/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS: S80053039 DESHPANDE KAUSTUBH SURYAKANT MANIK , 71200833M , S80053039 , PICT , S80053039 11. ENGINEERING MATHEMATICS III PP 100 40 90 P 67 P C 01. SIGNAL AND SYSTEMS 100 40 50 20 25 10 21 P 02. SIGNAL AND SYSTEMS OR 34 P C 12. ENGINEERING MATHEMATICS III TW 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 40 P C 53 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 33 P C 50 20 37 P 14. INTEGRATED CIRCUITS APPLICATIONS PR 100 40 41 P C 100 40 05. NETWORK ANALYSIS 15. ELECTROMAGNETIC 69 P 100 40 40 P C 16. ELECTROMAGNETIC 25 10 06. DIGITAL LOGIC DESIGN PP TW 21 P 50 20 38 P C 100 40 71 P 07. DIGITAL LOGIC DESIGN PR 17. DATA STRUCTURES PP 100 40 50 P C 50 20 08. POWER DEVICES AND MACHINES PP 18. DATA STRUCTURES 40 P 09. NETWORK AND POWER LAB. TW 50 20 39 P C 19. COMMUNICATION THEORY 100 40 64 P 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 35 P C 50 20 28 P 20. COMMUNICATION THEORY 50 20 38 P 21. CIRCUIT SIMULATION AND TOOLS TW GRAND TOTAL = 949/1500, RESULT: FIRST CLASS ORDN. 1 MARKS: , 71200834K , S80053040 , PICT , S80053040 S80053040 DESHPANDE NIKITA KISHOR NEETA 01. SIGNAL AND SYSTEMS PP 100 40 59 P C 11. ENGINEERING MATHEMATICS III PP 100 40 55 P OR 02. SIGNAL AND SYSTEMS 12. ENGINEERING MATHEMATICS III TW 50 20 22 P C 25 10 16 P 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 40 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 45 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 28 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 22 P 05. NETWORK ANALYSIS PP 100 40 42 P 100 40 80 P 15. ELECTROMAGNETIC PP 100 40 06. DIGITAL LOGIC DESIGN 40 P C TW 25 10 16 P 16. ELECTROMAGNETIC 07. DIGITAL LOGIC DESIGN 50 20 20 P C PP 100 40 63 P PR 17. DATA STRUCTURES 08. POWER DEVICES AND MACHINES PP 100 40 49 P C 18. DATA STRUCTURES PR 50 20 37 P 09. NETWORK AND POWER LAB. TW 50 20 29 P C 100 40 40 P 19. COMMUNICATION THEORY 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 31 P C 50 20 22 P 20. COMMUNICATION THEORY OR 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 29 P GRAND TOTAL = 785/1500, RESULT: SECOND CLASS ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER SUPRIYA , 71200835H , S80053041 , РІСТ , S80053041 S80053041 DESHPANDE SUBODH SANJAYRAO PP 100 40 54 P C 11. ENGINEERING MATHEMATICS III PP 100 40 60 P 01. SIGNAL AND SYSTEMS 50 20 30 P C 25 10 18 P 02. SIGNAL AND SYSTEMS 12. ENGINEERING MATHEMATICS III TW 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 40 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 43 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 25 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 38 P 05. NETWORK ANALYSIS 100 40 40 P 15. ELECTROMAGNETIC 100 40 60 P PP 100 40 47 P 25 10 20 P 06. DIGITAL LOGIC DESIGN 16. ELECTROMAGNETIC TW 50 20 37 P C 100 40 07. DIGITAL LOGIC DESIGN PP 56 P PR 17. DATA STRUCTURES 08. POWER DEVICES AND MACHINES PP 100 40 42 P C 50 20 18. DATA STRUCTURES 38 P 09. NETWORK AND POWER LAB. TW 50 20 41 P C 100 40 51 P 19. COMMUNICATION THEORY 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 41 P C 50 20 27 P 20. COMMUNICATION THEORY 50 20 32 P 21. CIRCUIT SIMULATION AND TOOLS TW GRAND TOTAL = 840/1500, RESULT: HIGHER SECOND CLASS ORDN. 1 MARKS : S80053042 DHAMAL NAMRATA MOHAN SAVITA , 71344353H , S80053042 , PICT , S80053042 11. ENGINEERING MATHEMATICS III PP 100 40 65 P PP 100 40 63 P C 01. SIGNAL AND SYSTEMS 02. SIGNAL AND SYSTEMS OR 50 20 32 P C 25 10 21 P 12. ENGINEERING MATHEMATICS III TW 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 53 P C 70 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 36 P C 50 20 36 P 14. INTEGRATED CIRCUITS APPLICATIONS PR 100 40 PP 100 40 05. NETWORK ANALYSIS 44 P C 15. ELECTROMAGNETIC 69 P 25 10 100 40 45 P C 16. ELECTROMAGNETIC 22 P 06. DIGITAL LOGIC DESIGN PP TW 50 20 36 P C 100 40 76 P 07. DIGITAL LOGIC DESIGN PR 17. DATA STRUCTURES PP 100 40 51 P C 50 20 39 P 08. POWER DEVICES AND MACHINES PP 18. DATA STRUCTURES 09. NETWORK AND POWER LAB. TW 50 20 40 P C 100 40 65 P 19. COMMUNICATION THEORY 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 38 P C 50 20 32 P 20. COMMUNICATION THEORY 50 20 43 P 21. CIRCUIT SIMULATION AND TOOLS TW GRAND TOTAL = 976/1500, RESULT: FIRST CLASS ORDN. 1 MARKS: , 71200842L , S80053043 , PICT , S80053043 S80053043 DHORE PRASHANT SHRIKRISHNA RANJANA 01. SIGNAL AND SYSTEMS 100 40 62 P C 11. ENGINEERING MATHEMATICS III PP 100 40 44 P OR 02. SIGNAL AND SYSTEMS 50 20 30 P C 12. ENGINEERING MATHEMATICS III TW 25 10 14 P 100 40 56 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 57 P 03. SOLID STATES DEVICES AND CIRCUITSPP 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 33 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 32 P 05. NETWORK ANALYSIS PP 100 40 40 P C 100 40 64 P 15. ELECTROMAGNETIC PP 100 40 06. DIGITAL LOGIC DESIGN 58 P TW 25 10 13 P 16. ELECTROMAGNETIC 07. DIGITAL LOGIC DESIGN 50 20 37 P C PP 100 40 55 P PR 17. DATA STRUCTURES 08. POWER DEVICES AND MACHINES PP 100 40 56 P C 18. DATA STRUCTURES PR 50 20 36 P 09. NETWORK AND POWER LAB. TW 100 40 45 P 50 20 31 P C 19. COMMUNICATION THEORY 50 20 35 P C 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 30 P 20. COMMUNICATION THEORY OR 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 30 P GRAND TOTAL = 858/1500, RESULT: HIGHER SECOND CLASS ORDN. 1 MARKS: 

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER SANGITA , 71200843J , S80053044 , PICT , S80053044 S80053044 DHOTEY RUTVIJ SUDHIR 11. ENGINEERING MATHEMATICS III PP 100 40 74 P PP 100 40 77 P C 01. SIGNAL AND SYSTEMS OR 50 20 30 P C 25 10 17 P 02. SIGNAL AND SYSTEMS 12. ENGINEERING MATHEMATICS III TW 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 48 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 60 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 43 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 35 P 05. NETWORK ANALYSIS PP 100 40 62 P C 15. ELECTROMAGNETIC 100 40 59 P PP 100 40 44 P C 25 10 18 P 06. DIGITAL LOGIC DESIGN 16. ELECTROMAGNETIC TW 50 20 47 P C 100 40 07. DIGITAL LOGIC DESIGN PP 84 P PR 17. DATA STRUCTURES 08. POWER DEVICES AND MACHINES PP 100 40 53 P C 50 20 37 P 18. DATA STRUCTURES 09. NETWORK AND POWER LAB. TW 50 20 41 P C 100 40 58 P 19. COMMUNICATION THEORY 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 38 P C 50 20 35 P 20. COMMUNICATION THEORY 50 20 33 P 21. CIRCUIT SIMULATION AND TOOLS TW GRAND TOTAL = 993/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS: BEBY , 71200847M , S80053045 , PICT , S80053045 S80053045 DOKADE ABHIJIT VISHNU 11. ENGINEERING MATHEMATICS III PP 100 40 01 F PP 100 40 40 P C 01. SIGNAL AND SYSTEMS or 50 20 20 P C 12. ENGINEERING MATHEMATICS III TW 25 10 13 P 02. SIGNAL AND SYSTEMS 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 19 F 15 F 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 20 P 50 20 20 P 14. INTEGRATED CIRCUITS APPLICATIONS PR 100 40 PP 100 40 22 F 05. NETWORK ANALYSIS 08 F 15. ELECTROMAGNETIC 40 P 25 10 100 40 16. ELECTROMAGNETIC 12 P 06. DIGITAL LOGIC DESIGN PP TW 50 20 10 F PP 100 40 44 P 07. DIGITAL LOGIC DESIGN PR 17. DATA STRUCTURES 100 40 40 P 50 20 AA F 08. POWER DEVICES AND MACHINES PP 18. DATA STRUCTURES 09. NETWORK AND POWER LAB. TW 50 20 26 P C PP 100 40 21 F 19. COMMUNICATION THEORY 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 31 P C 50 20 22 P 20. COMMUNICATION THEORY 50 20 22 P 21. CIRCUIT SIMULATION AND TOOLS TW GRAND TOTAL = 446/1500, RESULT: FAILS RESULT RESERVED FOR BKLG ORDN. 1 MARKS: , 71200848K , S80053046 , PICT , S80053046 MANISHA S80053046 DONDE YATHARTH ANAND 01. SIGNAL AND SYSTEMS PP 100 40 40 P 11. ENGINEERING MATHEMATICS III PP 100 40 18 F OR 02. SIGNAL AND SYSTEMS 12. ENGINEERING MATHEMATICS III TW 50 20 20 P 25 10 11 P 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 40 P 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 19 F 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 20 P 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 05 F 05. NETWORK ANALYSIS PP 100 40 15 F 100 40 25 F 15. ELECTROMAGNETIC PP 100 40 20 F 25 10 17 P 06. DIGITAL LOGIC DESIGN TW 16. ELECTROMAGNETIC 07. DIGITAL LOGIC DESIGN 50 20 20 P C PP 100 40 29 F PR 17. DATA STRUCTURES 08. POWER DEVICES AND MACHINES PP 100 40 26 F 18. DATA STRUCTURES PR 50 20 25 P 09. NETWORK AND POWER LAB. TW 50 20 32 P C 100 40 21 F 19. COMMUNICATION THEORY 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 28 P C 50 20 22 P 20. COMMUNICATION THEORY OR 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 27 P GRAND TOTAL = 480/1500, RESULT: FAILS RESULT RESERVED FOR BKLG ORDN. 1 MARKS:

DATE: 27 JULY 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 16 (410)

NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER SHITAL , 71344354F , S80053047 , PICT , S80053047 S80053047 GAIKWAD VAISHALI RAMESH 11. ENGINEERING MATHEMATICS III PP 100 40 58 P PP 100 40 57 P C 01. SIGNAL AND SYSTEMS 50 20 28 P C 25 10 22 P 02. SIGNAL AND SYSTEMS 12. ENGINEERING MATHEMATICS III TW 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 40 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 52 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 20 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 23 P 05. NETWORK ANALYSIS PP 100 40 40 P C 15. ELECTROMAGNETIC 100 40 50 P PP 100 40 66 P 25 10 20 P 06. DIGITAL LOGIC DESIGN 16. ELECTROMAGNETIC TW 50 20 100 40 07. DIGITAL LOGIC DESIGN 30 P C PP 54 P PR 17. DATA STRUCTURES 08. POWER DEVICES AND MACHINES PP 100 40 58 P C 50 20 30 P 18. DATA STRUCTURES 09. NETWORK AND POWER LAB. TW 50 20 40 P C 100 40 51 P 19. COMMUNICATION THEORY 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 38 P C 50 20 29 P 20. COMMUNICATION THEORY 50 20 38 P 21. CIRCUIT SIMULATION AND TOOLS TW GRAND TOTAL = 844/1500, RESULT: HIGHER SECOND CLASS ORDN. 1 MARKS: RAJASHREE , 71200857」 , S80053048 , PICT , S80053048 S80053048 GANGAL AKSHAY PARAG 11. ENGINEERING MATHEMATICS III PP 100 40 98 P PP 100 40 70 P C 01. SIGNAL AND SYSTEMS 50 20 38 P C 25 10 24 P 02. SIGNAL AND SYSTEMS OR 12. ENGINEERING MATHEMATICS III TW 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 66 P C 68 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 50 20 43 P 44 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 100 40 61 P C PP 100 40 77 P 05. NETWORK ANALYSIS 15. ELECTROMAGNETIC 25 10 100 40 54 P C 16. ELECTROMAGNETIC 23 P 06. DIGITAL LOGIC DESIGN PP TW 50 20 100 40 07. DIGITAL LOGIC DESIGN PR 44 P C 17. DATA STRUCTURES PP 81 P 100 40 50 20 08. POWER DEVICES AND MACHINES PP 67 P C 18. DATA STRUCTURES 45 P 09. NETWORK AND POWER LAB. TW 50 20 45 P C 19. COMMUNICATION THEORY 100 40 80 P 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 45 P C 50 20 35 P 20. COMMUNICATION THEORY 50 20 45 P 21. CIRCUIT SIMULATION AND TOOLS TW GRAND TOTAL = 1153/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS: , 71200863C , S80053049 , PICT , S80053049 S80053049 GHADGE GITANJALI SUBHASH MANJUSHA 01. SIGNAL AND SYSTEMS PP 100 40 71 P C 11. ENGINEERING MATHEMATICS III PP 100 40 63 P OR 02. SIGNAL AND SYSTEMS 12. ENGINEERING MATHEMATICS III TW 50 20 35 P C 25 10 22 P 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 42 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 57 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 38 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 40 P 05. NETWORK ANALYSIS PP 100 40 45 P C 100 40 64 P 15. ELECTROMAGNETIC PP 100 40 40 P C TW 25 10 21 P 06. DIGITAL LOGIC DESIGN 16. ELECTROMAGNETIC 07. DIGITAL LOGIC DESIGN 50 20 48 P C PP 100 40 65 P PR 17. DATA STRUCTURES 08. POWER DEVICES AND MACHINES PP 100 40 55 P C 18. DATA STRUCTURES PR 50 20 41 P 09. NETWORK AND POWER LAB. TW 50 20 42 P C 100 40 58 P 19. COMMUNICATION THEORY 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 39 P C 50 20 37 P 20. COMMUNICATION THEORY OR 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 38 P GRAND TOTAL = 961/1500, RESULT: FIRST CLASS ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER SANDHYA GIRI , 71344355D , S80053050 , PICT , S80053050 S80053050 GIRI PRATIK CHANDRAKANT PP 100 40 57 P C 11. ENGINEERING MATHEMATICS III PP 100 40 81 P 01. SIGNAL AND SYSTEMS 02. SIGNAL AND SYSTEMS OR 50 20 21 P C 25 10 21 P 12. ENGINEERING MATHEMATICS III TW 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 65 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 59 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 26 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 32 P 05. NETWORK ANALYSIS PP 100 40 40 P C 15. ELECTROMAGNETIC 100 40 65 P PP 100 40 46 P C 25 10 06. DIGITAL LOGIC DESIGN 16. ELECTROMAGNETIC TW 21 p 50 20 28 P C 100 40 07. DIGITAL LOGIC DESIGN PP 69 P PR 17. DATA STRUCTURES 08. POWER DEVICES AND MACHINES PP 100 40 61 P C 50 20 18. DATA STRUCTURES 37 P 09. NETWORK AND POWER LAB. TW 50 20 38 P C 100 40 71 P 19. COMMUNICATION THEORY 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 37 P C 50 20 37 P 20. COMMUNICATION THEORY 50 20 41 P 21. CIRCUIT SIMULATION AND TOOLS TW GRAND TOTAL = 953/1500, RESULT: FIRST CLASS ORDN. 1 MARKS: S80053051 GOKHALE SATYAJIT SHASHANK JAYASHREE , 71200866н , S80053051 , РІСТ , S80053051 PP 100 40 75 P C 11. ENGINEERING MATHEMATICS III PP 100 40 52 P 01. SIGNAL AND SYSTEMS OR 50 20 29 P C 25 10 20 P 02. SIGNAL AND SYSTEMS 12. ENGINEERING MATHEMATICS III TW 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 55 P 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 40 P C 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 34 P C 50 20 38 P 14. INTEGRATED CIRCUITS APPLICATIONS PR 100 40 40 P C PP 100 40 05. NETWORK ANALYSIS 15. ELECTROMAGNETIC 60 P PP 100 40 40 P C 16. ELECTROMAGNETIC 25 10 19 P 06. DIGITAL LOGIC DESIGN TW 50 20 30 P C PP 100 40 07. DIGITAL LOGIC DESIGN PR 17. DATA STRUCTURES 63 P 08. POWER DEVICES AND MACHINES PP 100 40 64 P C 50 20 18. DATA STRUCTURES 42 P 09. NETWORK AND POWER LAB. TW 50 20 41 P C 100 40 61 P 19. COMMUNICATION THEORY 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 41 P C 50 20 37 P 20. COMMUNICATION THEORY 50 20 39 P 21. CIRCUIT SIMULATION AND TOOLS TW GRAND TOTAL = 920/1500, RESULT: FIRST CLASS ORDN. 1 MARKS: , 71344356B , S80053052 , PICT , S80053052 MANISHA S80053052 GOSAVI ABHISHEK MAHESH 01. SIGNAL AND SYSTEMS PP 100 40 51 P C 11. ENGINEERING MATHEMATICS III PP 100 40 47 P OR 02. SIGNAL AND SYSTEMS 50 20 25 P C 12. ENGINEERING MATHEMATICS III TW 25 10 19 P 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 48 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 63 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 24 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 32 P PP 05. NETWORK ANALYSIS PP 100 40 40 P C 100 40 48 P 15. ELECTROMAGNETIC 100 40 06. DIGITAL LOGIC DESIGN 45 P C TW 25 10 18 P 16. ELECTROMAGNETIC 07. DIGITAL LOGIC DESIGN 50 20 30 P C PP 100 40 69 P PR 17. DATA STRUCTURES 08. POWER DEVICES AND MACHINES PP 100 40 48 P C 18. DATA STRUCTURES PR 50 20 37 P 09. NETWORK AND POWER LAB. TW 50 20 38 P C 100 40 65 P 19. COMMUNICATION THEORY 50 20 38 P C 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 32 P 20. COMMUNICATION THEORY OR 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 32 P GRAND TOTAL = 849/1500, RESULT: HIGHER SECOND CLASS ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER LATA , 71344357L , S80053053 , PICT , S80053053 S80053053 GUMBADE MAYURI RAGHUNATH PP 100 40 42 P C 11. ENGINEERING MATHEMATICS III PP 100 40 40 P 01. SIGNAL AND SYSTEMS 50 20 26 P C 25 10 18 P 02. SIGNAL AND SYSTEMS 12. ENGINEERING MATHEMATICS III TW 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 43 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 52 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 20 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 33 P 05. NETWORK ANALYSIS PP 100 40 45 P 15. ELECTROMAGNETIC 100 40 57 P PP 100 40 59 P 25 10 19 P 06. DIGITAL LOGIC DESIGN 16. ELECTROMAGNETIC TW 50 20 100 40 55 P 07. DIGITAL LOGIC DESIGN 20 P C PP PR 17. DATA STRUCTURES 08. POWER DEVICES AND MACHINES PP 100 40 48 P C 50 20 18. DATA STRUCTURES 25 P 09. NETWORK AND POWER LAB. TW 50 20 38 P C 100 40 55 P 19. COMMUNICATION THEORY 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 36 P C 50 20 27 P 20. COMMUNICATION THEORY 50 20 36 P 21. CIRCUIT SIMULATION AND TOOLS TW GRAND TOTAL = 794/1500, RESULT: SECOND CLASS ORDN. 1 MARKS: S80053054 GUTTE KISHOR SHIVAJI USHABAI , 71200874j , S80053054 , PICT , S80053054 PP 100 40 73 P C 11. ENGINEERING MATHEMATICS III PP 100 40 86 P 01. SIGNAL AND SYSTEMS OR 50 20 29 P C 25 10 21 P 02. SIGNAL AND SYSTEMS 12. ENGINEERING MATHEMATICS III TW 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 48 P C 59 p 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 36 P C 50 20 26 P 14. INTEGRATED CIRCUITS APPLICATIONS PR 100 40 46 P.C PP 100 40 05. NETWORK ANALYSIS 15. ELECTROMAGNETIC 68 P 25 10 100 40 40 P C 16. ELECTROMAGNETIC 06. DIGITAL LOGIC DESIGN PP TW 22 P 50 20 36 P C 100 40 72 P 07. DIGITAL LOGIC DESIGN PR 17. DATA STRUCTURES PP 100 40 58 P C 50 20 08. POWER DEVICES AND MACHINES PP 18. DATA STRUCTURES 42 P 09. NETWORK AND POWER LAB. TW 50 20 42 P C 19. COMMUNICATION THEORY 100 40 63 P 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 40 P C 50 20 27 P 20. COMMUNICATION THEORY 50 20 41 P 21. CIRCUIT SIMULATION AND TOOLS TW GRAND TOTAL = 975/1500, RESULT: FIRST CLASS ORDN. 1 MARKS: , 71200880C , S80053055 , PICT , S80053055 S80053055 HEGDE GAUTAMI GOPALKRISHNA APARNA 01. SIGNAL AND SYSTEMS 100 40 89 P C 11. ENGINEERING MATHEMATICS III PP 100 40 94 P OR 02. SIGNAL AND SYSTEMS 50 20 35 P C 12. ENGINEERING MATHEMATICS III TW 25 10 16 P 100 40 56 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 64 P 03. SOLID STATES DEVICES AND CIRCUITSPP 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 38 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 35 P 05. NETWORK ANALYSIS PP 100 40 70 P C 100 40 92 P 15. ELECTROMAGNETIC PP 100 40 19 P 06. DIGITAL LOGIC DESIGN 50 P C TW 25 10 16. ELECTROMAGNETIC 87 P 07. DIGITAL LOGIC DESIGN 50 20 37 P C PP 100 40 PR 17. DATA STRUCTURES 08. POWER DEVICES AND MACHINES PP 100 40 69 P C 18. DATA STRUCTURES PR 50 20 43 P 09. NETWORK AND POWER LAB. TW 100 40 76 P 50 20 44 P C 19. COMMUNICATION THEORY 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 44 P C 20. COMMUNICATION THEORY 50 20 30 P OR 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 36 P GRAND TOTAL = 1124/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER MADHURI , 71200881M , S80053056 , PICT , S80053056 S80053056 HINGANE SOURABH MADHUKAR 11. ENGINEERING MATHEMATICS III PP 100 40 84 P PP 100 40 86 P C 01. SIGNAL AND SYSTEMS 50 20 42 P C 25 10 22 P 02. SIGNAL AND SYSTEMS 12. ENGINEERING MATHEMATICS III TW 100 40 50 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 59 P 03. SOLID STATES DEVICES AND CIRCUITSPP 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 35 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 30 P 05. NETWORK ANALYSIS PP 100 40 41 P C 15. ELECTROMAGNETIC 100 40 80 P PP 100 40 40 P C 25 10 22 P 06. DIGITAL LOGIC DESIGN 16. ELECTROMAGNETIC TW 50 20 45 P C 100 40 78 P 07. DIGITAL LOGIC DESIGN PP PR 17. DATA STRUCTURES 08. POWER DEVICES AND MACHINES PP 100 40 66 P C 50 20 18. DATA STRUCTURES 45 P 09. NETWORK AND POWER LAB. TW 50 20 41 P C 100 40 72 P 19. COMMUNICATION THEORY 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 39 P C 50 20 30 P 20. COMMUNICATION THEORY 50 20 36 P 21. CIRCUIT SIMULATION AND TOOLS TW GRAND TOTAL = 1043/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS: LAMBOK , 71200883H , S80053057 , РІСТ , S80053057 S80053057 HOWNI SAMAN WANN 11. ENGINEERING MATHEMATICS III PP 100 40 40 P PP 100 40 40 P C 01. SIGNAL AND SYSTEMS 02. SIGNAL AND SYSTEMS OR 50 20 21 P C 25 10 14 P 12. ENGINEERING MATHEMATICS III TW 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 40 P 19 F 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 30 P 50 20 14. INTEGRATED CIRCUITS APPLICATIONS PR 08 F 100 40 40 P PP 100 40 05. NETWORK ANALYSIS 15. ELECTROMAGNETIC 42 P 40 P 25 10 100 40 16. ELECTROMAGNETIC 13 P 06. DIGITAL LOGIC DESIGN PP TW 50 20 20 P C 100 40 07. DIGITAL LOGIC DESIGN PR 17. DATA STRUCTURES PP 40 P 100 40 40 P C 50 20 28 P 08. POWER DEVICES AND MACHINES PP 18. DATA STRUCTURES 100 40 40 P 09. NETWORK AND POWER LAB. TW 50 20 27 P C 19. COMMUNICATION THEORY 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 31 P C 50 20 10 F 20. COMMUNICATION THEORY 50 20 25 P 21. CIRCUIT SIMULATION AND TOOLS TW GRAND TOTAL = 608/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS: , 71344358」 , S80053058 , PICT , S80053058 S80053058 INGLE BHAVANA KRUSHNAJI SHAILA 01. SIGNAL AND SYSTEMS PP 100 40 53 P C 11. ENGINEERING MATHEMATICS III PP 100 40 61 P OR 02. SIGNAL AND SYSTEMS 12. ENGINEERING MATHEMATICS III TW 50 20 25 P C 25 10 22 P 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 49 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 55 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 21 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 31 P 05. NETWORK ANALYSIS PP 100 40 40 P C 100 40 47 P 15. ELECTROMAGNETIC PP 100 40 06. DIGITAL LOGIC DESIGN 40 P C TW 25 10 21 P 16. ELECTROMAGNETIC 07. DIGITAL LOGIC DESIGN 50 20 23 P C PP 100 40 52 P PR 17. DATA STRUCTURES 08. POWER DEVICES AND MACHINES PP 100 40 63 P C 18. DATA STRUCTURES PR 50 20 30 P 09. NETWORK AND POWER LAB. TW 100 40 47 P 50 20 39 P C 19. COMMUNICATION THEORY 50 20 37 P C 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 40 P 20. COMMUNICATION THEORY OR 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 40 P GRAND TOTAL = 836/1500, RESULT: HIGHER SECOND CLASS ORDN. 1 MARKS: 

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER SANGITA , 71344359G , S80053059 , PICT , S80053059 S80053059 JADHAV POONAM SANJAY 11. ENGINEERING MATHEMATICS III PP 100 40 08 F PP 100 40 46 P C 01. SIGNAL AND SYSTEMS OR 50 20 22 P C 25 10 18 P 02. SIGNAL AND SYSTEMS 12. ENGINEERING MATHEMATICS III TW 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 40 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 44 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 21 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 28 P 05. NETWORK ANALYSIS 100 40 46 P 15. ELECTROMAGNETIC 100 40 42 P 100 40 41 P 25 10 18 P 06. DIGITAL LOGIC DESIGN PP 16. ELECTROMAGNETIC TW 50 20 25 P C 100 40 07. DIGITAL LOGIC DESIGN PP 40 P PR 17. DATA STRUCTURES 08. POWER DEVICES AND MACHINES PP 100 40 58 P C 50 20 32 P 18. DATA STRUCTURES 09. NETWORK AND POWER LAB. TW 50 20 39 P C 100 40 47 P 19. COMMUNICATION THEORY 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 37 P C 50 20 44 P 20. COMMUNICATION THEORY 50 20 35 P 21. CIRCUIT SIMULATION AND TOOLS TW GRAND TOTAL = 731/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS: LATA , 71344360L , S80053060 , PICT , S80053060 S80053060 JAGTAP SHITAL LAXMAN 11. ENGINEERING MATHEMATICS III PP 100 40 40 P PP 100 40 53 P C 01. SIGNAL AND SYSTEMS 50 20 23 P C 25 10 21 P 02. SIGNAL AND SYSTEMS OR 12. ENGINEERING MATHEMATICS III TW 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 40 P C 52 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 27 P C 50 20 22 P 14. INTEGRATED CIRCUITS APPLICATIONS PR 100 40 43 P PP 100 40 05. NETWORK ANALYSIS 15. ELECTROMAGNETIC 47 P 25 10 100 40 16. ELECTROMAGNETIC 06. DIGITAL LOGIC DESIGN PP 45 P C TW 21 P 50 20 22 P C 100 40 07. DIGITAL LOGIC DESIGN PR 17. DATA STRUCTURES PP 64 P 100 40 51 P C 08. POWER DEVICES AND MACHINES PP 18. DATA STRUCTURES 50 20 35 P 09. NETWORK AND POWER LAB. TW 50 20 40 P C 19. COMMUNICATION THEORY 100 40 58 P 50 20 38 P C 50 20 34 P 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 20. COMMUNICATION THEORY 50 20 41 P 21. CIRCUIT SIMULATION AND TOOLS TW GRAND TOTAL = 817+08/1500, RESULT: HIGHER SECOND CLASS [0.2] ORDN. 1 MARKS : , 71200887L , S80053061 , PICT , S80053061 S80053061 JAIN KAJAL HEMANTKUMAR RAJANI 01. SIGNAL AND SYSTEMS PP 100 40 53 P C 11. ENGINEERING MATHEMATICS III PP 100 40 91 P OR 02. SIGNAL AND SYSTEMS 12. ENGINEERING MATHEMATICS III TW 50 20 30 P C 25 10 18 P 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 40 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 63 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 39 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 35 P 05. NETWORK ANALYSIS PP 100 40 46 P C 100 40 72 P 15. ELECTROMAGNETIC PP 100 40 42 P C TW 25 10 21 P 06. DIGITAL LOGIC DESIGN 16. ELECTROMAGNETIC 07. DIGITAL LOGIC DESIGN 50 20 34 P C PP 100 40 71 P PR 17. DATA STRUCTURES 08. POWER DEVICES AND MACHINES PP 100 40 50 P C 18. DATA STRUCTURES PR 50 20 38 P 09. NETWORK AND POWER LAB. TW 50 20 38 P C 100 40 77 P 19. COMMUNICATION THEORY 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 35 P C 20. COMMUNICATION THEORY 50 20 25 P OR 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 38 P GRAND TOTAL = 956/1500, RESULT: FIRST CLASS ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER MANJU , 71200888J , S80053062 , PICT , S80053062 S80053062 JAIN KRUNAL LALIT 11. ENGINEERING MATHEMATICS III PP 100 40 78 P 01. SIGNAL AND SYSTEMS PP 100 40 81 P C OR 50 20 38 P C 25 10 24 P 02. SIGNAL AND SYSTEMS 12. ENGINEERING MATHEMATICS III TW 100 40 59 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 03. SOLID STATES DEVICES AND CIRCUITSPP 60 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 35 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 46 P 05. NETWORK ANALYSIS 100 40 49 P C 15. ELECTROMAGNETIC 100 40 86 P PP 100 40 40 P C 25 10 22 P 06. DIGITAL LOGIC DESIGN 16. ELECTROMAGNETIC TW 50 20 46 P C 07. DIGITAL LOGIC DESIGN PP 100 40 56 P PR 17. DATA STRUCTURES 08. POWER DEVICES AND MACHINES PP 100 40 71 P C 50 20 18. DATA STRUCTURES 43 P 09. NETWORK AND POWER LAB. TW 50 20 44 P C 100 40 68 P 19. COMMUNICATION THEORY 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 43 P C 50 20 37 P 20. COMMUNICATION THEORY 50 20 45 P 21. CIRCUIT SIMULATION AND TOOLS TW GRAND TOTAL = 1071/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS: S80053063 JAVANJAL MAUSAMI SHIVSHANKAR SANDHYA , 71344361J , S80053063 , PICT , S80053063 11. ENGINEERING MATHEMATICS III PP 100 40 86 P 01. SIGNAL AND SYSTEMS 100 40 77 P C 50 20 38 P C 25 10 19 P 02. SIGNAL AND SYSTEMS OR 12. ENGINEERING MATHEMATICS III TW 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 47 P C 46 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 34 P C 50 20 32 P 14. INTEGRATED CIRCUITS APPLICATIONS PR 100 40 PP 100 40 05. NETWORK ANALYSIS 62 P C 15. ELECTROMAGNETIC 69 P 25 10 100 40 16. ELECTROMAGNETIC 19 P 06. DIGITAL LOGIC DESIGN PP 45 P C TW 50 20 37 P C 100 40 07. DIGITAL LOGIC DESIGN PR 17. DATA STRUCTURES PP 64 P 100 40 08. POWER DEVICES AND MACHINES PP 61 P C 18. DATA STRUCTURES 50 20 38 P 09. NETWORK AND POWER LAB. TW 50 20 40 P C 19. COMMUNICATION THEORY 100 40 58 P 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 37 P C 50 20 33 P 20. COMMUNICATION THEORY 50 20 34 P 21. CIRCUIT SIMULATION AND TOOLS TW GRAND TOTAL = 976/1500, RESULT: FIRST CLASS ORDN. 1 MARKS: S80053064 JAWALIKAR AMEY RAVINDRA NIRMALA RAVINDRA JA , 71344362G , S80053064 , PICT , S80053064 64 P 01. SIGNAL AND SYSTEMS PP 100 40 11. ENGINEERING MATHEMATICS III PP 100 40 64 P OR 02. SIGNAL AND SYSTEMS 12. ENGINEERING MATHEMATICS III TW 50 20 20 P C 25 10 17 P 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 42 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 51 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 30 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 40 P 05. NETWORK ANALYSIS PP 100 40 48 P 100 40 73 P 15. ELECTROMAGNETIC PP 100 40 19 P 63 P TW 25 10 06. DIGITAL LOGIC DESIGN 16. ELECTROMAGNETIC 07. DIGITAL LOGIC DESIGN 50 20 27 P C PP 100 40 PR 17. DATA STRUCTURES 61 P 08. POWER DEVICES AND MACHINES PP 100 40 58 P C 18. DATA STRUCTURES PR 50 20 42 P 09. NETWORK AND POWER LAB. TW 50 20 39 P C 100 40 55 P 19. COMMUNICATION THEORY 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 39 P C 20. COMMUNICATION THEORY 50 20 33 P OR 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 28 P GRAND TOTAL = 913/1500, RESULT: FIRST CLASS ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71200894C , S80053065 , PICT , S80053065 S80053065 JOSHI NEEL RAJEEV PADMAJA 11. ENGINEERING MATHEMATICS III PP 100 40 60 P 01. SIGNAL AND SYSTEMS PP 100 40 53 P C 50 20 35 P C 25 10 18 P 02. SIGNAL AND SYSTEMS OR 12. ENGINEERING MATHEMATICS III TW 100 40 47 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 71 P 03. SOLID STATES DEVICES AND CIRCUITSPP 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 40 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 40 P 05. NETWORK ANALYSIS PP 100 40 40 P C 15. ELECTROMAGNETIC 100 40 PP 100 40 40 P C 25 10 19 P 06. DIGITAL LOGIC DESIGN 16. ELECTROMAGNETIC TW 100 40 07. DIGITAL LOGIC DESIGN 50 20 42 P C PP 69 P PR 17. DATA STRUCTURES 08. POWER DEVICES AND MACHINES PP 100 40 54 P C 50 20 18. DATA STRUCTURES 41 P 09. NETWORK AND POWER LAB. TW 50 20 42 P C 100 40 57 P 19. COMMUNICATION THEORY 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 40 P C 50 20 37 P 20. COMMUNICATION THEORY 50 20 37 P 21. CIRCUIT SIMULATION AND TOOLS TW GRAND TOTAL = 946/1500, RESULT: FIRST CLASS ORDN. 1 MARKS: MADHAVI , 71200895M , S80053066 , PICT , S80053066 S80053066 JOSHI NIKITA SANJAY 11. ENGINEERING MATHEMATICS III PP 100 40 53 P PP 100 40 66 P C 01. SIGNAL AND SYSTEMS 50 20 30 P C 25 10 20 P 02. SIGNAL AND SYSTEMS OR 12. ENGINEERING MATHEMATICS III TW 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 P 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 40 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 35 P C 50 20 39 P 14. INTEGRATED CIRCUITS APPLICATIONS PR 100 40 40 P C 100 40 05. NETWORK ANALYSIS 15. ELECTROMAGNETIC 54 P 25 10 100 40 45 P 16. ELECTROMAGNETIC 20 P 06. DIGITAL LOGIC DESIGN PP TW 50 20 32 P C 100 40 07. DIGITAL LOGIC DESIGN PR 17. DATA STRUCTURES PP 45 P 100 40 40 P C 08. POWER DEVICES AND MACHINES PP 18. DATA STRUCTURES 50 20 35 P 09. NETWORK AND POWER LAB. TW 50 20 38 P C 19. COMMUNICATION THEORY 100 40 48 P 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 42 P C 50 20 35 P 20. COMMUNICATION THEORY 50 20 39 P 21. CIRCUIT SIMULATION AND TOOLS TW GRAND TOTAL = 836/1500, RESULT: HIGHER SECOND CLASS ORDN. 1 MARKS: , 71200898F , S80053067 , PICT , S80053067 S80053067 KADAM AKSHAY VITTHALRAO LALITA 01. SIGNAL AND SYSTEMS PP 100 40 70 P C 11. ENGINEERING MATHEMATICS III PP 100 40 85 P OR 02. SIGNAL AND SYSTEMS 12. ENGINEERING MATHEMATICS III TW 50 20 32 P C 25 10 22 P 100 40 50 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 64 P 03. SOLID STATES DEVICES AND CIRCUITSPP 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 34 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 22 P 05. NETWORK ANALYSIS PP 100 40 66 P C 100 40 77 P 15. ELECTROMAGNETIC PP 100 40 22 P 06. DIGITAL LOGIC DESIGN 47 P C TW 25 10 16. ELECTROMAGNETIC 07. DIGITAL LOGIC DESIGN 50 20 32 P C PP 100 40 74 P PR 17. DATA STRUCTURES 08. POWER DEVICES AND MACHINES PP 100 40 65 P C 18. DATA STRUCTURES PR 50 20 37 P 09. NETWORK AND POWER LAB. TW 100 40 70 P 50 20 42 P C 19. COMMUNICATION THEORY 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 41 P C 20. COMMUNICATION THEORY 50 20 33 P OR 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 43 P GRAND TOTAL = 1028/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER SHARDA , 71200902н , S80053068 , РІСТ , S80053068 S80053068 KALE PRASHANT SUBHASH 11. ENGINEERING MATHEMATICS III PP 100 40 69 P 01. SIGNAL AND SYSTEMS PP 100 40 82 P C or 50 20 32 P C 25 10 19 P 02. SIGNAL AND SYSTEMS 12. ENGINEERING MATHEMATICS III TW 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 42 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 43 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 37 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 21 P 05. NETWORK ANALYSIS 100 40 48 P C 15. ELECTROMAGNETIC 100 40 71 P 100 40 70 P 25 10 19 P 06. DIGITAL LOGIC DESIGN PP 16. ELECTROMAGNETIC TW 50 20 27 P C 100 40 07. DIGITAL LOGIC DESIGN PP 46 P PR 17. DATA STRUCTURES 08. POWER DEVICES AND MACHINES PP 100 40 49 P C 50 20 18. DATA STRUCTURES 36 P 09. NETWORK AND POWER LAB. TW 50 20 35 P C 100 40 53 P 19. COMMUNICATION THEORY 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 37 P C 50 20 34 P 20. COMMUNICATION THEORY 50 20 36 P 21. CIRCUIT SIMULATION AND TOOLS TW GRAND TOTAL = 906/1500, RESULT: FIRST CLASS ORDN. 1 MARKS: S80053069 KAMBLE SANJEEVANI RAMESH JAYASHREE , 71200908G , S80053069 , PICT , S80053069 11. ENGINEERING MATHEMATICS III PP 100 40 40 P PP 100 40 40 P C 01. SIGNAL AND SYSTEMS 50 20 21 P 25 10 19 P 02. SIGNAL AND SYSTEMS OR 12. ENGINEERING MATHEMATICS III TW 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 40 P 40 p 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 33 P C 50 20 20 P 14. INTEGRATED CIRCUITS APPLICATIONS PR 100 40 22 F PP 100 40 05. NETWORK ANALYSIS 15. ELECTROMAGNETIC 40 P 40 P C 25 10 100 40 16. ELECTROMAGNETIC 06. DIGITAL LOGIC DESIGN PP TW 21 P 50 20 27 P C 100 40 07. DIGITAL LOGIC DESIGN PR 17. DATA STRUCTURES PP 48 P 100 40 40 P 50 20 08. POWER DEVICES AND MACHINES PP 18. DATA STRUCTURES 22 P 09. NETWORK AND POWER LAB. TW 50 20 37 P C 19. COMMUNICATION THEORY 100 40 40 P 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 36 P C 50 20 40 P 20. COMMUNICATION THEORY 50 20 39 P 21. CIRCUIT SIMULATION AND TOOLS TW GRAND TOTAL = 705/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS: , 71200909E , S80053070 , PICT , S80053070 SAVITRI S80053070 KAMBLE VIJAY SANJAY 01. SIGNAL AND SYSTEMS PP 100 40 68 P C 11. ENGINEERING MATHEMATICS III PP 100 40 69 P OR 02. SIGNAL AND SYSTEMS 12. ENGINEERING MATHEMATICS III TW 50 20 33 P C 25 10 20 P 100 40 61 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 56 P 03. SOLID STATES DEVICES AND CIRCUITSPP 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 35 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 36 P 05. NETWORK ANALYSIS PP 100 40 58 P C 100 40 80 P 15. ELECTROMAGNETIC PP 100 40 06. DIGITAL LOGIC DESIGN 48 P C TW 25 10 21 P 16. ELECTROMAGNETIC 07. DIGITAL LOGIC DESIGN 50 20 32 P C PP 100 40 75 P PR 17. DATA STRUCTURES 08. POWER DEVICES AND MACHINES PP 100 40 62 P C 18. DATA STRUCTURES PR 50 20 38 P 09. NETWORK AND POWER LAB. TW 50 20 39 P C 100 40 61 P 19. COMMUNICATION THEORY 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 40 P C 50 20 30 P 20. COMMUNICATION THEORY OR 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 40 P GRAND TOTAL = 1002/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER JAYSHREE , 71200911G , S80053071 , PICT , S80053071 S80053071 KAPRE SURAJ DNYANOBA 11. ENGINEERING MATHEMATICS III PP 100 40 57 P PP 100 40 68 P C 01. SIGNAL AND SYSTEMS OR 50 20 30 P C 25 10 18 P 02. SIGNAL AND SYSTEMS 12. ENGINEERING MATHEMATICS III TW 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 40 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 52 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 30 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 24 P 05. NETWORK ANALYSIS 100 40 40 P C 15. ELECTROMAGNETIC 100 40 51 P 100 40 68 P 25 10 19 P 06. DIGITAL LOGIC DESIGN PP 16. ELECTROMAGNETIC TW 50 20 100 40 07. DIGITAL LOGIC DESIGN 29 P C PP 67 P PR 17. DATA STRUCTURES 08. POWER DEVICES AND MACHINES PP 100 40 47 P C 50 20 18. DATA STRUCTURES 36 P 09. NETWORK AND POWER LAB. TW 50 20 37 P C 100 40 63 P 19. COMMUNICATION THEORY 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 34 P C 50 20 30 P 20. COMMUNICATION THEORY 50 20 33 P 21. CIRCUIT SIMULATION AND TOOLS TW GRAND TOTAL = 873/1500, RESULT: HIGHER SECOND CLASS ORDN. 1 MARKS: S80053072 KASAR SNEHAL NIVRUTTI SUREKHA , 71212196L , S80053072 , PICT , S80053072 11. ENGINEERING MATHEMATICS III PP 100 40 86 P PP 100 40 64 P C 01. SIGNAL AND SYSTEMS or 50 20 35 P C 25 10 21 P 02. SIGNAL AND SYSTEMS 12. ENGINEERING MATHEMATICS III TW 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 42 P 44 P C 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 29 P C 50 20 34 P 14. INTEGRATED CIRCUITS APPLICATIONS PR 100 40 46 P.C PP 100 40 72 P 05. NETWORK ANALYSIS 15. ELECTROMAGNETIC 25 10 100 40 16. ELECTROMAGNETIC 20 P 06. DIGITAL LOGIC DESIGN PP 46 P C TW 50 20 32 P C 100 40 79 P 07. DIGITAL LOGIC DESIGN PR 17. DATA STRUCTURES PP 100 40 58 P C 50 20 32 P 08. POWER DEVICES AND MACHINES PP 18. DATA STRUCTURES 09. NETWORK AND POWER LAB. TW 50 20 40 P C 19. COMMUNICATION THEORY 100 40 76 P 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 39 P C 50 20 30 P 20. COMMUNICATION THEORY 50 20 36 P 21. CIRCUIT SIMULATION AND TOOLS TW GRAND TOTAL = 961/1500, RESULT: FIRST CLASS ORDN. 1 MARKS: , 71200915к , S80053073 , РІСТ , S80053073 SWATI S80053073 KASLIKAR RASHMI MILIND 01. SIGNAL AND SYSTEMS PP 100 40 63 P C 11. ENGINEERING MATHEMATICS III PP 100 40 87 P OR 02. SIGNAL AND SYSTEMS 12. ENGINEERING MATHEMATICS III TW 50 20 39 P C 25 10 21 P 100 40 51 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 48 P 03. SOLID STATES DEVICES AND CIRCUITSPP 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 35 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 37 P 05. NETWORK ANALYSIS PP 100 40 56 P C 100 40 67 P 15. ELECTROMAGNETIC PP 100 40 06. DIGITAL LOGIC DESIGN 40 P C TW 25 10 20 P 16. ELECTROMAGNETIC 07. DIGITAL LOGIC DESIGN 50 20 28 P C PP 100 40 71 P PR 17. DATA STRUCTURES 08. POWER DEVICES AND MACHINES PP 100 40 54 P C 18. DATA STRUCTURES PR 50 20 37 P 09. NETWORK AND POWER LAB. TW 50 20 38 P C 100 40 71 P 19. COMMUNICATION THEORY 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 40 P C 20. COMMUNICATION THEORY 50 20 30 P OR 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 36 P GRAND TOTAL = 969/1500, RESULT: FIRST CLASS ORDN. 1 MARKS: 

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER SANGITA , 71200921D , S80053074 , PICT , S80053074 S80053074 KHAIRE SHUBHAM SUNIL 11. ENGINEERING MATHEMATICS III PP 100 40 79 P 01. SIGNAL AND SYSTEMS PP 100 40 54 P C OR 50 20 34 P C 25 10 16 P 02. SIGNAL AND SYSTEMS 12. ENGINEERING MATHEMATICS III TW 100 40 49 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 52 P 03. SOLID STATES DEVICES AND CIRCUITSPP 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 37 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 33 P 05. NETWORK ANALYSIS 100 40 46 P C 15. ELECTROMAGNETIC 100 40 63 P 100 40 47 P C 25 10 17 P 06. DIGITAL LOGIC DESIGN PP 16. ELECTROMAGNETIC TW 07. DIGITAL LOGIC DESIGN 50 20 30 P C PP 100 40 54 P PR 17. DATA STRUCTURES 08. POWER DEVICES AND MACHINES PP 100 40 51 P C 50 20 37 P 18. DATA STRUCTURES 09. NETWORK AND POWER LAB. TW 50 20 36 P C 100 40 76 P 19. COMMUNICATION THEORY 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 34 P C 50 20 30 P 20. COMMUNICATION THEORY 50 20 30 P 21. CIRCUIT SIMULATION AND TOOLS TW GRAND TOTAL = 905/1500, RESULT: FIRST CLASS ORDN. 1 MARKS: MANGAL , 71200929к , S80053075 , РІСТ , S80053075 S80053075 KONDHAWALE PRATIBHA MHATARABA PP 100 40 47 P C 11. ENGINEERING MATHEMATICS III PP 100 40 40 P 01. SIGNAL AND SYSTEMS OR 50 20 24 P C 25 10 20 P 02. SIGNAL AND SYSTEMS 12. ENGINEERING MATHEMATICS III TW 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 P 48 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 27 P C 50 20 27 P 14. INTEGRATED CIRCUITS APPLICATIONS PR 100 40 21 F PP 100 40 05. NETWORK ANALYSIS 15. ELECTROMAGNETIC 51 P 25 10 100 40 40 P C 16. ELECTROMAGNETIC 06. DIGITAL LOGIC DESIGN PP TW 21 P 50 20 25 P C PP 100 40 07. DIGITAL LOGIC DESIGN PR 17. DATA STRUCTURES 56 P 100 40 40 P 50 20 35 P 08. POWER DEVICES AND MACHINES PP 18. DATA STRUCTURES 09. NETWORK AND POWER LAB. TW 50 20 39 P C 19. COMMUNICATION THEORY PP 100 40 51 P 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 38 P C 50 20 24 P 20. COMMUNICATION THEORY 50 20 40 P 21. CIRCUIT SIMULATION AND TOOLS TW GRAND TOTAL = 754/1500, RESULT: FAILS A.T.K.T. RESULT RESERVED FOR BKLG ORDN. 1 MARKS: SHUBHADA KORANNE , 71344363E , S80053076 , PICT , S80053076 S80053076 KORANNE RENUKA SUNIL 01. SIGNAL AND SYSTEMS PP 100 40 55 P C 11. ENGINEERING MATHEMATICS III PP 100 40 53 P OR 02. SIGNAL AND SYSTEMS 12. ENGINEERING MATHEMATICS III TW 50 20 28 P C 25 10 19 P 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 47 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 44 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 38 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 37 P O5. NETWORK ANALYSIS PP 100 40 40 P C 100 40 42 P 15. ELECTROMAGNETIC PP 100 40 22 P 06. DIGITAL LOGIC DESIGN 54 P C TW 25 10 16. ELECTROMAGNETIC 07. DIGITAL LOGIC DESIGN 50 20 35 P C PP 100 40 65 P PR 17. DATA STRUCTURES 08. POWER DEVICES AND MACHINES PP 100 40 49 P C 18. DATA STRUCTURES PR 50 20 40 P 09. NETWORK AND POWER LAB. TW 100 40 50 P 50 20 40 P C 19. COMMUNICATION THEORY 50 20 37 P C 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 40 P 20. COMMUNICATION THEORY OR 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 37 P GRAND TOTAL = 872/1500, RESULT: HIGHER SECOND CLASS ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71200932K , S80053077 , PICT , S80053077 S80053077 KOTHARI GAURAO RAJENDRA VIJAYA 11. ENGINEERING MATHEMATICS III PP 100 40 74 P 01. SIGNAL AND SYSTEMS PP 100 40 48 P C 50 20 39 P C 25 10 20 P 02. SIGNAL AND SYSTEMS 12. ENGINEERING MATHEMATICS III TW 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 48 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 41 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 36 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 33 P 05. NETWORK ANALYSIS 100 40 42 P C 15. ELECTROMAGNETIC 100 40 79 P PP 100 40 54 P C 25 10 21 P 06. DIGITAL LOGIC DESIGN 16. ELECTROMAGNETIC TW 07. DIGITAL LOGIC DESIGN 50 20 32 P C PP 100 40 78 P PR 17. DATA STRUCTURES 08. POWER DEVICES AND MACHINES PP 100 40 53 P C 50 20 18. DATA STRUCTURES 38 P 09. NETWORK AND POWER LAB. TW 50 20 39 P C 100 40 61 P 19. COMMUNICATION THEORY 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 40 P C 50 20 29 P 20. COMMUNICATION THEORY 50 20 40 P 21. CIRCUIT SIMULATION AND TOOLS TW GRAND TOTAL = 945/1500, RESULT: FIRST CLASS ORDN. 1 MARKS : ALKA , 71200933H , S80053078 , PICT , S80053078 S80053078 KRITI KESARWANI 11. ENGINEERING MATHEMATICS III PP 100 40 61 P PP 100 40 58 P C 01. SIGNAL AND SYSTEMS 02. SIGNAL AND SYSTEMS OR 50 20 20 P C 12. ENGINEERING MATHEMATICS III TW 25 10 16 P 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 44 P C 40 p 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 34 P C 50 20 28 P 14. INTEGRATED CIRCUITS APPLICATIONS PR 100 40 43 P C PP 100 40 59 P 05. NETWORK ANALYSIS 15. ELECTROMAGNETIC 25 10 100 40 40 P C 16. ELECTROMAGNETIC 17 P 06. DIGITAL LOGIC DESIGN PP TW 50 20 28 P C 100 40 07. DIGITAL LOGIC DESIGN PR 17. DATA STRUCTURES PP 69 P 100 40 42 P C 08. POWER DEVICES AND MACHINES PP 18. DATA STRUCTURES 50 20 35 P 09. NETWORK AND POWER LAB. TW 50 20 38 P C 19. COMMUNICATION THEORY 100 40 60 P 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 35 P C 50 20 27 P 20. COMMUNICATION THEORY 50 20 32 P 21. CIRCUIT SIMULATION AND TOOLS TW GRAND TOTAL = 826/1500, RESULT: HIGHER SECOND CLASS ORDN. 1 MARKS: , 71200936B , S80053079 , PICT , S80053079 S80053079 KULKARNI PARIMAL SHRIKRISHNA VASUDHA 01. SIGNAL AND SYSTEMS 100 40 80 P C 11. ENGINEERING MATHEMATICS III PP 100 40 87 P OR 02. SIGNAL AND SYSTEMS 12. ENGINEERING MATHEMATICS III TW 50 20 42 P C 25 10 24 P 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 64 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 54 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 38 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 43 P 05. NETWORK ANALYSIS PP 100 40 76 P C 100 40 84 P 15. ELECTROMAGNETIC PP 100 40 06. DIGITAL LOGIC DESIGN 52 P C TW 25 10 23 P 16. ELECTROMAGNETIC 07. DIGITAL LOGIC DESIGN 50 20 40 P C PP 100 40 72 P PR 17. DATA STRUCTURES 08. POWER DEVICES AND MACHINES PP 100 40 74 P C 18. DATA STRUCTURES PR 50 20 45 P 09. NETWORK AND POWER LAB. TW 100 40 72 P 50 20 44 P C 19. COMMUNICATION THEORY 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 43 P C 20. COMMUNICATION THEORY 50 20 37 P OR 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 46 P GRAND TOTAL = 1140/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER VARSHA , 71200939G , S80053080 , PICT , S80053080 S80053080 KULKARNI VISHAL VINAYAK 11. ENGINEERING MATHEMATICS III PP 100 40 68 P 01. SIGNAL AND SYSTEMS PP 100 40 71 P C 50 20 37 P C 25 10 24 P 02. SIGNAL AND SYSTEMS 12. ENGINEERING MATHEMATICS III TW 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 51 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 56 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 35 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 36 P 05. NETWORK ANALYSIS PP 100 40 43 P C 15. ELECTROMAGNETIC 100 40 70 P PP 100 40 61 P C 25 10 23 P 06. DIGITAL LOGIC DESIGN 16. ELECTROMAGNETIC TW 100 40 83 P 07. DIGITAL LOGIC DESIGN 50 20 44 P C PP PR 17. DATA STRUCTURES 08. POWER DEVICES AND MACHINES PP 100 40 46 P C 50 20 18. DATA STRUCTURES 46 P 09. NETWORK AND POWER LAB. TW 50 20 37 P C 100 40 75 P 19. COMMUNICATION THEORY 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 36 P C 50 20 32 P 20. COMMUNICATION THEORY 50 20 45 P 21. CIRCUIT SIMULATION AND TOOLS TW GRAND TOTAL = 1019/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS: NEETA , 71200942G , S80053081 , PICT , S80053081 S80053081 LADIKKAR CHAITALI AVINASH 11. ENGINEERING MATHEMATICS III PP 100 40 66 P PP 100 40 67 P C 01. SIGNAL AND SYSTEMS 50 20 30 P C 25 10 22 P 02. SIGNAL AND SYSTEMS OR 12. ENGINEERING MATHEMATICS III TW 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 40 P C 40 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 41 P C 50 20 37 P 14. INTEGRATED CIRCUITS APPLICATIONS PR 100 40 53 P C PP 100 40 05. NETWORK ANALYSIS 15. ELECTROMAGNETIC 63 P 25 10 100 40 44 P C 16. ELECTROMAGNETIC 22 P 06. DIGITAL LOGIC DESIGN PP TW 50 20 45 P C 100 40 73 P 07. DIGITAL LOGIC DESIGN PR 17. DATA STRUCTURES PP 08. POWER DEVICES AND MACHINES PP 100 40 40 P C 50 20 18. DATA STRUCTURES 43 P 09. NETWORK AND POWER LAB. TW 50 20 44 P C 19. COMMUNICATION THEORY 100 40 43 P 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 44 P C 50 20 33 P 20. COMMUNICATION THEORY 50 20 44 P 21. CIRCUIT SIMULATION AND TOOLS TW GRAND TOTAL = 934/1500, RESULT: FIRST CLASS ORDN. 1 MARKS: , 71200944C , S80053082 , PICT , S80053082 MARIA S80053082 LASRADO VINOD WILFRED 01. SIGNAL AND SYSTEMS PP 100 40 68 P C 11. ENGINEERING MATHEMATICS III PP 100 40 88 P OR 02. SIGNAL AND SYSTEMS 50 20 45 P C 12. ENGINEERING MATHEMATICS III TW 25 10 23 P 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 70 P.C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 57 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 35 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 42 P 05. NETWORK ANALYSIS PP 100 40 83 P C 100 40 90 P 15. ELECTROMAGNETIC PP 100 40 06. DIGITAL LOGIC DESIGN 56 P C TW 25 10 23 P 16. ELECTROMAGNETIC 07. DIGITAL LOGIC DESIGN 50 20 45 P C PP 100 40 76 P PR 17. DATA STRUCTURES 08. POWER DEVICES AND MACHINES PP 100 40 79 P.C 18. DATA STRUCTURES PR 50 20 45 P 09. NETWORK AND POWER LAB. TW 100 40 79 P 50 20 46 P C 19. COMMUNICATION THEORY 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 43 P C 50 20 34 P 20. COMMUNICATION THEORY OR 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 44 P GRAND TOTAL = 1171/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER SANGITA , 71200960E , S80053086 , PICT , S80053086 S80053086 MHETRE ROSHANEE RAJENDRA 11. ENGINEERING MATHEMATICS III PP 100 40 90 P 01. SIGNAL AND SYSTEMS PP 100 40 61 P C 50 20 38 P C 25 10 21 P 02. SIGNAL AND SYSTEMS 12. ENGINEERING MATHEMATICS III TW 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 51 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 60 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 41 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 21 P 05. NETWORK ANALYSIS PP 100 40 60 P C 15. ELECTROMAGNETIC 100 40 PP 100 40 56 P C 25 10 06. DIGITAL LOGIC DESIGN 16. ELECTROMAGNETIC TW 21 p 50 20 35 P C 100 40 07. DIGITAL LOGIC DESIGN PP 65 P PR 17. DATA STRUCTURES 08. POWER DEVICES AND MACHINES PP 100 40 63 P C 50 20 18. DATA STRUCTURES 44 P 09. NETWORK AND POWER LAB. TW 50 20 38 P C 100 40 69 P 19. COMMUNICATION THEORY 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 40 P C 50 20 32 P 20. COMMUNICATION THEORY 50 20 39 P 21. CIRCUIT SIMULATION AND TOOLS TW GRAND TOTAL = 1013/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS: URMILA , 71200961C , S80053087 , PICT , S80053087 S80053087 MIHIR BIDWAI 11. ENGINEERING MATHEMATICS III PP 100 40 40 P 100 40 43 P C 01. SIGNAL AND SYSTEMS 02. SIGNAL AND SYSTEMS OR 50 20 21 P C 25 10 13 P 12. ENGINEERING MATHEMATICS III TW 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 40 P C 28 F 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 34 P C 50 20 25 P 14. INTEGRATED CIRCUITS APPLICATIONS PR 100 40 40 P C PP 100 40 05. NETWORK ANALYSIS 15. ELECTROMAGNETIC 56 P 25 10 100 40 40 P C 16. ELECTROMAGNETIC 13 P 06. DIGITAL LOGIC DESIGN PP TW 50 20 37 P C 100 40 07. DIGITAL LOGIC DESIGN PR 17. DATA STRUCTURES PP 40 P 100 40 40 P C 50 20 30 P 08. POWER DEVICES AND MACHINES PP 18. DATA STRUCTURES 09. NETWORK AND POWER LAB. TW 50 20 33 P C 19. COMMUNICATION THEORY 100 40 53 P 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 27 P C 50 20 09 F 20. COMMUNICATION THEORY 50 20 23 P 21. CIRCUIT SIMULATION AND TOOLS TW GRAND TOTAL = 685/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS: , 71200970B , S80053088 , PICT , S80053088 S80053088 MUNNA KUMAR URMILADEVI 01. SIGNAL AND SYSTEMS 100 40 71 P C 11. ENGINEERING MATHEMATICS III PP 100 40 66 P OR 02. SIGNAL AND SYSTEMS 12. ENGINEERING MATHEMATICS III TW 50 20 35 P C 25 10 18 P 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 48 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 55 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 37 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 28 P 05. NETWORK ANALYSIS PP 100 40 52 P C 100 40 70 P 15. ELECTROMAGNETIC PP 100 40 06. DIGITAL LOGIC DESIGN 45 P C TW 25 10 17 P 16. ELECTROMAGNETIC 07. DIGITAL LOGIC DESIGN 50 20 38 P C PP 100 40 71 P PR 17. DATA STRUCTURES 08. POWER DEVICES AND MACHINES PP 100 40 66 P C 18. DATA STRUCTURES PR 50 20 35 P 09. NETWORK AND POWER LAB. TW 100 40 64 P 50 20 38 P C 19. COMMUNICATION THEORY 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 39 P C 50 20 22 P 20. COMMUNICATION THEORY OR 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 32 P GRAND TOTAL = 947/1500, RESULT: FIRST CLASS ORDN. 1 MARKS: 

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71200984B , S80053092 , PICT , S80053092 S80053092 NEHA NIKUM KAUSHALYA PP 100 40 40 P C 11. ENGINEERING MATHEMATICS III PP 100 40 25 F 01. SIGNAL AND SYSTEMS 50 20 23 P 25 10 11 P 02. SIGNAL AND SYSTEMS OR 12. ENGINEERING MATHEMATICS III TW 100 40 40 P 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 19 F 03. SOLID STATES DEVICES AND CIRCUITSPP 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 20 P 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 05 F 05. NETWORK ANALYSIS 100 40 24 F 15. ELECTROMAGNETIC 100 40 40 P 100 40 25 F 25 10 10 P 06. DIGITAL LOGIC DESIGN PP 16. ELECTROMAGNETIC TW 50 20 20 P C 100 40 20 F 07. DIGITAL LOGIC DESIGN PP PR 17. DATA STRUCTURES 08. POWER DEVICES AND MACHINES PP 100 40 14 F 50 20 AA F 18. DATA STRUCTURES 09. NETWORK AND POWER LAB. TW 50 20 28 P C 100 40 12 F 19. COMMUNICATION THEORY 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 32 P C 50 20 07 F 20. COMMUNICATION THEORY 50 20 22 P 21. CIRCUIT SIMULATION AND TOOLS TW GRAND TOTAL = 437/1500, RESULT: FAILS ORDN. 1 MARKS : S80053093 NEVPURKAR MANDAR SANTOSH ANAGHA , 71200985L , S80053093 , PICT , S80053093 11. ENGINEERING MATHEMATICS III PP 100 40 67 P PP 100 40 62 P C 01. SIGNAL AND SYSTEMS 50 20 25 10 21 P 02. SIGNAL AND SYSTEMS OR 41 P C 12. ENGINEERING MATHEMATICS III TW 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 66 P C 45 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 41 P C 50 20 37 P 14. INTEGRATED CIRCUITS APPLICATIONS PR 100 40 57 P C PP 100 40 76 P 05. NETWORK ANALYSIS 15. ELECTROMAGNETIC 25 10 100 40 53 P C 16. ELECTROMAGNETIC 22 P 06. DIGITAL LOGIC DESIGN PP TW 50 20 46 P C 100 40 78 P 07. DIGITAL LOGIC DESIGN PR 17. DATA STRUCTURES PP 100 40 59 P C 50 20 44 P 08. POWER DEVICES AND MACHINES PP 18. DATA STRUCTURES 09. NETWORK AND POWER LAB. TW 50 20 44 P C 100 40 77 P 19. COMMUNICATION THEORY 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 42 P C 50 20 32 P 20. COMMUNICATION THEORY 50 20 42 P 21. CIRCUIT SIMULATION AND TOOLS TW GRAND TOTAL = 1052/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS: , 71205847」 , S80053094 , PICT , S80053094 S80053094 NIMISHA NITIN MISHRA KIRTI 01. SIGNAL AND SYSTEMS PP 100 40 77 P C 11. ENGINEERING MATHEMATICS III PP 100 40 82 P OR 02. SIGNAL AND SYSTEMS 12. ENGINEERING MATHEMATICS III TW 50 20 40 P C 25 10 19 P 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 69 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 54 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 42 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 42 P 05. NETWORK ANALYSIS PP 100 40 61 P C 100 40 75 P 15. ELECTROMAGNETIC PP 100 40 19 P 06. DIGITAL LOGIC DESIGN 42 P C TW 25 10 16. ELECTROMAGNETIC 07. DIGITAL LOGIC DESIGN 50 20 47 P C PP 100 40 69 P PR 17. DATA STRUCTURES 08. POWER DEVICES AND MACHINES PP 100 40 74 P C 18. DATA STRUCTURES PR 50 20 45 P 09. NETWORK AND POWER LAB. TW 100 40 74 P 50 20 39 P C 19. COMMUNICATION THEORY 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 38 P C 50 20 39 P 20. COMMUNICATION THEORY OR 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 35 P GRAND TOTAL = 1082/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER KRUPA , 71200991E , S80053095 , PICT , S80053095 S80053095 OMKAR NAGESHKAR 11. ENGINEERING MATHEMATICS III PP 100 40 62 P 01. SIGNAL AND SYSTEMS PP 100 40 84 P C OR 50 20 43 P C 25 10 20 P 02. SIGNAL AND SYSTEMS 12. ENGINEERING MATHEMATICS III TW 100 40 43 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 P 03. SOLID STATES DEVICES AND CIRCUITSPP 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 41 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 36 P 05. NETWORK ANALYSIS PP 100 40 56 P C 15. ELECTROMAGNETIC 100 40 74 P PP 100 40 40 P C 25 10 06. DIGITAL LOGIC DESIGN 16. ELECTROMAGNETIC TW 21 P 50 20 42 P C 100 40 07. DIGITAL LOGIC DESIGN PP 61 P PR 17. DATA STRUCTURES 08. POWER DEVICES AND MACHINES PP 100 40 58 P C 50 20 18. DATA STRUCTURES 42 P 09. NETWORK AND POWER LAB. TW 50 20 41 P C 100 40 57 P 19. COMMUNICATION THEORY 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 37 P C 50 20 40 P 20. COMMUNICATION THEORY 50 20 40 P 21. CIRCUIT SIMULATION AND TOOLS TW GRAND TOTAL = 978/1500, RESULT: FIRST CLASS ORDN. 1 MARKS: S80053096 OSWAL ARCHA AMRUT ALKA OSWAL , 71344365M , S80053096 , PICT , S80053096 PP 100 40 46 P 11. ENGINEERING MATHEMATICS III PP 100 40 17 F 01. SIGNAL AND SYSTEMS 02. SIGNAL AND SYSTEMS OR 50 20 27 P 25 10 17 P 12. ENGINEERING MATHEMATICS III TW 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 P 40 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 24 P 50 20 11 F 14. INTEGRATED CIRCUITS APPLICATIONS PR 100 40 43 P PP 100 40 05. NETWORK ANALYSIS 15. ELECTROMAGNETIC 47 P 25 10 100 40 29 F 16. ELECTROMAGNETIC 06. DIGITAL LOGIC DESIGN PP TW 17 P 50 20 30 P C 100 40 07. DIGITAL LOGIC DESIGN PR 17. DATA STRUCTURES PP 40 P 100 40 19 F 50 20 08. POWER DEVICES AND MACHINES PP 18. DATA STRUCTURES 33 P 09. NETWORK AND POWER LAB. TW 50 20 40 P C 19. COMMUNICATION THEORY 100 40 40 P 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 38 P C 50 20 26 P 20. COMMUNICATION THEORY 50 20 32 P 21. CIRCUIT SIMULATION AND TOOLS TW GRAND TOTAL = 656/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS: , 71344366K , S80053097 , PICT , S80053097 HIRA S80053097 PANMAND SUHAS DASHRATH 01. SIGNAL AND SYSTEMS PP 100 40 63 P C 11. ENGINEERING MATHEMATICS III PP 100 40 87 P OR 02. SIGNAL AND SYSTEMS 12. ENGINEERING MATHEMATICS III TW 50 20 31 P C 25 10 24 P 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 71 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 57 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 30 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 37 P 05. NETWORK ANALYSIS PP 100 40 54 P C PP 100 40 83 P 15. ELECTROMAGNETIC 100 40 06. DIGITAL LOGIC DESIGN 48 P C TW 25 10 22 P 16. ELECTROMAGNETIC 07. DIGITAL LOGIC DESIGN 50 20 28 P C PP 100 40 77 P PR 17. DATA STRUCTURES 08. POWER DEVICES AND MACHINES PP 100 40 73 P C 18. DATA STRUCTURES PR 50 20 35 P 09. NETWORK AND POWER LAB. TW 100 40 75 P 50 20 41 P C 19. COMMUNICATION THEORY 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 42 P C 50 20 41 P 20. COMMUNICATION THEORY OR 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 44 P GRAND TOTAL = 1063/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER PUSHPA , 71344367H , S80053098 , PICT , S80053098 S80053098 PARAKH BHUSHAN RAJENDRA 11. ENGINEERING MATHEMATICS III PP 100 40 66 P PP 100 40 54 P C 01. SIGNAL AND SYSTEMS or 50 20 20 P C 25 10 22 P 02. SIGNAL AND SYSTEMS 12. ENGINEERING MATHEMATICS III TW 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 40 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 62 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 28 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 40 P 05. NETWORK ANALYSIS PP 100 40 40 P C 15. ELECTROMAGNETIC 100 40 73 P PP 100 40 40 P C 25 10 21 P 06. DIGITAL LOGIC DESIGN 16. ELECTROMAGNETIC TW 07. DIGITAL LOGIC DESIGN 50 20 30 P PP 100 40 77 P PR 17. DATA STRUCTURES 08. POWER DEVICES AND MACHINES PP 100 40 54 P C 50 20 39 P 18. DATA STRUCTURES 09. NETWORK AND POWER LAB. TW 50 20 38 P C 100 40 73 P 19. COMMUNICATION THEORY 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 37 P C 50 20 35 P 20. COMMUNICATION THEORY 50 20 41 P 21. CIRCUIT SIMULATION AND TOOLS TW GRAND TOTAL = 930/1500, RESULT: FIRST CLASS ORDN. 1 MARKS : S80053099 PATEL KINNARY VIPIN VINAYA PATEL , 71344368F , S80053099 , PICT , S80053099 11. ENGINEERING MATHEMATICS III PP 100 40 90 P PP 100 40 83 P C 01. SIGNAL AND SYSTEMS 02. SIGNAL AND SYSTEMS OR 50 20 24 P C 25 10 22 P 12. ENGINEERING MATHEMATICS III TW 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 79 P.C 63 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 36 P C 50 20 33 P 14. INTEGRATED CIRCUITS APPLICATIONS PR 100 40 PP 100 40 77 P 05. NETWORK ANALYSIS 63 P C 15. ELECTROMAGNETIC 25 10 100 40 58 P C 16. ELECTROMAGNETIC 06. DIGITAL LOGIC DESIGN PP TW 21 P 50 20 39 P C 100 40 85 P 07. DIGITAL LOGIC DESIGN PR 17. DATA STRUCTURES PP 100 40 50 20 08. POWER DEVICES AND MACHINES PP 63 P C 18. DATA STRUCTURES 41 P 09. NETWORK AND POWER LAB. TW 50 20 40 P C 19. COMMUNICATION THEORY 100 40 76 P 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 39 P C 50 20 40 P 20. COMMUNICATION THEORY 50 20 43 P 21. CIRCUIT SIMULATION AND TOOLS TW GRAND TOTAL = 1115/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS: PATIL SUVARNA SAMPAT , 71344369D , S80053100 , PICT , S80053100 S80053100 PATIL CHINAR SAMPATRAO 11. ENGINEERING MATHEMATICS III PP 100 40 57 P 01. SIGNAL AND SYSTEMS PP 100 40 40 P C OR 02. SIGNAL AND SYSTEMS 12. ENGINEERING MATHEMATICS III TW 50 20 21 P C 25 10 20 P 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 40 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 53 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 27 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 38 P 05. NETWORK ANALYSIS PP 100 40 40 P C 100 40 48 P 15. ELECTROMAGNETIC PP 100 40 20 P 06. DIGITAL LOGIC DESIGN 55 P C TW 25 10 16. ELECTROMAGNETIC 07. DIGITAL LOGIC DESIGN 50 20 32 P C PP 100 40 82 P PR 17. DATA STRUCTURES 08. POWER DEVICES AND MACHINES PP 100 40 45 P C 18. DATA STRUCTURES PR 50 20 41 P 09. NETWORK AND POWER LAB. TW 100 40 50 P 50 20 34 P C 19. COMMUNICATION THEORY 50 20 32 P C 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 45 P 20. COMMUNICATION THEORY OR 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 38 P GRAND TOTAL = 858/1500, RESULT: HIGHER SECOND CLASS ORDN. 1 MARKS: 

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER NILIMA , 71201009C , S80053101 , PICT , S80053101 S80053101 PATIL SAYALI ABHAY 11. ENGINEERING MATHEMATICS III PP 100 40 93 P 01. SIGNAL AND SYSTEMS PP 100 40 54 P C 50 20 41 P C 25 10 20 P O2. SIGNAL AND SYSTEMS OR 12. ENGINEERING MATHEMATICS III TW 100 40 55 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 56 P 03. SOLID STATES DEVICES AND CIRCUITSPP 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 36 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 37 P 05. NETWORK ANALYSIS PP 100 40 52 P C 15. ELECTROMAGNETIC 100 40 PP 100 40 45 P C 25 10 06. DIGITAL LOGIC DESIGN 16. ELECTROMAGNETIC TW 21 P 100 40 79 P 07. DIGITAL LOGIC DESIGN 50 20 44 P C PP PR 17. DATA STRUCTURES 08. POWER DEVICES AND MACHINES PP 100 40 50 P C 50 20 42 P 18. DATA STRUCTURES 09. NETWORK AND POWER LAB. TW 50 20 42 P C 100 40 77 P 19. COMMUNICATION THEORY 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 39 P C 50 20 37 P 20. COMMUNICATION THEORY 50 20 40 P 21. CIRCUIT SIMULATION AND TOOLS TW GRAND TOTAL = 1027/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS: S80053102 PATIL TUSHAR TUKARAM KAMAL , 71201010G , S80053102 , PICT , S80053102 11. ENGINEERING MATHEMATICS III PP 100 40 99 P PP 100 40 62 P C 01. SIGNAL AND SYSTEMS OR 50 20 43 P C 25 10 23 P 02. SIGNAL AND SYSTEMS 12. ENGINEERING MATHEMATICS III TW 66 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 51 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 39 P C 50 20 40 P 14. INTEGRATED CIRCUITS APPLICATIONS PR 100 40 62 P C PP 100 40 05. NETWORK ANALYSIS 15. ELECTROMAGNETIC 88 P 25 10 100 40 64 P 16. ELECTROMAGNETIC 06. DIGITAL LOGIC DESIGN PP TW 22 P 50 20 44 P C 100 40 76 P 07. DIGITAL LOGIC DESIGN PR 17. DATA STRUCTURES PP 100 40 59 P C 50 20 40 P 08. POWER DEVICES AND MACHINES PP 18. DATA STRUCTURES 09. NETWORK AND POWER LAB. TW 50 20 42 P C 19. COMMUNICATION THEORY 100 40 73 P 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 42 P C 50 20 35 P 20. COMMUNICATION THEORY 50 20 44 P 21. CIRCUIT SIMULATION AND TOOLS TW GRAND TOTAL = 1114/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS: , 71201012C , S80053103 , PICT , S80053103 S80053103 PAWAR GAURAV GURUNATH JYOTSNA 01. SIGNAL AND SYSTEMS PP 100 40 55 P C 11. ENGINEERING MATHEMATICS III PP 100 40 70 P OR 02. SIGNAL AND SYSTEMS 50 20 23 P C 12. ENGINEERING MATHEMATICS III TW 25 10 21 P 100 40 57 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 P 03. SOLID STATES DEVICES AND CIRCUITSPP 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 25 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 28 P 05. NETWORK ANALYSIS PP 100 40 42 P C 100 40 63 P 15. ELECTROMAGNETIC PP 100 40 06. DIGITAL LOGIC DESIGN 44 P C 16. ELECTROMAGNETIC TW 25 10 21 P 07. DIGITAL LOGIC DESIGN 50 20 40 P C PP 100 40 63 P PR 17. DATA STRUCTURES 08. POWER DEVICES AND MACHINES PP 100 40 54 P C 18. DATA STRUCTURES PR 50 20 25 P 09. NETWORK AND POWER LAB. TW 100 40 57 P 50 20 41 P C 19. COMMUNICATION THEORY 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 39 P C 50 20 32 P 20. COMMUNICATION THEORY OR 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 41 P GRAND TOTAL = 881/1500, RESULT: HIGHER SECOND CLASS ORDN. 1 MARKS: 

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71201013M , S80053104 , PICT , S80053104 S80053104 PHADKE ASHWINI SHEKHAR ANJALI 11. ENGINEERING MATHEMATICS III PP 100 40 63 P PP 100 40 42 P C 01. SIGNAL AND SYSTEMS OR 50 20 33 P C 25 10 19 P 02. SIGNAL AND SYSTEMS 12. ENGINEERING MATHEMATICS III TW 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 40 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 41 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 30 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 34 P 05. NETWORK ANALYSIS PP 100 40 44 P C 15. ELECTROMAGNETIC 100 40 68 P PP 100 40 40 P C 25 10 18 P 06. DIGITAL LOGIC DESIGN 16. ELECTROMAGNETIC TW 100 40 61 P 07. DIGITAL LOGIC DESIGN 50 20 35 P C PP PR 17. DATA STRUCTURES 08. POWER DEVICES AND MACHINES PP 100 40 40 P C 50 20 38 P 18. DATA STRUCTURES 09. NETWORK AND POWER LAB. TW 50 20 40 P C 100 40 55 P 19. COMMUNICATION THEORY 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 42 P C 50 20 28 P 20. COMMUNICATION THEORY 50 20 35 P 21. CIRCUIT SIMULATION AND TOOLS TW GRAND TOTAL = 846/1500, RESULT: HIGHER SECOND CLASS ORDN. 1 MARKS : S80053105 PRANITA PRAMOD GHOLE SANGITA , 71201020D , S80053105 , PICT , S80053105 11. ENGINEERING MATHEMATICS III PP 100 40 93 P PP 100 40 89 P C 01. SIGNAL AND SYSTEMS or 50 20 25 10 21 P 02. SIGNAL AND SYSTEMS 44 P C 12. ENGINEERING MATHEMATICS III TW 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 P 59 P C 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 40 P C 50 20 38 P 14. INTEGRATED CIRCUITS APPLICATIONS PR 100 40 56 P C PP 100 40 72 P 05. NETWORK ANALYSIS 15. ELECTROMAGNETIC 51 P C 25 10 100 40 16. ELECTROMAGNETIC 19 P 06. DIGITAL LOGIC DESIGN PP TW 50 20 43 P C 100 40 07. DIGITAL LOGIC DESIGN PR 17. DATA STRUCTURES PP 68 P 100 40 70 P C 50 20 36 P 08. POWER DEVICES AND MACHINES PP 18. DATA STRUCTURES 09. NETWORK AND POWER LAB. TW 50 20 40 P C 19. COMMUNICATION THEORY 100 40 57 P 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 39 P C 50 20 48 P 20. COMMUNICATION THEORY 50 20 38 P 21. CIRCUIT SIMULATION AND TOOLS TW GRAND TOTAL = 1061/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS: , 71201021B , S80053106 , PICT , S80053106 S80053106 PRASHANT KUMAR LALITA DEVI 01. SIGNAL AND SYSTEMS 100 40 55 P C 11. ENGINEERING MATHEMATICS III PP 100 40 78 P O2. SIGNAL AND SYSTEMS OR 12. ENGINEERING MATHEMATICS III TW 50 20 30 P C 25 10 19 P 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 47 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 41 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 37 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 35 P 05. NETWORK ANALYSIS PP 100 40 40 P C 100 40 59 P 15. ELECTROMAGNETIC PP 100 40 40 P C 06. DIGITAL LOGIC DESIGN TW 25 10 17 P 16. ELECTROMAGNETIC 07. DIGITAL LOGIC DESIGN 50 20 35 P C PP 100 40 53 P PR 17. DATA STRUCTURES 08. POWER DEVICES AND MACHINES PP 100 40 51 P C 18. DATA STRUCTURES PR 50 20 37 P 09. NETWORK AND POWER LAB. TW 50 20 38 P C 100 40 41 P 19. COMMUNICATION THEORY 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 38 P C 50 20 35 P 20. COMMUNICATION THEORY OR 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 32 P GRAND TOTAL = 858/1500, RESULT: HIGHER SECOND CLASS ORDN. 1 MARKS: 

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							R TECHNOLOGY, PUNE.					
NOTE: FIRST LINE : SEAT NO., NAME							REG. NO PREVIOUS SEAT NO C					
·				-	•		KS OBTAINED, P/F:PASS/FAIL, C:P	•				
S80053107 PRIYANKA BALRAJ				R	MANORAMA		, 71201024G , S80053107 ,	PICT		,	s8005	3107
01. SIGNAL AND SYSTEMS	PP	100	40	66	P C	11.	ENGINEERING MATHEMATICS III	PP :	L00	40	84	Р
02. SIGNAL AND SYSTEMS	OR	50	20	41	P C	12.	ENGINEERING MATHEMATICS III	TW	25	10	20	Р
03. SOLID STATES DEVICES AND CIRCU	JITSPP	100	40	72	P C	13.	INTEGRATED CIRCUITS APPLICATIONS	PP 1	L00	40	58	Р
04. SOLID STATES DEVICES AND CIRCU	JITSPR	50	20	35	P C	14.	INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	38	Р
05. NETWORK ANALYSIS	PP	100	40	58	P C	15.	ELECTROMAGNETIC	PP 1	L00	40	67	Р
06. DIGITAL LOGIC DESIGN	PP	100	40	81	P C	16.	ELECTROMAGNETIC	TW	25	10	20	Р
07. DIGITAL LOGIC DESIGN	PR	50	20	39	P C	17.	DATA STRUCTURES	PP 1	L00	40	76	Р
08. POWER DEVICES AND MACHINES	PP	100	40	74	P C		DATA STRUCTURES	PR	50	20	26	Р
09. NETWORK AND POWER LAB.	TW	50	20	38	P C	19.	COMMUNICATION THEORY	PP :	L00	40	66	Р
10. ELECTRONIC INSTRUMENTS AND TOO	LS TW	50	20	37	P C	20.	COMMUNICATION THEORY	OR	50	20	41	Р
						21.	CIRCUIT SIMULATION AND TOOLS	TW	50	20	40	Р
GRAND TOTAL = $1077/1500$ , RESULT: FIR	RST CLAS	S WITH	DIS	TINCT	TION							
ORDN. 1 MARKS :												
S80053108 PULEKAR ADITYA CHANDRA	SHEKHAR				ADHAVI		, 71201025E , S80053108 ,			-	S8005	
01. SIGNAL AND SYSTEMS	PP	100			PC	11.	ENGINEERING MATHEMATICS III		L00		82	
02. SIGNAL AND SYSTEMS	OR		20		PC	12.	ENGINEERING MATHEMATICS III		25		20	
03. SOLID STATES DEVICES AND CIRCU		100			PC		INTEGRATED CIRCUITS APPLICATIONS		L00		45	
04. SOLID STATES DEVICES AND CIRCU	JITSPR	50	20		PC	14.	INTEGRATED CIRCUITS APPLICATIONS		50		40	Р
05. NETWORK ANALYSIS	PP	100	-		PC	15.	ELECTROMAGNETIC		L00		67	Р
06. DIGITAL LOGIC DESIGN	PP	100	40	40	P C	16.	ELECTROMAGNETIC		25		18	Р
07. DIGITAL LOGIC DESIGN					PC		DATA STRUCTURES		L00	_	67	
08. POWER DEVICES AND MACHINES	PP	100		63			DATA STRUCTURES	PR	50	20	39	
09. NETWORK AND POWER LAB.	TW	50	20	37	P C	19.	COMMUNICATION THEORY	PP 1	L00	40	61	Р
10. ELECTRONIC INSTRUMENTS AND TOO	LS TW	50	20	37	P C	20.	COMMUNICATION THEORY	OR	50		42	
						21.	CIRCUIT SIMULATION AND TOOLS	TW	50	20	39	Р
GRAND TOTAL = $1008/1500$ , RESULT: FIR	RST CLAS	S WITH	DIS	TINCT	TION							
ORDN. 1 MARKS :												
S80053109 R GOKUL KRISHNAN					RAJESWARI		, 71201026c , s80053109 ,			-	S8005	
01. SIGNAL AND SYSTEMS	PP	100	40		P C	11.	ENGINEERING MATHEMATICS III	PP 1	L00	40	79	
02. SIGNAL AND SYSTEMS	OR	50	20	40	P C	12.	ENGINEERING MATHEMATICS III	TW	25	10	21	Р
03. SOLID STATES DEVICES AND CIRCU	JITSPP	100	40	50	P C	13.	INTEGRATED CIRCUITS APPLICATIONS	PP :	L00	40	42	Р
04. SOLID STATES DEVICES AND CIRCU	JITSPR	50	20	38	P C	14.	INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	45	Р
05. NETWORK ANALYSIS	PP	100	40	48	P C	15.	ELECTROMAGNETIC	PP 1	L00	40	76	Р
06. DIGITAL LOGIC DESIGN	PP	100	40	54	P C	16.	ELECTROMAGNETIC	TW	25	10	23	Р
07. DIGITAL LOGIC DESIGN	PR	50	20	43	P C	17.	DATA STRUCTURES	PP 1	L00	40	65	Р
08. POWER DEVICES AND MACHINES	PP	100	40	50	P C	18.	DATA STRUCTURES	PR	50	20	41	Р
09. NETWORK AND POWER LAB.	TW	50	20	41	P C	19.	COMMUNICATION THEORY	PP 1	L00	40	55	Р
10. ELECTRONIC INSTRUMENTS AND TOO	LS TW	50	20	39	P C	20.	COMMUNICATION THEORY	OR	50	20	38	Р
						21.	CIRCUIT SIMULATION AND TOOLS	TW	50	20	44	Р
GRAND TOTAL = $998/1500$ , RESULT: FIR	ST CLAS	S WITH	DIS	TINCT	TION							
ORDN. 1 MARKS :												

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER VIDYA , 71201027M , S80053110 , PICT , S80053110 S80053110 RADHIKA RAJE 11. ENGINEERING MATHEMATICS III PP 100 40 64 P 01. SIGNAL AND SYSTEMS PP 100 40 59 P C 02. SIGNAL AND SYSTEMS OR 50 20 25 P C 25 10 20 P 12. ENGINEERING MATHEMATICS III TW 100 40 52 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 55 P 03. SOLID STATES DEVICES AND CIRCUITSPP 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 35 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 27 P 05. NETWORK ANALYSIS PP 100 40 40 P C 15. ELECTROMAGNETIC 100 40 PP 100 40 40 P C 25 10 19 P 06. DIGITAL LOGIC DESIGN 16. ELECTROMAGNETIC TW 100 40 68 P 07. DIGITAL LOGIC DESIGN 50 20 37 P C PP PR 17. DATA STRUCTURES 08. POWER DEVICES AND MACHINES PP 100 40 58 P C 50 20 38 P 18. DATA STRUCTURES 09. NETWORK AND POWER LAB. TW 50 20 40 P C 100 40 76 P 19. COMMUNICATION THEORY 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 40 P C 50 20 34 P 20. COMMUNICATION THEORY 50 20 37 P 21. CIRCUIT SIMULATION AND TOOLS TW GRAND TOTAL = 933/1500, RESULT: FIRST CLASS ORDN. 1 MARKS: S80053111 RAHEGAONKAR RUSHIKESH VIJAY LALITA , 71201028к , S80053111 , РІСТ , S80053111 11. ENGINEERING MATHEMATICS III PP 100 40 90 P PP 100 40 75 P C 01. SIGNAL AND SYSTEMS OR 50 20 29 P C 25 10 22 P 02. SIGNAL AND SYSTEMS 12. ENGINEERING MATHEMATICS III TW 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 68 P C 55 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 33 P C 50 20 29 P 14. INTEGRATED CIRCUITS APPLICATIONS PR 100 40 40 P C PP 100 40 05. NETWORK ANALYSIS 15. ELECTROMAGNETIC 68 P 100 40 60 P C 16. ELECTROMAGNETIC 25 10 23 P 06. DIGITAL LOGIC DESIGN PP TW 50 20 39 P C PP 100 40 73 P 07. DIGITAL LOGIC DESIGN PR 17. DATA STRUCTURES 100 40 71 P C 50 20 36 P 08. POWER DEVICES AND MACHINES PP 18. DATA STRUCTURES 09. NETWORK AND POWER LAB. TW 50 20 41 P C 19. COMMUNICATION THEORY 100 40 82 P 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 38 P C 50 20 32 P 20. COMMUNICATION THEORY 50 20 43 P 21. CIRCUIT SIMULATION AND TOOLS TW GRAND TOTAL = 1047/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS: , 71201030M , S80053112 , PICT , S80053112 S80053112 RAHUL MAHIPATI KALE **SUVARNA** 01. SIGNAL AND SYSTEMS PP 100 40 59 P C 11. ENGINEERING MATHEMATICS III PP 100 40 75 P OR 02. SIGNAL AND SYSTEMS 12. ENGINEERING MATHEMATICS III TW 50 20 32 P C 25 10 22 P 68 P C 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 48 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 30 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 34 P PP 05. NETWORK ANALYSIS PP 100 40 40 P C 100 40 70 P 15. ELECTROMAGNETIC 100 40 53 P C 25 10 21 P 06. DIGITAL LOGIC DESIGN 16. ELECTROMAGNETIC TW 07. DIGITAL LOGIC DESIGN 50 20 42 P C PP 100 40 70 P PR 17. DATA STRUCTURES 08. POWER DEVICES AND MACHINES PP 100 40 58 P C 18. DATA STRUCTURES PR 50 20 35 P 09. NETWORK AND POWER LAB. TW 100 40 75 P 50 20 39 P C 19. COMMUNICATION THEORY 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 38 P C 50 20 30 P 20. COMMUNICATION THEORY OR 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 42 P GRAND TOTAL = 981/1500, RESULT: FIRST CLASS ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71201034D , S80053113 , PICT , S80053113 S80053113 RATHOD ABHIJIT KALURAM ALKA 11. ENGINEERING MATHEMATICS III PP 100 40 63 P PP 100 40 49 P C 01. SIGNAL AND SYSTEMS or 50 20 23 P C 25 10 19 P 02. SIGNAL AND SYSTEMS 12. ENGINEERING MATHEMATICS III TW 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 40 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 33 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 32 P 05. NETWORK ANALYSIS PP 100 40 45 P 15. ELECTROMAGNETIC 100 40 42 P 100 40 56 P 25 10 20 P 06. DIGITAL LOGIC DESIGN PP 16. ELECTROMAGNETIC TW 50 20 100 40 46 P 07. DIGITAL LOGIC DESIGN 34 P C PP PR 17. DATA STRUCTURES 08. POWER DEVICES AND MACHINES PP 100 40 40 P 50 20 42 P 18. DATA STRUCTURES 09. NETWORK AND POWER LAB. TW 50 20 40 P C 100 40 40 P 19. COMMUNICATION THEORY 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 36 P C 50 20 31 P 20. COMMUNICATION THEORY 50 20 39 P 21. CIRCUIT SIMULATION AND TOOLS TW GRAND TOTAL = 810/1500, RESULT: SECOND CLASS ORDN. 1 MARKS: S80053114 RATHOD PRIYANKA NEMICHAND SHOBHA , 71201036L , S80053114 , PICT , S80053114 PP 100 40 40 P C 11. ENGINEERING MATHEMATICS III PP 100 40 63 P 01. SIGNAL AND SYSTEMS 50 20 30 P 25 10 19 P 02. SIGNAL AND SYSTEMS OR 12. ENGINEERING MATHEMATICS III TW 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 P 40 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 24 P C 50 20 22 P 14. INTEGRATED CIRCUITS APPLICATIONS PR 100 40 40 P PP 100 40 05. NETWORK ANALYSIS 15. ELECTROMAGNETIC 58 P 41 P C 25 10 100 40 16. ELECTROMAGNETIC 18 P 06. DIGITAL LOGIC DESIGN PP TW 50 20 30 P C 100 40 57 P 07. DIGITAL LOGIC DESIGN PR 17. DATA STRUCTURES PP 100 40 40 P C 50 20 08. POWER DEVICES AND MACHINES PP 18. DATA STRUCTURES 37 P 09. NETWORK AND POWER LAB. TW 50 20 37 P C 19. COMMUNICATION THEORY 100 40 40 P 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 35 P C 50 20 30 P 20. COMMUNICATION THEORY 50 20 34 P 21. CIRCUIT SIMULATION AND TOOLS TW GRAND TOTAL = 775/1500, RESULT: SECOND CLASS ORDN. 1 MARKS: , 71344370н , S80053115 , РІСТ , S80053115 S80053115 RITAPURE KIRAN KAMLAKAR SUVARNA 01. SIGNAL AND SYSTEMS PP 100 40 45 P C 11. ENGINEERING MATHEMATICS III PP 100 40 65 P OR 02. SIGNAL AND SYSTEMS 12. ENGINEERING MATHEMATICS III TW 50 20 25 P C 25 10 22 P 100 40 53 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 57 P 03. SOLID STATES DEVICES AND CIRCUITSPP 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 38 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 30 P 05. NETWORK ANALYSIS PP 100 40 40 P PP 100 40 60 P 15. ELECTROMAGNETIC 100 40 06. DIGITAL LOGIC DESIGN 45 P C 16. ELECTROMAGNETIC TW 25 10 21 P 07. DIGITAL LOGIC DESIGN 50 20 40 P C PP 100 40 66 P PR 17. DATA STRUCTURES 08. POWER DEVICES AND MACHINES PP 100 40 51 P C 18. DATA STRUCTURES PR 50 20 41 P 09. NETWORK AND POWER LAB. TW 50 20 40 P C 100 40 68 P 19. COMMUNICATION THEORY 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 38 P C 50 20 35 P 20. COMMUNICATION THEORY OR 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 38 P GRAND TOTAL = 918/1500, RESULT: FIRST CLASS ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER REKHA , 71344371F , S80053116 , PICT , S80053116 S80053116 ROHANKAR AKSHAY ANILRAO 11. ENGINEERING MATHEMATICS III PP 100 40 43 P PP 100 40 46 P C 01. SIGNAL AND SYSTEMS 02. SIGNAL AND SYSTEMS OR 50 20 23 P C 25 10 19 P 12. ENGINEERING MATHEMATICS III TW 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 40 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 34 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 28 P 05. NETWORK ANALYSIS PP 100 40 40 P 15. ELECTROMAGNETIC 100 40 40 P 100 40 42 P C 25 10 19 P 06. DIGITAL LOGIC DESIGN PP 16. ELECTROMAGNETIC TW 50 20 42 P C 100 40 57 P 07. DIGITAL LOGIC DESIGN PP PR 17. DATA STRUCTURES 08. POWER DEVICES AND MACHINES PP 100 40 46 P C 50 20 22 P 18. DATA STRUCTURES 09. NETWORK AND POWER LAB. TW 50 20 40 P C 100 40 40 P 19. COMMUNICATION THEORY 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 38 P C 50 20 35 P 20. COMMUNICATION THEORY 50 20 36 P 21. CIRCUIT SIMULATION AND TOOLS TW GRAND TOTAL = 770/1500, RESULT: SECOND CLASS ORDN. 1 MARKS: S80053117 ROHIT GUPTA GEETA , 71201042E , S80053117 , PICT , S80053117 11. ENGINEERING MATHEMATICS III PP 100 40 79 P PP 100 40 55 P C 01. SIGNAL AND SYSTEMS 02. SIGNAL AND SYSTEMS 50 20 31 P 25 10 11 P OR 12. ENGINEERING MATHEMATICS III TW 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 P 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 49 P C 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 21 P C 50 20 11 F 14. INTEGRATED CIRCUITS APPLICATIONS PR 100 40 40 P C PP 100 40 05. NETWORK ANALYSIS 15. ELECTROMAGNETIC 46 P 25 10 100 40 40 P C 16. ELECTROMAGNETIC 06. DIGITAL LOGIC DESIGN PP TW 10 P 50 20 30 P C 100 40 57 P 07. DIGITAL LOGIC DESIGN PR 17. DATA STRUCTURES PP 100 40 52 P C 50 20 08. POWER DEVICES AND MACHINES PP 18. DATA STRUCTURES 05 F 09. NETWORK AND POWER LAB. TW 50 20 29 P C 19. COMMUNICATION THEORY 100 40 43 P 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 31 P C 50 20 10 F 20. COMMUNICATION THEORY 50 20 20 P 21. CIRCUIT SIMULATION AND TOOLS TW GRAND TOTAL = 710/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS: , 71201043C , S80053118 , PICT , S80053118 SUREKHA S80053118 ROKADE SUYOG SUDARSHAN 01. SIGNAL AND SYSTEMS PP 100 40 44 P C 11. ENGINEERING MATHEMATICS III PP 100 40 29 F OR 02. SIGNAL AND SYSTEMS 50 20 35 P 12. ENGINEERING MATHEMATICS III TW 25 10 18 P 40 P 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 27 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 28 P 05. NETWORK ANALYSIS PP 100 40 20 F PP 100 40 27 F 15. ELECTROMAGNETIC 100 40 20 P 06. DIGITAL LOGIC DESIGN 40 P C 16. ELECTROMAGNETIC TW 25 10 07. DIGITAL LOGIC DESIGN 50 20 30 P C PP 100 40 57 P PR 17. DATA STRUCTURES 08. POWER DEVICES AND MACHINES PP 100 40 46 P 18. DATA STRUCTURES PR 50 20 07 F 09. NETWORK AND POWER LAB. TW 50 20 39 P C 100 40 44 P 19. COMMUNICATION THEORY 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 37 P C 20. COMMUNICATION THEORY 50 20 22 P OR 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 39 P GRAND TOTAL = 689/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71201045к , S80053119 , РІСТ , S80053119 S80053119 RUTUJA S JADHAV BHARATI 11. ENGINEERING MATHEMATICS III PP 100 40 78 P PP 100 40 47 P C 01. SIGNAL AND SYSTEMS OR 50 20 32 P C 25 10 19 P 02. SIGNAL AND SYSTEMS 12. ENGINEERING MATHEMATICS III TW 100 40 46 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 51 P 03. SOLID STATES DEVICES AND CIRCUITSPP 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 34 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 34 P 05. NETWORK ANALYSIS PP 100 40 40 P C 15. ELECTROMAGNETIC 100 40 53 P PP 100 40 57 P C 25 10 18 P 06. DIGITAL LOGIC DESIGN 16. ELECTROMAGNETIC TW 07. DIGITAL LOGIC DESIGN 50 20 46 P C PP 100 40 77 P PR 17. DATA STRUCTURES 08. POWER DEVICES AND MACHINES PP 100 40 46 P C 50 20 38 P 18. DATA STRUCTURES 09. NETWORK AND POWER LAB. TW 50 20 40 P C 100 40 60 P 19. COMMUNICATION THEORY 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 40 P C 50 20 23 P 20. COMMUNICATION THEORY 50 20 33 P 21. CIRCUIT SIMULATION AND TOOLS TW GRAND TOTAL = 912/1500, RESULT: FIRST CLASS ORDN. 1 MARKS: MANGAL , 71201051D , S80053120 , PICT , S80053120 S80053120 SALVE PRAJAKTA BHAUSAHEB PP 100 40 52 P C 11. ENGINEERING MATHEMATICS III PP 100 40 68 P 01. SIGNAL AND SYSTEMS 02. SIGNAL AND SYSTEMS OR 50 20 28 P C 25 10 20 P 12. ENGINEERING MATHEMATICS III TW 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 42 P 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 46 P.C 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 35 P C 50 20 21 P 14. INTEGRATED CIRCUITS APPLICATIONS PR 100 40 40 P PP 100 40 05. NETWORK ANALYSIS 15. ELECTROMAGNETIC 25 10 100 40 16. ELECTROMAGNETIC 06. DIGITAL LOGIC DESIGN PP 42 P C TW 21 P 50 20 38 P C 100 40 59 P 07. DIGITAL LOGIC DESIGN PR 17. DATA STRUCTURES PP 100 40 40 P C 50 20 08. POWER DEVICES AND MACHINES PP 18. DATA STRUCTURES 25 P 09. NETWORK AND POWER LAB. TW 50 20 40 P C 19. COMMUNICATION THEORY 100 40 61 P 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 37 P C 50 20 37 P 20. COMMUNICATION THEORY 50 20 41 P 21. CIRCUIT SIMULATION AND TOOLS TW GRAND TOTAL = 837/1500, RESULT: HIGHER SECOND CLASS ORDN. 1 MARKS : , 71201055G , S80053121 , PICT , S80053121 S80053121 SATPUTE SHIVRAJ PRAKASH VIJAYA 01. SIGNAL AND SYSTEMS PP 100 40 60 P C 11. ENGINEERING MATHEMATICS III PP 100 40 70 P OR 02. SIGNAL AND SYSTEMS 50 20 33 P C 12. ENGINEERING MATHEMATICS III TW 25 10 21 P 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 40 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 50 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 27 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 21 P 05. NETWORK ANALYSIS PP 100 40 44 P C 100 40 61 P 15. ELECTROMAGNETIC PP 100 40 20 P 06. DIGITAL LOGIC DESIGN 42 P C TW 25 10 16. ELECTROMAGNETIC 07. DIGITAL LOGIC DESIGN 50 20 37 P C PP 100 40 72 P PR 17. DATA STRUCTURES 08. POWER DEVICES AND MACHINES PP 100 40 66 P C 18. DATA STRUCTURES PR 50 20 32 P 09. NETWORK AND POWER LAB. TW 100 40 59 P 50 20 39 P C 19. COMMUNICATION THEORY 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 37 P C 20. COMMUNICATION THEORY 50 20 30 P OR 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 37 P GRAND TOTAL = 898+02/1500, RESULT: FIRST CLASS [0.2] ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER SANDHYA , 71201056E , S80053122 , PICT , S80053122 S80053122 SAURABH NANGLIA 11. ENGINEERING MATHEMATICS III PP 100 40 58 P 01. SIGNAL AND SYSTEMS PP 100 40 56 P C O2. SIGNAL AND SYSTEMS OR 50 20 23 P C 25 10 14 P 12. ENGINEERING MATHEMATICS III TW 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 53 P 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 20 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 22 P 05. NETWORK ANALYSIS PP 100 40 40 P C 15. ELECTROMAGNETIC 100 40 AA F PP 100 40 45 P C 25 10 16 P 06. DIGITAL LOGIC DESIGN 16. ELECTROMAGNETIC TW 50 20 28 P C 100 40 56 P 07. DIGITAL LOGIC DESIGN PP PR 17. DATA STRUCTURES 08. POWER DEVICES AND MACHINES PP 100 40 42 P C 50 20 23 P 18. DATA STRUCTURES 09. NETWORK AND POWER LAB. TW 50 20 34 P C 100 40 52 P 19. COMMUNICATION THEORY 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 30 P C 50 20 22 P 20. COMMUNICATION THEORY 50 20 28 P 21. CIRCUIT SIMULATION AND TOOLS TW GRAND TOTAL = 702/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS: S80053123 SAURAV KUMAR RAUSHAN DEVI , 71201057C , S80053123 , PICT , S80053123 11. ENGINEERING MATHEMATICS III PP 100 40 78 P PP 100 40 60 P C 01. SIGNAL AND SYSTEMS 02. SIGNAL AND SYSTEMS OR 50 20 35 P C 12. ENGINEERING MATHEMATICS III TW 25 10 18 P 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 49 P 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 51 PC 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 35 P C 50 20 21 P 14. INTEGRATED CIRCUITS APPLICATIONS PR 100 40 41 P C PP 100 40 05. NETWORK ANALYSIS 15. ELECTROMAGNETIC 61 P 100 40 46 P C 16. ELECTROMAGNETIC 25 10 18 P 06. DIGITAL LOGIC DESIGN PP TW 50 20 39 P C PP 100 40 72 P 07. DIGITAL LOGIC DESIGN PR 17. DATA STRUCTURES 100 40 56 P C 50 20 35 P 08. POWER DEVICES AND MACHINES PP 18. DATA STRUCTURES 09. NETWORK AND POWER LAB. TW 50 20 39 P C 19. COMMUNICATION THEORY 100 40 75 P 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 40 P C 50 20 32 P 20. COMMUNICATION THEORY 50 20 33 P 21. CIRCUIT SIMULATION AND TOOLS TW GRAND TOTAL = 934/1500, RESULT: FIRST CLASS ORDN. 1 MARKS: , 71201060C , S80053124 , PICT , S80053124 S80053124 SHAH PARIN LITESH BHAVYA 01. SIGNAL AND SYSTEMS PP 100 40 56 P C 11. ENGINEERING MATHEMATICS III PP 100 40 83 P OR 02. SIGNAL AND SYSTEMS 50 20 28 P C 12. ENGINEERING MATHEMATICS III TW 25 10 16 P 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 56 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 63 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 34 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 35 P PP 05. NETWORK ANALYSIS PP 100 40 43 P C 100 40 71 P 15. ELECTROMAGNETIC 100 40 52 P C 25 10 17 P 06. DIGITAL LOGIC DESIGN TW 16. ELECTROMAGNETIC 07. DIGITAL LOGIC DESIGN 50 20 42 P C PP 100 40 81 P PR 17. DATA STRUCTURES 08. POWER DEVICES AND MACHINES PP 100 40 63 P C 18. DATA STRUCTURES PR 50 20 33 P 09. NETWORK AND POWER LAB. TW 50 20 40 P C 100 40 72 P 19. COMMUNICATION THEORY 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 38 P C 50 20 27 P 20. COMMUNICATION THEORY OR 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 32 P GRAND TOTAL = 982/1500, RESULT: FIRST CLASS ORDN. 1 MARKS:

UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(ELECTRONICS & TELECOM.) DATE : 27 JULY 2013 CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 42 ( 436) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER S80053125 SHANTANU SHEKAR JAGTAP , 71201063H , S80053125 , РІСТ , S80053125 SHUBHANGI 01. SIGNAL AND SYSTEMS PP 100 40 56 P 02. SIGNAL AND SYSTEMS 50 20 22 P OR 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 40 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 20 P 05. NETWORK ANALYSIS 100 40 40 P

100 40 40 P

50 20 08 F

100 40 27 F

50 20 23 P C

PP

PR

10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 P C FIRST TERM TOTAL = 296/750.

08. POWER DEVICES AND MACHINES PP

09. NETWORK AND POWER LAB. TW

ORDN. 1 MARKS:

06. DIGITAL LOGIC DESIGN

07. DIGITAL LOGIC DESIGN

S80053126 SHELAR RAHUL BAPU MEENA , 71201065D , S80053126 , PICT , S80053126

01. SIGNAL AND SYSTEMS	PP	100	40	48	P C	11	ENGINEERING MATHEMATICS III	PP	100	40	61	Р
02. SIGNAL AND SYSTEMS	OR	50	20	23	P C	12	ENGINEERING MATHEMATICS III	TW	25	10	18	Р
03. SOLID STATES DEVICES AND C	IRCUITSPP	100	40	58	P C	13	INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	50	Р
04. SOLID STATES DEVICES AND C	CIRCUITSPR	50	20	35	РС	14	INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	36	Р
05. NETWORK ANALYSIS	PP	100	40	40	РС	15	ELECTROMAGNETIC	PP	100	40	72	Р
06. DIGITAL LOGIC DESIGN	PP	100	40	59	РС	16	ELECTROMAGNETIC	TW	25	10	20	Р
07. DIGITAL LOGIC DESIGN	PR	50	20	42	РС	17	DATA STRUCTURES	PP	100	40	78	Р
08. POWER DEVICES AND MACHINES	PP	100	40	51	РС	18	DATA STRUCTURES	PR	50	20	35	Р
09. NETWORK AND POWER LAB.	TW	50	20	39	РС	19	COMMUNICATION THEORY	PP	100	40	75	Р
10. ELECTRONIC INSTRUMENTS AND	TOOLS TW	50	20	39	P C	20	COMMUNICATION THEORY	OR	50	20	27	Р
						21	CIRCUIT SIMULATION AND TOOLS	TW	50	20	38	Р

GRAND TOTAL = 944/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS:

S80053127 SHELAR SAGAR PRAKASH				RE	KHA	, 71201066в , S80053127 , РІСТ , S80053127	,
01. SIGNAL AND SYSTEMS	PP	100	40	78	РС	11. ENGINEERING MATHEMATICS III PP 100 40 83 P	
02. SIGNAL AND SYSTEMS	OR	50	20	25	P C	12. ENGINEERING MATHEMATICS III TW 25 10 20 P	
03. SOLID STATES DEVICES AND CIRC	CUITSPP	100	40	66	P C	13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 P	
04. SOLID STATES DEVICES AND CIRC	CUITSPR	50	20	37	P C	14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 30 P	
05. NETWORK ANALYSIS	PP	100	40	45	Р	15. ELECTROMAGNETIC PP 100 40 80 P	
06. DIGITAL LOGIC DESIGN	PP	100	40	63	РС	16. ELECTROMAGNETIC TW 25 10 22 P	
07. DIGITAL LOGIC DESIGN	PR	50	20	44	РС	17. DATA STRUCTURES PP 100 40 72 P	
08. POWER DEVICES AND MACHINES	PP	100	40	56	РС	18. DATA STRUCTURES PR 50 20 36 P	
09. NETWORK AND POWER LAB.	TW	50	20	40	РС	19. COMMUNICATION THEORY PP 100 40 68 P	
10. ELECTRONIC INSTRUMENTS AND TO	OLS TW	50	20	41	РС	20. COMMUNICATION THEORY OR 50 20 27 P	
						21. CIRCUIT SIMULATION AND TOOLS TW 50 20 39 P	

GRAND TOTAL = 1012/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS:

DATE: 27 JULY 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 43 (437)

NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71344372D , S80053128 , PICT , S80053128 S80053128 SHELKE AMOL MARUTI SHARADA 11. ENGINEERING MATHEMATICS III PP 100 40 23 F PP 100 40 25 F 01. SIGNAL AND SYSTEMS 50 20 20 P 25 10 19 P O2. SIGNAL AND SYSTEMS OR 12. ENGINEERING MATHEMATICS III TW 100 40 25 F 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 28 F 03. SOLID STATES DEVICES AND CIRCUITSPP 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 25 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 07 F 05. NETWORK ANALYSIS PP 100 40 24 F 15. ELECTROMAGNETIC 100 40 20 F PP 100 40 58 P 25 10 20 P 06. DIGITAL LOGIC DESIGN 16. ELECTROMAGNETIC TW 50 20 34 P C 100 40 07. DIGITAL LOGIC DESIGN PP 42 P PR 17. DATA STRUCTURES 08. POWER DEVICES AND MACHINES PP 100 40 24 F 50 20 26 P 18. DATA STRUCTURES 09. NETWORK AND POWER LAB. TW 50 20 35 P C 100 40 40 P 19. COMMUNICATION THEORY 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 32 P C 50 20 29 P 20. COMMUNICATION THEORY 50 20 38 P 21. CIRCUIT SIMULATION AND TOOLS TW GRAND TOTAL = 594/1500, RESULT: FAILS ORDN. 1 MARKS: S80053129 SHEP VISHWAJIT CHANDRAKANT URMILA , 71201068J , S80053129 , PICT , S80053129 11. ENGINEERING MATHEMATICS III PP 100 40 62 P 100 40 54 P C 01. SIGNAL AND SYSTEMS 50 20 22 P C 25 10 16 P 02. SIGNAL AND SYSTEMS OR 12. ENGINEERING MATHEMATICS III TW 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 P 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 40 P C 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 22 P C 50 20 08 F 14. INTEGRATED CIRCUITS APPLICATIONS PR 100 40 41 P PP 100 40 51 P 05. NETWORK ANALYSIS 15. ELECTROMAGNETIC 25 10 100 40 54 P 16. ELECTROMAGNETIC 17 P 06. DIGITAL LOGIC DESIGN PP TW 50 20 32 P C 100 40 07. DIGITAL LOGIC DESIGN PR 17. DATA STRUCTURES PP 61 P 100 40 40 P C 50 20 08. POWER DEVICES AND MACHINES PP 18. DATA STRUCTURES 06 F 09. NETWORK AND POWER LAB. TW 50 20 35 P C 19. COMMUNICATION THEORY 100 40 48 P 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 31 P C 50 20 30 P 20. COMMUNICATION THEORY 50 20 32 P 21. CIRCUIT SIMULATION AND TOOLS TW GRAND TOTAL = 742/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS: , 71201071J , S80053130 , PICT , S80053130 SANGEETA S80053130 SHINDE SANIKA HANMANT 01. SIGNAL AND SYSTEMS PP 100 40 88 P C 11. ENGINEERING MATHEMATICS III PP 100 40 91 P OR 02. SIGNAL AND SYSTEMS 12. ENGINEERING MATHEMATICS III TW 50 20 40 P C 25 10 21 P 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 74 P.C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 44 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 38 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 33 P 05. NETWORK ANALYSIS PP 100 40 42 P C PP 100 40 55 P 15. ELECTROMAGNETIC 100 40 20 P 06. DIGITAL LOGIC DESIGN 56 P C TW 25 10 16. ELECTROMAGNETIC 07. DIGITAL LOGIC DESIGN 50 20 36 P C PP 100 40 69 P PR 17. DATA STRUCTURES 08. POWER DEVICES AND MACHINES PP 100 40 57 P C 18. DATA STRUCTURES PR 50 20 43 P 09. NETWORK AND POWER LAB. TW 50 20 43 P C 100 40 60 P 19. COMMUNICATION THEORY 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 43 P C 50 20 30 P 20. COMMUNICATION THEORY OR 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 31 P GRAND TOTAL = 1014/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS:

DATE: 27 JULY 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 44 ( 438)

NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71201072G , S80053131 , PICT , S80053131 S80053131 SHINDE SIDDESH BALASAHEB ASHA PP 100 40 73 P C 11. ENGINEERING MATHEMATICS III PP 100 40 76 P 01. SIGNAL AND SYSTEMS 50 20 45 P C 25 10 22 P 02. SIGNAL AND SYSTEMS 12. ENGINEERING MATHEMATICS III TW 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 63 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 59 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 44 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 42 P 05. NETWORK ANALYSIS PP 100 40 67 P C 15. ELECTROMAGNETIC 100 40 80 P PP 100 40 48 P C 25 10 23 P 06. DIGITAL LOGIC DESIGN 16. ELECTROMAGNETIC TW 50 20 42 P C 100 40 86 P 07. DIGITAL LOGIC DESIGN PP PR 17. DATA STRUCTURES 08. POWER DEVICES AND MACHINES PP 100 40 64 P C 50 20 40 P 18. DATA STRUCTURES 09. NETWORK AND POWER LAB. TW 50 20 43 P C 100 40 53 P 19. COMMUNICATION THEORY 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 43 P C 50 20 40 P 20. COMMUNICATION THEORY 50 20 41 P 21. CIRCUIT SIMULATION AND TOOLS TW GRAND TOTAL = 1094/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS: S80053132 SHINDE VAISHALI GULAB SHOBHA , 71344373B , S80053132 , PICT , S80053132 11. ENGINEERING MATHEMATICS III PP 100 40 65 P PP 100 40 64 P C 01. SIGNAL AND SYSTEMS OR 50 20 27 P C 25 10 20 P 02. SIGNAL AND SYSTEMS 12. ENGINEERING MATHEMATICS III TW 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 54 P C 50 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 28 P C 50 20 32 P 14. INTEGRATED CIRCUITS APPLICATIONS PR 100 40 40 P C PP 100 40 05. NETWORK ANALYSIS 15. ELECTROMAGNETIC 63 P 53 P C 25 10 100 40 16. ELECTROMAGNETIC 20 P 06. DIGITAL LOGIC DESIGN PP TW 50 20 39 P C PP 100 40 77 P 07. DIGITAL LOGIC DESIGN PR 17. DATA STRUCTURES 100 40 50 20 36 P 08. POWER DEVICES AND MACHINES PP 44 P C 18. DATA STRUCTURES 100 40 09. NETWORK AND POWER LAB. TW 50 20 39 P C 19. COMMUNICATION THEORY 62 P 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 36 P C 50 20 30 P 20. COMMUNICATION THEORY 50 20 38 P 21. CIRCUIT SIMULATION AND TOOLS TW GRAND TOTAL = 917/1500, RESULT: FIRST CLASS ORDN. 1 MARKS: , 71201079D , S80053133 , PICT , S80053133 SEEMA S80053133 SONAWANE YASHWANT DEEPAK 01. SIGNAL AND SYSTEMS PP 100 40 43 P C 11. ENGINEERING MATHEMATICS III PP 100 40 62 P OR 02. SIGNAL AND SYSTEMS 50 20 25 P C 12. ENGINEERING MATHEMATICS III TW 25 10 17 P 41 P C 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 26 F 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 20 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 38 P 05. NETWORK ANALYSIS PP 100 40 40 P PP 100 40 53 P 15. ELECTROMAGNETIC 100 40 06. DIGITAL LOGIC DESIGN 40 P C 16. ELECTROMAGNETIC TW 25 10 14 P 07. DIGITAL LOGIC DESIGN 50 20 40 P C PP 100 40 PR 17. DATA STRUCTURES 58 P 08. POWER DEVICES AND MACHINES PP 100 40 40 P C 18. DATA STRUCTURES PR 50 20 31 P 09. NETWORK AND POWER LAB. TW 50 20 31 P C 100 40 61 P 19. COMMUNICATION THEORY 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 33 P C 20. COMMUNICATION THEORY 50 20 27 P OR 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 21 P GRAND TOTAL = 761/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS:

DATE: 27 JULY 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 45 (439)

NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER NALINI , 71344374L , S80053134 , PICT , S80053134 S80053134 SORATE TEJASWI TANAJI 11. ENGINEERING MATHEMATICS III PP 100 40 66 P PP 100 40 52 P C 01. SIGNAL AND SYSTEMS OR 50 20 25 P C 25 10 20 P 02. SIGNAL AND SYSTEMS 12. ENGINEERING MATHEMATICS III TW 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 40 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 51 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 30 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 30 P 05. NETWORK ANALYSIS PP 100 40 40 P C 15. ELECTROMAGNETIC 100 40 47 P PP 100 40 46 P C 25 10 20 P 06. DIGITAL LOGIC DESIGN 16. ELECTROMAGNETIC TW 50 20 39 P C 100 40 07. DIGITAL LOGIC DESIGN PP 64 P PR 17. DATA STRUCTURES 08. POWER DEVICES AND MACHINES PP 100 40 58 P C 50 20 18. DATA STRUCTURES 35 P 09. NETWORK AND POWER LAB. TW 50 20 40 P C 100 40 70 P 19. COMMUNICATION THEORY 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 38 P C 50 20 36 P 20. COMMUNICATION THEORY 50 20 39 P 21. CIRCUIT SIMULATION AND TOOLS TW GRAND TOTAL = 886/1500, RESULT: HIGHER SECOND CLASS ORDN. 1 MARKS: NISHA , 71201080н , S80053135 , РІСТ , S80053135 S80053135 SRISHTI GANJOO 11. ENGINEERING MATHEMATICS III PP 100 40 73 P PP 100 40 62 P C 01. SIGNAL AND SYSTEMS 02. SIGNAL AND SYSTEMS OR 50 20 35 P C 25 10 17 P 12. ENGINEERING MATHEMATICS III TW 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 56 P C 51 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 44 P C 50 20 32 P 14. INTEGRATED CIRCUITS APPLICATIONS PR 100 40 40 P C PP 100 40 05. NETWORK ANALYSIS 15. ELECTROMAGNETIC 68 P 25 10 100 40 54 P C 16. ELECTROMAGNETIC 06. DIGITAL LOGIC DESIGN PP TW 18 P 50 20 40 P C 100 40 78 P 07. DIGITAL LOGIC DESIGN PR 17. DATA STRUCTURES PP 08. POWER DEVICES AND MACHINES PP 100 40 44 P C 50 20 39 P 18. DATA STRUCTURES 09. NETWORK AND POWER LAB. TW 50 20 38 P C 19. COMMUNICATION THEORY 100 40 65 P 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 36 P C 50 20 41 P 20. COMMUNICATION THEORY 50 20 34 P 21. CIRCUIT SIMULATION AND TOOLS TW GRAND TOTAL = 965/1500, RESULT: FIRST CLASS ORDN. 1 MARKS: SHAKUNTALA DEVI , 71201085J , S80053136 , PICT , S80053136 S80053136 SUSHIL KUMAR 01. SIGNAL AND SYSTEMS PP 100 40 51 P C 11. ENGINEERING MATHEMATICS III PP 100 40 79 P O2. SIGNAL AND SYSTEMS OR 12. ENGINEERING MATHEMATICS III TW 50 20 33 P C 25 10 19 P 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 51 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 20 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 25 P PP 05. NETWORK ANALYSIS PP 100 40 59 P C 100 40 72 P 15. ELECTROMAGNETIC 100 40 19 P 06. DIGITAL LOGIC DESIGN 45 P C TW 25 10 16. ELECTROMAGNETIC 07. DIGITAL LOGIC DESIGN 50 20 30 P C PP 100 40 48 P PR 17. DATA STRUCTURES 08. POWER DEVICES AND MACHINES PP 100 40 46 P C 18. DATA STRUCTURES PR 50 20 27 P 09. NETWORK AND POWER LAB. TW 50 20 37 P C 100 40 66 P 19. COMMUNICATION THEORY 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 36 P C 50 20 27 P 20. COMMUNICATION THEORY OR 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 36 P GRAND TOTAL = 866/1500, RESULT: HIGHER SECOND CLASS ORDN. 1 MARKS: 

DATE: 27 JULY 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 46 (440)

DATE . 27 JULY 2013	CENT	KE . P	UNE	TN211	LIUIE	OF COMP	UIE	R TECHNOLOGY, PUNE.	PAGE	NO.	40	( 44	+0)
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			-			•							
S80053137 TAMBE MAITREYEE MILIND					RUDULA			, 71201090E , S80053137 ,				S80053	
01. SIGNAL AND SYSTEMS	PP	100	40	69	РС		11.	ENGINEERING MATHEMATICS III		100	-	86	
02. SIGNAL AND SYSTEMS	OR	50	20	40	РС		12.	ENGINEERING MATHEMATICS III	TW	25	10	23	Р
03. SOLID STATES DEVICES AND CIRCU	ITSPP	100	40	74	РС		13.	INTEGRATED CIRCUITS APPLICATIONS		100	40	40	Р
04. SOLID STATES DEVICES AND CIRCU	ITSPR	50	20	45	РС		14.	INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	36	Р
05. NETWORK ANALYSIS	PP	100	40	56	РС		15.	ELECTROMAGNETIC	PP	100	40	72	Р
06. DIGITAL LOGIC DESIGN	PP	100	40	54	РС		16.	ELECTROMAGNETIC	TW	25	10	24	Р
07. DIGITAL LOGIC DESIGN	PR	50	20	42	РС		17.	DATA STRUCTURES	PP	100	40	73	Р
08. POWER DEVICES AND MACHINES	PP	100	40	49	РС		18.	DATA STRUCTURES	PR	50	20	45	Р
09. NETWORK AND POWER LAB.	TW	50	20	45	РС		19.	COMMUNICATION THEORY	PP	100	40	69	Р
10. ELECTRONIC INSTRUMENTS AND TOO	LS TW	50	20	45	РС		20.	COMMUNICATION THEORY	OR	50	20	44	Р
							21.	CIRCUIT SIMULATION AND TOOLS	TW	50	20	45	Р
GRAND TOTAL = 1076/1500, RESULT: FIR	ST CLAS	S WITH	DIS	TINCT	TION								
ORDN. 1 MARKS :													
S80053138 TANDEL PRASAD SADANAND				SN	NEHA			, 71201092M , S80053138 ,	PICT	-	,	s80053	3138
01. SIGNAL AND SYSTEMS	PP	100	40	76	РС		11.	ENGINEERING MATHEMATICS III	PP	100	40	96	Р
02. SIGNAL AND SYSTEMS	OR	50	20	28	РС		12.	ENGINEERING MATHEMATICS III	TW	25	10	20	Р
03. SOLID STATES DEVICES AND CIRCU	ITSPP	100	40	71	РС		13.	INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	59	Р
04. SOLID STATES DEVICES AND CIRCU	ITSPR	50	20	30	РС		14.	INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	42	Р
05. NETWORK ANALYSIS	PP	100	40	55	РС		15.	ELECTROMAGNETIC	PP	100	40	63	Р
06. DIGITAL LOGIC DESIGN	PP	100	40	60	РС		16.	ELECTROMAGNETIC	TW	25	10	21	Р
07. DIGITAL LOGIC DESIGN	PR	50	20	42	РС		17.	DATA STRUCTURES	PP	100	40	78	Р
08. POWER DEVICES AND MACHINES	PP	100	40	72	РС		18.	DATA STRUCTURES	PR	50	20	31	Р
09. NETWORK AND POWER LAB.	TW	50	20	41	РС		19.	COMMUNICATION THEORY	PP	100	40	87	Р
10. ELECTRONIC INSTRUMENTS AND TOO	LS TW	50	20	41	РС		20.	COMMUNICATION THEORY	OR	50	20	42	Р
							21.	CIRCUIT SIMULATION AND TOOLS	TW	50	20	38	Р
GRAND TOTAL = $1093/1500$ , RESULT: FIR	ST CLAS	S WITH	DIS	TINCT	TION								
ORDN. 1 MARKS :													
S80053139 TATHE SAKET NANDKISHOR				ME	ENA			, 71201093к , s80053139 ,	PICT	-	,	s80053	3139
01. SIGNAL AND SYSTEMS	PP	100	40	54	РС		11.	ENGINEERING MATHEMATICS III	PP	100	40	70	Р
02. SIGNAL AND SYSTEMS	OR	50	20	26	РС		12.	ENGINEERING MATHEMATICS III	TW	25	10	16	Р
03. SOLID STATES DEVICES AND CIRCU	ITSPP	100	40	57	РС		13.	INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	49	Р
04. SOLID STATES DEVICES AND CIRCU	ITSPR	50	20	20	РС		14.	INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	36	Р
05. NETWORK ANALYSIS	PP	100	40	47	Р		15.	ELECTROMAGNETIC	PP	100	40	52	Р
06. DIGITAL LOGIC DESIGN	PP	100	40	40	РС		16.	ELECTROMAGNETIC	TW	25	10	15	Р
07. DIGITAL LOGIC DESIGN	PR	50	20	39	РС		17.	DATA STRUCTURES	PP	100	40	69	Р
08. POWER DEVICES AND MACHINES	PP	100	40		РС		18.	DATA STRUCTURES	PR	50	20	29	
09. NETWORK AND POWER LAB.	TW	50	20		РС		19.	COMMUNICATION THEORY	PP	100	40	55	Р
10. ELECTRONIC INSTRUMENTS AND TOO	LS TW	50	20	37	РС		20.	COMMUNICATION THEORY	OR		20	27	
							21.	CIRCUIT SIMULATION AND TOOLS	TW	50	20	28	Р
GRAND TOTAL = $856/1500$ , RESULT: HIG	HER SEC	OND CL	.ASS										
ORDN. 1 MARKS :													

DATE: 27 JULY 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 47 ( 441)

NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER SWATI , 71201096D , S80053140 , PICT , S80053140 S80053140 TIKHE SHIRIN ABHAY 11. ENGINEERING MATHEMATICS III PP 100 40 86 P 01. SIGNAL AND SYSTEMS PP 100 40 66 P C 50 20 24 P C 25 10 17 P 02. SIGNAL AND SYSTEMS OR 12. ENGINEERING MATHEMATICS III TW 100 40 40 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 P 03. SOLID STATES DEVICES AND CIRCUITSPP 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 32 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 34 P 05. NETWORK ANALYSIS PP 100 40 47 P C 15. ELECTROMAGNETIC 100 40 66 P PP 100 40 40 P C 25 10 18 P 06. DIGITAL LOGIC DESIGN 16. ELECTROMAGNETIC TW 50 20 42 P C 100 40 07. DIGITAL LOGIC DESIGN PP 61 P PR 17. DATA STRUCTURES 08. POWER DEVICES AND MACHINES PP 100 40 52 P 50 20 37 P 18. DATA STRUCTURES 09. NETWORK AND POWER LAB. TW 50 20 41 P C 100 40 70 P 19. COMMUNICATION THEORY 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 38 P C 50 20 27 P 20. COMMUNICATION THEORY 50 20 36 P 21. CIRCUIT SIMULATION AND TOOLS TW GRAND TOTAL = 914/1500, RESULT: FIRST CLASS ORDN. 1 MARKS: SANJIVANI , 71201098L , S80053141 , PICT , S80053141 S80053141 TUPE NITIN BABASAHEB 11. ENGINEERING MATHEMATICS III PP 100 40 21 F PP 100 40 52 P C 01. SIGNAL AND SYSTEMS 50 20 21 P C 25 10 19 P 02. SIGNAL AND SYSTEMS OR 12. ENGINEERING MATHEMATICS III TW 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 P 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 47 P.C 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 22 P C 50 20 13 F 14. INTEGRATED CIRCUITS APPLICATIONS PR 100 40 40 P C PP 100 40 57 P 05. NETWORK ANALYSIS 15. ELECTROMAGNETIC 100 40 50 P C 16. ELECTROMAGNETIC 25 10 19 P 06. DIGITAL LOGIC DESIGN PP TW 50 20 38 P 100 40 AA F 07. DIGITAL LOGIC DESIGN PR 17. DATA STRUCTURES PP 100 40 40 P C 50 20 AA F 08. POWER DEVICES AND MACHINES PP 18. DATA STRUCTURES 09. NETWORK AND POWER LAB. TW 50 20 37 P C 19. COMMUNICATION THEORY 100 40 40 P 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 41 P C 50 20 29 P 20. COMMUNICATION THEORY 50 20 27 P 21. CIRCUIT SIMULATION AND TOOLS TW GRAND TOTAL = 653/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS: , 71344375」 , S80053142 , PICT , S80053142 LAXMI S80053142 UNDE MADHURI BALASAHEB 01. SIGNAL AND SYSTEMS PP 100 40 40 P C 11. ENGINEERING MATHEMATICS III PP 100 40 P OR 02. SIGNAL AND SYSTEMS 50 20 21 P 12. ENGINEERING MATHEMATICS III TW 25 10 17 P 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 54 P 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 21 PC 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 25 P 05. NETWORK ANALYSIS PP 100 40 40 P PP 100 40 41 P 15. ELECTROMAGNETIC 100 40 06. DIGITAL LOGIC DESIGN 58 P 16. ELECTROMAGNETIC TW 25 10 18 P 07. DIGITAL LOGIC DESIGN 50 20 25 P PP 100 40 40 P PR 17. DATA STRUCTURES 08. POWER DEVICES AND MACHINES PP 100 40 46 P 18. DATA STRUCTURES PR 50 20 22 P 09. NETWORK AND POWER LAB. TW 50 20 39 P C 100 40 40 P 19. COMMUNICATION THEORY 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 38 P C 20. COMMUNICATION THEORY 50 20 27 P OR 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 31 P GRAND TOTAL = 723/1500, RESULT: PASS CLASS ORDN. 1 MARKS:

DATE: 27 JULY 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 48 ( 442)

NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER MANISHA , 71201101D , S80053143 , PICT , S80053143 S80053143 UPADHYE VEDA UDAY 11. ENGINEERING MATHEMATICS III PP 100 40 90 P 01. SIGNAL AND SYSTEMS PP 100 40 64 P C 50 20 35 P C 25 10 19 P 02. SIGNAL AND SYSTEMS OR 12. ENGINEERING MATHEMATICS III TW 100 40 62 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 45 P 03. SOLID STATES DEVICES AND CIRCUITSPP 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 36 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 45 P 05. NETWORK ANALYSIS PP 100 40 77 P C 15. ELECTROMAGNETIC 100 40 75 P PP 100 40 60 P C 25 10 21 P 06. DIGITAL LOGIC DESIGN 16. ELECTROMAGNETIC TW 07. DIGITAL LOGIC DESIGN 50 20 36 P C PP 100 40 71 P PR 17. DATA STRUCTURES 08. POWER DEVICES AND MACHINES PP 100 40 49 P C 50 20 39 P 18. DATA STRUCTURES 09. NETWORK AND POWER LAB. TW 50 20 39 P C 100 40 88 P 19. COMMUNICATION THEORY 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 38 P C 50 20 30 P 20. COMMUNICATION THEORY 50 20 39 P 21. CIRCUIT SIMULATION AND TOOLS TW GRAND TOTAL = 1058/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS: , 71201102B , S80053144 , PICT , S80053144 S80053144 USTAD SABA ABDUL SHAKOOR ATIYA 11. ENGINEERING MATHEMATICS III PP 100 40 97 P PP 100 40 75 P C 01. SIGNAL AND SYSTEMS 50 20 43 P C 25 10 23 P 02. SIGNAL AND SYSTEMS OR 12. ENGINEERING MATHEMATICS III TW 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 76 P C 52 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 42 P C 50 20 35 P 14. INTEGRATED CIRCUITS APPLICATIONS PR 100 40 PP 100 40 05. NETWORK ANALYSIS 66 P C 15. ELECTROMAGNETIC 92 P 25 10 100 40 70 P C 16. ELECTROMAGNETIC 23 P 06. DIGITAL LOGIC DESIGN PP TW 50 20 38 P C 100 40 78 P 07. DIGITAL LOGIC DESIGN PR 17. DATA STRUCTURES PP 100 40 45 P C 50 20 36 P 08. POWER DEVICES AND MACHINES PP 18. DATA STRUCTURES 09. NETWORK AND POWER LAB. TW 50 20 43 P C 19. COMMUNICATION THEORY 100 40 67 P 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 44 P C 50 20 40 P 20. COMMUNICATION THEORY 50 20 44 P 21. CIRCUIT SIMULATION AND TOOLS TW GRAND TOTAL = 1129/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS: , 71201103L , S80053145 , PICT , S80053145 S80053145 VADGAONKAR MANALI ABHAY ROOPA 01. SIGNAL AND SYSTEMS PP 100 40 49 P C 11. ENGINEERING MATHEMATICS III PP 100 40 16 F OR 02. SIGNAL AND SYSTEMS 50 20 28 P C 12. ENGINEERING MATHEMATICS III TW 25 10 13 P 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 40 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 17 F 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 35 P.C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 12 F 05. NETWORK ANALYSIS PP 100 40 AA F 100 40 27 F 15. ELECTROMAGNETIC PP 100 40 25 10 15 P 06. DIGITAL LOGIC DESIGN 28 F TW 16. ELECTROMAGNETIC 07. DIGITAL LOGIC DESIGN 50 20 38 P C PP 100 40 40 P PR 17. DATA STRUCTURES 08. POWER DEVICES AND MACHINES PP 100 40 10 F 18. DATA STRUCTURES PR 50 20 36 P 09. NETWORK AND POWER LAB. TW 100 40 25 F 50 20 37 P C 19. COMMUNICATION THEORY 50 20 34 P C 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 20. COMMUNICATION THEORY 50 20 25 P OR 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 32 P GRAND TOTAL = 557/1500, RESULT: FAILS ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER SUMITRA , 71201110C , S80053146 , PICT , S80053146 S80053146 VIKRANT SINGH 11. ENGINEERING MATHEMATICS III PP 100 40 AA F 01. SIGNAL AND SYSTEMS PP 100 40 59 P C OR 50 20 30 P C 25 10 12 P 02. SIGNAL AND SYSTEMS 12. ENGINEERING MATHEMATICS III TW 100 40 51 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 24 F 03. SOLID STATES DEVICES AND CIRCUITSPP 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 20 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 24 P 05. NETWORK ANALYSIS PP 100 40 40 P C 15. ELECTROMAGNETIC 100 40 49 P 100 40 45 P 25 10 11 P 06. DIGITAL LOGIC DESIGN PP 16. ELECTROMAGNETIC TW 50 20 32 P C 100 40 22 F 07. DIGITAL LOGIC DESIGN PP PR 17. DATA STRUCTURES 08. POWER DEVICES AND MACHINES PP 100 40 43 P C 50 20 30 P 18. DATA STRUCTURES 09. NETWORK AND POWER LAB. TW 50 20 32 P C 100 40 69 P 19. COMMUNICATION THEORY 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 32 P C 50 20 10 F 20. COMMUNICATION THEORY 50 20 20 P 21. CIRCUIT SIMULATION AND TOOLS TW GRAND TOTAL = 655/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS: ASHA , 71201111M , S80053147 , PICT , S80053147 S80053147 VISHAL VATSA 11. ENGINEERING MATHEMATICS III PP 100 40 83 P PP 100 40 61 P C 01. SIGNAL AND SYSTEMS 02. SIGNAL AND SYSTEMS OR 50 20 29 P C 25 10 17 P 12. ENGINEERING MATHEMATICS III TW 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 45 P 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 48 P C 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 36 P C 50 20 40 P 14. INTEGRATED CIRCUITS APPLICATIONS PR 100 40 54 P C PP 100 40 05. NETWORK ANALYSIS 15. ELECTROMAGNETIC 68 P 25 10 100 40 48 P C 16. ELECTROMAGNETIC 06. DIGITAL LOGIC DESIGN PP TW 17 P 50 20 41 P C 100 40 07. DIGITAL LOGIC DESIGN PR 17. DATA STRUCTURES PP 62 P 100 40 40 P C 50 20 08. POWER DEVICES AND MACHINES PP 18. DATA STRUCTURES 33 P 09. NETWORK AND POWER LAB. TW 50 20 39 P C 19. COMMUNICATION THEORY 100 40 50 P 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 35 P C 50 20 20 P 20. COMMUNICATION THEORY 50 20 35 P 21. CIRCUIT SIMULATION AND TOOLS TW GRAND TOTAL = 901/1500, RESULT: FIRST CLASS ORDN. 1 MARKS: , 71201113H , S80053148 , PICT , S80053148 MEENAL S80053148 WADIKAR MUGDHA RAVINDRA 01. SIGNAL AND SYSTEMS PP 100 40 63 P C 11. ENGINEERING MATHEMATICS III PP 100 40 78 P OR 02. SIGNAL AND SYSTEMS 12. ENGINEERING MATHEMATICS III TW 50 20 31 P C 25 10 18 P 100 40 52 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 P 03. SOLID STATES DEVICES AND CIRCUITSPP 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 35 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 35 P 05. NETWORK ANALYSIS PP 100 40 46 P C PP 100 40 72 P 15. ELECTROMAGNETIC 100 40 06. DIGITAL LOGIC DESIGN 40 P C 16. ELECTROMAGNETIC TW 25 10 17 P 07. DIGITAL LOGIC DESIGN 50 20 37 P C PP 100 40 74 P PR 17. DATA STRUCTURES 08. POWER DEVICES AND MACHINES PP 100 40 46 P C 18. DATA STRUCTURES PR 50 20 39 P 09. NETWORK AND POWER LAB. TW 100 40 54 P 50 20 34 P C 19. COMMUNICATION THEORY 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 37 P C 20. COMMUNICATION THEORY 50 20 30 P OR 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 33 P GRAND TOTAL = 911/1500, RESULT: FIRST CLASS ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER SMITA , 71201119G , S80053149 , PICT , S80053149 S80053149 WARPE HRUSHEEKESH SUNIL 11. ENGINEERING MATHEMATICS III PP 100 40 78 P 01. SIGNAL AND SYSTEMS PP 100 40 59 P C or 50 20 35 P C 25 10 20 P 02. SIGNAL AND SYSTEMS 12. ENGINEERING MATHEMATICS III TW 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 66 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 47 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 32 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 34 P 05. NETWORK ANALYSIS PP 100 40 48 P C 15. ELECTROMAGNETIC 100 40 75 P PP 100 40 45 P C 25 10 19 P 06. DIGITAL LOGIC DESIGN 16. ELECTROMAGNETIC TW 50 20 40 P C 100 40 60 P 07. DIGITAL LOGIC DESIGN PP PR 17. DATA STRUCTURES 08. POWER DEVICES AND MACHINES PP 100 40 51 P C 50 20 34 P 18. DATA STRUCTURES 09. NETWORK AND POWER LAB. TW 50 20 40 P C 100 40 55 P 19. COMMUNICATION THEORY 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 41 P C 50 20 38 P 20. COMMUNICATION THEORY 50 20 33 P 21. CIRCUIT SIMULATION AND TOOLS TW GRAND TOTAL = 950/1500, RESULT: FIRST CLASS ORDN. 1 MARKS: S80053150 WAYKOS ABHIJIT JINCHANDRA NAYANA , 71201120L , S80053150 , PICT , S80053150 PP 100 40 81 P C 11. ENGINEERING MATHEMATICS III PP 100 40 86 P 01. SIGNAL AND SYSTEMS OR 50 20 38 P C 25 10 19 P 02. SIGNAL AND SYSTEMS 12. ENGINEERING MATHEMATICS III TW 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 57 P 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 58 P C 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 33 P C 50 20 38 P 14. INTEGRATED CIRCUITS APPLICATIONS PR 100 40 55 P C PP 100 40 05. NETWORK ANALYSIS 15. ELECTROMAGNETIC 67 P PP 100 40 40 P C 16. ELECTROMAGNETIC 25 10 19 P 06. DIGITAL LOGIC DESIGN TW 50 20 40 P C PP 100 40 72 P 07. DIGITAL LOGIC DESIGN PR 17. DATA STRUCTURES 100 40 55 P C 50 20 37 P 08. POWER DEVICES AND MACHINES PP 18. DATA STRUCTURES 09. NETWORK AND POWER LAB. TW 50 20 40 P C 19. COMMUNICATION THEORY 100 40 79 P 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 38 P C 50 20 35 P 20. COMMUNICATION THEORY 50 20 39 P 21. CIRCUIT SIMULATION AND TOOLS TW GRAND TOTAL = 1026/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS: , 71201123E , S80053151 , PICT , S80053151 MANJULA S80053151 YEMULA AKHIL 01. SIGNAL AND SYSTEMS 100 40 68 P C 11. ENGINEERING MATHEMATICS III PP 100 40 66 P O2. SIGNAL AND SYSTEMS OR 12. ENGINEERING MATHEMATICS III TW 50 20 21 P C 25 10 17 P 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 40 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 20 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 24 P 05. NETWORK ANALYSIS PP 100 40 40 P C PP 100 40 66 P 15. ELECTROMAGNETIC 100 40 42 P C 06. DIGITAL LOGIC DESIGN 16. ELECTROMAGNETIC TW 25 10 17 P 07. DIGITAL LOGIC DESIGN 50 20 32 P C PP 100 40 65 P PR 17. DATA STRUCTURES 08. POWER DEVICES AND MACHINES PP 100 40 40 P C 18. DATA STRUCTURES PR 50 20 36 P 09. NETWORK AND POWER LAB. TW 100 40 52 P 50 20 35 P C 19. COMMUNICATION THEORY 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 36 P C 50 20 25 P 20. COMMUNICATION THEORY OR 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 29 P GRAND TOTAL = 811/1500, RESULT: SECOND CLASS ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER S80053160 DESHMUKH ASHUTOSH VILASRAO SUNITA , 71100780C , S80053160 , PICT , S80053160 PP 100 40 49 P C 11. ENGINEERING MATHEMATICS III PP 100 40 54 PC 01. SIGNAL AND SYSTEMS 50 20 22 P C 25 10 11 P C 02. SIGNAL AND SYSTEMS OR 12. ENGINEERING MATHEMATICS III TW 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 41 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 40 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 24 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 20 P 05. NETWORK ANALYSIS PP 100 40 46 P C 15. ELECTROMAGNETIC 100 40 48 P PP 100 40 40 P C 25 10 11 P C 06. DIGITAL LOGIC DESIGN 16. ELECTROMAGNETIC TW 100 40 AA F 07. DIGITAL LOGIC DESIGN 50 20 27 P C PP PR 17. DATA STRUCTURES 08. POWER DEVICES AND MACHINES PP 100 40 40 P C 50 20 30 P C 18. DATA STRUCTURES 09. NETWORK AND POWER LAB. TW 50 20 29 P C PP 100 40 40 P C 19. COMMUNICATION THEORY 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 25 P C 50 20 20 P C 20. COMMUNICATION THEORY 50 20 24 P C 21. CIRCUIT SIMULATION AND TOOLS TW GRAND TOTAL = 641/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS: ARUNA , 71045415F , S80053161 , PICT , S80053161 S80053161 DHODI SHRADDHA MADHU 11. ENGINEERING MATHEMATICS III PP 100 40 40 PC PP 100 40 49 P C 01. SIGNAL AND SYSTEMS OR 50 20 25 P C 25 10 18 PC 02. SIGNAL AND SYSTEMS 12. ENGINEERING MATHEMATICS III TW 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 23 F 40 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 24 P C 50 20 27 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 100 40 40 P PP 100 40 40 P 05. NETWORK ANALYSIS 15. ELECTROMAGNETIC 100 40 46 P C 16. ELECTROMAGNETIC 25 10 16 PC 06. DIGITAL LOGIC DESIGN PP TW 50 20 22 P C 100 40 40 P C 07. DIGITAL LOGIC DESIGN PR 17. DATA STRUCTURES PP 100 40 17 F 50 20 39 P C 08. POWER DEVICES AND MACHINES PP 18. DATA STRUCTURES PR 09. NETWORK AND POWER LAB. TW 50 20 P C 19. COMMUNICATION THEORY 100 40 40 P C 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 25 P C 50 20 20 P C 20. COMMUNICATION THEORY 50 20 32 P C 21. CIRCUIT SIMULATION AND TOOLS TW GRAND TOTAL = 643/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS: , 71236310G , S80053162 , PICT , S80053162 S80053162 DOLAS LUBDHA RAJU SUNITA 11. ENGINEERING MATHEMATICS III PP 100 40 23 F 01. SIGNAL AND SYSTEMS PP 100 40 40 P C OR 02. SIGNAL AND SYSTEMS 50 20 22 P C 12. ENGINEERING MATHEMATICS III TW 25 10 14 PC 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 41 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 PC 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 21 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 20 P C 05. NETWORK ANALYSIS PP 100 40 40 P C PP 100 40 40 P 15. ELECTROMAGNETIC 100 40 06. DIGITAL LOGIC DESIGN 40 P C 16. ELECTROMAGNETIC TW 25 10 14 P C 07. DIGITAL LOGIC DESIGN 50 20 28 P C PP 100 40 40 P C PR 17. DATA STRUCTURES 08. POWER DEVICES AND MACHINES PP 100 40 40 P C 18. DATA STRUCTURES PR 50 20 36 P C 09. NETWORK AND POWER LAB. TW 50 20 32 P C 100 40 40 P C PP 19. COMMUNICATION THEORY 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 36 P C 50 20 29 P C 20. COMMUNICATION THEORY OR 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 30 P C GRAND TOTAL = 666/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS: 

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER DIPALI , 71100792G , S80053164 , PICT , S80053164 S80053164 GAIKWAD GAYATRI DILIP 11. ENGINEERING MATHEMATICS III PP 100 40 40 PC PP 100 40 40 P 01. SIGNAL AND SYSTEMS OR 50 20 29 P C 25 10 18 P C 02. SIGNAL AND SYSTEMS 12. ENGINEERING MATHEMATICS III TW 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 40 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 25 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 25 P C 05. NETWORK ANALYSIS PP 100 40 40 P 15. ELECTROMAGNETIC 100 40 22 F PP 100 40 47 P C 25 10 06. DIGITAL LOGIC DESIGN 16. ELECTROMAGNETIC TW 18 P C 50 20 20 P C 100 40 44 P C 07. DIGITAL LOGIC DESIGN PP PR 17. DATA STRUCTURES 08. POWER DEVICES AND MACHINES PP 100 40 23 F 50 20 41 P C 18. DATA STRUCTURES PR PP 09. NETWORK AND POWER LAB. TW 50 20 30 P C 100 40 40 P C 19. COMMUNICATION THEORY 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 31 P C 50 20 22 P C 20. COMMUNICATION THEORY 50 20 36 P C 21. CIRCUIT SIMULATION AND TOOLS TW GRAND TOTAL = 671/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS: SUREKHA , 71236312C , S80053165 , PICT , S80053165 S80053165 GENGAJE PRAJAKTA SHANTARAM 11. ENGINEERING MATHEMATICS III PP 100 40 28 F PP 100 40 49 P C 01. SIGNAL AND SYSTEMS 50 20 25 P C 25 10 21 PC 02. SIGNAL AND SYSTEMS OR 12. ENGINEERING MATHEMATICS III TW 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 P C 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 40 P C 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 25 P C 50 20 22 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 100 40 40 P PP 100 40 40 P 05. NETWORK ANALYSIS 15. ELECTROMAGNETIC 100 40 40 P C 16. ELECTROMAGNETIC 25 10 17 PC 06. DIGITAL LOGIC DESIGN PP TW 50 20 22 P C 100 40 07. DIGITAL LOGIC DESIGN PR 17. DATA STRUCTURES PP 45 P C 100 40 48 P C 08. POWER DEVICES AND MACHINES PP 18. DATA STRUCTURES PR 50 20 38 P C 100 40 45 P C 09. NETWORK AND POWER LAB. TW 50 20 40 P C 19. COMMUNICATION THEORY 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 40 P C 50 20 23 P C 20. COMMUNICATION THEORY 50 20 40 P C 21. CIRCUIT SIMULATION AND TOOLS TW GRAND TOTAL = 728/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS: , 71100866D , S80053172 , PICT , S80053172 S80053172 KOLI VIJAYA VYANKATRAO SEETA 11. ENGINEERING MATHEMATICS III PP 100 40 P 01. SIGNAL AND SYSTEMS PP 100 40 40 P C OR 02. SIGNAL AND SYSTEMS 12. ENGINEERING MATHEMATICS III TW 50 20 22 P C 25 10 17 PC 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 40 P 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 PC 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 20 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 07 F 05. NETWORK ANALYSIS PP 100 40 17 F PP 100 40 40 P C 15. ELECTROMAGNETIC 06. DIGITAL LOGIC DESIGN 100 40 23 F TW 25 10 18 P C PP 16. ELECTROMAGNETIC 07. DIGITAL LOGIC DESIGN 50 20 30 P C PP 100 40 43 P C PR 17. DATA STRUCTURES 08. POWER DEVICES AND MACHINES PP 100 40 22 F 18. DATA STRUCTURES PR 50 20 32 P C 09. NETWORK AND POWER LAB. TW 50 20 27 P C PP 100 40 40 P C 19. COMMUNICATION THEORY 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 30 P C 50 20 10 F 20. COMMUNICATION THEORY OR 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 32 P C GRAND TOTAL = 590/1500, RESULT: FAILS A.T.K.T. RESULT RESERVED FOR BKLG ORDN. 1 MARKS: 

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER LAXMI , 71100900H , S80053173 , РІСТ , S80053173 S80053173 MORE ANKUR SURESHRAO 11. ENGINEERING MATHEMATICS III PP 100 40 44 PC PP 100 40 40 P C 01. SIGNAL AND SYSTEMS 02. SIGNAL AND SYSTEMS OR 50 20 26 P C 25 10 12 P C 12. ENGINEERING MATHEMATICS III TW 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 AA F 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 AA F 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 20 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 AA F 05. NETWORK ANALYSIS PP 15. ELECTROMAGNETIC 100 40 17 F 100 40 10 F PP 100 40 AA F 25 10 11 P C 06. DIGITAL LOGIC DESIGN 16. ELECTROMAGNETIC TW 50 20 25 P C PP 100 40 07. DIGITAL LOGIC DESIGN 40 P PR 17. DATA STRUCTURES 08. POWER DEVICES AND MACHINES PP 100 40 40 P 50 20 32 P C 18. DATA STRUCTURES PR 09. NETWORK AND POWER LAB. TW PP 50 20 24 P C 100 40 16 F 19. COMMUNICATION THEORY 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 27 P C 50 20 AA F 20. COMMUNICATION THEORY 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 23 P C GRAND TOTAL = 407/1500, RESULT: FAILS ORDN. 1 MARKS : DRAUPADA , 71100910E , S80053176 , PICT , S80053176 S80053176 NANDNAWARE AKASH JAGNNATH PP 100 40 22 F 11. ENGINEERING MATHEMATICS III PP 100 40 40 PC 01. SIGNAL AND SYSTEMS 02. SIGNAL AND SYSTEMS OR 50 20 12. ENGINEERING MATHEMATICS III TW 25 10 11 P C 26 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 P 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 40 P C 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 25 P C 50 20 AA F 14. INTEGRATED CIRCUITS APPLICATIONS PR 100 40 12 F PP 100 40 13 F 05. NETWORK ANALYSIS 15. ELECTROMAGNETIC 25 10 100 40 49 P C 16. ELECTROMAGNETIC 06. DIGITAL LOGIC DESIGN PP TW 10 P C 50 20 AA F PP 100 40 07. DIGITAL LOGIC DESIGN PR 17. DATA STRUCTURES 40 P C 100 40 16 F 50 20 35 P C 08. POWER DEVICES AND MACHINES PP 18. DATA STRUCTURES PR 09. NETWORK AND POWER LAB. TW 50 20 30 P C 19. COMMUNICATION THEORY 100 40 22 F 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 28 P C 50 20 30 P C 20. COMMUNICATION THEORY OR 50 20 25 P C 21. CIRCUIT SIMULATION AND TOOLS TW GRAND TOTAL = 514/1500, RESULT: FAILS ORDN. 1 MARKS: , 71100946F , S80053179 , PICT , S80053179 S80053179 PAVAN TENKALE SHIVKANTA 11. ENGINEERING MATHEMATICS III PP 100 40 61 PC 01. SIGNAL AND SYSTEMS PP 100 40 54 P C O2. SIGNAL AND SYSTEMS OR 50 20 32 P C 12. ENGINEERING MATHEMATICS III TW 25 10 17 PC 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 48 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 56 P C 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 35 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 20 P C 05. NETWORK ANALYSIS PP 100 40 42 P C PP 100 40 40 P 15. ELECTROMAGNETIC 100 40 46 P C 06. DIGITAL LOGIC DESIGN TW 25 10 13 P C 16. ELECTROMAGNETIC 07. DIGITAL LOGIC DESIGN 50 20 23 P C PP 100 40 43 P C PR 17. DATA STRUCTURES 08. POWER DEVICES AND MACHINES PP 100 40 40 P C 18. DATA STRUCTURES PR 50 20 25 P C 09. NETWORK AND POWER LAB. TW 100 40 53 P C 50 20 38 P C PP 19. COMMUNICATION THEORY 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 32 P C 50 20 23 P C 20. COMMUNICATION THEORY OR 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 30 P C GRAND TOTAL = 771/1500, RESULT: SECOND CLASS ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER S80053185 SARODE BHUSHAN CHANDRAKANT , 71236328K , S80053185 , PICT , S80053185 ALKA PP 100 40 48 P C 11. ENGINEERING MATHEMATICS III PP 100 40 59 P 01. SIGNAL AND SYSTEMS 50 20 37 P C 25 10 20 P C 02. SIGNAL AND SYSTEMS 12. ENGINEERING MATHEMATICS III TW 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 40 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 63 P C 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 35 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 38 P C 05. NETWORK ANALYSIS PP 100 40 40 P C 15. ELECTROMAGNETIC 100 40 40 P.C PP 100 40 69 P C 25 10 19 P C 06. DIGITAL LOGIC DESIGN 16. ELECTROMAGNETIC TW 50 20 22 P C 100 40 67 P C 07. DIGITAL LOGIC DESIGN PP PR 17. DATA STRUCTURES 08. POWER DEVICES AND MACHINES PP 100 40 54 P C 50 20 18. DATA STRUCTURES 45 P C 09. NETWORK AND POWER LAB. TW 50 20 41 P C PP 100 40 70 P C 19. COMMUNICATION THEORY 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 42 P C 50 20 35 P C 20. COMMUNICATION THEORY 50 20 38 P C 21. CIRCUIT SIMULATION AND TOOLS TW GRAND TOTAL = 922/1500, RESULT: FIRST CLASS ORDN. 1 MARKS: S80053186 SARWAR ABHIJIT ASHOK MEERA , 71100988M , S80053186 , PICT , S80053186 11. ENGINEERING MATHEMATICS III PP 100 40 52 P PP 100 40 40 P C 01. SIGNAL AND SYSTEMS 50 20 20 P C 25 10 12 PC 02. SIGNAL AND SYSTEMS OR 12. ENGINEERING MATHEMATICS III TW 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 45 P C 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 46 P.C 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 33 P C 50 20 30 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 100 40 40 P C PP 100 40 41 P C 05. NETWORK ANALYSIS 15. ELECTROMAGNETIC 100 40 52 P C 16. ELECTROMAGNETIC 25 10 12 P C 06. DIGITAL LOGIC DESIGN PP TW 50 20 32 P C 100 40 07. DIGITAL LOGIC DESIGN PR 17. DATA STRUCTURES PP 51 P C 100 40 40 P C 50 20 08. POWER DEVICES AND MACHINES PP 18. DATA STRUCTURES PR 34 P C 09. NETWORK AND POWER LAB. TW 100 40 46 P C 50 20 39 P C 19. COMMUNICATION THEORY 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 35 P C 50 20 30 P C 20. COMMUNICATION THEORY 50 20 30 P C 21. CIRCUIT SIMULATION AND TOOLS TW GRAND TOTAL = 760/1500, RESULT: SECOND CLASS ORDN. 1 MARKS : , 71100997L , S80053187 , PICT , S80053187 S80053187 SHAHA SANKET MANOJKUMAR NEETA 01. SIGNAL AND SYSTEMS PP 100 40 51 P C 11. ENGINEERING MATHEMATICS III PP 100 40 72 P OR 02. SIGNAL AND SYSTEMS 12. ENGINEERING MATHEMATICS III TW 50 20 24 P C 25 10 23 P 100 40 40 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 P 03. SOLID STATES DEVICES AND CIRCUITSPP 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 22 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 38 P 05. NETWORK ANALYSIS PP 100 40 58 P C 100 40 81 P 15. ELECTROMAGNETIC PP 100 40 23 P 06. DIGITAL LOGIC DESIGN 48 P C TW 25 10 16. ELECTROMAGNETIC 07. DIGITAL LOGIC DESIGN 50 20 39 P C PP 100 40 55 P PR 17. DATA STRUCTURES 08. POWER DEVICES AND MACHINES PP 100 40 40 P C 18. DATA STRUCTURES PR 50 20 35 P 09. NETWORK AND POWER LAB. TW 50 20 20 P C 100 40 40 P 19. COMMUNICATION THEORY 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 22 P C 20. COMMUNICATION THEORY 50 20 21 P OR 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 45 P GRAND TOTAL = 837/1500, RESULT: HIGHER SECOND CLASS ORDN. 1 MARKS: 

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER VAISHALI , 71236329н , S80053192 , РІСТ , S80053192 S80053192 TANNA VATSAL AJAY 11. ENGINEERING MATHEMATICS III PP 100 40 44 P PP 100 40 40 P C 01. SIGNAL AND SYSTEMS 50 20 30 P C 25 10 16 P C O2. SIGNAL AND SYSTEMS OR 12. ENGINEERING MATHEMATICS III TW 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 46 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 P C 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 39 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 27 P C 05. NETWORK ANALYSIS PP 100 40 43 P C 15. ELECTROMAGNETIC 100 40 40 P C PP 100 40 53 P C 25 10 13 P C 06. DIGITAL LOGIC DESIGN 16. ELECTROMAGNETIC TW 100 40 54 P C 07. DIGITAL LOGIC DESIGN 50 20 23 P C PP PR 17. DATA STRUCTURES 08. POWER DEVICES AND MACHINES PP 100 40 44 P C 50 20 18. DATA STRUCTURES 33 P C 09. NETWORK AND POWER LAB. TW 50 20 37 P C PP 100 40 48 P C 19. COMMUNICATION THEORY 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 36 P C 50 20 40 P C 20. COMMUNICATION THEORY 50 20 32 P C 21. CIRCUIT SIMULATION AND TOOLS TW GRAND TOTAL = 778/1500, RESULT: SECOND CLASS ORDN. 1 MARKS : S80053194 THAKARE APURV VIJAY MEENA , 71101031F , S80053194 , PICT , S80053194 11. ENGINEERING MATHEMATICS III PP 100 40 51 P PP 100 40 40 P C 01. SIGNAL AND SYSTEMS 50 20 26 P C 25 10 11 PC 02. SIGNAL AND SYSTEMS OR 12. ENGINEERING MATHEMATICS III TW 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 P C 56 P 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 21 P C 50 20 22 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 100 40 40 P C PP 100 40 53 P C 05. NETWORK ANALYSIS 15. ELECTROMAGNETIC 100 40 40 P C 16. ELECTROMAGNETIC 25 10 11 PC 06. DIGITAL LOGIC DESIGN PP TW 50 20 20 P C 100 40 07. DIGITAL LOGIC DESIGN PR 17. DATA STRUCTURES PP 46 P C 100 40 40 P C 50 20 33 P C 08. POWER DEVICES AND MACHINES PP 18. DATA STRUCTURES PR 09. NETWORK AND POWER LAB. TW 50 20 P C 19. COMMUNICATION THEORY 100 40 40 P C 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 22 P C 50 20 26 P C 20. COMMUNICATION THEORY OR 50 20 23 P C 21. CIRCUIT SIMULATION AND TOOLS TW GRAND TOTAL = 681/1500, RESULT: PASS CLASS ORDN. 1 MARKS: , 71101045F , S80053196 , PICT , S80053196 S80053196 VARADE RUSHIKESH DILIP CHHAYA 01. SIGNAL AND SYSTEMS PP 100 40 42 P C 11. ENGINEERING MATHEMATICS III PP 100 40 PC OR 02. SIGNAL AND SYSTEMS 50 20 30 P C 12. ENGINEERING MATHEMATICS III TW 25 10 12 PC 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 49 P.C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 51 P C 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 36 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 33 P C PP 05. NETWORK ANALYSIS PP 100 40 40 P C 100 40 40 P 15. ELECTROMAGNETIC 100 40 06. DIGITAL LOGIC DESIGN 53 P C TW 25 10 11 P C 16. ELECTROMAGNETIC 07. DIGITAL LOGIC DESIGN 50 20 32 P C PP 100 40 41 P C PR 17. DATA STRUCTURES 08. POWER DEVICES AND MACHINES PP 100 40 46 P C 18. DATA STRUCTURES PR 50 20 32 P C 09. NETWORK AND POWER LAB. TW 50 20 28 P C 100 40 40 P C PP 19. COMMUNICATION THEORY 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 27 P C 50 20 28 P C 20. COMMUNICATION THEORY OR 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 25 P C GRAND TOTAL = 736/1500, RESULT: PASS CLASS ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER JYOTI , 71236332н , S80053197 , РІСТ , S80053197 S80053197 YEILE PRAJKT PRAMOD 11. ENGINEERING MATHEMATICS III PP 100 40 40 P 100 40 41 P C 01. SIGNAL AND SYSTEMS 50 20 23 P C 12. ENGINEERING MATHEMATICS III TW 25 10 15 P C 02. SIGNAL AND SYSTEMS OR 100 40 40 P C 100 40 40 P C 03. SOLID STATES DEVICES AND CIRCUITSPP 13. INTEGRATED CIRCUITS APPLICATIONS PP 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 26 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 50 20 20 P C 05. NETWORK ANALYSIS 100 40 40 P 15. ELECTROMAGNETIC 100 40 28 F 100 40 43 P 25 10 06. DIGITAL LOGIC DESIGN PP 16. ELECTROMAGNETIC TW 12 P C 100 40 40 P C 07. DIGITAL LOGIC DESIGN 50 20 30 P C PP PR 17. DATA STRUCTURES 08. POWER DEVICES AND MACHINES PP 100 40 41 P C 50 20 35 P C 18. DATA STRUCTURES PR 09. NETWORK AND POWER LAB. TW 50 20 35 P C 100 40 43 P 19. COMMUNICATION THEORY PP 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 29 P C 50 20 28 P C 20. COMMUNICATION THEORY 50 20 31 P C 21. CIRCUIT SIMULATION AND TOOLS TW GRAND TOTAL = 680/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS : S80053199 KHANDELWAL DIVYANG KAMALKISHOR , s80053199 SANGEETA 01. SIGNAL AND SYSTEMS PP 100 40 43 P C 50 20 20 P C 02. SIGNAL AND SYSTEMS OR 100 40 03. SOLID STATES DEVICES AND CIRCUITSPP AA F 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 AA F 100 40 05. NETWORK ANALYSIS AA F 100 40 06. DIGITAL LOGIC DESIGN PP AA F 07. DIGITAL LOGIC DESIGN 50 20 29 P C 100 40 AA F 08. POWER DEVICES AND MACHINES PP 09. NETWORK AND POWER LAB. TW 50 20 21 P C 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 23 P C FIRST TERM TOTAL = 136/750. ORDN. 1 MARKS: S80053200 PITRODA UTSAV RAJENDRA , 71129936G , , PICT , S80053200 KAILASH 100 40 40 P C 100 40 AA F 01. SIGNAL AND SYSTEMS 11. ENGINEERING MATHEMATICS III 02. SIGNAL AND SYSTEMS 50 20 21 P C 12. ENGINEERING MATHEMATICS III TW 25 10 18 P C 03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 49 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 51 P.C 50 20 04. SOLID STATES DEVICES AND CIRCUITSPR 50 20 28 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR 30 P C 05. NETWORK ANALYSIS 100 40 40 P C 15. ELECTROMAGNETIC 100 40 40 P C 06. DIGITAL LOGIC DESIGN 100 40 40 P C 25 10 16 P C PP 16. ELECTROMAGNETIC TW 50 20 100 40 43 P C 07. DIGITAL LOGIC DESIGN 24 P C PP PR 17. DATA STRUCTURES 08. POWER DEVICES AND MACHINES 100 40 48 P C 50 20 45 P C PP 18. DATA STRUCTURES PR 09. NETWORK AND POWER LAB. TW 50 20 41 P C 19. COMMUNICATION THEORY PP 100 40 40 P C 50 20 38 P C 10. ELECTRONIC INSTRUMENTS AND TOOLS TW 20. COMMUNICATION THEORY 50 20 20 P C 21. CIRCUIT SIMULATION AND TOOLS TW 50 20 42 P C GRAND TOTAL = 714/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS:

DATE: 27 JULY 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 01 (454)

NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER DINAZ , 71200745J , S80054201 , PICT , S80054201 S80054201 AADITYA RAMESH 11. ENGINEERING MATHEMATICS III PP 100 40 52 P PP 100 40 79 P C 01. DISCRETE STRUCTURES 100 40 57 P C 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 02. PROGRAMMING & PROBLEM SOLVING PP 100 40 64 P C 100 40 73 P 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 13. DATA STRUCTURES PP 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 59 P C PP 100 40 70 P 14. COMPUTER GRAPHICS 05. HUMANITIES AND SOCIAL SCIENCE PP 100 40 57 P C 15. COMPUTER ORGANIZATION 100 40 73 P 25 10 20 P C 50 20 39 P 06. PROGRAMMING LABORATORY 16. O. O. PROG. & COMP. GRAPH. LAB TW TW 07. PROGRAMMING LABORATORY 50 20 43 P C 17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 43 P PR 25 10 18 P C 18. MICROPROCESSORS & INTERFACING LABTW 50 20 37 P 08. DIGITAL ELECTRONICS LABORATORY TW 50 20 37 P C 50 20 43 P 09. DIGITAL ELECTRONICS LABORATORY PR 19. MICROPROCESSORS & INTERFACING LABPR 10. SOFT SKILLS 50 20 35 P C 20. DATA STRUCTURES LABORATORY 50 20 40 P TW 50 20 40 P 21. DATA STRUCTURES LABORATORY PR GRAND TOTAL = 1044/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS: S80054202 ABHINAV KAUL SUREKHA , 71200747E , S80054202 , PICT , S80054202 11. ENGINEERING MATHEMATICS III PP 100 40 40 P 01. DISCRETE STRUCTURES 100 40 53 P C 100 40 40 P C 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 14 F 02. PROGRAMMING & PROBLEM SOLVING PP 40 P C 13. DATA STRUCTURES 100 40 100 40 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 40 P 100 40 40 P C 100 40 48 P 04. DATA STRUCTURES AND ALGORITHMS PP 14. COMPUTER GRAPHICS PP 100 40 47 P.C 100 40 29 F 05. HUMANITIES AND SOCIAL SCIENCE PP 15. COMPUTER ORGANIZATION 25 10 50 20 06. PROGRAMMING LABORATORY 33 P 18 P C 16. O. O. PROG. & COMP. GRAPH. LAB TW TW 13 F 50 20 38 P 07. PROGRAMMING LABORATORY 50 20 17. O. O. PROG. & COMP. GRAPH. LAB PR 25 10 50 20 35 P 08. DIGITAL ELECTRONICS LABORATORY TW 20 P C 18. MICROPROCESSORS & INTERFACING LABTW 50 20 24 P 09. DIGITAL ELECTRONICS LABORATORY PR 50 20 32 P C 19. MICROPROCESSORS & INTERFACING LABPR 10. SOFT SKILLS 50 20 32 P C 50 20 35 P TW 20. DATA STRUCTURES LABORATORY 50 20 31 P 21. DATA STRUCTURES LABORATORY PR GRAND TOTAL = 702/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS : ARTI , 71200749M , S80054203 , PICT , S80054203 S80054203 ABHYANKAR RUCHA RHISHIKESH 01. DISCRETE STRUCTURES 100 40 71 P C 11. ENGINEERING MATHEMATICS III PP 100 40 58 P 02. PROGRAMMING & PROBLEM SOLVING PP 100 40 58 P C 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 57 P 100 40 100 40 73 P C 13. DATA STRUCTURES 65 P 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 61 P C 14. COMPUTER GRAPHICS 100 40 72 P 05. HUMANITIES AND SOCIAL SCIENCE PP 100 40 64 P C 15. COMPUTER ORGANIZATION 100 40 65 P 25 10 45 P 06. PROGRAMMING LABORATORY 22 P C 50 20 TW 16. O. O. PROG. & COMP. GRAPH. LAB TW 07. PROGRAMMING LABORATORY 50 20 40 P C 17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 40 P PR 08. DIGITAL ELECTRONICS LABORATORY TW 25 10 20 P C 18. MICROPROCESSORS & INTERFACING LABTW 50 20 44 P 50 20 39 P 09. DIGITAL ELECTRONICS LABORATORY PR 50 20 35 P C 19. MICROPROCESSORS & INTERFACING LABPR 10. SOFT SKILLS 50 20 40 P C 50 20 45 P TW 20. DATA STRUCTURES LABORATORY TW 21. DATA STRUCTURES LABORATORY PR 50 20 41 P GRAND TOTAL = 1055/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS: 

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER MEENAKSHI , 71200753к , S80054204 , РІСТ , S80054204 S80054204 AJINKYA DNYANESHWAR RAJPUT O1. DISCRETE STRUCTURES PP 11. ENGINEERING MATHEMATICS III PP 100 40 40 P 100 40 50 P C 100 40 50 P C 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 02. PROGRAMMING & PROBLEM SOLVING PP 100 40 66 P C 100 40 58 P 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 13. DATA STRUCTURES PP 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 48 P C PP 100 40 65 P 14. COMPUTER GRAPHICS 05. HUMANITIES AND SOCIAL SCIENCE PP 100 40 53 P C 15. COMPUTER ORGANIZATION 100 40 54 P 25 10 20 P C 50 20 41 P 06. PROGRAMMING LABORATORY 16. O. O. PROG. & COMP. GRAPH. LAB TW TW 07. PROGRAMMING LABORATORY 50 20 41 P C 17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 42 P PR 25 10 20 P C 18. MICROPROCESSORS & INTERFACING LABTW 50 20 38 P 08. DIGITAL ELECTRONICS LABORATORY TW 50 20 35 P C 50 20 27 P 09. DIGITAL ELECTRONICS LABORATORY PR 19. MICROPROCESSORS & INTERFACING LABPR 50 20 31 P C 10. SOFT SKILLS 20. DATA STRUCTURES LABORATORY 50 20 39 P TW 50 20 39 P 21. DATA STRUCTURES LABORATORY PR GRAND TOTAL = 921/1500, RESULT: FIRST CLASS ORDN. 1 MARKS : S80054205 AKASH MAHAPATRA , 71200754H , S80054205 , РІСТ , S80054205 SUJATA 11. ENGINEERING MATHEMATICS III PP 100 40 58 P 01. DISCRETE STRUCTURES 63 P C PP 100 40 53 P C 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 49 P 02. PROGRAMMING & PROBLEM SOLVING PP 100 40 13. DATA STRUCTURES 100 40 66 P C 100 40 55 P 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 40 51 P C 100 40 04. DATA STRUCTURES AND ALGORITHMS PP 14. COMPUTER GRAPHICS PP 45 P 100 40 54 P C 100 40 05. HUMANITIES AND SOCIAL SCIENCE PP 15. COMPUTER ORGANIZATION 56 P 25 10 50 20 40 P 06. PROGRAMMING LABORATORY 21 P C 16. O. O. PROG. & COMP. GRAPH. LAB TW TW 38 P C 50 20 42 P 07. PROGRAMMING LABORATORY 50 20 17. O. O. PROG. & COMP. GRAPH. LAB PR 25 10 50 20 40 P 08. DIGITAL ELECTRONICS LABORATORY TW 20 P C 18. MICROPROCESSORS & INTERFACING LABTW 50 20 25 P 50 20 36 P C 09. DIGITAL ELECTRONICS LABORATORY PR 19. MICROPROCESSORS & INTERFACING LABPR 10. SOFT SKILLS 50 20 41 P C 50 20 43 P TW 20. DATA STRUCTURES LABORATORY 50 20 35 P 21. DATA STRUCTURES LABORATORY PR GRAND TOTAL = 931/1500, RESULT: FIRST CLASS ORDN. 1 MARKS: , 71200759J , S80054206 , PICT , S80054206 S80054206 AMAN RATHI GEETA 11. ENGINEERING MATHEMATICS III PP 100 40 21 F 01. DISCRETE STRUCTURES 100 40 45 P C 02. PROGRAMMING & PROBLEM SOLVING PP 100 40 47 P C 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 50 P PP 100 40 100 40 55 P C 13. DATA STRUCTURES 49 P 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 47 P C 14. COMPUTER GRAPHICS 100 40 60 P 100 40 66 P C 15. COMPUTER ORGANIZATION 100 40 58 P 05. HUMANITIES AND SOCIAL SCIENCE PP 25 10 38 P 06. PROGRAMMING LABORATORY 21 P C 50 20 16. O. O. PROG. & COMP. GRAPH. LAB TW 07. PROGRAMMING LABORATORY 50 20 21 P C 17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 46 P PR 08. DIGITAL ELECTRONICS LABORATORY TW 25 10 22 P C 18. MICROPROCESSORS & INTERFACING LABTW 50 20 36 P 06 F 09. DIGITAL ELECTRONICS LABORATORY PR 50 20 34 P C 50 20 19. MICROPROCESSORS & INTERFACING LABPR 10. SOFT SKILLS 50 20 41 P C 50 20 38 P TW 20. DATA STRUCTURES LABORATORY TW 21. DATA STRUCTURES LABORATORY PR 50 20 40 P GRAND TOTAL = 841/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS:

DATE: 27 JULY 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 03 (456)

NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71200760B , S80054207 , PICT , S80054207 S80054207 AMAN SINGH KIRAN 11. ENGINEERING MATHEMATICS III PP 100 40 40 P 01. DISCRETE STRUCTURES PP 100 40 45 P C 100 40 51 P C 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 41 P 02. PROGRAMMING & PROBLEM SOLVING PP 100 40 47 P C 100 40 50 P 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 13. DATA STRUCTURES PP 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 40 P C PP 100 40 54 P 14. COMPUTER GRAPHICS 05. HUMANITIES AND SOCIAL SCIENCE PP 100 40 56 P C 15. COMPUTER ORGANIZATION 100 40 42 P 25 10 12 P C 50 20 22 P 06. PROGRAMMING LABORATORY 16. O. O. PROG. & COMP. GRAPH. LAB TW TW 07. PROGRAMMING LABORATORY 50 20 40 P C 50 20 35 P PR 17. O. O. PROG. & COMP. GRAPH. LAB PR 25 10 15 P C 18. MICROPROCESSORS & INTERFACING LABTW 50 20 22 P 08. DIGITAL ELECTRONICS LABORATORY TW 50 20 33 P C 50 20 02 F 09. DIGITAL ELECTRONICS LABORATORY PR 19. MICROPROCESSORS & INTERFACING LABPR 50 20 30 P C 50 20 25 P 10. SOFT SKILLS TW 20. DATA STRUCTURES LABORATORY 50 20 25 P 21. DATA STRUCTURES LABORATORY PR GRAND TOTAL = 727/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS: PADMAPRIYA , 71200762J , S80054208 , PICT , S80054208 S80054208 ANIRUDH SUDARSHAN 11. ENGINEERING MATHEMATICS III PP 100 40 58 P 01. DISCRETE STRUCTURES 81 P C PP 100 40 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 59 P 02. PROGRAMMING & PROBLEM SOLVING PP 100 40 67 P C 13. DATA STRUCTURES 100 40 100 40 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 56 P C 64 P 100 40 56 P C 100 40 04. DATA STRUCTURES AND ALGORITHMS PP 14. COMPUTER GRAPHICS PP 62 P 100 40 100 40 05. HUMANITIES AND SOCIAL SCIENCE PP 62 P C 15. COMPUTER ORGANIZATION 75 P 25 10 06. PROGRAMMING LABORATORY 50 20 38 P 23 P C 16. O. O. PROG. & COMP. GRAPH. LAB TW TW 50 20 07. PROGRAMMING LABORATORY 50 20 41 P C 17. O. O. PROG. & COMP. GRAPH. LAB PR 43 P 22 P C 50 20 08. DIGITAL ELECTRONICS LABORATORY TW 25 10 18. MICROPROCESSORS & INTERFACING LABTW 38 P 50 20 32 P C 50 20 43 P 09. DIGITAL ELECTRONICS LABORATORY PR 19. MICROPROCESSORS & INTERFACING LABPR 10. SOFT SKILLS 50 20 40 P C 50 20 40 P TW 20. DATA STRUCTURES LABORATORY 50 20 41 P 21. DATA STRUCTURES LABORATORY PR GRAND TOTAL = 1041/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS: , 71347755F , S80054209 , PICT , S80054209 S80054209 ANTHONY ELIZABETH XAVIER ANJALI 01. DISCRETE STRUCTURES 11. ENGINEERING MATHEMATICS III PP 100 40 41 P 100 40 57 P C 02. PROGRAMMING & PROBLEM SOLVING PP 100 40 49 P C 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 51 P 100 40 100 40 72 P C 13. DATA STRUCTURES PP 58 P 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 56 P C 14. COMPUTER GRAPHICS 100 40 63 P 100 40 51 P C 15. COMPUTER ORGANIZATION 100 40 56 P 05. HUMANITIES AND SOCIAL SCIENCE PP 25 10 20 P C 06. PROGRAMMING LABORATORY 50 20 42 P TW 16. O. O. PROG. & COMP. GRAPH. LAB TW 07. PROGRAMMING LABORATORY 50 20 23 P C 17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 40 P PR 08. DIGITAL ELECTRONICS LABORATORY TW 25 10 20 P C 18. MICROPROCESSORS & INTERFACING LABTW 50 20 40 P 20 P 09. DIGITAL ELECTRONICS LABORATORY PR 50 20 35 P C 50 20 19. MICROPROCESSORS & INTERFACING LABPR 10. SOFT SKILLS 50 20 42 P C 50 20 40 P TW 20. DATA STRUCTURES LABORATORY TW 21. DATA STRUCTURES LABORATORY PR 50 20 39 P GRAND TOTAL = 915/1500, RESULT: FIRST CLASS ORDN. 1 MARKS: 

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER S80054210 ANVEKAR ASHWINI KISHOR KANCHANA , 71200763G , S80054210 , PICT , S80054210 01. DISCRETE STRUCTURES 11. ENGINEERING MATHEMATICS III PP 100 40 46 P PP 100 40 73 P C 100 40 51 P C 12. MICROPROC. & INTERFACING TECHNIO.PP 100 40 47 P 02. PROGRAMMING & PROBLEM SOLVING PP 100 40 56 P C 100 40 58 P 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 13. DATA STRUCTURES PP 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 40 P C PP 100 40 53 P 14. COMPUTER GRAPHICS 05. HUMANITIES AND SOCIAL SCIENCE PP 100 40 57 P C 15. COMPUTER ORGANIZATION 100 40 58 P 25 10 20 P C 50 20 35 P 06. PROGRAMMING LABORATORY 16. O. O. PROG. & COMP. GRAPH. LAB TW TW 22 P C 07. PROGRAMMING LABORATORY 50 20 17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 40 P PR 25 10 21 P C 18. MICROPROCESSORS & INTERFACING LABTW 50 20 35 P 08. DIGITAL ELECTRONICS LABORATORY TW 50 20 32 P C 50 20 33 P 09. DIGITAL ELECTRONICS LABORATORY PR 19. MICROPROCESSORS & INTERFACING LABPR 50 20 40 P C 10. SOFT SKILLS 20. DATA STRUCTURES LABORATORY 50 20 35 P TW 50 20 37 P 21. DATA STRUCTURES LABORATORY PR GRAND TOTAL = 889/1500, RESULT: HIGHER SECOND CLASS ORDN. 1 MARKS: VEENA , 71200771H , S80054211 , PICT , S80054211 S80054211 BAGDIYA TUSHAR VIJAYCHAND 01. DISCRETE STRUCTURES 11. ENGINEERING MATHEMATICS III PP 100 40 68 P 100 40 73 P C 100 40 49 P C 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 47 P 02. PROGRAMMING & PROBLEM SOLVING PP 13. DATA STRUCTURES 100 40 58 P C 100 40 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 55 P 100 40 49 P C 100 40 50 P 04. DATA STRUCTURES AND ALGORITHMS PP 14. COMPUTER GRAPHICS PP 100 40 43 P C 100 40 05. HUMANITIES AND SOCIAL SCIENCE PP 15. COMPUTER ORGANIZATION 54 P 25 10 06. PROGRAMMING LABORATORY 21 P C 50 20 39 P 16. O. O. PROG. & COMP. GRAPH. LAB TW TW 50 20 43 P C 50 20 07. PROGRAMMING LABORATORY 17. O. O. PROG. & COMP. GRAPH. LAB PR 40 P 25 10 50 20 08. DIGITAL ELECTRONICS LABORATORY TW 21 P C 18. MICROPROCESSORS & INTERFACING LABTW 41 P 09. DIGITAL ELECTRONICS LABORATORY PR 50 20 30 P C 19. MICROPROCESSORS & INTERFACING LABPR 50 20 40 P 10. SOFT SKILLS 50 20 35 P C 50 20 39 P TW 20. DATA STRUCTURES LABORATORY 50 20 41 P 21. DATA STRUCTURES LABORATORY PR GRAND TOTAL = 936/1500, RESULT: FIRST CLASS ORDN. 1 MARKS: , 71200772F , S80054212 , PICT , S80054212 S80054212 BAGUL PALLAVI DEEPAK MADHAVI 01. DISCRETE STRUCTURES 11. ENGINEERING MATHEMATICS III PP 100 40 52 P PP 100 40 73 P C 02. PROGRAMMING & PROBLEM SOLVING PP 100 40 43 P C 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 57 P PP 100 40 50 P 100 40 46 P C 13. DATA STRUCTURES 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 40 P C 14. COMPUTER GRAPHICS 100 40 51 P 05. HUMANITIES AND SOCIAL SCIENCE PP 100 40 45 P C 15. COMPUTER ORGANIZATION 100 40 62 P 25 10 39 P 06. PROGRAMMING LABORATORY 19 P C 50 20 TW 16. O. O. PROG. & COMP. GRAPH. LAB TW 07. PROGRAMMING LABORATORY 50 20 36 P C 17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 34 P PR 08. DIGITAL ELECTRONICS LABORATORY TW 25 10 19 P C 18. MICROPROCESSORS & INTERFACING LABTW 50 20 40 P 50 20 35 P 09. DIGITAL ELECTRONICS LABORATORY PR 50 20 29 P C 19. MICROPROCESSORS & INTERFACING LABPR 10. SOFT SKILLS 50 20 35 P C 50 20 40 P TW 20. DATA STRUCTURES LABORATORY TW 21. DATA STRUCTURES LABORATORY PR 50 20 32 P GRAND TOTAL = 877/1500, RESULT: HIGHER SECOND CLASS ORDN. 1 MARKS: 

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER JYOTI , 71200780G , S80054216 , PICT , S80054216 S80054216 BANG VIJAY SANTOSHKUMAR 01. DISCRETE STRUCTURES 11. ENGINEERING MATHEMATICS III PP 100 40 48 P PP 100 40 77 P C 100 40 56 P C 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 58 P 02. PROGRAMMING & PROBLEM SOLVING PP 100 40 67 P C 100 40 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 13. DATA STRUCTURES PP 67 P 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 59 P C PP 100 40 65 P 14. COMPUTER GRAPHICS 05. HUMANITIES AND SOCIAL SCIENCE PP 100 40 62 P C 15. COMPUTER ORGANIZATION 100 40 65 P 25 10 21 PC 50 20 41 P 06. PROGRAMMING LABORATORY 16. O. O. PROG. & COMP. GRAPH. LAB TW TW 07. PROGRAMMING LABORATORY 50 20 46 P C 17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 38 P PR 25 10 21 P C 18. MICROPROCESSORS & INTERFACING LABTW 50 20 38 P 08. DIGITAL ELECTRONICS LABORATORY TW 50 20 41 P C 50 20 40 P 09. DIGITAL ELECTRONICS LABORATORY PR 19. MICROPROCESSORS & INTERFACING LABPR 10. SOFT SKILLS 50 20 39 P C 20. DATA STRUCTURES LABORATORY 50 20 42 P TW 50 20 44 P 21. DATA STRUCTURES LABORATORY GRAND TOTAL = 1035/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS: USHA , 71200781E , S80054217 , PICT , S80054217 S80054217 BANGAD ANKITA ISHWARPRASAD 11. ENGINEERING MATHEMATICS III PP 100 40 79 P 01. DISCRETE STRUCTURES 78 P C 100 40 100 40 47 P C 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 02. PROGRAMMING & PROBLEM SOLVING PP 61 P 13. DATA STRUCTURES 100 40 100 40 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 66 P C 62 P 100 40 53 P C 100 40 04. DATA STRUCTURES AND ALGORITHMS PP 14. COMPUTER GRAPHICS PP 68 P 100 40 100 40 05. HUMANITIES AND SOCIAL SCIENCE PP 62 P C 15. COMPUTER ORGANIZATION 65 P 25 10 06. PROGRAMMING LABORATORY 19 P C 50 20 38 P 16. O. O. PROG. & COMP. GRAPH. LAB TW TW 50 20 42 P C 50 20 42 P 07. PROGRAMMING LABORATORY 17. O. O. PROG. & COMP. GRAPH. LAB PR 25 10 20 P C 50 20 40 P 08. DIGITAL ELECTRONICS LABORATORY TW 18. MICROPROCESSORS & INTERFACING LABTW 50 20 33 P C 09. DIGITAL ELECTRONICS LABORATORY PR 19. MICROPROCESSORS & INTERFACING LABPR 50 20 44 P 10. SOFT SKILLS 50 20 37 P C 20. DATA STRUCTURES LABORATORY 50 20 40 P TW 50 20 41 P 21. DATA STRUCTURES LABORATORY PR GRAND TOTAL = 1037/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS: , 71200786F , S80054218 , PICT , S80054218 S80054218 BAT PANKAJ RAMSINGH PUSHPA 11. ENGINEERING MATHEMATICS III PP 100 40 54 P 01. DISCRETE STRUCTURES PP 100 40 50 P C 02. PROGRAMMING & PROBLEM SOLVING PP 100 40 40 P C 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 42 P PP 100 40 49 P 100 40 74 P C 13. DATA STRUCTURES 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 41 P C 14. COMPUTER GRAPHICS 100 40 27 F 05. HUMANITIES AND SOCIAL SCIENCE PP 100 40 41 P C 15. COMPUTER ORGANIZATION 100 40 58 P 25 10 30 P 06. PROGRAMMING LABORATORY 18 P C 50 20 16. O. O. PROG. & COMP. GRAPH. LAB TW 07. PROGRAMMING LABORATORY 50 20 39 P C 17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 35 P PR 08. DIGITAL ELECTRONICS LABORATORY TW 25 10 16 P C 18. MICROPROCESSORS & INTERFACING LABTW 50 20 25 P 50 20 35 P 09. DIGITAL ELECTRONICS LABORATORY PR 50 20 30 P C 19. MICROPROCESSORS & INTERFACING LABPR 10. SOFT SKILLS 50 20 32 P C 50 20 28 P TW 20. DATA STRUCTURES LABORATORY TW 21. DATA STRUCTURES LABORATORY PR 50 20 32 P GRAND TOTAL = 796/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER JAYSHREE , 71347758L , S80054222 , PICT , S80054222 S80054222 BHAWSAR SHOBHANA JAYVANT 01. DISCRETE STRUCTURES PP 100 40 47 P C 11. ENGINEERING MATHEMATICS III PP 100 40 51 P 100 40 59 P C 12. MICROPROC. & INTERFACING TECHNIO.PP 100 40 55 P 02. PROGRAMMING & PROBLEM SOLVING PP 100 40 56 P C 100 40 63 P 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 13. DATA STRUCTURES PP 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 49 P C PP 100 40 47 P 14. COMPUTER GRAPHICS 05. HUMANITIES AND SOCIAL SCIENCE PP 100 40 57 P C 15. COMPUTER ORGANIZATION 100 40 72 P 25 10 20 P C 50 20 38 P 06. PROGRAMMING LABORATORY 16. O. O. PROG. & COMP. GRAPH. LAB TW TW 25 P C 07. PROGRAMMING LABORATORY 50 20 17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 38 P PR 25 10 21 P C 18. MICROPROCESSORS & INTERFACING LABTW 50 20 38 P 08. DIGITAL ELECTRONICS LABORATORY TW 50 20 28 P C 50 20 37 P 09. DIGITAL ELECTRONICS LABORATORY PR 19. MICROPROCESSORS & INTERFACING LABPR 50 20 37 P C 10. SOFT SKILLS 20. DATA STRUCTURES LABORATORY 50 20 40 P TW 50 20 39 P 21. DATA STRUCTURES LABORATORY PR GRAND TOTAL = 917/1500, RESULT: FIRST CLASS ORDN. 1 MARKS : SUNITA , 71200798к , S80054223 , РІСТ , S80054223 S80054223 BHUTADA RUTUSHA KAMALKISHOR 01. DISCRETE STRUCTURES 11. ENGINEERING MATHEMATICS III PP 100 40 66 P 100 40 69 P C 100 40 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 02. PROGRAMMING & PROBLEM SOLVING PP 51 P C 58 P 13. DATA STRUCTURES 100 40 100 40 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 63 P C 63 P 100 40 53 P C 100 40 65 P 04. DATA STRUCTURES AND ALGORITHMS PP 14. COMPUTER GRAPHICS PP 100 40 100 40 05. HUMANITIES AND SOCIAL SCIENCE PP 64 P C 15. COMPUTER ORGANIZATION 69 P 25 10 06. PROGRAMMING LABORATORY 22 P C 50 20 44 P 16. O. O. PROG. & COMP. GRAPH. LAB TW TW 41 P C 50 20 32 P 07. PROGRAMMING LABORATORY 50 20 17. O. O. PROG. & COMP. GRAPH. LAB PR 25 10 21 P C 50 20 08. DIGITAL ELECTRONICS LABORATORY TW 18. MICROPROCESSORS & INTERFACING LABTW 43 P 50 20 38 P C 50 20 40 P 09. DIGITAL ELECTRONICS LABORATORY PR 19. MICROPROCESSORS & INTERFACING LABPR 10. SOFT SKILLS 50 20 37 P C 20. DATA STRUCTURES LABORATORY 50 20 43 P TW 50 20 40 P 21. DATA STRUCTURES LABORATORY PR GRAND TOTAL = 1022/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS: , 71200800E , S80054224 , PICT , S80054224 S80054224 BINO JOSEPH JASMINE 01. DISCRETE STRUCTURES 100 40 79 P C 11. ENGINEERING MATHEMATICS III PP 100 40 81 P 02. PROGRAMMING & PROBLEM SOLVING PP 100 40 63 P C 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 66 P PP 100 40 100 40 75 P C 13. DATA STRUCTURES 60 P 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 57 P C 14. COMPUTER GRAPHICS 100 40 59 P 05. HUMANITIES AND SOCIAL SCIENCE PP 100 40 67 P C 15. COMPUTER ORGANIZATION 100 40 76 P 25 10 38 P 06. PROGRAMMING LABORATORY 23 P C 50 20 16. O. O. PROG. & COMP. GRAPH. LAB TW 07. PROGRAMMING LABORATORY 50 20 42 P C 17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 32 P PR 08. DIGITAL ELECTRONICS LABORATORY TW 25 10 21 P C 18. MICROPROCESSORS & INTERFACING LABTW 50 20 40 P 09. DIGITAL ELECTRONICS LABORATORY PR 50 20 40 P C 50 20 40 P 19. MICROPROCESSORS & INTERFACING LABPR 10. SOFT SKILLS 50 20 38 P C 50 20 40 P TW 20. DATA STRUCTURES LABORATORY TW 21. DATA STRUCTURES LABORATORY PR 50 20 38 P GRAND TOTAL = 1075/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS: 

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71200837D , S80054235 , PICT , S80054235 S80054235 DHAGE SHRADDHA BHAUSAHEB SUMAN 01. DISCRETE STRUCTURES PP 100 40 66 P C 11. ENGINEERING MATHEMATICS III PP 100 40 54 P 100 40 56 P C 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 02. PROGRAMMING & PROBLEM SOLVING PP 100 40 59 P C 100 40 60 P 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 13. DATA STRUCTURES PP 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 50 P C PP 100 40 61 P 14. COMPUTER GRAPHICS 05. HUMANITIES AND SOCIAL SCIENCE PP 100 40 63 P C 15. COMPUTER ORGANIZATION 100 40 72 P 25 10 21 PC 50 20 41 P 06. PROGRAMMING LABORATORY 16. O. O. PROG. & COMP. GRAPH. LAB TW TW 07. PROGRAMMING LABORATORY 50 20 29 P C 17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 33 P PR 25 10 19 P C 18. MICROPROCESSORS & INTERFACING LABTW 50 20 43 P 08. DIGITAL ELECTRONICS LABORATORY TW 50 20 34 P C 50 20 37 P 09. DIGITAL ELECTRONICS LABORATORY PR 19. MICROPROCESSORS & INTERFACING LABPR 50 20 37 P C 10. SOFT SKILLS 20. DATA STRUCTURES LABORATORY 50 20 42 P TW 50 20 38 P 21. DATA STRUCTURES LABORATORY PR GRAND TOTAL = 982/1500, RESULT: FIRST CLASS ORDN. 1 MARKS: SMITA , 71200839L , S80054236 , PICT , S80054236 S80054236 DHAVALIKAR RAHUL DEEPAK 01. DISCRETE STRUCTURES 11. ENGINEERING MATHEMATICS III PP 100 40 70 P 74 P C 100 40 100 40 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 71 P 02. PROGRAMMING & PROBLEM SOLVING PP 68 P C 60 P C 13. DATA STRUCTURES 100 40 100 40 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 58 P 100 40 60 P C 100 40 04. DATA STRUCTURES AND ALGORITHMS PP 14. COMPUTER GRAPHICS PP 61 P 100 40 100 40 05. HUMANITIES AND SOCIAL SCIENCE PP 56 P C 15. COMPUTER ORGANIZATION 67 P 25 10 23 P C 50 20 45 P 06. PROGRAMMING LABORATORY 16. O. O. PROG. & COMP. GRAPH. LAB TW TW 42 P C 50 20 44 P 07. PROGRAMMING LABORATORY 50 20 17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 08. DIGITAL ELECTRONICS LABORATORY TW 25 10 21 P C 18. MICROPROCESSORS & INTERFACING LABTW 45 P 50 20 44 P 50 20 38 P C 09. DIGITAL ELECTRONICS LABORATORY PR 19. MICROPROCESSORS & INTERFACING LABPR 10. SOFT SKILLS 50 20 38 P C 20. DATA STRUCTURES LABORATORY 50 20 45 P TW 50 20 45 P 21. DATA STRUCTURES LABORATORY PR GRAND TOTAL = 1075/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS: , 71200840D , S80054237 , PICT , S80054237 S80054237 DHEKALE AMRUTA RAMBHAU KANTABAI 01. DISCRETE STRUCTURES PP 100 40 81 P C 11. ENGINEERING MATHEMATICS III PP 100 40 42 P 02. PROGRAMMING & PROBLEM SOLVING PP 100 40 49 P C 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 59 P PP 100 40 100 40 56 P C 13. DATA STRUCTURES 60 P 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 41 P C 14. COMPUTER GRAPHICS 100 40 63 P 100 40 60 P C 15. COMPUTER ORGANIZATION 100 40 55 P 05. HUMANITIES AND SOCIAL SCIENCE PP 25 10 06. PROGRAMMING LABORATORY 20 P C 50 20 40 P TW 16. O. O. PROG. & COMP. GRAPH. LAB TW 07. PROGRAMMING LABORATORY 50 20 38 P C 17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 35 P PR 08. DIGITAL ELECTRONICS LABORATORY TW 25 10 18 P C 18. MICROPROCESSORS & INTERFACING LABTW 50 20 42 P 50 20 35 P 09. DIGITAL ELECTRONICS LABORATORY PR 50 20 30 P C 19. MICROPROCESSORS & INTERFACING LABPR 10. SOFT SKILLS 50 20 36 P C 50 20 43 P TW 20. DATA STRUCTURES LABORATORY TW 21. DATA STRUCTURES LABORATORY PR 50 20 32 P GRAND TOTAL = 935/1500, RESULT: FIRST CLASS ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71200870F , S80054247 , PICT , S80054247 S80054247 GOVIND BAPU CHAUDHARY SUMAN 01. DISCRETE STRUCTURES 11. ENGINEERING MATHEMATICS III PP 100 40 54 P PP 100 40 57 P C 100 40 54 P C 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 02. PROGRAMMING & PROBLEM SOLVING PP 100 40 66 P C 100 40 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 13. DATA STRUCTURES PP 62 P 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 45 P C PP 100 40 51 P 14. COMPUTER GRAPHICS 05. HUMANITIES AND SOCIAL SCIENCE PP 100 40 65 P C 15. COMPUTER ORGANIZATION 100 40 25 10 22 P C 50 20 44 P 06. PROGRAMMING LABORATORY 16. O. O. PROG. & COMP. GRAPH. LAB TW TW 07. PROGRAMMING LABORATORY 50 20 40 P C 50 20 46 P PR 17. O. O. PROG. & COMP. GRAPH. LAB PR 25 10 22 P C 18. MICROPROCESSORS & INTERFACING LABTW 50 20 45 P 08. DIGITAL ELECTRONICS LABORATORY TW 50 20 38 P C 50 20 44 P 09. DIGITAL ELECTRONICS LABORATORY PR 19. MICROPROCESSORS & INTERFACING LABPR 50 20 43 P C 10. SOFT SKILLS 50 20 43 P TW 20. DATA STRUCTURES LABORATORY 50 20 38 P 21. DATA STRUCTURES LABORATORY PR GRAND TOTAL = 1014/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS: , 71200876E , S80054248 , PICT , S80054248 S80054248 HALKARE PRAJAKTA DNYANDEO SANGITA 11. ENGINEERING MATHEMATICS III PP 100 40 78 P 01. DISCRETE STRUCTURES 100 40 58 P C 100 40 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 02. PROGRAMMING & PROBLEM SOLVING PP 65 P C 64 P 100 40 100 40 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 58 P C 13. DATA STRUCTURES 55 P 100 40 59 P C 100 40 53 P 04. DATA STRUCTURES AND ALGORITHMS PP 14. COMPUTER GRAPHICS PP 100 40 100 40 05. HUMANITIES AND SOCIAL SCIENCE PP 56 P C 15. COMPUTER ORGANIZATION 25 10 20 P C 50 20 44 P 06. PROGRAMMING LABORATORY 16. O. O. PROG. & COMP. GRAPH. LAB TW TW 38 P C 50 20 41 P 07. PROGRAMMING LABORATORY 50 20 17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 08. DIGITAL ELECTRONICS LABORATORY TW 25 10 20 P C 18. MICROPROCESSORS & INTERFACING LABTW 45 P 50 20 39 P C 09. DIGITAL ELECTRONICS LABORATORY PR 19. MICROPROCESSORS & INTERFACING LABPR 50 20 42 P 10. SOFT SKILLS 50 20 41 P C 50 20 41 P TW 20. DATA STRUCTURES LABORATORY 50 20 39 P 21. DATA STRUCTURES LABORATORY PR GRAND TOTAL = 1020/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS: , 71200882K , S80054249 , PICT , S80054249 S80054249 HOOLI MAYURESH BASAVARAJ SHAILA 01. DISCRETE STRUCTURES 11. ENGINEERING MATHEMATICS III PP 100 40 P 100 40 54 P C 02. PROGRAMMING & PROBLEM SOLVING PP 100 40 40 P C 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 60 P 100 40 100 40 57 P C 13. DATA STRUCTURES 42 P 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 49 P C 14. COMPUTER GRAPHICS 100 40 47 P 100 40 57 P C 15. COMPUTER ORGANIZATION 100 40 45 P 05. HUMANITIES AND SOCIAL SCIENCE PP 25 10 06. PROGRAMMING LABORATORY 23 P C 50 20 40 P TW 16. O. O. PROG. & COMP. GRAPH. LAB TW 07. PROGRAMMING LABORATORY 50 20 40 P C 17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 39 P PR 08. DIGITAL ELECTRONICS LABORATORY TW 25 10 21 P C 18. MICROPROCESSORS & INTERFACING LABTW 50 20 35 P 05 F 09. DIGITAL ELECTRONICS LABORATORY PR 50 20 32 P C 50 20 19. MICROPROCESSORS & INTERFACING LABPR 10. SOFT SKILLS 50 20 38 P C 50 20 40 P TW 20. DATA STRUCTURES LABORATORY TW 21. DATA STRUCTURES LABORATORY PR 50 20 40 P GRAND TOTAL = 844/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER NANDA , 71347762」 , S80054250 , PICT , S80054250 S80054250 JADHAV RUTUJA RAMESH 01. DISCRETE STRUCTURES 11. ENGINEERING MATHEMATICS III PP 100 40 63 P PP 100 40 62 P C 100 40 63 P C 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 73 P 02. PROGRAMMING & PROBLEM SOLVING PP 100 40 71 P C 100 40 63 P 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 13. DATA STRUCTURES PP 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 56 P C PP 100 40 68 P 14. COMPUTER GRAPHICS 05. HUMANITIES AND SOCIAL SCIENCE PP 100 40 58 P C 15. COMPUTER ORGANIZATION 100 40 76 P 25 10 21 PC 50 20 39 P 06. PROGRAMMING LABORATORY 16. O. O. PROG. & COMP. GRAPH. LAB TW TW 07. PROGRAMMING LABORATORY 50 20 38 P C 17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 39 P PR 25 10 21 P C 18. MICROPROCESSORS & INTERFACING LABTW 50 20 40 P 08. DIGITAL ELECTRONICS LABORATORY TW 50 20 33 P C 50 20 40 P 09. DIGITAL ELECTRONICS LABORATORY PR 19. MICROPROCESSORS & INTERFACING LABPR 10. SOFT SKILLS 50 20 40 P C 20. DATA STRUCTURES LABORATORY 50 20 40 P TW 50 20 30 P 21. DATA STRUCTURES LABORATORY PR GRAND TOTAL = 1034/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS: S80054251 JAGTAP AMRUTA SATISH SANJIVANI , 71200885D , S80054251 , PICT , S80054251 01. DISCRETE STRUCTURES 11. ENGINEERING MATHEMATICS III PP 100 40 71 P PP 100 40 77 P C 100 40 60 P C 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 71 P 02. PROGRAMMING & PROBLEM SOLVING PP 13. DATA STRUCTURES 100 40 70 P C 100 40 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 64 P 100 40 58 P C 100 40 71 P 04. DATA STRUCTURES AND ALGORITHMS PP 14. COMPUTER GRAPHICS PP 100 40 71 PC 100 40 77 P 05. HUMANITIES AND SOCIAL SCIENCE PP 15. COMPUTER ORGANIZATION 25 10 06. PROGRAMMING LABORATORY 23 P C 50 20 45 P 16. O. O. PROG. & COMP. GRAPH. LAB TW TW 27 P C 50 20 40 P 07. PROGRAMMING LABORATORY 50 20 17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 08. DIGITAL ELECTRONICS LABORATORY TW 25 10 23 P C 18. MICROPROCESSORS & INTERFACING LABTW 45 P 09. DIGITAL ELECTRONICS LABORATORY PR 50 20 41 P C 19. MICROPROCESSORS & INTERFACING LABPR 50 20 43 P 10. SOFT SKILLS 50 20 43 P C 20. DATA STRUCTURES LABORATORY 50 20 43 P TW 50 20 44 P 21. DATA STRUCTURES LABORATORY PR GRAND TOTAL = 1107/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS: , 71200892G , S80054252 , PICT , S80054252 S80054252 JOSHI AKSHAY ANIL JYOTI 11. ENGINEERING MATHEMATICS III PP 100 40 58 P 01. DISCRETE STRUCTURES PP 100 40 71 P C 02. PROGRAMMING & PROBLEM SOLVING PP 100 40 45 P C 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 65 P PP 100 40 56 P 100 40 74 P C 13. DATA STRUCTURES 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 48 P C 14. COMPUTER GRAPHICS 100 40 58 P 05. HUMANITIES AND SOCIAL SCIENCE PP 100 40 60 P C 15. COMPUTER ORGANIZATION 100 40 73 P 25 10 06. PROGRAMMING LABORATORY 18 P C 50 20 43 P 16. O. O. PROG. & COMP. GRAPH. LAB TW 07. PROGRAMMING LABORATORY 50 20 35 P C 17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 46 P PR 08. DIGITAL ELECTRONICS LABORATORY TW 25 10 21 P C 18. MICROPROCESSORS & INTERFACING LABTW 50 20 44 P 09. DIGITAL ELECTRONICS LABORATORY PR 50 20 42 P C 50 20 28 P 19. MICROPROCESSORS & INTERFACING LABPR 10. SOFT SKILLS 50 20 40 P C 50 20 42 P TW 20. DATA STRUCTURES LABORATORY TW 21. DATA STRUCTURES LABORATORY PR 50 20 41 P GRAND TOTAL = 1008/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS: 

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER PALLAVEE , 71200904D , S80054259 , PICT , S80054259 S80054259 KALUSKAR SANIYA VIKRAM 01. DISCRETE STRUCTURES 11. ENGINEERING MATHEMATICS III PP 100 40 67 P PP 100 40 75 P C 100 40 61 P C 12. MICROPROC. & INTERFACING TECHNIO.PP 100 40 75 P 02. PROGRAMMING & PROBLEM SOLVING PP 100 40 57 P C 100 40 53 P 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 13. DATA STRUCTURES PP 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 40 P C PP 100 40 69 P 14. COMPUTER GRAPHICS 05. HUMANITIES AND SOCIAL SCIENCE PP 100 40 57 P C 15. COMPUTER ORGANIZATION 100 40 25 10 17 P C 50 20 40 P 06. PROGRAMMING LABORATORY 16. O. O. PROG. & COMP. GRAPH. LAB TW TW 07. PROGRAMMING LABORATORY 50 20 41 P C 17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 41 P PR 25 10 19 P C 18. MICROPROCESSORS & INTERFACING LABTW 50 20 43 P 08. DIGITAL ELECTRONICS LABORATORY TW 50 20 39 P C 50 20 40 P 09. DIGITAL ELECTRONICS LABORATORY PR 19. MICROPROCESSORS & INTERFACING LABPR 50 20 39 P C 10. SOFT SKILLS 20. DATA STRUCTURES LABORATORY 50 20 40 P TW 50 20 41 P 21. DATA STRUCTURES LABORATORY PR GRAND TOTAL = 1022/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS: S80054260 KAMBALE NILESH VIJAY JAYMALA , 71347766M , S80054260 , PICT , S80054260 01. DISCRETE STRUCTURES 11. ENGINEERING MATHEMATICS III PP 100 40 09 F 40 P PP 100 40 60 P 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 25 F 02. PROGRAMMING & PROBLEM SOLVING PP 100 40 13. DATA STRUCTURES 100 40 42 P 100 40 29 F 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 40 42 P 100 40 45 P 04. DATA STRUCTURES AND ALGORITHMS PP 14. COMPUTER GRAPHICS PP 100 40 100 40 05. HUMANITIES AND SOCIAL SCIENCE PP 52 P C 15. COMPUTER ORGANIZATION 47 P 20 P C 06. PROGRAMMING LABORATORY 25 10 50 20 28 P 16. O. O. PROG. & COMP. GRAPH. LAB TW TW 50 20 23 P 07. PROGRAMMING LABORATORY 50 20 AA F 17. O. O. PROG. & COMP. GRAPH. LAB PR 25 10 50 20 08. DIGITAL ELECTRONICS LABORATORY TW 18 P C 18. MICROPROCESSORS & INTERFACING LABTW 30 P 50 20 20 P 09. DIGITAL ELECTRONICS LABORATORY PR 50 20 22 P C 19. MICROPROCESSORS & INTERFACING LABPR 10. SOFT SKILLS 50 20 29 P C 50 20 30 P TW 20. DATA STRUCTURES LABORATORY 50 20 22 P 21. DATA STRUCTURES LABORATORY PR GRAND TOTAL = 633/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS : LATA , 71200905B , S80054261 , PICT , S80054261 S80054261 KAMBLE ASHAY UTTAM 01. DISCRETE STRUCTURES 11. ENGINEERING MATHEMATICS III PP 100 40 42 P PP 100 40 44 P C 02. PROGRAMMING & PROBLEM SOLVING PP 100 40 50 P C 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 50 P 100 40 55 P 100 40 59 P C 13. DATA STRUCTURES 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 47 P C 14. COMPUTER GRAPHICS 100 40 61 P 100 40 61 P C 15. COMPUTER ORGANIZATION 100 40 56 P 05. HUMANITIES AND SOCIAL SCIENCE PP 25 10 30 P 06. PROGRAMMING LABORATORY 20 P C 50 20 TW 16. O. O. PROG. & COMP. GRAPH. LAB TW 07. PROGRAMMING LABORATORY 50 20 41 P C 17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 38 P PR 08. DIGITAL ELECTRONICS LABORATORY TW 25 10 18 P C 18. MICROPROCESSORS & INTERFACING LABTW 50 20 27 P 50 20 38 P 09. DIGITAL ELECTRONICS LABORATORY PR 50 20 34 P C 19. MICROPROCESSORS & INTERFACING LABPR 10. SOFT SKILLS 50 20 35 P C 50 20 27 P TW 20. DATA STRUCTURES LABORATORY TW 21. DATA STRUCTURES LABORATORY PR 50 20 34 P GRAND TOTAL = 867/1500, RESULT: HIGHER SECOND CLASS ORDN. 1 MARKS: 

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER KETAKI , 71200910J , S80054262 , PICT , S80054262 S80054262 KAPILESHWAR KAUSTUBH DATTATRAYA 11. ENGINEERING MATHEMATICS III PP 100 40 76 P PP 100 40 67 P C 01. DISCRETE STRUCTURES 100 40 60 P C 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 02. PROGRAMMING & PROBLEM SOLVING PP 100 40 71 P C 100 40 51 P 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 13. DATA STRUCTURES PP 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 49 P C PP 100 40 65 P 14. COMPUTER GRAPHICS 05. HUMANITIES AND SOCIAL SCIENCE PP 100 40 56 P C 15. COMPUTER ORGANIZATION 100 40 25 10 20 P C 50 20 42 P 06. PROGRAMMING LABORATORY 16. O. O. PROG. & COMP. GRAPH. LAB TW TW 07. PROGRAMMING LABORATORY 50 20 43 P C 17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 42 P PR 25 10 22 P C 18. MICROPROCESSORS & INTERFACING LABTW 50 20 42 P 08. DIGITAL ELECTRONICS LABORATORY TW 50 20 40 P C 50 20 40 P 09. DIGITAL ELECTRONICS LABORATORY PR 19. MICROPROCESSORS & INTERFACING LABPR 50 20 42 P C 10. SOFT SKILLS 50 20 42 P TW 20. DATA STRUCTURES LABORATORY 50 20 41 P 21. DATA STRUCTURES LABORATORY PR GRAND TOTAL = 1036/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS: , 71200912E , S80054263 , PICT , S80054263 S80054263 KARPE AKASH TUKARAM ASHA 11. ENGINEERING MATHEMATICS III PP 100 40 41 P 01. DISCRETE STRUCTURES 55 P C PP 100 40 100 40 52 P C 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 02. PROGRAMMING & PROBLEM SOLVING PP 43 P 61 P C 100 40 100 40 53 P 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 13. DATA STRUCTURES 100 40 58 P C 100 40 58 P 04. DATA STRUCTURES AND ALGORITHMS PP 14. COMPUTER GRAPHICS PP 100 40 59 P C 100 40 70 P 05. HUMANITIES AND SOCIAL SCIENCE PP 15. COMPUTER ORGANIZATION 25 10 06. PROGRAMMING LABORATORY 21 P C 50 20 39 P 16. O. O. PROG. & COMP. GRAPH. LAB TW TW 35 P C 50 20 31 P 07. PROGRAMMING LABORATORY 50 20 17. O. O. PROG. & COMP. GRAPH. LAB PR 25 10 50 20 37 P 08. DIGITAL ELECTRONICS LABORATORY TW 22 P C 18. MICROPROCESSORS & INTERFACING LABTW 50 20 22 P 09. DIGITAL ELECTRONICS LABORATORY PR 50 20 35 P C 19. MICROPROCESSORS & INTERFACING LABPR 10. SOFT SKILLS 50 20 38 P C 50 20 40 P TW 20. DATA STRUCTURES LABORATORY 50 20 39 P 21. DATA STRUCTURES LABORATORY PR GRAND TOTAL = 909/1500, RESULT: FIRST CLASS ORDN. 1 MARKS: , 71200913C , S80054264 , PICT , S80054264 S80054264 KARTIK ANIL REDDY NAMRATA 01. DISCRETE STRUCTURES 100 40 58 P C 11. ENGINEERING MATHEMATICS III PP 100 40 P 02. PROGRAMMING & PROBLEM SOLVING PP 100 40 61 P C 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 58 P 100 40 57 P 100 40 57 P C 13. DATA STRUCTURES 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 47 P C 14. COMPUTER GRAPHICS 100 40 63 P 05. HUMANITIES AND SOCIAL SCIENCE PP 100 40 57 P C 15. COMPUTER ORGANIZATION 100 40 69 P 25 10 17 P C 06. PROGRAMMING LABORATORY 50 20 40 P TW 16. O. O. PROG. & COMP. GRAPH. LAB TW 07. PROGRAMMING LABORATORY 50 20 40 P C 17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 42 P PR 08. DIGITAL ELECTRONICS LABORATORY TW 25 10 18 P C 18. MICROPROCESSORS & INTERFACING LABTW 50 20 38 P 50 20 34 P 09. DIGITAL ELECTRONICS LABORATORY PR 50 20 32 P C 19. MICROPROCESSORS & INTERFACING LABPR 10. SOFT SKILLS 50 20 35 P C 50 20 39 P TW 20. DATA STRUCTURES LABORATORY TW 21. DATA STRUCTURES LABORATORY PR 50 20 37 P GRAND TOTAL = 939/1500, RESULT: FIRST CLASS ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER SWARUPA , 71200914M , S80054265 , PICT , S80054265 S80054265 KASAT SARVESH BALKISAN 01. DISCRETE STRUCTURES 11. ENGINEERING MATHEMATICS III PP 100 40 70 P PP 100 40 65 P C 100 40 53 P C 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 02. PROGRAMMING & PROBLEM SOLVING PP 100 40 69 P C 100 40 60 P 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 13. DATA STRUCTURES PP 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 55 P C PP 100 40 65 P 14. COMPUTER GRAPHICS 05. HUMANITIES AND SOCIAL SCIENCE PP 100 40 66 P C 15. COMPUTER ORGANIZATION 100 40 70 P 25 10 23 P C 50 20 41 P 06. PROGRAMMING LABORATORY 16. O. O. PROG. & COMP. GRAPH. LAB TW TW 07. PROGRAMMING LABORATORY 50 20 41 P C 17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 42 P PR 25 10 22 P C 18. MICROPROCESSORS & INTERFACING LABTW 50 20 39 P 08. DIGITAL ELECTRONICS LABORATORY TW 50 20 40 P C 50 20 30 P 09. DIGITAL ELECTRONICS LABORATORY PR 19. MICROPROCESSORS & INTERFACING LABPR 50 20 40 P C 10. SOFT SKILLS 20. DATA STRUCTURES LABORATORY 50 20 39 P TW 50 20 39 P 21. DATA STRUCTURES LABORATORY PR GRAND TOTAL = 1029/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS: , 71200919B , S80054266 , PICT , S80054266 S80054266 KHADE SOURABH DURYODHAN SANGITA 11. ENGINEERING MATHEMATICS III PP 100 40 14 F 01. DISCRETE STRUCTURES 100 40 56 P C 100 40 45 P C 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 02. PROGRAMMING & PROBLEM SOLVING PP 43 P 13. DATA STRUCTURES 100 40 62 P C 100 40 49 P 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 40 51 P C 100 40 58 P 04. DATA STRUCTURES AND ALGORITHMS PP 14. COMPUTER GRAPHICS PP 100 40 48 P C 100 40 49 P 05. HUMANITIES AND SOCIAL SCIENCE PP 15. COMPUTER ORGANIZATION 25 10 06. PROGRAMMING LABORATORY 50 20 30 P 16 P C 16. O. O. PROG. & COMP. GRAPH. LAB TW TW 50 20 36 P C 50 20 32 P 07. PROGRAMMING LABORATORY 17. O. O. PROG. & COMP. GRAPH. LAB PR 25 10 17 P C 50 20 08. DIGITAL ELECTRONICS LABORATORY TW 18. MICROPROCESSORS & INTERFACING LABTW 32 P 50 20 30 P 09. DIGITAL ELECTRONICS LABORATORY PR 50 20 42 P C 19. MICROPROCESSORS & INTERFACING LABPR 10. SOFT SKILLS 50 20 34 P C 50 20 33 P TW 20. DATA STRUCTURES LABORATORY 50 20 30 P 21. DATA STRUCTURES LABORATORY PR GRAND TOTAL = 807/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS: , 71200920F , S80054267 , PICT , S80054267 USHA S80054267 KHADKE SRUSHTI SANJEEV 01. DISCRETE STRUCTURES 11. ENGINEERING MATHEMATICS III PP 100 40 P 100 40 50 P C 02. PROGRAMMING & PROBLEM SOLVING PP 100 40 46 P C 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 47 P 100 40 100 40 57 P C 13. DATA STRUCTURES 42 P 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 43 P C 14. COMPUTER GRAPHICS 100 40 50 P 100 40 55 P C 15. COMPUTER ORGANIZATION 100 40 59 P 05. HUMANITIES AND SOCIAL SCIENCE PP 25 10 38 P 06. PROGRAMMING LABORATORY 21 P C 50 20 TW 16. O. O. PROG. & COMP. GRAPH. LAB TW 07. PROGRAMMING LABORATORY 50 20 40 P C 17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 36 P PR 08. DIGITAL ELECTRONICS LABORATORY TW 25 10 22 P C 18. MICROPROCESSORS & INTERFACING LABTW 50 20 38 P 50 20 25 P 09. DIGITAL ELECTRONICS LABORATORY PR 50 20 32 P C 19. MICROPROCESSORS & INTERFACING LABPR 10. SOFT SKILLS 50 20 38 P C 50 20 41 P TW 20. DATA STRUCTURES LABORATORY TW 21. DATA STRUCTURES LABORATORY PR 50 20 35 P GRAND TOTAL = 855/1500, RESULT: HIGHER SECOND CLASS ORDN. 1 MARKS: 

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER PUSHPA , 71200923L , S80054268 , PICT , S80054268 S80054268 KHANDAVE MAYUR NARAYANRAO O1. DISCRETE STRUCTURES PP 11. ENGINEERING MATHEMATICS III PP 100 40 54 P 100 40 58 P C 100 40 57 P C 12. MICROPROC. & INTERFACING TECHNIO.PP 100 40 02. PROGRAMMING & PROBLEM SOLVING PP 100 40 62 P C 100 40 56 P 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 13. DATA STRUCTURES PP 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 51 P C PP 100 40 56 P 14. COMPUTER GRAPHICS 05. HUMANITIES AND SOCIAL SCIENCE PP 100 40 66 P C 15. COMPUTER ORGANIZATION 100 40 25 10 16 P C 50 20 39 P 06. PROGRAMMING LABORATORY 16. O. O. PROG. & COMP. GRAPH. LAB TW TW 07. PROGRAMMING LABORATORY 50 20 41 P C 17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 39 P PR 25 10 17 P C 18. MICROPROCESSORS & INTERFACING LABTW 50 20 39 P 08. DIGITAL ELECTRONICS LABORATORY TW 50 20 44 P C 50 20 40 P 09. DIGITAL ELECTRONICS LABORATORY PR 19. MICROPROCESSORS & INTERFACING LABPR 50 20 33 P C 10. SOFT SKILLS 50 20 39 P TW 20. DATA STRUCTURES LABORATORY 50 20 39 P 21. DATA STRUCTURES LABORATORY PR GRAND TOTAL = 984/1500, RESULT: FIRST CLASS ORDN. 1 MARKS : S80054269 KOTHARI APARNA ASHOK ARUNA , 71200931M , S80054269 , PICT , S80054269 01. DISCRETE STRUCTURES 11. ENGINEERING MATHEMATICS III PP 100 40 64 P PP 100 40 73 P C 100 40 57 P C 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 02. PROGRAMMING & PROBLEM SOLVING PP 66 P 60 P C 100 40 100 40 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 13. DATA STRUCTURES 62 P 100 40 52 P C 100 40 62 P 04. DATA STRUCTURES AND ALGORITHMS PP 14. COMPUTER GRAPHICS PP 100 40 53 P C 100 40 05. HUMANITIES AND SOCIAL SCIENCE PP 15. COMPUTER ORGANIZATION 63 P 25 10 06. PROGRAMMING LABORATORY 21 P C 50 20 42 P 16. O. O. PROG. & COMP. GRAPH. LAB TW TW 37 P C 50 20 07. PROGRAMMING LABORATORY 50 20 17. O. O. PROG. & COMP. GRAPH. LAB PR 44 P 50 20 08. DIGITAL ELECTRONICS LABORATORY TW 25 10 21 P C 18. MICROPROCESSORS & INTERFACING LABTW 40 P 50 20 09. DIGITAL ELECTRONICS LABORATORY PR 50 20 46 P C 19. MICROPROCESSORS & INTERFACING LABPR 38 P 10. SOFT SKILLS 50 20 36 P C 50 20 43 P TW 20. DATA STRUCTURES LABORATORY 50 20 40 P 21. DATA STRUCTURES LABORATORY PR GRAND TOTAL = 1020/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS: , 71200934F , S80054270 , PICT , S80054270 SAVANI S80054270 KULKARNI JANHAVI SUHASCHANDRA 11. ENGINEERING MATHEMATICS III PP 100 40 69 P 01. DISCRETE STRUCTURES 100 40 74 P C 02. PROGRAMMING & PROBLEM SOLVING PP 100 40 69 P C 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 75 P 100 40 100 40 63 P C 13. DATA STRUCTURES 58 P 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 49 P C 14. COMPUTER GRAPHICS 100 40 62 P 100 40 62 P C 15. COMPUTER ORGANIZATION 100 40 63 P 05. HUMANITIES AND SOCIAL SCIENCE PP 25 10 36 P 06. PROGRAMMING LABORATORY 18 P C 50 20 TW 16. O. O. PROG. & COMP. GRAPH. LAB TW 07. PROGRAMMING LABORATORY 50 20 40 P C 17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 42 P PR 08. DIGITAL ELECTRONICS LABORATORY TW 25 10 18 P C 18. MICROPROCESSORS & INTERFACING LABTW 50 20 38 P 39 P 09. DIGITAL ELECTRONICS LABORATORY PR 50 20 41 P C 50 20 19. MICROPROCESSORS & INTERFACING LABPR 10. SOFT SKILLS 50 20 38 P C 50 20 38 P TW 20. DATA STRUCTURES LABORATORY TW 21. DATA STRUCTURES LABORATORY PR 50 20 41 P GRAND TOTAL = 1033/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS: 

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DATE : 27 JULY 2013							C TECHNOLOGY, TONE.	1713	L 110.	۷,		,,,
NOTE: FIRST LINE : SEAT NO., NAME												
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·			-			•						
COOF4271 VIII KARNT MALIJAR CINTI												
S80054271 KULKARNI MALHAR SUNIL	DD	100	40		JAYANT		, 71200935D , S80054271			-	S8005	
01. DISCRETE STRUCTURES	PP	100			PC		ENGINEERING MATHEMATICS III	PP	100	_	43	
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40		P C		MICROPROC. & INTERFACING TECHNI	•	100		57	
03. DIGIT. ELECTRONICS & LOGIC DESI		100	40	_	PC	_	DATA STRUCTURES	PP	100		53	
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40		PC		COMPUTER GRAPHICS		100	_	66	
05. HUMANITIES AND SOCIAL SCIENCE	PP — .	100			P C		COMPUTER ORGANIZATION		100		63	
06. PROGRAMMING LABORATORY	TW	25	10		P C		O. O. PROG. & COMP. GRAPH. LAB		50		45	
07. PROGRAMMING LABORATORY	PR	50	20		РС		O. O. PROG. & COMP. GRAPH. LAB	PR		20	46	
08. DIGITAL ELECTRONICS LABORATORY	TW		10		РС		MICROPROCESSORS & INTERFACING L			20	43	
09. DIGITAL ELECTRONICS LABORATORY	PR	50		_	РС		MICROPROCESSORS & INTERFACING L			20	45	
10. SOFT SKILLS	TW	50	20	39	РС		DATA STRUCTURES LABORATORY			20	45	
						21.	DATA STRUCTURES LABORATORY	PR	50	20	45	Р
GRAND TOTAL = $1024/1500$ , RESULT: FIRS	T CLASS	S WITH	DIS	TINCT:	ION							
ORDN. 1 MARKS :												
S80054272 LANKE PRASAD PRAVIN				SUI	NANDA		, 71200943E , S80054272	, PI	CT	,	S8005	
01. DISCRETE STRUCTURES	PP	100	40	84	РС	11.	ENGINEERING MATHEMATICS III	PP	100	40	93	Р
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	74	PC	12.	MICROPROC. & INTERFACING TECHNI	Q.PP	100	40	73	Р
03. DIGIT. ELECTRONICS & LOGIC DESI	GNPP	100	40	87	PC	13.	DATA STRUCTURES	PP	100	40	66	Р
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	63	РС	14.	COMPUTER GRAPHICS	PP	100	40	81	Р
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	73	PC	15.	COMPUTER ORGANIZATION	PP	100	40	83	Р
06. PROGRAMMING LABORATORY	TW	25	10	24	РС	16.	O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	47	Р
07. PROGRAMMING LABORATORY	PR	50	20	42	PC	17.	O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	44	Р
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	24	PC	18.	MICROPROCESSORS & INTERFACING L	ABTW	50	20	47	Р
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	48	PC	19.	MICROPROCESSORS & INTERFACING L	ABPR	50	20	46	Р
10. SOFT SKILLS	TW	50	20	46	PC	20.	DATA STRUCTURES LABORATORY	TW	50	20	47	Р
						21.	DATA STRUCTURES LABORATORY	PR	50	20	43	Р
GRAND TOTAL = 1235/1500, RESULT: FIRS	T CLAS	S WITH	DIS	TINCT:	ION							
ORDN. 1 MARKS :												
S80054273 MAHAJAN ABOLI VIKRANT				AR	CHANA		, 71200946к , s80054273	, PI	CT	,	s8005	4273
01. DISCRETE STRUCTURES	PP	100	40	49	РС	11.	ENGINEERING MATHEMATICS III	PP	100	40	81	Р
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	47	РС	12.	MICROPROC. & INTERFACING TECHNI	Q.PP	100	40	60	Р
03. DIGIT. ELECTRONICS & LOGIC DESI	GNPP	100	40	75	РС	13.	DATA STRUCTURES	PP	100	40	54	Р
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	48	РС	14.	COMPUTER GRAPHICS	PP	100	40	68	Р
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	61	РС	15.	COMPUTER ORGANIZATION	PP	100	40	57	Р
06. PROGRAMMING LABORATORY	TW	25	10	23	РС	16.	O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	37	Р
07. PROGRAMMING LABORATORY	PR	50	20	43	РС	17.	O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	45	Р
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	23	РС	18.	MICROPROCESSORS & INTERFACING L	ABTW	50	20	37	Р
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	45	РС	19.	MICROPROCESSORS & INTERFACING L	ABPR	50	20	38	Р
10. SOFT SKILLS	TW	50	20	42	РС	20.	DATA STRUCTURES LABORATORY	TW	50	20	40	Р
						21.	DATA STRUCTURES LABORATORY	PR	50	20	42	Р
GRAND TOTAL = 1015/1500, RESULT: FIRS	T CLASS	S WITH	DIS	TINCT:	ION							
ORDN. 1 MARKS :												

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71200947H , S80054274 , PICT , S80054274 S80054274 MAHAJAN PRITESH JAYRAM ANUSAYA 01. DISCRETE STRUCTURES 11. ENGINEERING MATHEMATICS III PP 100 40 AA F PP 100 40 40 P C 100 40 AA F 12. MICROPROC. & INTERFACING TECHNIO.PP 100 40 10 F 02. PROGRAMMING & PROBLEM SOLVING PP 100 40 40 P C 100 40 40 P 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 13. DATA STRUCTURES PP 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 AA F PP 100 40 40 P 14. COMPUTER GRAPHICS 05. HUMANITIES AND SOCIAL SCIENCE PP 100 40 52 P C 15. COMPUTER ORGANIZATION 100 40 24 F 25 10 22 P C 50 20 35 P 06. PROGRAMMING LABORATORY 16. O. O. PROG. & COMP. GRAPH. LAB TW TW 50 20 22 P 07. PROGRAMMING LABORATORY 50 20 24 P C PR 17. O. O. PROG. & COMP. GRAPH. LAB PR 25 10 21 P C 50 20 32 P 08. DIGITAL ELECTRONICS LABORATORY TW 18. MICROPROCESSORS & INTERFACING LABTW 50 20 21 P 50 20 05 F 09. DIGITAL ELECTRONICS LABORATORY PR 19. MICROPROCESSORS & INTERFACING LABPR 50 20 38 P C 10. SOFT SKILLS 20. DATA STRUCTURES LABORATORY 50 20 34 P TW 50 20 10 F 21. DATA STRUCTURES LABORATORY PR GRAND TOTAL = 510/1500, RESULT: FAILS ORDN. 1 MARKS: MANGALA , 71200948F , S80054275 , PICT , S80054275 S80054275 MAHALE DHANSHRI RAJENDRA 01. DISCRETE STRUCTURES 11. ENGINEERING MATHEMATICS III PP 100 40 58 P 100 40 60 P C 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 02. PROGRAMMING & PROBLEM SOLVING PP 100 40 45 P C 60 P 13. DATA STRUCTURES 100 40 46 P C 100 40 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 60 P 100 40 40 P C 100 40 04. DATA STRUCTURES AND ALGORITHMS PP 14. COMPUTER GRAPHICS PP 58 P 100 40 51 P C 100 40 05. HUMANITIES AND SOCIAL SCIENCE PP 15. COMPUTER ORGANIZATION 67 P 25 10 18 P C 50 20 40 P 06. PROGRAMMING LABORATORY 16. O. O. PROG. & COMP. GRAPH. LAB TW TW 36 P C 50 20 41 P 07. PROGRAMMING LABORATORY 50 20 17. O. O. PROG. & COMP. GRAPH. LAB PR 25 10 50 20 08. DIGITAL ELECTRONICS LABORATORY TW 18 P C 18. MICROPROCESSORS & INTERFACING LABTW 42 P 50 20 28 P 09. DIGITAL ELECTRONICS LABORATORY PR 50 20 29 P C 19. MICROPROCESSORS & INTERFACING LABPR 10. SOFT SKILLS 50 20 37 P C 50 20 40 P TW 20. DATA STRUCTURES LABORATORY 50 20 38 P 21. DATA STRUCTURES LABORATORY PR GRAND TOTAL = 912/1500, RESULT: FIRST CLASS ORDN. 1 MARKS: , 71200950H , S80054276 , PICT , S80054276 S80054276 MAITHILI TAKALE MEENA 11. ENGINEERING MATHEMATICS III PP 100 40 17 F 01. DISCRETE STRUCTURES PP 100 40 48 P C 02. PROGRAMMING & PROBLEM SOLVING PP 100 40 50 P C 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 49 P PP 100 40 49 P 100 40 51 P C 13. DATA STRUCTURES 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 40 P C 14. COMPUTER GRAPHICS 100 40 59 P 100 40 64 P C 15. COMPUTER ORGANIZATION 100 40 55 P 05. HUMANITIES AND SOCIAL SCIENCE PP 25 10 36 P 06. PROGRAMMING LABORATORY 21 P C 50 20 16. O. O. PROG. & COMP. GRAPH. LAB TW 07. PROGRAMMING LABORATORY 50 20 37 P C 17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 34 P PR 08. DIGITAL ELECTRONICS LABORATORY TW 25 10 21 P C 18. MICROPROCESSORS & INTERFACING LABTW 50 20 35 P 09. DIGITAL ELECTRONICS LABORATORY PR 50 20 37 P C 50 20 07 F 19. MICROPROCESSORS & INTERFACING LABPR 10. SOFT SKILLS 50 20 40 P C 50 20 38 P TW 20. DATA STRUCTURES LABORATORY TW 21. DATA STRUCTURES LABORATORY PR 50 20 32 P GRAND TOTAL = 820/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS:

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NOTE: FIRST LINE : SEAT NO., NAME				-	-			<pre>KEG. NO., PREVIOUS SEAT NO.,  (S OBTAINED, P/F:PASS/FAIL, C</pre>		•			
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S80054277 MALI PARAG SHRIKRISHNA					 JNANDA		• •	, 71200951F , S80054277				 S8005	
01. DISCRETE STRUCTURES	PP	100	40		P C		11		, PP	100	-	68	
02. PROGRAMMING & PROBLEM SOLVING	PP	100		_	PC			MICROPROC. & INTERFACING TECHNI		100		61	
03. DIGIT. ELECTRONICS & LOGIC DESI		100	40		PC			DATA STRUCTURES	PP	100		68	
04. DATA STRUCTURES AND ALGORITHMS	PP	100	_		PC			COMPUTER GRAPHICS		100		74	
05. HUMANITIES AND SOCIAL SCIENCE	PP	100			PC			COMPUTER GRAPHICS  COMPUTER ORGANIZATION		100		82	
06. PROGRAMMING LABORATORY			10		PC		_	O. O. PROG. & COMP. GRAPH. LAB			20	45	
07. PROGRAMMING LABORATORY			20		РС			O. O. PROG. & COMP. GRAPH. LAB	PR		20	40	
08. DIGITAL ELECTRONICS LABORATORY	TW		10		РС			MICROPROCESSORS & INTERFACING I			20	45	
09. DIGITAL ELECTRONICS LABORATORY	PR	_	20		PC	_		MICROPROCESSORS & INTERFACING I			20	43	
10. SOFT SKILLS	TW		20		PC						20	45	
10. SUFT SKILLS	I VV	30	20	43	PC			DATA STRUCTURES LABORATORY			20	43 41	
CRAND TOTAL = 1122/1500 DECULT: FIRE	T CLAS	C WITT	ı DTC	TTNCT	CTON!	2	ZI.	DATA STRUCTURES LABORATORY	PR	30	20	41	Р
GRAND TOTAL = 1132/1500, RESULT: FIRS	I CLAS	S WIIF	ı DI2	IINCI	I TON								
ORDN. 1 MARKS :													
S80054278 MANE SAGAR ASHOK					 ANGITA			, 71200955J , S80054278				s8005	
01. DISCRETE STRUCTURES	DD	100	40		P C		11	ENGINEERING MATHEMATICS III	•	100	-	14	
02. PROGRAMMING & PROBLEM SOLVING	PP	100			РС			MICROPROC. & INTERFACING TECHNI		100		40	
03. DIGIT. ELECTRONICS & LOGIC DESI		100	_		РС			DATA STRUCTURES	PP	100		47	
04. DATA STRUCTURES AND ALGORITHMS	PP	100		42				COMPUTER GRAPHICS		100		63	
05. HUMANITIES AND SOCIAL SCIENCE	PP	100			P C			COMPUTER GRAPHICS  COMPUTER ORGANIZATION		100		62	
06. PROGRAMMING LABORATORY			10		PC			O. O. PROG. & COMP. GRAPH. LAB			20	37	
07. PROGRAMMING LABORATORY	PR		20	14				O. O. PROG. & COMP. GRAPH. LAB	PR		20	30	=
08. DIGITAL ELECTRONICS LABORATORY	TW		10		РC			MICROPROCESSORS & INTERFACING I			20	36	
09. DIGITAL ELECTRONICS LABORATORY	PR	50			PC			MICROPROCESSORS & INTERFACING I			20	07	
10. SOFT SKILLS	TW		20		РС			DATA STRUCTURES LABORATORY	TW		20	38	=
10. 3011 3KILLS	I VV	30	20	33	7 C			DATA STRUCTURES LABORATORY			20	20	
GRAND TOTAL = 763/1500, RESULT: FAIL	слт	κт				2	<b>41.</b>	DATA STRUCTURES LABORATORT	FIX	30	20	20	Г
ORDN. 1 MARKS:	3 A.I.	K. I .											
ONDIN. I MARKS .													
S80054279 MANTRI SHARAYU SHARAD					 RIYA			, 71200957E , S80054279				 S8005	
01. DISCRETE STRUCTURES	PP	100	40		PC	1	11	ENGINEERING MATHEMATICS III	-	100	-	86	
02. PROGRAMMING & PROBLEM SOLVING	PP	100			РС			MICROPROC. & INTERFACING TECHNIC		100			
03. DIGIT. ELECTRONICS & LOGIC DESI		100			PC			DATA STRUCTURES	PP	100	_	66	
04. DATA STRUCTURES AND ALGORITHMS	PP	100			РС			COMPUTER GRAPHICS		100		67	
05. HUMANITIES AND SOCIAL SCIENCE	PP	100			РС			COMPUTER ORGANIZATION		100		69	
06. PROGRAMMING LABORATORY			10		PC			O. O. PROG. & COMP. GRAPH. LAB			20	41	
07. PROGRAMMING LABORATORY			20		РС			O. O. PROG. & COMP. GRAPH. LAB	PR		20	43	
08. DIGITAL ELECTRONICS LABORATORY			10		РС			MICROPROCESSORS & INTERFACING I			20	45	
09. DIGITAL ELECTRONICS LABORATORY			20		РС			MICROPROCESSORS & INTERFACING I			20	30	
10. SOFT SKILLS	TW		20		РС			DATA STRUCTURES LABORATORY	TW		20	43	
		50	20	12				DATA STRUCTURES LABORATORY			20	39	
GRAND TOTAL = 1064/1500, RESULT: FIRS	T CLAS	S WTTL	אחו	TINCT	LION			Z SINGELONES ENDOVATION	1 11	30	-0	33	•
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DATE : 27 JULY 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 27 ( 480)

NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER MANISHA , 71347767к , S80054280 , РІСТ , S80054280 S80054280 MHASKE POOJA DAGDU 11. ENGINEERING MATHEMATICS III PP 100 40 40 P 01. DISCRETE STRUCTURES PP 100 40 41 P C 100 40 52 P C 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 61 P 02. PROGRAMMING & PROBLEM SOLVING PP 100 40 60 P C 100 40 64 P 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 13. DATA STRUCTURES PP 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 47 P C PP 100 40 61 P 14. COMPUTER GRAPHICS 05. HUMANITIES AND SOCIAL SCIENCE PP 100 40 55 P C 15. COMPUTER ORGANIZATION 100 40 66 P 25 10 20 P C 50 20 42 P 06. PROGRAMMING LABORATORY 16. O. O. PROG. & COMP. GRAPH. LAB TW TW 07. PROGRAMMING LABORATORY 50 20 29 P C 17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 39 P PR 25 10 20 P C 18. MICROPROCESSORS & INTERFACING LABTW 50 20 43 P 08. DIGITAL ELECTRONICS LABORATORY TW 50 20 30 P C 50 20 20 P 09. DIGITAL ELECTRONICS LABORATORY PR 19. MICROPROCESSORS & INTERFACING LABPR TW 50 20 42 P C 10. SOFT SKILLS 20. DATA STRUCTURES LABORATORY 50 20 44 P 50 20 39 P 21. DATA STRUCTURES LABORATORY PR GRAND TOTAL = 915/1500, RESULT: FIRST CLASS ORDN. 1 MARKS : LATA , 71347768H , S80054281 , PICT , S80054281 S80054281 MHASKULE YOGESH DATTATRAYA 11. ENGINEERING MATHEMATICS III PP 100 40 26 F 01. DISCRETE STRUCTURES PP 46 P C 100 40 100 40 58 P C 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 02. PROGRAMMING & PROBLEM SOLVING PP 63 P 13. DATA STRUCTURES 100 40 56 P C 100 40 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 58 P 100 40 46 P C 100 40 65 P 04. DATA STRUCTURES AND ALGORITHMS PP 14. COMPUTER GRAPHICS PP 100 40 64 P C 100 40 05. HUMANITIES AND SOCIAL SCIENCE PP 15. COMPUTER ORGANIZATION 71 P 25 10 21 P C 50 20 40 P 06. PROGRAMMING LABORATORY 16. O. O. PROG. & COMP. GRAPH. LAB TW TW 24 P C 50 20 31 P 07. PROGRAMMING LABORATORY 50 20 17. O. O. PROG. & COMP. GRAPH. LAB PR 25 10 19 P C 50 20 08. DIGITAL ELECTRONICS LABORATORY TW 18. MICROPROCESSORS & INTERFACING LABTW 42 P 50 20 34 P 09. DIGITAL ELECTRONICS LABORATORY PR 50 20 28 P C 19. MICROPROCESSORS & INTERFACING LABPR 10. SOFT SKILLS 50 20 38 P C 50 20 39 P TW 20. DATA STRUCTURES LABORATORY 50 20 39 P 21. DATA STRUCTURES LABORATORY PR GRAND TOTAL = 908/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS: , 71200962M , S80054282 , PICT , S80054282 S80054282 MISAL AKSHAYA ARJUN SANGITA 11. ENGINEERING MATHEMATICS III PP 100 40 45 P 01. DISCRETE STRUCTURES PP 100 40 61 P C 02. PROGRAMMING & PROBLEM SOLVING PP 100 40 56 P C 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 53 P PP 100 40 100 40 58 P C 13. DATA STRUCTURES 63 P 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 53 P C 14. COMPUTER GRAPHICS 100 40 57 P 100 40 59 P C 15. COMPUTER ORGANIZATION 100 40 73 P 05. HUMANITIES AND SOCIAL SCIENCE PP 25 10 22 P C 06. PROGRAMMING LABORATORY 50 20 40 P 16. O. O. PROG. & COMP. GRAPH. LAB TW 07. PROGRAMMING LABORATORY 50 20 26 P C 17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 38 P PR 08. DIGITAL ELECTRONICS LABORATORY TW 25 10 22 P C 18. MICROPROCESSORS & INTERFACING LABTW 50 20 40 P 09. DIGITAL ELECTRONICS LABORATORY PR 50 20 29 P C 50 20 05 F 19. MICROPROCESSORS & INTERFACING LABPR 10. SOFT SKILLS 50 20 37 P C 50 20 42 P TW 20. DATA STRUCTURES LABORATORY TW 21. DATA STRUCTURES LABORATORY PR 50 20 40 P GRAND TOTAL = 919/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71200965F , S80054283 , PICT , S80054283 S80054283 MORDANI NAVIN BHAGWAN SUPARNA 01. DISCRETE STRUCTURES 11. ENGINEERING MATHEMATICS III PP 100 40 51 P PP 100 40 67 P C 100 40 63 P C 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 49 P 02. PROGRAMMING & PROBLEM SOLVING PP 100 40 70 P C 100 40 62 P 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 13. DATA STRUCTURES PP 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 52 P C PP 100 40 60 P 14. COMPUTER GRAPHICS 05. HUMANITIES AND SOCIAL SCIENCE PP 100 40 58 P C 15. COMPUTER ORGANIZATION 100 40 73 P 25 10 18 P C 50 20 41 P 06. PROGRAMMING LABORATORY 16. O. O. PROG. & COMP. GRAPH. LAB TW TW 07. PROGRAMMING LABORATORY 50 20 38 P C 17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 42 P PR 25 10 20 P C 18. MICROPROCESSORS & INTERFACING LABTW 50 20 44 P 08. DIGITAL ELECTRONICS LABORATORY TW 50 20 34 P C 50 20 46 P 09. DIGITAL ELECTRONICS LABORATORY PR 19. MICROPROCESSORS & INTERFACING LABPR 10. SOFT SKILLS 50 20 38 P C 50 20 41 P TW 20. DATA STRUCTURES LABORATORY 50 20 41 P 21. DATA STRUCTURES LABORATORY PR GRAND TOTAL = 1008/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS: S80054284 MORE SAYALI SHANKAR MORE ANITA SHANKAR , 71347769F , S80054284 , PICT , S80054284 11. ENGINEERING MATHEMATICS III PP 100 40 40 P 01. DISCRETE STRUCTURES PP 100 40 60 P C 100 40 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 02. PROGRAMMING & PROBLEM SOLVING PP 67 P C 45 P 13. DATA STRUCTURES 100 40 100 40 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 53 P C 56 P 100 40 52 P C 100 40 64 P 04. DATA STRUCTURES AND ALGORITHMS PP 14. COMPUTER GRAPHICS PP 100 40 100 40 79 P 05. HUMANITIES AND SOCIAL SCIENCE PP 69 P C 15. COMPUTER ORGANIZATION 25 10 06. PROGRAMMING LABORATORY 50 20 44 P 21 P C 16. O. O. PROG. & COMP. GRAPH. LAB TW TW 28 P C 50 20 30 P 07. PROGRAMMING LABORATORY 50 20 17. O. O. PROG. & COMP. GRAPH. LAB PR 25 10 50 20 08. DIGITAL ELECTRONICS LABORATORY TW 20 P C 18. MICROPROCESSORS & INTERFACING LABTW 44 P 50 20 25 P 09. DIGITAL ELECTRONICS LABORATORY PR 50 20 42 P C 19. MICROPROCESSORS & INTERFACING LABPR 10. SOFT SKILLS 50 20 43 P C 50 20 42 P TW 20. DATA STRUCTURES LABORATORY 50 20 34 P 21. DATA STRUCTURES LABORATORY PR GRAND TOTAL = 958/1500, RESULT: FIRST CLASS ORDN. 1 MARKS: , 71200966D , S80054285 , PICT , S80054285 S80054285 MORE SOHAM SAMEER HARSHA 01. DISCRETE STRUCTURES PP 100 40 68 P C 11. ENGINEERING MATHEMATICS III PP 100 40 25 F 02. PROGRAMMING & PROBLEM SOLVING PP 100 40 47 P C 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 41 P PP 100 40 47 P 100 40 61 P C 13. DATA STRUCTURES 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 51 P C 14. COMPUTER GRAPHICS 100 40 53 P 100 40 53 P C 15. COMPUTER ORGANIZATION 100 40 55 P 05. HUMANITIES AND SOCIAL SCIENCE PP 25 10 30 P 06. PROGRAMMING LABORATORY 16 P C 50 20 TW 16. O. O. PROG. & COMP. GRAPH. LAB TW 07. PROGRAMMING LABORATORY 50 20 41 P C 17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 38 P PR 08. DIGITAL ELECTRONICS LABORATORY TW 25 10 17 P C 18. MICROPROCESSORS & INTERFACING LABTW 50 20 28 P 50 20 35 P 09. DIGITAL ELECTRONICS LABORATORY PR 50 20 40 P C 19. MICROPROCESSORS & INTERFACING LABPR 10. SOFT SKILLS 50 20 29 P C 50 20 29 P TW 20. DATA STRUCTURES LABORATORY TW 21. DATA STRUCTURES LABORATORY PR 50 20 35 P GRAND TOTAL = 839/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER AMENA KHANUM , 71200967B , S80054286 , PICT , S80054286 S80054286 MUDASSIR KHAN MASOOD KHAN O1. DISCRETE STRUCTURES PP 11. ENGINEERING MATHEMATICS III PP 100 40 57 P 100 40 66 P C 100 40 61 P C 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 02. PROGRAMMING & PROBLEM SOLVING PP 100 40 62 P C 100 40 55 P 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 13. DATA STRUCTURES PP 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 57 P C PP 100 40 54 P 14. COMPUTER GRAPHICS 05. HUMANITIES AND SOCIAL SCIENCE PP 100 40 55 P C 15. COMPUTER ORGANIZATION 100 40 59 P 25 10 17 P C 50 20 40 P 06. PROGRAMMING LABORATORY 16. O. O. PROG. & COMP. GRAPH. LAB TW TW 07. PROGRAMMING LABORATORY 50 20 43 P C 17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 38 P PR 25 10 17 P C 18. MICROPROCESSORS & INTERFACING LABTW 50 20 43 P 08. DIGITAL ELECTRONICS LABORATORY TW 50 20 30 P C 50 20 30 P 09. DIGITAL ELECTRONICS LABORATORY PR 19. MICROPROCESSORS & INTERFACING LABPR TW 50 20 35 P C 10. SOFT SKILLS 20. DATA STRUCTURES LABORATORY 50 20 40 P 50 20 39 P 21. DATA STRUCTURES LABORATORY PR GRAND TOTAL = 938/1500, RESULT: FIRST CLASS ORDN. 1 MARKS : S80054287 MUNOT BHAVESH RAJENDRA MANORAMA , 71200971L , S80054287 , PICT , S80054287 01. DISCRETE STRUCTURES 11. ENGINEERING MATHEMATICS III PP 100 40 46 P PP 100 40 82 P C 100 40 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 02. PROGRAMMING & PROBLEM SOLVING PP 68 P C 47 P 13. DATA STRUCTURES 100 40 100 40 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 53 P C 64 P 100 40 55 P C 100 40 04. DATA STRUCTURES AND ALGORITHMS PP 14. COMPUTER GRAPHICS PP 49 P 100 40 52 P C 100 40 05. HUMANITIES AND SOCIAL SCIENCE PP 15. COMPUTER ORGANIZATION 63 P 25 10 06. PROGRAMMING LABORATORY 22 P C 50 20 44 P 16. O. O. PROG. & COMP. GRAPH. LAB TW TW 40 P C 50 20 07. PROGRAMMING LABORATORY 50 20 17. O. O. PROG. & COMP. GRAPH. LAB PR 42 P 50 20 40 P 08. DIGITAL ELECTRONICS LABORATORY TW 25 10 21 P C 18. MICROPROCESSORS & INTERFACING LABTW 50 20 38 P 50 20 40 P C 09. DIGITAL ELECTRONICS LABORATORY PR 19. MICROPROCESSORS & INTERFACING LABPR 10. SOFT SKILLS 50 20 41 P C 20. DATA STRUCTURES LABORATORY 50 20 41 P TW 50 20 43 P 21. DATA STRUCTURES LABORATORY PR GRAND TOTAL = 991/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS: , 71200972J , S80054288 , PICT , S80054288 S80054288 MURARKA UTKARSH VINAY HEMLATA 01. DISCRETE STRUCTURES 11. ENGINEERING MATHEMATICS III PP 100 40 P PP 100 40 68 P C 02. PROGRAMMING & PROBLEM SOLVING PP 100 40 56 P C 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 41 P PP 100 40 100 40 55 P C 13. DATA STRUCTURES 51 P 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 41 P C 14. COMPUTER GRAPHICS 100 40 63 P 05. HUMANITIES AND SOCIAL SCIENCE PP 100 40 60 P C 15. COMPUTER ORGANIZATION 100 40 63 P 25 10 20 P 06. PROGRAMMING LABORATORY 15 P C 50 20 TW 16. O. O. PROG. & COMP. GRAPH. LAB TW 07. PROGRAMMING LABORATORY 50 20 22 P C 17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 25 P PR 08. DIGITAL ELECTRONICS LABORATORY TW 25 10 15 P C 18. MICROPROCESSORS & INTERFACING LABTW 50 20 20 P 50 20 30 P 09. DIGITAL ELECTRONICS LABORATORY PR 50 20 33 P C 19. MICROPROCESSORS & INTERFACING LABPR 10. SOFT SKILLS 50 20 33 P C 50 20 22 P TW 20. DATA STRUCTURES LABORATORY TW 21. DATA STRUCTURES LABORATORY PR 50 20 38 P GRAND TOTAL = 811/1500, RESULT: SECOND CLASS ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER S80054298 PAGAR PRITESH DILIP PAGAR PALLAVI DILIP , 71347771H , S80054298 , PICT , S80054298 01. DISCRETE STRUCTURES 11. ENGINEERING MATHEMATICS III PP 100 40 41 P PP 100 40 62 P C 100 40 60 P C 12. MICROPROC. & INTERFACING TECHNIO.PP 100 40 49 P 02. PROGRAMMING & PROBLEM SOLVING PP 100 40 68 P C 100 40 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 13. DATA STRUCTURES PP 63 P 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 50 P C PP 100 40 63 P 14. COMPUTER GRAPHICS 05. HUMANITIES AND SOCIAL SCIENCE PP 100 40 63 P C 15. COMPUTER ORGANIZATION 100 40 70 P 25 10 20 P C 50 20 42 P 06. PROGRAMMING LABORATORY 16. O. O. PROG. & COMP. GRAPH. LAB TW TW 07. PROGRAMMING LABORATORY 50 20 41 P C 50 20 37 P PR 17. O. O. PROG. & COMP. GRAPH. LAB PR 25 10 20 P C 18. MICROPROCESSORS & INTERFACING LABTW 50 20 42 P 08. DIGITAL ELECTRONICS LABORATORY TW 50 20 25 P C 50 20 38 P 09. DIGITAL ELECTRONICS LABORATORY PR 19. MICROPROCESSORS & INTERFACING LABPR 50 20 38 P C 10. SOFT SKILLS 50 20 41 P TW 20. DATA STRUCTURES LABORATORY 50 20 38 P 21. DATA STRUCTURES LABORATORY PR GRAND TOTAL = 971/1500, RESULT: FIRST CLASS ORDN. 1 MARKS: UTKARSHA , 71200995н , S80054299 , РІСТ , S80054299 S80054299 PAITHANKAR PRANETA VISHWANATH 01. DISCRETE STRUCTURES 11. ENGINEERING MATHEMATICS III PP 100 40 63 P 100 40 71 P C 100 40 57 P C 100 40 02. PROGRAMMING & PROBLEM SOLVING PP 12. MICROPROC. & INTERFACING TECHNIQ.PP 62 P 100 40 100 40 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 61 P C 13. DATA STRUCTURES 62 P 100 40 58 P C 100 40 68 P 04. DATA STRUCTURES AND ALGORITHMS PP 14. COMPUTER GRAPHICS PP 100 40 100 40 05. HUMANITIES AND SOCIAL SCIENCE PP 62 P C 15. COMPUTER ORGANIZATION 68 P 25 10 06. PROGRAMMING LABORATORY 22 P C 50 20 42 P 16. O. O. PROG. & COMP. GRAPH. LAB TW TW 50 20 36 P C 50 20 37 P 07. PROGRAMMING LABORATORY 17. O. O. PROG. & COMP. GRAPH. LAB PR 25 10 21 P C 50 20 08. DIGITAL ELECTRONICS LABORATORY TW 18. MICROPROCESSORS & INTERFACING LABTW 38 P 09. DIGITAL ELECTRONICS LABORATORY PR 50 20 25 P C 19. MICROPROCESSORS & INTERFACING LABPR 50 20 42 P 10. SOFT SKILLS 50 20 40 P C 50 20 40 P TW 20. DATA STRUCTURES LABORATORY 50 20 42 P 21. DATA STRUCTURES LABORATORY PR GRAND TOTAL = 1017/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS: , 71200998B , S80054300 , PICT , S80054300 S80054300 PANDHARE PRIYANKA RAVINDRA RAJANI 11. ENGINEERING MATHEMATICS III PP 100 40 19 F 01. DISCRETE STRUCTURES 100 40 49 P C 02. PROGRAMMING & PROBLEM SOLVING PP 100 40 55 P 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 40 P 100 40 100 40 50 P C 13. DATA STRUCTURES 40 P 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 40 P C 14. COMPUTER GRAPHICS 100 40 42 P 05. HUMANITIES AND SOCIAL SCIENCE PP 100 40 53 P C 15. COMPUTER ORGANIZATION 100 40 44 P 25 10 19 P C 06. PROGRAMMING LABORATORY 50 20 38 P TW 16. O. O. PROG. & COMP. GRAPH. LAB TW 07. PROGRAMMING LABORATORY 50 20 20 P 17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 35 P PR 08. DIGITAL ELECTRONICS LABORATORY TW 25 10 20 P C 18. MICROPROCESSORS & INTERFACING LABTW 50 20 35 P 28 P 09. DIGITAL ELECTRONICS LABORATORY PR 50 20 30 P C 50 20 19. MICROPROCESSORS & INTERFACING LABPR 10. SOFT SKILLS 50 20 40 P C 50 20 38 P TW 20. DATA STRUCTURES LABORATORY TW 21. DATA STRUCTURES LABORATORY PR 50 20 06 F GRAND TOTAL = 741/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER CHANDA , 71201105G , S80054341 , PICT , S80054341 S80054341 VARADE POOJA VIJAY 11. ENGINEERING MATHEMATICS III PP 100 40 65 P 01. DISCRETE STRUCTURES PP 100 40 84 P C 100 40 67 P C 12. MICROPROC. & INTERFACING TECHNIO.PP 100 40 57 P 02. PROGRAMMING & PROBLEM SOLVING PP 100 40 69 P C 100 40 65 P 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 13. DATA STRUCTURES PP 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 50 P C PP 100 40 67 P 14. COMPUTER GRAPHICS 05. HUMANITIES AND SOCIAL SCIENCE PP 100 40 66 P C 15. COMPUTER ORGANIZATION 100 40 70 P 25 10 21 P C 50 20 40 P 06. PROGRAMMING LABORATORY 16. O. O. PROG. & COMP. GRAPH. LAB TW TW 07. PROGRAMMING LABORATORY 50 20 42 P C 50 20 35 P PR 17. O. O. PROG. & COMP. GRAPH. LAB PR 25 10 19 P C 18. MICROPROCESSORS & INTERFACING LABTW 50 20 39 P 08. DIGITAL ELECTRONICS LABORATORY TW 50 20 39 P C 50 20 30 P 09. DIGITAL ELECTRONICS LABORATORY PR 19. MICROPROCESSORS & INTERFACING LABPR 10. SOFT SKILLS 50 20 39 P C 50 20 38 P TW 20. DATA STRUCTURES LABORATORY 50 20 40 P 21. DATA STRUCTURES LABORATORY PR GRAND TOTAL = 1042/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS: S80054342 VARMA RASHMI RAVI LEELA , 71201106E , S80054342 , PICT , S80054342 11. ENGINEERING MATHEMATICS III PP 100 40 24 F 01. DISCRETE STRUCTURES 66 P C PP 100 40 100 40 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 02. PROGRAMMING & PROBLEM SOLVING PP 46 P C 100 40 60 P C 100 40 58 P 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 13. DATA STRUCTURES 100 40 54 P C 100 40 56 P 04. DATA STRUCTURES AND ALGORITHMS PP 14. COMPUTER GRAPHICS PP 100 40 60 P C 100 40 05. HUMANITIES AND SOCIAL SCIENCE PP 15. COMPUTER ORGANIZATION 66 P 25 10 06. PROGRAMMING LABORATORY 50 20 32 P 18 P C 16. O. O. PROG. & COMP. GRAPH. LAB TW TW 33 P C 50 20 35 P 07. PROGRAMMING LABORATORY 50 20 17. O. O. PROG. & COMP. GRAPH. LAB PR 25 10 50 20 08. DIGITAL ELECTRONICS LABORATORY TW 16 P C 18. MICROPROCESSORS & INTERFACING LABTW 28 P 50 20 41 P 09. DIGITAL ELECTRONICS LABORATORY PR 50 20 25 P C 19. MICROPROCESSORS & INTERFACING LABPR 10. SOFT SKILLS 50 20 33 P C 50 20 29 P TW 20. DATA STRUCTURES LABORATORY 50 20 15 F 21. DATA STRUCTURES LABORATORY PR GRAND TOTAL = 839/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS: , 71201107C , S80054343 , PICT , S80054343 INDU S80054343 VASAVE SUSMITA MANUVEL 01. DISCRETE STRUCTURES PP 100 40 66 P C 11. ENGINEERING MATHEMATICS III PP 100 40 41 P 02. PROGRAMMING & PROBLEM SOLVING PP 100 40 40 P C 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 47 P 100 40 100 40 58 P C 13. DATA STRUCTURES 63 P 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 40 P C 14. COMPUTER GRAPHICS 100 40 59 P 100 40 46 P C 15. COMPUTER ORGANIZATION 100 40 56 P 05. HUMANITIES AND SOCIAL SCIENCE PP 25 10 06. PROGRAMMING LABORATORY 20 P C 50 20 41 P TW 16. O. O. PROG. & COMP. GRAPH. LAB TW 07. PROGRAMMING LABORATORY 50 20 30 P C 17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 21 P PR 08. DIGITAL ELECTRONICS LABORATORY TW 25 10 22 P C 18. MICROPROCESSORS & INTERFACING LABTW 50 20 40 P 50 20 25 P 09. DIGITAL ELECTRONICS LABORATORY PR 50 20 29 P C 19. MICROPROCESSORS & INTERFACING LABPR 10. SOFT SKILLS 50 20 40 P C 50 20 40 P TW 20. DATA STRUCTURES LABORATORY TW 21. DATA STRUCTURES LABORATORY PR 50 20 33 P GRAND TOTAL = 857/1500, RESULT: HIGHER SECOND CLASS ORDN. 1 MARKS: 

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71347778E , S80054347 , PICT , S80054347 S80054347 WAGH SHARAD DADASAHEB DAYABAI 01. DISCRETE STRUCTURES 11. ENGINEERING MATHEMATICS III PP 100 40 40 P PP 100 40 50 P C 100 40 43 P C 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 02. PROGRAMMING & PROBLEM SOLVING PP 100 40 56 P C 100 40 52 P 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 13. DATA STRUCTURES PP 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 48 P C PP 100 40 60 P 14. COMPUTER GRAPHICS 05. HUMANITIES AND SOCIAL SCIENCE PP 100 40 54 P C 15. COMPUTER ORGANIZATION 100 40 65 P 25 10 18 P C 50 20 35 P 06. PROGRAMMING LABORATORY 16. O. O. PROG. & COMP. GRAPH. LAB TW TW 27 P 07. PROGRAMMING LABORATORY 50 20 50 20 10 F PR 17. O. O. PROG. & COMP. GRAPH. LAB PR 25 10 50 20 35 P 08. DIGITAL ELECTRONICS LABORATORY TW 18 P C 18. MICROPROCESSORS & INTERFACING LABTW 50 20 29 P C 50 20 23 P 09. DIGITAL ELECTRONICS LABORATORY PR 19. MICROPROCESSORS & INTERFACING LABPR 50 20 32 P C 10. SOFT SKILLS 50 20 32 P TW 20. DATA STRUCTURES LABORATORY 50 20 30 P 21. DATA STRUCTURES LABORATORY PR GRAND TOTAL = 801/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS : SAMPADA , 71201116B , S80054348 , PICT , S80054348 S80054348 WALVE SUWAS NANDAKUMAR 11. ENGINEERING MATHEMATICS III PP 100 40 44 P 01. DISCRETE STRUCTURES PP 100 40 51 P C 100 40 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 02. PROGRAMMING & PROBLEM SOLVING PP 40 P C 43 P 100 40 49 P C 100 40 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 13. DATA STRUCTURES 58 P 100 40 100 40 65 P 04. DATA STRUCTURES AND ALGORITHMS PP 40 P C 14. COMPUTER GRAPHICS PP 100 40 51 P C 100 40 05. HUMANITIES AND SOCIAL SCIENCE PP 15. COMPUTER ORGANIZATION 56 P 50 20 25 10 21 P C 38 P 06. PROGRAMMING LABORATORY 16. O. O. PROG. & COMP. GRAPH. LAB TW TW 35 P 50 20 39 P 07. PROGRAMMING LABORATORY 50 20 17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 08. DIGITAL ELECTRONICS LABORATORY TW 25 10 21 P C 18. MICROPROCESSORS & INTERFACING LABTW 38 P 50 20 32 P 50 20 30 P C 09. DIGITAL ELECTRONICS LABORATORY PR 19. MICROPROCESSORS & INTERFACING LABPR 10. SOFT SKILLS 50 20 38 P C 50 20 39 P TW 20. DATA STRUCTURES LABORATORY 50 20 31 P 21. DATA STRUCTURES LABORATORY PR GRAND TOTAL = 859/1500, RESULT: HIGHER SECOND CLASS ORDN. 1 MARKS: , 71201117L , S80054349 , PICT , S80054349 S80054349 WALVEKAR PARITOSH MANDAR APARNA 11. ENGINEERING MATHEMATICS III PP 100 40 54 P 01. DISCRETE STRUCTURES 100 40 63 P C 02. PROGRAMMING & PROBLEM SOLVING PP 100 40 42 P C 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 47 P 100 40 64 P C 13. DATA STRUCTURES 100 40 53 P 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 42 P C 14. COMPUTER GRAPHICS 100 40 60 P 100 40 60 P C 15. COMPUTER ORGANIZATION 100 40 61 P 05. HUMANITIES AND SOCIAL SCIENCE PP 25 10 40 P 06. PROGRAMMING LABORATORY 16 P C 50 20 TW 16. O. O. PROG. & COMP. GRAPH. LAB TW 07. PROGRAMMING LABORATORY 50 20 35 P C 17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 38 P PR 08. DIGITAL ELECTRONICS LABORATORY TW 25 10 16 P C 18. MICROPROCESSORS & INTERFACING LABTW 50 20 42 P 50 20 39 P 09. DIGITAL ELECTRONICS LABORATORY PR 50 20 38 P C 19. MICROPROCESSORS & INTERFACING LABPR 10. SOFT SKILLS 50 20 30 P C 50 20 39 P TW 20. DATA STRUCTURES LABORATORY TW 21. DATA STRUCTURES LABORATORY PR 50 20 33 P GRAND TOTAL = 912/1500, RESULT: FIRST CLASS ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71201122G , S80054350 , PICT , S80054350 S80054350 YAWALKAR VIBHAV VIRENDRA SHARAYU 01. DISCRETE STRUCTURES PP 100 40 58 P C 11. ENGINEERING MATHEMATICS III PP 100 40 70 P 100 40 40 P C 12. MICROPROC. & INTERFACING TECHNIO.PP 100 40 57 P 02. PROGRAMMING & PROBLEM SOLVING PP 100 40 58 P C 100 40 40 P 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 13. DATA STRUCTURES PP 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 52 P C PP 100 40 62 P 14. COMPUTER GRAPHICS 05. HUMANITIES AND SOCIAL SCIENCE PP 100 40 62 P C 15. COMPUTER ORGANIZATION 100 40 25 10 21 PC 50 20 45 P 06. PROGRAMMING LABORATORY 16. O. O. PROG. & COMP. GRAPH. LAB TW TW 07. PROGRAMMING LABORATORY 50 20 40 P C 17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 38 P PR 25 10 22 P C 18. MICROPROCESSORS & INTERFACING LABTW 50 20 45 P 08. DIGITAL ELECTRONICS LABORATORY TW 50 20 45 P C 50 20 44 P 09. DIGITAL ELECTRONICS LABORATORY PR 19. MICROPROCESSORS & INTERFACING LABPR 50 20 42 P C 10. SOFT SKILLS 20. DATA STRUCTURES LABORATORY 50 20 44 P TW 50 20 39 P 21. DATA STRUCTURES LABORATORY PR GRAND TOTAL = 986/1500, RESULT: FIRST CLASS ORDN. 1 MARKS: S80054351 ZAGADE VISHAL RAJU SARALA , 71201124C , S80054351 , PICT , S80054351 11. ENGINEERING MATHEMATICS III PP 100 40 67 P 01. DISCRETE STRUCTURES 56 P C PP 100 40 100 40 40 P C 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 53 P 02. PROGRAMMING & PROBLEM SOLVING PP 13. DATA STRUCTURES 100 40 54 P C 100 40 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 50 P 100 40 100 40 66 P 04. DATA STRUCTURES AND ALGORITHMS PP 44 P C 14. COMPUTER GRAPHICS PP 100 40 52 P C 100 40 05. HUMANITIES AND SOCIAL SCIENCE PP 15. COMPUTER ORGANIZATION 62 P 25 10 06. PROGRAMMING LABORATORY 19 P C 50 20 35 P 16. O. O. PROG. & COMP. GRAPH. LAB TW TW 25 P C 50 20 38 P 07. PROGRAMMING LABORATORY 50 20 17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 37 P 08. DIGITAL ELECTRONICS LABORATORY TW 25 10 19 P C 18. MICROPROCESSORS & INTERFACING LABTW 50 20 38 P 50 20 34 P C 09. DIGITAL ELECTRONICS LABORATORY PR 19. MICROPROCESSORS & INTERFACING LABPR 10. SOFT SKILLS 50 20 36 P C 50 20 35 P TW 20. DATA STRUCTURES LABORATORY 50 20 36 P 21. DATA STRUCTURES LABORATORY PR GRAND TOTAL = 896+04/1500, RESULT: FIRST CLASS [0.2] ORDN. 1 MARKS: , 71045358C , S80054352 , PICT , S80054352 S80054352 ABNANE OMKAR RAJENDRA SHOBHA 01. DISCRETE STRUCTURES 11. ENGINEERING MATHEMATICS III PP 100 40 42 PC PP 100 40 40 P C 02. PROGRAMMING & PROBLEM SOLVING PP 100 40 40 P C 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 43 P C PP 100 40 40 P C 100 40 40 P C 13. DATA STRUCTURES 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 40 P C 14. COMPUTER GRAPHICS 100 40 57 P C 100 40 41 P C 15. COMPUTER ORGANIZATION 100 40 45 P C 05. HUMANITIES AND SOCIAL SCIENCE PP PP 25 10 50 20 23 P C 06. PROGRAMMING LABORATORY 13 P C TW 16. O. O. PROG. & COMP. GRAPH. LAB TW 07. PROGRAMMING LABORATORY 50 20 17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 PR AA F AA F 08. DIGITAL ELECTRONICS LABORATORY TW 25 10 13 P C 18. MICROPROCESSORS & INTERFACING LABTW 50 20 21 P C 09. DIGITAL ELECTRONICS LABORATORY PR 50 20 23 P C 50 20 20 P C 19. MICROPROCESSORS & INTERFACING LABPR 10. SOFT SKILLS 50 20 22 P C 50 20 22 P C TW 20. DATA STRUCTURES LABORATORY TW 21. DATA STRUCTURES LABORATORY PR 50 20 AA F GRAND TOTAL = 585/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS:

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NOTE: FIRST LINE : SEAT NO., NAME												
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S80054353 AMAN TEWARY				SU	INANDA		, 71100725L , S80054353	, PIC	:Τ	,	s8005	4353
01. DISCRETE STRUCTURES	PP	100	40	43	РС	11.	ENGINEERING MATHEMATICS III	PP	100	40	AA	F
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	AA	F	12.	MICROPROC. & INTERFACING TECHNI	[Q.PP	100	40	AA	F
03. DIGIT. ELECTRONICS & LOGIC DESI	GNPP	100	40	AA	F	13.	DATA STRUCTURES	PP	100	40	AA	F
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	AA	F	14.	COMPUTER GRAPHICS	PP	100	40	AA	F
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	40	РС	15.	COMPUTER ORGANIZATION	PP	100	40	AA	F
06. PROGRAMMING LABORATORY	TW	25	10	17	РС	16.	O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	30	РС
07. PROGRAMMING LABORATORY	PR	50	20	AA	F	17.	O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	25	РС
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	17	РС	18.	MICROPROCESSORS & INTERFACING I	_ABTW	50	20	21	РС
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	34	РС	19.	MICROPROCESSORS & INTERFACING I	_ABPR	50	20	21	РС
10. SOFT SKILLS	TW	50	20	33	РС	20.	DATA STRUCTURES LABORATORY	TW	50	20	22	РС
						21.	DATA STRUCTURES LABORATORY	PR	50	20	AA	F
GRAND TOTAL = 303/1500, RESULT: FAIL	.S											
ORDN. 1 MARKS :												
S80054356 BAPAT RUTUGANDHA SUNIL				AN	IAGHA		, 71239286G , S80054356	, PIC	ΞT	,	s8005	4356
01. DISCRETE STRUCTURES	PP	100	40	45	РС	11.	ENGINEERING MATHEMATICS III	PP	100	40	40	Р
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	54	РС	12.	MICROPROC. & INTERFACING TECHN	[Q.PP	100	40	46	P C
03. DIGIT. ELECTRONICS & LOGIC DESI	GNPP	100	40	50	РС	13.	DATA STRUCTURES	PP	100	40	71	P C
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	54	РС	14.	COMPUTER GRAPHICS	PP	100	40	59	P C
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	61	РС	15.	COMPUTER ORGANIZATION	PP	100	40	61	P C
06. PROGRAMMING LABORATORY	TW	25	10	19	РС	16.	O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	38	P C
07. PROGRAMMING LABORATORY	PR	50	20	30	РС	17.	O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	36	P C
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	22	РС	18.	MICROPROCESSORS & INTERFACING I	_ABTW	50	20	44	P C
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	32	РС	19.	MICROPROCESSORS & INTERFACING I	_ABPR	50	20	39	P C
10. SOFT SKILLS	TW	50	20	40	PС	20.	DATA STRUCTURES LABORATORY	TW	50	20	37	P C
						21.	DATA STRUCTURES LABORATORY	PR	50	20	24	P C
GRAND TOTAL = 902/1500, RESULT: FIRS	T CLAS	S										
ORDN. 1 MARKS :												
S80054357 BASAPURE SHRAVYA RAMESH	I			VI	JAYA		, 71100743J , S80054357	, PIC	.T	,	s8005	4357
01. DISCRETE STRUCTURES	PP	100	40	AA	F	11.	ENGINEERING MATHEMATICS III	PP	100	40	16	F
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	13	F	12.	MICROPROC. & INTERFACING TECHN	[Q.PP	100	40	AA	F
03. DIGIT. ELECTRONICS & LOGIC DESI	GNPP	100	40	18	F	13.	DATA STRUCTURES	PP	100	40	AA	F
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	18	F	14.	COMPUTER GRAPHICS	PP	100	40	AA	F
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	27	F	15.	COMPUTER ORGANIZATION	PP	100	40	AA	F
06. PROGRAMMING LABORATORY	TW	25	10	21	P C	16.	O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	32	P C
07. PROGRAMMING LABORATORY	PR	50	20	AA	F	17.	O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	AA	F
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	18	РС	18.	MICROPROCESSORS & INTERFACING I	_ABTW	50	20	21	P C
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	25	РС	19.	MICROPROCESSORS & INTERFACING I	_ABPR	50	20	AA	F
10. SOFT SKILLS	TW	50	20	36	РС	20.	DATA STRUCTURES LABORATORY	TW	50	20	26	P C
						21.	DATA STRUCTURES LABORATORY	PR	50	20	AA	F
GRAND TOTAL = 271/1500, RESULT: FAIL	.S							RESUL	T RES	ERVE	D FOR	BKLG
ORDN. 1 MARKS :												

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER SMITA , 71239290E , S80054358 , PICT , S80054358 S80054358 BHANDARI RUHI BABU 01. DISCRETE STRUCTURES 11. ENGINEERING MATHEMATICS III PP 100 40 49 P PP 100 40 41 P C 100 40 57 P C 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 49 P C 02. PROGRAMMING & PROBLEM SOLVING PP 100 40 40 P C 100 40 72 P C 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 13. DATA STRUCTURES PP 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 49 P C PP 100 40 57 P C 14. COMPUTER GRAPHICS 05. HUMANITIES AND SOCIAL SCIENCE PP 100 40 58 P C 15. COMPUTER ORGANIZATION 100 40 54 P.C 25 10 17 P C 50 20 39 P C 06. PROGRAMMING LABORATORY 16. O. O. PROG. & COMP. GRAPH. LAB TW TW 07. PROGRAMMING LABORATORY 50 20 20 P C 17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 42 P C PR 25 10 18 P C 18. MICROPROCESSORS & INTERFACING LABTW 50 20 41 P C 08. DIGITAL ELECTRONICS LABORATORY TW 50 20 33 P C 50 20 36 P C 09. DIGITAL ELECTRONICS LABORATORY PR 19. MICROPROCESSORS & INTERFACING LABPR 10. SOFT SKILLS 50 20 31 P C 20. DATA STRUCTURES LABORATORY 50 20 33 P C TW 50 20 30 P C 21. DATA STRUCTURES LABORATORY PR GRAND TOTAL = 866/1500, RESULT: HIGHER SECOND CLASS ORDN. 1 MARKS: S80054359 BHOIR RUPALI LAXMAN VANITA , 71045390G , S80054359 , PICT , S80054359 11. ENGINEERING MATHEMATICS III PP 100 40 40 PC 01. DISCRETE STRUCTURES PP 100 40 42 P C 100 40 40 P C 02. PROGRAMMING & PROBLEM SOLVING PP 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 40 P C 13. DATA STRUCTURES 100 40 100 40 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 46 P PP 49 P C 100 40 100 40 42 P C 04. DATA STRUCTURES AND ALGORITHMS PP 41 P C 14. COMPUTER GRAPHICS PP 100 40 40 P C 100 40 40 P 05. HUMANITIES AND SOCIAL SCIENCE PP 15. COMPUTER ORGANIZATION 25 10 19 P C 50 20 43 P C 06. PROGRAMMING LABORATORY 16. O. O. PROG. & COMP. GRAPH. LAB TW TW 24 P C 50 20 23 P C 07. PROGRAMMING LABORATORY 50 20 17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 08. DIGITAL ELECTRONICS LABORATORY TW 25 10 18 P C 18. MICROPROCESSORS & INTERFACING LABTW 39 P C 50 20 21 P C 50 20 27 P C 09. DIGITAL ELECTRONICS LABORATORY PR 19. MICROPROCESSORS & INTERFACING LABPR 10. SOFT SKILLS 50 20 34 P C 50 20 38 P C TW 20. DATA STRUCTURES LABORATORY 50 20 30 P C 21. DATA STRUCTURES LABORATORY PR GRAND TOTAL = 736/1500, RESULT: PASS CLASS ORDN. 1 MARKS : , 71132426D , S80054360 , PICT , S80054360 S80054360 BHOSALE SHITAL MANOHAR JAYA 01. DISCRETE STRUCTURES 11. ENGINEERING MATHEMATICS III PP 100 40 21 F 100 40 40 P C 02. PROGRAMMING & PROBLEM SOLVING PP 100 40 44 P C 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 40 P C 100 40 100 40 41 P C 13. DATA STRUCTURES PP 40 P C 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 47 P C 14. COMPUTER GRAPHICS 100 40 46 P.C 05. HUMANITIES AND SOCIAL SCIENCE PP 100 40 40 P C 15. COMPUTER ORGANIZATION 100 40 40 P C PP 25 10 06. PROGRAMMING LABORATORY 17 P C 50 20 39 P C TW 16. O. O. PROG. & COMP. GRAPH. LAB TW 07. PROGRAMMING LABORATORY 50 20 25 P C 17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 20 P C PR 08. DIGITAL ELECTRONICS LABORATORY TW 25 10 19 P C 18. MICROPROCESSORS & INTERFACING LABTW 50 20 34 P C 09. DIGITAL ELECTRONICS LABORATORY PR 50 20 20 P C 50 20 28 P C 19. MICROPROCESSORS & INTERFACING LABPR 10. SOFT SKILLS 50 20 34 P C 50 20 32 P C TW 20. DATA STRUCTURES LABORATORY TW 21. DATA STRUCTURES LABORATORY PR 50 20 20 P C GRAND TOTAL = 687/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER LATA , 71100774J , S80054362 , PICT , S80054362 S80054362 DAMSE SAYALI LAXMAN 11. ENGINEERING MATHEMATICS III PP 100 40 P 01. DISCRETE STRUCTURES PP 100 40 40 P C 100 40 40 P C 12. MICROPROC. & INTERFACING TECHNIO.PP 100 40 02. PROGRAMMING & PROBLEM SOLVING PP 100 40 40 P C 100 40 58 P.C 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 13. DATA STRUCTURES PP 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 40 P C PP 100 40 41 P C 14. COMPUTER GRAPHICS 05. HUMANITIES AND SOCIAL SCIENCE PP 100 40 40 P C 15. COMPUTER ORGANIZATION 100 40 40 P.C 25 10 18 P C 50 20 34 P C 06. PROGRAMMING LABORATORY 16. O. O. PROG. & COMP. GRAPH. LAB TW TW 07. PROGRAMMING LABORATORY 50 20 21 P C 17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 35 P C PR 25 10 17 P C 18. MICROPROCESSORS & INTERFACING LABTW 50 20 08. DIGITAL ELECTRONICS LABORATORY TW 36 P C 50 20 21 P C 50 20 27 P C 09. DIGITAL ELECTRONICS LABORATORY PR 19. MICROPROCESSORS & INTERFACING LABPR 50 20 34 P C 10. SOFT SKILLS 50 20 34 P C TW 20. DATA STRUCTURES LABORATORY 50 20 28 P 21. DATA STRUCTURES LABORATORY PR GRAND TOTAL = 724/1500, RESULT: PASS CLASS ORDN. 1 MARKS: , 71045426M , S80054366 , PICT , S80054366 S80054366 GABHALE VARSHA LIMBAJI JANABAI 01. DISCRETE STRUCTURES 11. ENGINEERING MATHEMATICS III PP 100 40 40 PC 100 40 52 P C 100 40 02. PROGRAMMING & PROBLEM SOLVING PP 44 P C 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 40 P C 40 P C 13. DATA STRUCTURES 100 40 100 40 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP PP 53 P C 100 40 100 40 04. DATA STRUCTURES AND ALGORITHMS PP 48 P C 14. COMPUTER GRAPHICS PP 40 P C 100 40 40 P C 51 P 05. HUMANITIES AND SOCIAL SCIENCE PP 15. COMPUTER ORGANIZATION 100 40 25 10 06. PROGRAMMING LABORATORY 50 20 21 P C 16. O. O. PROG. & COMP. GRAPH. LAB TW 39 P C TW 30 P C 50 20 07. PROGRAMMING LABORATORY 50 20 17. O. O. PROG. & COMP. GRAPH. LAB PR 34 P C 25 10 50 20 39 P C 08. DIGITAL ELECTRONICS LABORATORY TW 20 P C 18. MICROPROCESSORS & INTERFACING LABTW 50 20 36 P C 09. DIGITAL ELECTRONICS LABORATORY PR 50 20 27 P C 19. MICROPROCESSORS & INTERFACING LABPR 10. SOFT SKILLS 50 20 35 P C 50 20 36 P C TW 20. DATA STRUCTURES LABORATORY 50 20 22 P C 21. DATA STRUCTURES LABORATORY PR GRAND TOTAL = 787/1500, RESULT: SECOND CLASS ORDN. 1 MARKS: , 71100797H , S80054368 , PICT , S80054368 S80054368 GANVIR AJAY SUDHAKAR SUREKHA 01. DISCRETE STRUCTURES 11. ENGINEERING MATHEMATICS III PP 100 40 41 P C PP 100 40 58 P C 02. PROGRAMMING & PROBLEM SOLVING PP 100 40 50 P C 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 40 P C 100 40 66 P C 13. DATA STRUCTURES PP 100 40 71 P C 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 63 P C 14. COMPUTER GRAPHICS 100 40 40 P.C 100 40 50 P C 15. COMPUTER ORGANIZATION 100 40 41 P C 05. HUMANITIES AND SOCIAL SCIENCE PP PP 25 10 06. PROGRAMMING LABORATORY 18 P C 50 20 26 P C TW 16. O. O. PROG. & COMP. GRAPH. LAB TW 07. PROGRAMMING LABORATORY 50 20 30 P C 17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 28 P C PR 08. DIGITAL ELECTRONICS LABORATORY TW 25 10 14 P C 18. MICROPROCESSORS & INTERFACING LABTW 50 20 28 P C 09. DIGITAL ELECTRONICS LABORATORY PR 50 20 23 P C 50 20 24 P 19. MICROPROCESSORS & INTERFACING LABPR 10. SOFT SKILLS 50 20 32 P C 50 20 20 P C TW 20. DATA STRUCTURES LABORATORY TW 21. DATA STRUCTURES LABORATORY PR 50 20 20 P C GRAND TOTAL = 783/1500, RESULT: SECOND CLASS ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER PARVATA , 71100799D , S80054369 , PICT , S80054369 S80054369 GAVALI TANUJA KASHINATH 01. DISCRETE STRUCTURES 11. ENGINEERING MATHEMATICS III PP 100 40 P C PP 100 40 46 P C 100 40 41 P C 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 02. PROGRAMMING & PROBLEM SOLVING PP 100 40 44 P C 100 40 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 13. DATA STRUCTURES PP 61 P C 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 40 P C PP 100 40 40 P C 14. COMPUTER GRAPHICS 05. HUMANITIES AND SOCIAL SCIENCE PP 100 40 46 P C 15. COMPUTER ORGANIZATION 100 40 40 P.C 25 10 21 PC 50 20 35 P C 06. PROGRAMMING LABORATORY 16. O. O. PROG. & COMP. GRAPH. LAB TW TW 25 P C 07. PROGRAMMING LABORATORY 50 20 50 20 29 P C PR 17. O. O. PROG. & COMP. GRAPH. LAB PR 25 10 17 P C 18. MICROPROCESSORS & INTERFACING LABTW 50 20 31 P C 08. DIGITAL ELECTRONICS LABORATORY TW 50 20 21 P C 50 20 21 P C 09. DIGITAL ELECTRONICS LABORATORY PR 19. MICROPROCESSORS & INTERFACING LABPR 50 20 31 P C 10. SOFT SKILLS 50 20 37 P C TW 20. DATA STRUCTURES LABORATORY 50 20 25 P 21. DATA STRUCTURES LABORATORY PR GRAND TOTAL = 731/1500, RESULT: PASS CLASS ORDN. 1 MARKS: , 71100812E , S80054370 , PICT , S80054370 SUREKHA S80054370 GUNJAL DIPALI BHANUDAS 11. ENGINEERING MATHEMATICS III PP 100 40 40 P 01. DISCRETE STRUCTURES 100 40 41 P C 40 P C 02. PROGRAMMING & PROBLEM SOLVING PP 100 40 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 48 P 100 40 44 P C 100 40 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 13. DATA STRUCTURES 51 P 100 40 50 P C 100 40 40 P 04. DATA STRUCTURES AND ALGORITHMS PP 14. COMPUTER GRAPHICS PP 100 40 52 P C 100 40 05. HUMANITIES AND SOCIAL SCIENCE PP 15. COMPUTER ORGANIZATION 57 P 25 10 06. PROGRAMMING LABORATORY 15 P C 50 20 35 P 16. O. O. PROG. & COMP. GRAPH. LAB TW TW 20 P C 50 20 25 P 07. PROGRAMMING LABORATORY 50 20 17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 08. DIGITAL ELECTRONICS LABORATORY TW 25 10 14 P C 18. MICROPROCESSORS & INTERFACING LABTW 30 P 50 20 21 P 09. DIGITAL ELECTRONICS LABORATORY PR 50 20 24 P C 19. MICROPROCESSORS & INTERFACING LABPR 10. SOFT SKILLS 50 20 27 P C 50 20 33 P TW 20. DATA STRUCTURES LABORATORY 50 20 15# P 21. DATA STRUCTURES LABORATORY PR GRAND TOTAL = 722/1500, RESULT: PASS CLASS # [0.4] ORDN. 1 MARKS : , 71045479B , S80054375 , PICT , S80054375 S80054375 KEDARE SWAPNIL PANDIT NIRMALA 01. DISCRETE STRUCTURES 100 40 40 P C 11. ENGINEERING MATHEMATICS III PP 100 40 06 F 02. PROGRAMMING & PROBLEM SOLVING PP 100 40 40 P C 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 40 P C 100 40 40 P C 100 40 40 P C 13. DATA STRUCTURES PP 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 43 P C 14. COMPUTER GRAPHICS 100 40 43 P.C 100 40 40 P C 15. COMPUTER ORGANIZATION 100 40 47 P 05. HUMANITIES AND SOCIAL SCIENCE PP PP 25 10 06. PROGRAMMING LABORATORY 10 P C 50 20 20 P C TW 16. O. O. PROG. & COMP. GRAPH. LAB TW 07. PROGRAMMING LABORATORY 50 20 20 P 17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 20 P PR 20 P C 08. DIGITAL ELECTRONICS LABORATORY TW 25 10 10 P C 18. MICROPROCESSORS & INTERFACING LABTW 50 20 09. DIGITAL ELECTRONICS LABORATORY PR 50 20 21 P 50 20 AA F 19. MICROPROCESSORS & INTERFACING LABPR 10. SOFT SKILLS 50 20 20 P C 50 20 20 P C TW 20. DATA STRUCTURES LABORATORY TW 21. DATA STRUCTURES LABORATORY PR 50 20 AA F GRAND TOTAL = 540/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS: 

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 70925587L , S80054395 , PICT , S80054395 S80054395 SANDEEP AGARWAL RITA 11. ENGINEERING MATHEMATICS III PP 100 40 53 P C 01. DISCRETE STRUCTURES PP 100 40 59 P C 100 40 40 P C 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 02. PROGRAMMING & PROBLEM SOLVING PP 100 40 47 P C 100 40 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 13. DATA STRUCTURES PP 43 P C 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 40 P C PP 100 40 40 P C 14. COMPUTER GRAPHICS 05. HUMANITIES AND SOCIAL SCIENCE PP 100 40 52 P C 15. COMPUTER ORGANIZATION 100 40 43 P.C 25 10 20 P C 50 20 30 P C 06. PROGRAMMING LABORATORY 16. O. O. PROG. & COMP. GRAPH. LAB TW TW 07. PROGRAMMING LABORATORY 50 20 30 P C 17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 24 P C PR 25 10 17 P C 18. MICROPROCESSORS & INTERFACING LABTW 50 20 32 P C 08. DIGITAL ELECTRONICS LABORATORY TW 50 20 25 P C 50 20 20 P C 09. DIGITAL ELECTRONICS LABORATORY PR 19. MICROPROCESSORS & INTERFACING LABPR 50 20 40 P C 10. SOFT SKILLS 20. DATA STRUCTURES LABORATORY 50 20 22 P C TW 50 20 38 P C 21. DATA STRUCTURES LABORATORY PR GRAND TOTAL = 757/1500, RESULT: SECOND CLASS ORDN. 1 MARKS: S80054397 SAURAV JAUHARI POONAM , 70925593E , S80054397 , PICT , S80054397 11. ENGINEERING MATHEMATICS III PP 100 40 00 F 19 F 01. DISCRETE STRUCTURES 100 40 02. PROGRAMMING & PROBLEM SOLVING PP 100 40 40 P C 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 AA F 13. DATA STRUCTURES 100 40 40 P C 100 40 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 40 P 100 40 25 F 100 40 04. DATA STRUCTURES AND ALGORITHMS PP 14. COMPUTER GRAPHICS PP 40 P 100 40 100 40 41 P 05. HUMANITIES AND SOCIAL SCIENCE PP 47 P.C 15. COMPUTER ORGANIZATION 25 10 13 P C 50 20 20 P C 06. PROGRAMMING LABORATORY 16. O. O. PROG. & COMP. GRAPH. LAB TW TW 20 P C 50 20 20 P C 07. PROGRAMMING LABORATORY 50 20 17. O. O. PROG. & COMP. GRAPH. LAB PR 25 10 50 20 08. DIGITAL ELECTRONICS LABORATORY TW 10 P C 18. MICROPROCESSORS & INTERFACING LABTW 20 P C 50 20 22 P C 50 20 20 P C 09. DIGITAL ELECTRONICS LABORATORY PR 19. MICROPROCESSORS & INTERFACING LABPR 10. SOFT SKILLS 50 20 26 P C 50 20 20 P C TW 20. DATA STRUCTURES LABORATORY 50 20 20 P 21. DATA STRUCTURES LABORATORY PR GRAND TOTAL = 503/1500, RESULT: FAILS ORDN. 1 MARKS: , 71100992K , S80054398 , PICT , S80054398 S80054398 SAWANT TEJASWINI NANASAHEB SUNITA 11. ENGINEERING MATHEMATICS III PP 100 40 P C 01. DISCRETE STRUCTURES 100 40 48 P C 02. PROGRAMMING & PROBLEM SOLVING PP 100 40 47 P C 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 52 P C 100 40 40 P C 13. DATA STRUCTURES PP 100 40 57 P C 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 40 P C 14. COMPUTER GRAPHICS 100 40 42 P.C 05. HUMANITIES AND SOCIAL SCIENCE PP 100 40 58 P C 15. COMPUTER ORGANIZATION 100 40 43 P C PP 25 10 06. PROGRAMMING LABORATORY 19 P C 50 20 38 P C TW 16. O. O. PROG. & COMP. GRAPH. LAB TW 07. PROGRAMMING LABORATORY 50 20 21 P C 17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 34 P C PR 08. DIGITAL ELECTRONICS LABORATORY TW 25 10 16 P C 18. MICROPROCESSORS & INTERFACING LABTW 50 20 38 P C 09. DIGITAL ELECTRONICS LABORATORY PR 50 20 29 P C 50 20 29 P C 19. MICROPROCESSORS & INTERFACING LABPR 10. SOFT SKILLS 50 20 29 P C 50 20 37 P C TW 20. DATA STRUCTURES LABORATORY TW 21. DATA STRUCTURES LABORATORY PR 50 20 26 P GRAND TOTAL = 783/1500, RESULT: SECOND CLASS ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER SEETA , 71101000F , S80054399 , PICT , S80054399 S80054399 SHELKANDE AKASH DAGADU 01. DISCRETE STRUCTURES 11. ENGINEERING MATHEMATICS III PP 100 40 10 F PP 100 40 41 P C 100 40 40 P C 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 40 P C 02. PROGRAMMING & PROBLEM SOLVING PP 100 40 54 P C 100 40 AA F 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 13. DATA STRUCTURES PP 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 43 P C PP 100 40 40 P C 14. COMPUTER GRAPHICS 05. HUMANITIES AND SOCIAL SCIENCE PP 100 40 40 P C 15. COMPUTER ORGANIZATION 100 40 40 P 25 10 17 P C 50 20 26 P C 06. PROGRAMMING LABORATORY 16. O. O. PROG. & COMP. GRAPH. LAB TW TW 07. PROGRAMMING LABORATORY 50 20 21 P C 17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 28 P C PR 25 10 17 P C 18. MICROPROCESSORS & INTERFACING LABTW 50 20 33 P C 08. DIGITAL ELECTRONICS LABORATORY TW 50 20 20 P C 50 20 23 P 09. DIGITAL ELECTRONICS LABORATORY PR 19. MICROPROCESSORS & INTERFACING LABPR 50 20 33 P C 10. SOFT SKILLS 50 20 32 P C TW 20. DATA STRUCTURES LABORATORY 50 20 24 P 21. DATA STRUCTURES LABORATORY PR GRAND TOTAL = 622/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS: S80054401 SHINDE AMIT KEDARNATH SANGEETA , 71045614L , S80054401 , PICT , S80054401 11. ENGINEERING MATHEMATICS III PP 100 40 40 PC 01. DISCRETE STRUCTURES 53 P C PP 100 40 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 02. PROGRAMMING & PROBLEM SOLVING PP 100 40 40 P C 49 P 13. DATA STRUCTURES 100 40 49 P C 100 40 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP PP 54 P C 100 40 100 40 46 P C 04. DATA STRUCTURES AND ALGORITHMS PP 40 P C 14. COMPUTER GRAPHICS PP 100 40 40 P C 100 40 05. HUMANITIES AND SOCIAL SCIENCE PP 15. COMPUTER ORGANIZATION 54 P C 25 10 06. PROGRAMMING LABORATORY 17 P C 50 20 16. O. O. PROG. & COMP. GRAPH. LAB TW 32 P C TW 35 P C 50 20 32 P C 07. PROGRAMMING LABORATORY 50 20 17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 08. DIGITAL ELECTRONICS LABORATORY TW 25 10 16 P C 18. MICROPROCESSORS & INTERFACING LABTW 36 P C 50 20 38 P C 09. DIGITAL ELECTRONICS LABORATORY PR 50 20 21 P C 19. MICROPROCESSORS & INTERFACING LABPR 10. SOFT SKILLS 50 20 34 P C 50 20 30 P C TW 20. DATA STRUCTURES LABORATORY 50 20 21 P C 21. DATA STRUCTURES LABORATORY PR GRAND TOTAL = 777/1500, RESULT: SECOND CLASS ORDN. 1 MARKS: , 71045624H , S80054403 , PICT , S80054403 S80054403 SIDDHARTH BATRA SANGEETA 01. DISCRETE STRUCTURES 100 40 71 P C 11. ENGINEERING MATHEMATICS III PP 100 40 59 P 02. PROGRAMMING & PROBLEM SOLVING PP 100 40 40 P C 12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 55 P C 100 40 100 40 40 P C 13. DATA STRUCTURES PP 40 P C 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 48 P C 14. COMPUTER GRAPHICS 100 40 43 P.C. 05. HUMANITIES AND SOCIAL SCIENCE PP 100 40 43 P C 15. COMPUTER ORGANIZATION 100 40 61 P C PP 25 10 06. PROGRAMMING LABORATORY 14 P C 50 20 21 P C TW 16. O. O. PROG. & COMP. GRAPH. LAB TW 07. PROGRAMMING LABORATORY 50 20 24 P C 17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 30 P C PR 24 P C 08. DIGITAL ELECTRONICS LABORATORY TW 25 10 10 P C 18. MICROPROCESSORS & INTERFACING LABTW 50 20 20 P C 09. DIGITAL ELECTRONICS LABORATORY PR 50 20 20 P C 50 20 19. MICROPROCESSORS & INTERFACING LABPR 10. SOFT SKILLS 50 20 23 P C 50 20 26 P C TW 20. DATA STRUCTURES LABORATORY TW 21. DATA STRUCTURES LABORATORY PR 50 20 30 P GRAND TOTAL = 742+08/1500, RESULT: SECOND CLASS [0.2] ORDN. 1 MARKS: 

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UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(COMPUTER) EXAMINATION MAY 2013

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.  OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER												
S80054412 ABHISHEK KUMAR SAURAV					MAN	, 71045357E , , , , PICT , S8005						
01. DISCRETE STRUCTURES	PP	100	40	40	P C	11. ENGINEERING MATHEMATICS III PP 100 40 40	PC					
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	AA	F	12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 AA	F					
03. DIGIT. ELECTRONICS & LOGIC DESI	GNPP	100	40	43	P C	13. DATA STRUCTURES PP 100 40 43	РС					
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	41	P C	14. COMPUTER GRAPHICS PP 100 40 40	Р					
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	41	PC	15. COMPUTER ORGANIZATION PP 100 40 25	F					
06. PROGRAMMING LABORATORY	TW	25	10	10	PC	16. O. O. PROG. & COMP. GRAPH. LAB TW 50 20 38	РС					
07. PROGRAMMING LABORATORY	PR	50	20	AA	F	17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 AA	F					
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	10	P C	18. MICROPROCESSORS & INTERFACING LABTW 50 20 22	РС					
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	25	Р	19. MICROPROCESSORS & INTERFACING LABPR 50 20 AA	F					
10. SOFT SKILLS	TW	50	20	20	РС	20. DATA STRUCTURES LABORATORY TW 50 20 20	РС					
						21. DATA STRUCTURES LABORATORY PR 50 20 05	F					
GRAND TOTAL = 463/1500, RESULT: FAIL	S					RESULT RESERVED FOR	BKLG					

ORDN. 1 MARKS :

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER SUNITA , 71200746G , S80058501 , PICT , S80058501 S80058501 ABHALE SAURABH BHANUDAS 01. DISCRETE STRUCTURES 100 40 61 P 100 40 72 P C 11. ENG MATHS III PP PP 100 40 70 P C 12. COMPUTER GRAPHICS 100 40 49 P 02. COMPUTER ORGANIZATION 100 40 75 P C 100 40 41 P 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 13. PROCESSOR ARCHITECTURE & INTER. PP 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 63 P C PP 100 40 57 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 55 P C 100 40 15. DATA COMMUNICATION TW 50 20 46 P.C 25 10 19 P 06. DIGITAL LABORATORY 16. PROCESSOR INTERFACING LABORATORY TW 07. DIGITAL LABORATORY 50 20 43 P C 50 20 42 P PR 17. PROCESSOR INTERFACING LABORATORY OR 08. PROGRAMMING LABORATORY 50 20 48 P C 25 10 22 P TW 18. DATA STRUCTURES AND FILES LAB TW 09. PROGRAMMING LABORATORY 50 20 42 P C 50 20 38 P PR 19. DATA STRUCTURES AND FILES LAB PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 46 P C 50 20 43 P 20. OBJECT ORIENTED PROGRAMMING LAB TW 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 40 P GRAND TOTAL = 1037/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS: , 71200750E , S80058502 , PICT , S80058502 S80058502 ADARSH KUMAR KIRAN PP 57 P C 11. ENG MATHS III 100 40 40 P 01. DISCRETE STRUCTURES 100 40 02. COMPUTER ORGANIZATION 100 40 55 P 100 40 40 P PP 12. COMPUTER GRAPHICS 100 40 40 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 40 P 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 14. DATA STRUCTURES AND FILES 100 40 43 P C 100 40 04. FUNDAMENTAL OF DATA STRUCTURES PP PP 40 P 100 40 50 P C 100 40 49 P 05. HUMANITIES AND SOCIAL SCIENCES PP 15. DATA COMMUNICATION 50 20 28 P C 10 P 06. DIGITAL LABORATORY 25 10 TW 16. PROCESSOR INTERFACING LABORATORY TW 50 20 32 P 50 20 07. DIGITAL LABORATORY PR 17. PROCESSOR INTERFACING LABORATORY OR AA F 50 20 25 10 08. PROGRAMMING LABORATORY TW 22 P C 18. DATA STRUCTURES AND FILES LAB TW 10 P 09. PROGRAMMING LABORATORY 50 20 00 F PR 19. DATA STRUCTURES AND FILES LAB PR 50 20 38 P 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 31 P C 50 20 20 P 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 AA F 21. OBJECT ORIENTED PROGRAMMING LAB PR GRAND TOTAL = 645/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS: , 71200752M , S80058503 , PICT , S80058503 S80058503 AHIRE AKSHATA RAJENDRA MEENAKSHI 01. DISCRETE STRUCTURES 100 40 75 P C 11. ENG MATHS III 100 40 40 P 02. COMPUTER ORGANIZATION PP 100 40 53 P C 12. COMPUTER GRAPHICS 100 40 40 P 100 40 59 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 40 P 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 52 P C 14. DATA STRUCTURES AND FILES 100 40 58 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 50 P C PP 100 40 60 P 15. DATA COMMUNICATION TW 50 20 19 P 06. DIGITAL LABORATORY 43 P C 25 10 16. PROCESSOR INTERFACING LABORATORY TW 50 20 40 P C 17. PROCESSOR INTERFACING LABORATORY OR 50 20 30 P 07. DIGITAL LABORATORY PR 08. PROGRAMMING LABORATORY TW 50 20 44 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 22 P 50 20 30 P C 50 20 45 P 09. PROGRAMMING LABORATORY PR 19. DATA STRUCTURES AND FILES LAB PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 47 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 43 P 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 36 P GRAND TOTAL = 926/1500, RESULT: FIRST CLASS ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71200755F , S80058504 , PICT , S80058504 SHANTHI S80058504 AKSHAY A ARLIKATTI 01. DISCRETE STRUCTURES 100 40 66 P 100 40 80 P C 11. ENG MATHS III PP 100 40 66 P C 12. COMPUTER GRAPHICS 100 40 47 P 02. COMPUTER ORGANIZATION PP 100 40 64 P C 100 40 47 P 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 13. PROCESSOR ARCHITECTURE & INTER. PP 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 50 P PP 100 40 57 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 44 P C 100 40 15. DATA COMMUNICATION TW 50 20 39 P C 25 10 18 P 06. DIGITAL LABORATORY 16. PROCESSOR INTERFACING LABORATORY TW 07. DIGITAL LABORATORY 50 20 37 P C 50 20 38 P PR 17. PROCESSOR INTERFACING LABORATORY OR 08. PROGRAMMING LABORATORY 50 20 40 P C 25 10 24 P TW 18. DATA STRUCTURES AND FILES LAB TW 09. PROGRAMMING LABORATORY PR 50 20 48 P C 50 20 46 P 19. DATA STRUCTURES AND FILES LAB PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 39 P C 50 20 42 P 20. OBJECT ORIENTED PROGRAMMING LAB TW 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 43 P GRAND TOTAL = 997/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS: , 71350914н , S80058506 , РІСТ , S80058506 RAJISHREE S80058506 AKSHAY SHATRUGHAN DHOTRE PP 01. DISCRETE STRUCTURES 11. ENG MATHS III 100 40 17 F PP 100 40 48 P C 100 40 100 40 52 P 02. COMPUTER ORGANIZATION PP 48 P C 12. COMPUTER GRAPHICS 100 40 51 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 28 F 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 47 P C 100 40 PP 40 P 100 40 47 P.C 100 40 05. HUMANITIES AND SOCIAL SCIENCES PP 15. DATA COMMUNICATION 57 P TW 50 20 27 P C 10 P 06. DIGITAL LABORATORY 25 10 16. PROCESSOR INTERFACING LABORATORY TW 50 20 25 P 50 20 07. DIGITAL LABORATORY PR 17. PROCESSOR INTERFACING LABORATORY OR AA F 50 20 25 10 12 P 08. PROGRAMMING LABORATORY TW 33 P C 18. DATA STRUCTURES AND FILES LAB TW 09. PROGRAMMING LABORATORY 50 20 38 P 50 20 20 P C PR 19. DATA STRUCTURES AND FILES LAB PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 33 P C 50 20 23 P 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 AA F 21. OBJECT ORIENTED PROGRAMMING LAB PR GRAND TOTAL = 656/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS: ROOPALI NIGAM , 71200758L , S80058507 , PICT , S80058507 S80058507 AMAN KUMAR NIGAM 01. DISCRETE STRUCTURES 100 40 56 P C 11. ENG MATHS III 100 40 59 P 02. COMPUTER ORGANIZATION PP 100 40 57 P C 12. COMPUTER GRAPHICS 100 40 44 P 100 40 57 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 44 P 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 53 P C 14. DATA STRUCTURES AND FILES 100 40 44 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 46 P C PP 100 40 51 P 15. DATA COMMUNICATION TW 50 20 20 P 06. DIGITAL LABORATORY 44 P C 25 10 16. PROCESSOR INTERFACING LABORATORY TW 50 20 40 P C 17. PROCESSOR INTERFACING LABORATORY OR 50 20 39 P 07. DIGITAL LABORATORY PR 08. PROGRAMMING LABORATORY TW 50 20 43 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 22 P 50 20 40 P C 50 20 42 P 09. PROGRAMMING LABORATORY PR 19. DATA STRUCTURES AND FILES LAB PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 43 P C 50 20 39 P 20. OBJECT ORIENTED PROGRAMMING LAB TW 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 36 P GRAND TOTAL = 919/1500, RESULT: FIRST CLASS ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71200764E , S80058508 , PICT , S80058508 S80058508 ARIJIT PANDE ANNAPURNA 62 P C 100 40 79 P 01. DISCRETE STRUCTURES 100 40 11. ENG MATHS III PP 100 40 69 P C 12. COMPUTER GRAPHICS 100 40 49 P 02. COMPUTER ORGANIZATION 100 40 68 P C 100 40 54 P 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 13. PROCESSOR ARCHITECTURE & INTER. PP 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 47 P C PP 100 40 51 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 56 P C 100 40 15. DATA COMMUNICATION TW 50 20 43 P C 25 10 21 P 06. DIGITAL LABORATORY 16. PROCESSOR INTERFACING LABORATORY TW 07. DIGITAL LABORATORY 50 20 42 P C 50 20 40 P PR 17. PROCESSOR INTERFACING LABORATORY OR 08. PROGRAMMING LABORATORY 50 20 48 P C 25 10 23 P TW 18. DATA STRUCTURES AND FILES LAB TW 09. PROGRAMMING LABORATORY 50 20 37 P C 50 20 43 P PR 19. DATA STRUCTURES AND FILES LAB PR 50 20 47 P C 50 20 46 P 10. COMMUNICATION AND LANGUAGE LAB. TW 20. OBJECT ORIENTED PROGRAMMING LAB TW 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 37 P GRAND TOTAL = 1031/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS: , 71200765C , S80058509 , PICT , S80058509 S80058509 ASHISH GUPTA MEENA PP 75 P C 11. ENG MATHS III 100 40 46 P 01. DISCRETE STRUCTURES 100 40 02. COMPUTER ORGANIZATION 100 40 50 P C 100 40 PP 12. COMPUTER GRAPHICS 45 P 100 40 56 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 46 P 14. DATA STRUCTURES AND FILES 100 40 54 P C 100 40 04. FUNDAMENTAL OF DATA STRUCTURES PP PP 48 P 100 40 45 P C 100 40 54 P 05. HUMANITIES AND SOCIAL SCIENCES PP 15. DATA COMMUNICATION 50 20 36 P C 20 P 06. DIGITAL LABORATORY 25 10 TW 16. PROCESSOR INTERFACING LABORATORY TW 50 20 35 P C 50 20 33 P 07. DIGITAL LABORATORY PR 17. PROCESSOR INTERFACING LABORATORY OR 50 20 37 P C 08. PROGRAMMING LABORATORY TW 18. DATA STRUCTURES AND FILES LAB TW 25 10 23 P 09. PROGRAMMING LABORATORY 50 20 43 P PR 50 20 45 P C 19. DATA STRUCTURES AND FILES LAB PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 37 P C 50 20 40 P 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 41 P 21. OBJECT ORIENTED PROGRAMMING LAB PR GRAND TOTAL = 909/1500, RESULT: FIRST CLASS ORDN. 1 MARKS: , 71350915F , S80058510 , PICT , S80058510 S80058510 BAGMAR ADESH SANTOSH ASHA 01. DISCRETE STRUCTURES PP 100 40 47 P C 11. ENG MATHS III 100 40 05 F 02. COMPUTER ORGANIZATION PP 100 40 63 P C 12. COMPUTER GRAPHICS 100 40 43 P 100 40 41 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 28 F 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 51 P C 14. DATA STRUCTURES AND FILES 100 40 27 F 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 57 P C 100 40 68 P 15. DATA COMMUNICATION TW 50 20 15 P 06. DIGITAL LABORATORY 42 P C 25 10 16. PROCESSOR INTERFACING LABORATORY TW 50 20 26 P C 17. PROCESSOR INTERFACING LABORATORY OR 50 20 20 P 07. DIGITAL LABORATORY PR 08. PROGRAMMING LABORATORY TW 50 20 40 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 20 P 09. PROGRAMMING LABORATORY 25 P 50 20 23 P C 50 20 PR 19. DATA STRUCTURES AND FILES LAB PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 43 P C 50 20 33 P 20. OBJECT ORIENTED PROGRAMMING LAB TW 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 20\$ P GRAND TOTAL = 737/1500, RESULT: FAILS A.T.K.T. [\$ 0.1] ORDN. 1 MARKS: (21)2, 

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71200778E , S80058511 , PICT , S80058511 USHA S80058511 BALDAWA KOMAL DWARKADAS 01. DISCRETE STRUCTURES PP 100 40 73 P C PP 100 40 58 P 11. ENG MATHS III 100 40 57 P C 12. COMPUTER GRAPHICS 100 40 52 P 02. COMPUTER ORGANIZATION 100 40 65 P C 100 40 46 P 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 13. PROCESSOR ARCHITECTURE & INTER. PP 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 49 P C PP 100 40 55 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 44 P C 100 40 53 P 15. DATA COMMUNICATION TW 50 20 32 P C 25 10 14 P 06. DIGITAL LABORATORY 16. PROCESSOR INTERFACING LABORATORY TW 27 P C 07. DIGITAL LABORATORY 50 20 50 20 07 F PR 17. PROCESSOR INTERFACING LABORATORY OR 08. PROGRAMMING LABORATORY 50 20 31 P C 25 10 TW 18. DATA STRUCTURES AND FILES LAB TW 18 P 09. PROGRAMMING LABORATORY 50 20 35 P C 50 20 38 P PR 19. DATA STRUCTURES AND FILES LAB PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 36 P C 50 20 33 P 20. OBJECT ORIENTED PROGRAMMING LAB TW 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 32 P GRAND TOTAL = 855/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS: , 71200782C , S80058512 , PICT , S80058512 SUREKHA S80058512 BANSODE ASHLESHA ANKUSH PP 01. DISCRETE STRUCTURES 100 40 70 P C 11. ENG MATHS III 100 40 41 P 100 40 70 P C 100 40 53 P 02. COMPUTER ORGANIZATION PP 12. COMPUTER GRAPHICS 100 40 67 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 48 P 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 61 P C 100 40 PP 60 P 100 40 52 P C 100 40 05. HUMANITIES AND SOCIAL SCIENCES PP 15. DATA COMMUNICATION 63 P TW 50 20 17 P 06. DIGITAL LABORATORY 41 P C 25 10 16. PROCESSOR INTERFACING LABORATORY TW 50 20 38 P C 50 20 28 P 07. DIGITAL LABORATORY PR 17. PROCESSOR INTERFACING LABORATORY OR 50 20 37 P C 08. PROGRAMMING LABORATORY TW 18. DATA STRUCTURES AND FILES LAB TW 25 10 20 P 09. PROGRAMMING LABORATORY 50 20 33 P PR 50 20 34 P C 19. DATA STRUCTURES AND FILES LAB PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 44 P C 50 20 42 P 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 39 P 21. OBJECT ORIENTED PROGRAMMING LAB PR GRAND TOTAL = 958/1500, RESULT: FIRST CLASS ORDN. 1 MARKS: , 71200783M , S80058513 , PICT , S80058513 S80058513 BARASKAR ADITI NARENDRA LEENA 01. DISCRETE STRUCTURES 100 40 66 P C 11. ENG MATHS III 100 40 58 P 02. COMPUTER ORGANIZATION PP 100 40 69 P C 12. COMPUTER GRAPHICS 100 40 49 P 100 40 53 P 100 40 67 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 53 P C 14. DATA STRUCTURES AND FILES 100 40 65 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 56 P C PP 100 40 62 P 15. DATA COMMUNICATION TW 50 20 17 P 06. DIGITAL LABORATORY 40 P C 25 10 16. PROCESSOR INTERFACING LABORATORY TW 50 20 38 P C 17. PROCESSOR INTERFACING LABORATORY OR 50 20 28 P 07. DIGITAL LABORATORY PR 08. PROGRAMMING LABORATORY TW 50 20 38 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 20 P 50 20 41 P C 50 20 36 P 09. PROGRAMMING LABORATORY PR 19. DATA STRUCTURES AND FILES LAB PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 41 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 40 P 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 40 P GRAND TOTAL = 977/1500, RESULT: FIRST CLASS ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71200784к , S80058514 , РІСТ , S80058514 JYOTI S80058514 BARDE REEMA ANIL 01. DISCRETE STRUCTURES 100 40 25 F 100 40 59 P C 11. ENG MATHS III PP 100 40 55 P C 12. COMPUTER GRAPHICS 100 40 40 P 02. COMPUTER ORGANIZATION PP 100 40 67 P C 100 40 40 P 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 13. PROCESSOR ARCHITECTURE & INTER. PP 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 43 P C PP 100 40 51 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 53 P C 100 40 57 P 15. DATA COMMUNICATION TW 50 20 44 P C 25 10 17 P 06. DIGITAL LABORATORY 16. PROCESSOR INTERFACING LABORATORY TW 07. DIGITAL LABORATORY 50 20 36 P C 50 20 22 P PR 17. PROCESSOR INTERFACING LABORATORY OR 08. PROGRAMMING LABORATORY 50 20 40 P C 25 10 21 P TW 18. DATA STRUCTURES AND FILES LAB TW 09. PROGRAMMING LABORATORY PR 50 20 40 P C 50 20 43 P 19. DATA STRUCTURES AND FILES LAB PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 47 P C 50 20 43 P 20. OBJECT ORIENTED PROGRAMMING LAB TW 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 37 P GRAND TOTAL = 880/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS : , 71350916D , S80058515 , PICT , S80058515 KALPANA S80058515 BENDHALE AJAY MARUTI 01. DISCRETE STRUCTURES PP 100 40 64 P 11. ENG MATHS III 100 40 40 P 100 40 77 P C 100 40 02. COMPUTER ORGANIZATION PP 12. COMPUTER GRAPHICS 48 P 100 40 70 P.C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 55 P 14. DATA STRUCTURES AND FILES 100 40 56 P C 100 40 70 P 04. FUNDAMENTAL OF DATA STRUCTURES PP PP 100 40 76 P.C 100 40 05. HUMANITIES AND SOCIAL SCIENCES PP 15. DATA COMMUNICATION 64 P TW 50 20 34 P C 20 P 06. DIGITAL LABORATORY 25 10 16. PROCESSOR INTERFACING LABORATORY TW 50 20 34 P C 50 20 22 P 07. DIGITAL LABORATORY PR 17. PROCESSOR INTERFACING LABORATORY OR 50 20 08. PROGRAMMING LABORATORY TW 34 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 23 P 09. PROGRAMMING LABORATORY 50 20 40 P PR 50 20 44 P C 19. DATA STRUCTURES AND FILES LAB PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 34 P C 50 20 47 P 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 43 P 21. OBJECT ORIENTED PROGRAMMING LAB PR GRAND TOTAL = 995/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS: , 71350917B , S80058516 , PICT , S80058516 S80058516 BHALCHIM NILESH LAKSHMAN JIJABAI 01. DISCRETE STRUCTURES 100 40 AA F 11. ENG MATHS III 100 40 01 F 02. COMPUTER ORGANIZATION PP 100 40 AA F 12. COMPUTER GRAPHICS 100 40 AA F 100 40 AA F 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 AA F 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 14. DATA STRUCTURES AND FILES 100 40 AA F AA F 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 45 P C 100 40 AA F 15. DATA COMMUNICATION TW 50 20 06. DIGITAL LABORATORY 30 P C 25 10 14 P 16. PROCESSOR INTERFACING LABORATORY TW 50 20 20 P C 17. PROCESSOR INTERFACING LABORATORY OR 50 20 25 P 07. DIGITAL LABORATORY PR 08. PROGRAMMING LABORATORY TW 50 20 30 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 14 P PR 50 20 50 20 20 P 37 P 09. PROGRAMMING LABORATORY 19. DATA STRUCTURES AND FILES LAB PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 32 P C 50 20 29 P 20. OBJECT ORIENTED PROGRAMMING LAB TW 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 AA F GRAND TOTAL = 297/1500, RESULT: FAILS ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71200790D , S80058517 , PICT , S80058517 S80058517 BHALERAO DHAIRYASHEEL RATNAKAR RENUKA 01. DISCRETE STRUCTURES 100 40 45 P C 100 40 19 F PP 11. ENG MATHS III PP 100 40 64 P C 12. COMPUTER GRAPHICS 100 40 45 P 02. COMPUTER ORGANIZATION 100 40 40 P C 100 40 40 P 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 13. PROCESSOR ARCHITECTURE & INTER. PP 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 40 P C PP 100 40 40 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 44 P C 100 40 56 P 15. DATA COMMUNICATION TW 50 20 41 P C 25 10 19 P 06. DIGITAL LABORATORY 16. PROCESSOR INTERFACING LABORATORY TW 27 P C 50 20 25 P 07. DIGITAL LABORATORY 50 20 PR 17. PROCESSOR INTERFACING LABORATORY OR 08. PROGRAMMING LABORATORY 50 20 37 P C 25 10 TW 18. DATA STRUCTURES AND FILES LAB TW 18 P 09. PROGRAMMING LABORATORY 50 20 25 P C 50 20 17 F PR 19. DATA STRUCTURES AND FILES LAB PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 43 P C 50 20 36 P 20. OBJECT ORIENTED PROGRAMMING LAB TW 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 12 F GRAND TOTAL = 733/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS: , 71200792L , S80058518 , PICT , S80058518 UMA S80058518 BHANDARKUMTHE MADHUR MADHAVRAO PP 01. DISCRETE STRUCTURES 11. ENG MATHS III 100 40 55 P 100 40 44 P C 100 40 100 40 02. COMPUTER ORGANIZATION 66 P C 12. COMPUTER GRAPHICS 47 P 100 40 57 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 46 P 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 48 P C 100 40 55 P PP 100 40 49 P.C 100 40 05. HUMANITIES AND SOCIAL SCIENCES PP 15. DATA COMMUNICATION 69 P TW 43 P C 50 20 23 P 06. DIGITAL LABORATORY 25 10 16. PROCESSOR INTERFACING LABORATORY TW 50 20 25 P C 50 20 32 P 07. DIGITAL LABORATORY PR 17. PROCESSOR INTERFACING LABORATORY OR 50 20 08. PROGRAMMING LABORATORY TW 46 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 23 P 09. PROGRAMMING LABORATORY 50 20 39 P C PR 19. DATA STRUCTURES AND FILES LAB PR 50 20 39 P 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 47 P C 50 20 47 P 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 40 P 21. OBJECT ORIENTED PROGRAMMING LAB PR GRAND TOTAL = 940/1500, RESULT: FIRST CLASS ORDN. 1 MARKS: , 71200793J , S80058519 , PICT , S80058519 S80058519 BHANDE KRISHNA SHRIDHAR SHILABAI 01. DISCRETE STRUCTURES 100 40 60 P C 11. ENG MATHS III 100 40 40 P 02. COMPUTER ORGANIZATION PP 100 40 52 P C 12. COMPUTER GRAPHICS 100 40 48 P 100 40 54 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 47 P 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 52 P C 14. DATA STRUCTURES AND FILES 100 40 61 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 56 P C PP 100 40 53 P 15. DATA COMMUNICATION TW 50 20 19 P 06. DIGITAL LABORATORY 36 P C 25 10 16. PROCESSOR INTERFACING LABORATORY TW 50 20 32 P C 17. PROCESSOR INTERFACING LABORATORY OR 50 20 20 P 07. DIGITAL LABORATORY PR 08. PROGRAMMING LABORATORY TW 50 20 38 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 20 P 39 P 50 20 32 P C 50 20 09. PROGRAMMING LABORATORY PR 19. DATA STRUCTURES AND FILES LAB PR 50 20 41 P C 10. COMMUNICATION AND LANGUAGE LAB. TW 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 36 P 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 37 P GRAND TOTAL = 873/1500, RESULT: HIGHER SECOND CLASS ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71200796C , S80058520 , PICT , S80058520 S80058520 BHOIR KALPESH SHIVRAM SOMABAI 01. DISCRETE STRUCTURES 100 40 13 F PP 100 40 40 P C 11. ENG MATHS III PP 100 40 45 P C 12. COMPUTER GRAPHICS 100 40 29 F 02. COMPUTER ORGANIZATION 100 40 49 P C 100 40 14 F 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 13. PROCESSOR ARCHITECTURE & INTER. PP 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 44 P C PP 100 40 40 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 42 P C 100 40 45 P 15. DATA COMMUNICATION TW 50 20 37 P.C 25 10 12 P 06. DIGITAL LABORATORY 16. PROCESSOR INTERFACING LABORATORY TW 27 P C 07. DIGITAL LABORATORY 50 20 50 20 21 P PR 17. PROCESSOR INTERFACING LABORATORY OR 08. PROGRAMMING LABORATORY 50 20 35 P C 25 10 15 P TW 18. DATA STRUCTURES AND FILES LAB TW 09. PROGRAMMING LABORATORY 50 20 37 P C 50 20 30 P PR 19. DATA STRUCTURES AND FILES LAB PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 43 P C 50 20 29 P 20. OBJECT ORIENTED PROGRAMMING LAB TW 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 22 P GRAND TOTAL = 669/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS: , 71350918L , S80058521 , PICT , S80058521 S80058521 BHOSURE YOGESH KASHINATH SHEVANTA PP 01. DISCRETE STRUCTURES PP 100 40 59 P C 11. ENG MATHS III 100 40 23 F 100 40 57 P C 100 40 51 P 02. COMPUTER ORGANIZATION PP 12. COMPUTER GRAPHICS 59 P C 100 40 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 51 P 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 56 P C 100 40 49 P PP 100 40 58 P C 100 40 05. HUMANITIES AND SOCIAL SCIENCES PP 15. DATA COMMUNICATION 57 P TW 50 20 37 P C 22 P 06. DIGITAL LABORATORY 25 10 16. PROCESSOR INTERFACING LABORATORY TW 50 20 31 P C 50 20 07. DIGITAL LABORATORY PR 17. PROCESSOR INTERFACING LABORATORY OR 40 P 50 20 08. PROGRAMMING LABORATORY TW 36 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 22 P 09. PROGRAMMING LABORATORY 50 20 40 P PR 50 20 25 P C 19. DATA STRUCTURES AND FILES LAB PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 39 P C 50 20 40 P 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 35 P 21. OBJECT ORIENTED PROGRAMMING LAB PR GRAND TOTAL = 887/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS: , 71200801C , S80058522 , PICT , S80058522 S80058522 BIRHADE NIKETAN SHASHIKANT VANDANA 01. DISCRETE STRUCTURES 100 40 52 P C 11. ENG MATHS III 100 40 40 P 02. COMPUTER ORGANIZATION PP 100 40 45 P C 12. COMPUTER GRAPHICS 100 40 45 P 100 40 63 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 40 P 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 46 P C 14. DATA STRUCTURES AND FILES 100 40 45 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 52 P C PP 100 40 49 P 15. DATA COMMUNICATION TW 50 20 06. DIGITAL LABORATORY 41 P C 25 10 16 P 16. PROCESSOR INTERFACING LABORATORY TW 50 20 25 P C 17. PROCESSOR INTERFACING LABORATORY OR 50 20 28 P 07. DIGITAL LABORATORY PR 08. PROGRAMMING LABORATORY TW 50 20 38 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 19 P 50 20 22 P 50 20 22 P C 09. PROGRAMMING LABORATORY PR 19. DATA STRUCTURES AND FILES LAB PR 50 20 43 P C 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 41 P 20. OBJECT ORIENTED PROGRAMMING LAB TW 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 44 P GRAND TOTAL = 816+09/1500, RESULT: HIGHER SECOND CLASS [0.2] ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71350919J , S80058523 , PICT , S80058523 S80058523 BORATE SHRADDHA RAJARAM KAMAL 01. DISCRETE STRUCTURES 100 40 11 F PP 100 40 40 P C 11. ENG MATHS III PP 100 40 45 P C 12. COMPUTER GRAPHICS 100 40 47 P 02. COMPUTER ORGANIZATION 100 40 44 P C 100 40 56 P 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 13. PROCESSOR ARCHITECTURE & INTER. PP 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 40 P C PP 100 40 53 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 56 P C 100 40 54 P 15. DATA COMMUNICATION TW 50 20 43 P C 25 10 22 P 06. DIGITAL LABORATORY 16. PROCESSOR INTERFACING LABORATORY TW 50 20 25 P 07. DIGITAL LABORATORY 50 20 20 P C PR 17. PROCESSOR INTERFACING LABORATORY OR 08. PROGRAMMING LABORATORY 50 20 44 P C 25 10 23 P TW 18. DATA STRUCTURES AND FILES LAB TW 09. PROGRAMMING LABORATORY 50 20 20 P C 50 20 40 P PR 19. DATA STRUCTURES AND FILES LAB PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 43 P C 50 20 43 P 20. OBJECT ORIENTED PROGRAMMING LAB TW 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 35 P GRAND TOTAL = 804/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS : , 71200807в , S80058524 , РІСТ , S80058524 ARCHANA S80058524 CHANDRATRE ABHISHEK SURENDRA 01. DISCRETE STRUCTURES 66 P C 11. ENG MATHS III 100 40 69 P 100 40 PP 100 40 100 40 02. COMPUTER ORGANIZATION 62 P C 12. COMPUTER GRAPHICS 46 P 100 40 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 40 P 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 61 P C 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 50 P C 100 40 PP 40 P 100 40 46 P C 100 40 05. HUMANITIES AND SOCIAL SCIENCES PP 15. DATA COMMUNICATION 66 P TW 50 20 36 P C 19 P 06. DIGITAL LABORATORY 25 10 16. PROCESSOR INTERFACING LABORATORY TW 50 20 45 P C 50 20 07. DIGITAL LABORATORY PR 17. PROCESSOR INTERFACING LABORATORY OR 40 P 50 20 08. PROGRAMMING LABORATORY TW 38 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 20 P 09. PROGRAMMING LABORATORY 50 20 42 P C 50 20 39 P PR 19. DATA STRUCTURES AND FILES LAB PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 36 P C 50 20 43 P 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 41 P 21. OBJECT ORIENTED PROGRAMMING LAB PR GRAND TOTAL = 945/1500, RESULT: FIRST CLASS ORDN. 1 MARKS: , 71200808L , S80058525 , PICT , S80058525 S80058525 CHANDRIKA PARIMOO ANITA 01. DISCRETE STRUCTURES 100 40 88 P C 11. ENG MATHS III 100 40 84 P 02. COMPUTER ORGANIZATION PP 100 40 79 P C 12. COMPUTER GRAPHICS 100 40 58 P 73 P C 100 40 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 55 P 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 60 P C 14. DATA STRUCTURES AND FILES 100 40 63 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 59 P C PP 100 40 75 P 15. DATA COMMUNICATION TW 50 20 22 P 06. DIGITAL LABORATORY 40 P C 25 10 16. PROCESSOR INTERFACING LABORATORY TW 50 20 41 P C 17. PROCESSOR INTERFACING LABORATORY OR 50 20 42 P 07. DIGITAL LABORATORY PR 08. PROGRAMMING LABORATORY TW 50 20 45 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 24 P 50 20 46 P C 50 20 44 P 09. PROGRAMMING LABORATORY PR 19. DATA STRUCTURES AND FILES LAB PR 50 20 41 P C 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 45 P 20. OBJECT ORIENTED PROGRAMMING LAB TW 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 45 P GRAND TOTAL = 1129/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71200809J , S80058526 , PICT , S80058526 SEEMA S80058526 CHARKHA BHUSHAN HEMANT 01. DISCRETE STRUCTURES 100 40 54 P PP 100 40 53 P C 11. ENG MATHS III PP 100 40 53 P C 12. COMPUTER GRAPHICS 100 40 45 P 02. COMPUTER ORGANIZATION PP 100 40 52 P C 100 40 47 P 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 13. PROCESSOR ARCHITECTURE & INTER. PP 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 46 P C PP 100 40 46 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 49 P C 100 40 46 P 15. DATA COMMUNICATION TW 50 20 34 P C 25 10 16 P 06. DIGITAL LABORATORY 16. PROCESSOR INTERFACING LABORATORY TW 07. DIGITAL LABORATORY 50 20 30 P C 50 20 09 F PR 17. PROCESSOR INTERFACING LABORATORY OR 08. PROGRAMMING LABORATORY 50 20 29 P C 25 10 17 P TW 18. DATA STRUCTURES AND FILES LAB TW 09. PROGRAMMING LABORATORY PR 50 20 40 P C 50 20 32 P 19. DATA STRUCTURES AND FILES LAB PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 37 P C 50 20 33 P 20. OBJECT ORIENTED PROGRAMMING LAB TW 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 40 P GRAND TOTAL = 808/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS : , 71200811L , S80058527 , PICT , S80058527 POOJA S80058527 CHASKAR VIPUL VINAYAK PP 01. DISCRETE STRUCTURES 77 P C 11. ENG MATHS III 100 40 60 P PP 100 40 100 40 100 40 51 P 02. COMPUTER ORGANIZATION PP 63 P C 12. COMPUTER GRAPHICS 100 40 64 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 54 P 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 100 40 49 P C PP 59 P 100 40 53 P C 100 40 05. HUMANITIES AND SOCIAL SCIENCES PP 15. DATA COMMUNICATION 73 P TW 50 20 44 P C 24 P 06. DIGITAL LABORATORY 25 10 16. PROCESSOR INTERFACING LABORATORY TW 50 20 42 P C 50 20 07. DIGITAL LABORATORY PR 17. PROCESSOR INTERFACING LABORATORY OR 40 P 50 20 08. PROGRAMMING LABORATORY TW 46 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 23 P 09. PROGRAMMING LABORATORY PR 50 20 42 P C 19. DATA STRUCTURES AND FILES LAB PR 50 20 39 P 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 44 P C 50 20 46 P 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 38 P 21. OBJECT ORIENTED PROGRAMMING LAB PR GRAND TOTAL = 1031/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS: MADHUCHHANDA , 71200812J , S80058528 , PICT , S80058528 S80058528 CHATTERJEE SUDIPTO ANJAN 01. DISCRETE STRUCTURES 100 40 58 P C 11. ENG MATHS III 100 40 57 P 02. COMPUTER ORGANIZATION PP 100 40 78 P C 12. COMPUTER GRAPHICS 100 40 48 P 100 40 63 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 44 P 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 50 P C 14. DATA STRUCTURES AND FILES 100 40 50 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 49 P C PP 100 40 63 P 15. DATA COMMUNICATION TW 50 20 20 P 06. DIGITAL LABORATORY 41 P C 25 10 16. PROCESSOR INTERFACING LABORATORY TW 50 20 39 P C 17. PROCESSOR INTERFACING LABORATORY OR 50 20 42 P 07. DIGITAL LABORATORY PR 08. PROGRAMMING LABORATORY TW 50 20 42 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 23 P 50 20 45 P C 50 20 40 P 09. PROGRAMMING LABORATORY PR 19. DATA STRUCTURES AND FILES LAB PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 42 P C 50 20 45 P 20. OBJECT ORIENTED PROGRAMMING LAB TW 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 44 P GRAND TOTAL = 983/1500, RESULT: FIRST CLASS ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER SAROJ , 71200817к , S80058530 , РІСТ , S80058530 S80058530 CHAVAN SONAL NILKANTH 01. DISCRETE STRUCTURES 100 40 49 P PP 100 40 51 P C 11. ENG MATHS III PP 100 40 57 P C 12. COMPUTER GRAPHICS 100 40 48 P 02. COMPUTER ORGANIZATION 100 40 61 P C 100 40 70 P 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 13. PROCESSOR ARCHITECTURE & INTER. PP 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 53 P C PP 100 40 46 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 47 P C 100 40 15. DATA COMMUNICATION TW 50 20 31 P C 25 10 16 P 06. DIGITAL LABORATORY 16. PROCESSOR INTERFACING LABORATORY TW 32 P C 07. DIGITAL LABORATORY 50 20 50 20 28 P PR 17. PROCESSOR INTERFACING LABORATORY OR 08. PROGRAMMING LABORATORY 50 20 34 P C 25 10 17 P TW 18. DATA STRUCTURES AND FILES LAB TW 09. PROGRAMMING LABORATORY 50 20 44 P C 50 20 40 P PR 19. DATA STRUCTURES AND FILES LAB PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 39 P C 50 20 35 P 20. OBJECT ORIENTED PROGRAMMING LAB TW 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 41 P GRAND TOTAL = 902/1500, RESULT: FIRST CLASS ORDN. 1 MARKS: , 71200820K , S80058531 , PICT , S80058531 MANJUSHA S80058531 CHAWARE PURUSHOTTAM GAJANAN PP 01. DISCRETE STRUCTURES 86 P C 11. ENG MATHS III 100 40 94 P 100 40 100 40 74 P C 100 40 53 P 02. COMPUTER ORGANIZATION 12. COMPUTER GRAPHICS 100 40 72 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 53 P 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 58 P C 100 40 65 P PP 100 40 54 P C 100 40 05. HUMANITIES AND SOCIAL SCIENCES PP 15. DATA COMMUNICATION 61 P TW 50 20 45 P C 23 P 06. DIGITAL LABORATORY 25 10 16. PROCESSOR INTERFACING LABORATORY TW 50 20 40 P C 50 20 07. DIGITAL LABORATORY PR 17. PROCESSOR INTERFACING LABORATORY OR 44 P 50 20 08. PROGRAMMING LABORATORY TW 48 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 22 P 09. PROGRAMMING LABORATORY 50 20 42 P PR 50 20 44 P C 19. DATA STRUCTURES AND FILES LAB PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 48 P C 50 20 44 P 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 35 P 21. OBJECT ORIENTED PROGRAMMING LAB PR GRAND TOTAL = 1105/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS: , 71200822F , S80058532 , PICT , S80058532 POONAM S80058532 CHOUDHARI PRIYANKA SANJAY 01. DISCRETE STRUCTURES 100 40 63 P C 11. ENG MATHS III 100 40 40 P 02. COMPUTER ORGANIZATION PP 100 40 40 P C 12. COMPUTER GRAPHICS 100 40 40 P 100 40 55 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 28 F 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 53 P C 14. DATA STRUCTURES AND FILES 100 40 40 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 43 P C PP 100 40 24 F 15. DATA COMMUNICATION TW 50 20 06. DIGITAL LABORATORY 40 P C 25 10 16 P 16. PROCESSOR INTERFACING LABORATORY TW 50 20 30 P 17. PROCESSOR INTERFACING LABORATORY OR 50 20 25 P 07. DIGITAL LABORATORY PR 08. PROGRAMMING LABORATORY TW 50 20 37 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 16 P 50 20 36 P C 50 20 41 P 09. PROGRAMMING LABORATORY PR 19. DATA STRUCTURES AND FILES LAB PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 42 P C 50 20 35 P 20. OBJECT ORIENTED PROGRAMMING LAB TW 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 31 P GRAND TOTAL = 775/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71200824B , S80058533 , PICT , S80058533 ANNIE S80058533 D CUNHA JOANNE JOHN 01. DISCRETE STRUCTURES 100 40 40 P PP 100 40 69 P C 11. ENG MATHS III PP 100 40 45 P C 12. COMPUTER GRAPHICS 100 40 18 F 02. COMPUTER ORGANIZATION PP 100 40 50 P 100 40 20 F 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 13. PROCESSOR ARCHITECTURE & INTER. PP 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 59 P PP 100 40 40 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 64 P 15. DATA COMMUNICATION 100 40 40 P TW 50 20 45 P C 25 10 20 P 06. DIGITAL LABORATORY 16. PROCESSOR INTERFACING LABORATORY TW 27 P C 07. DIGITAL LABORATORY 50 20 50 20 38 P PR 17. PROCESSOR INTERFACING LABORATORY OR 08. PROGRAMMING LABORATORY 50 20 46 P C 25 10 20 P TW 18. DATA STRUCTURES AND FILES LAB TW 09. PROGRAMMING LABORATORY 50 20 38 P C 50 20 40 P PR 19. DATA STRUCTURES AND FILES LAB PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 47 P C 50 20 42 P 20. OBJECT ORIENTED PROGRAMMING LAB TW 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 38 P GRAND TOTAL = 846/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS : , 71200825L , S80058534 , PICT , S80058534 S80058534 DABARE DEEP RAJESH RASHMI 01. DISCRETE STRUCTURES 100 40 75 P C 11. ENG MATHS III 100 40 40 P PP 100 40 50 P C 100 40 02. COMPUTER ORGANIZATION PP 12. COMPUTER GRAPHICS 40 P 100 40 42 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 48 P 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 53 P C 100 40 PP 51 P 100 40 52 P C 100 40 05. HUMANITIES AND SOCIAL SCIENCES PP 15. DATA COMMUNICATION TW 50 20 35 P C 15 P 06. DIGITAL LABORATORY 25 10 16. PROCESSOR INTERFACING LABORATORY TW 50 20 37 P C 50 20 27 P 07. DIGITAL LABORATORY PR 17. PROCESSOR INTERFACING LABORATORY OR 50 20 08. PROGRAMMING LABORATORY TW 32 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 16 P 09. PROGRAMMING LABORATORY 50 20 30 P 50 20 32 P C PR 19. DATA STRUCTURES AND FILES LAB PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 40 P C 50 20 27 P 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 37 P 21. OBJECT ORIENTED PROGRAMMING LAB PR GRAND TOTAL = 843/1500, RESULT: HIGHER SECOND CLASS ORDN. 1 MARKS: , 71200827G , S80058535 , PICT , S80058535 JYOTI S80058535 DARAK SHREYA TEJRAJ 01. DISCRETE STRUCTURES 100 40 71 P C 11. ENG MATHS III 100 40 40 P 02. COMPUTER ORGANIZATION PP 100 40 67 P C 12. COMPUTER GRAPHICS 100 40 55 P 100 40 67 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 56 P 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 60 P C 14. DATA STRUCTURES AND FILES 100 40 46 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 51 P C PP 100 40 55 P 15. DATA COMMUNICATION TW 50 20 20 P 06. DIGITAL LABORATORY 38 P C 25 10 16. PROCESSOR INTERFACING LABORATORY TW 50 20 30 P C 17. PROCESSOR INTERFACING LABORATORY OR 50 20 32 P 07. DIGITAL LABORATORY PR 08. PROGRAMMING LABORATORY TW 50 20 32 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 20 P 50 20 35 P C 50 20 38 P 09. PROGRAMMING LABORATORY PR 19. DATA STRUCTURES AND FILES LAB PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 39 P C 50 20 45 P 20. OBJECT ORIENTED PROGRAMMING LAB TW 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 36 P GRAND TOTAL = 933/1500, RESULT: FIRST CLASS ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71200829C , S80058536 , PICT , S80058536 S80058536 DEOGHATKAR BRAMHESH NAMDEO MAYALI 01. DISCRETE STRUCTURES 100 40 40 P 100 40 64 P C 11. ENG MATHS III PP 100 40 57 P C 12. COMPUTER GRAPHICS 100 40 02. COMPUTER ORGANIZATION 100 40 49 P C 100 40 40 P 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 13. PROCESSOR ARCHITECTURE & INTER. PP 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 51 P C PP 100 40 54 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 53 P C 100 40 51 P 15. DATA COMMUNICATION TW 50 20 34 P C 25 10 14 P 06. DIGITAL LABORATORY 16. PROCESSOR INTERFACING LABORATORY TW 07. DIGITAL LABORATORY 50 20 33 P C 50 20 09 F PR 17. PROCESSOR INTERFACING LABORATORY OR 08. PROGRAMMING LABORATORY 50 20 29 P C 25 10 15 P TW 18. DATA STRUCTURES AND FILES LAB TW 09. PROGRAMMING LABORATORY PR 50 20 27 P C 50 20 42 P 19. DATA STRUCTURES AND FILES LAB PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 34 P C 50 20 30 P 20. OBJECT ORIENTED PROGRAMMING LAB TW 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 43 P GRAND TOTAL = 813/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS : , 71200831E , S80058538 , PICT , S80058538 JYOTI S80058538 DESHMUKH MONIKA JAYANT PP 01. DISCRETE STRUCTURES 100 40 79 P C 11. ENG MATHS III 100 40 59 P 100 40 100 40 59 P 02. COMPUTER ORGANIZATION PP 69 P C 12. COMPUTER GRAPHICS 100 40 71 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 64 P 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 60 P C 100 40 55 P PP 100 40 100 40 05. HUMANITIES AND SOCIAL SCIENCES PP 68 P C 15. DATA COMMUNICATION 66 P TW 50 20 20 P 06. DIGITAL LABORATORY 43 P C 25 10 16. PROCESSOR INTERFACING LABORATORY TW 50 20 34 P C 50 20 07. DIGITAL LABORATORY PR 17. PROCESSOR INTERFACING LABORATORY OR 40 P 50 20 08. PROGRAMMING LABORATORY TW 42 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 23 P 09. PROGRAMMING LABORATORY 50 20 40 P C 50 20 43 P PR 19. DATA STRUCTURES AND FILES LAB PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 43 P C 50 20 44 P 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 37 P 21. OBJECT ORIENTED PROGRAMMING LAB PR GRAND TOTAL = 1059/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS: , 71200836F , S80058539 , PICT , S80058539 S80058539 DEVASHISH SINGH SUSHMA 01. DISCRETE STRUCTURES 100 40 77 P C 11. ENG MATHS III 100 40 61 P 02. COMPUTER ORGANIZATION PP 100 40 65 P C 12. COMPUTER GRAPHICS 100 40 54 P 100 40 56 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 65 P 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 55 P C 14. DATA STRUCTURES AND FILES 100 40 59 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 59 P C PP 100 40 64 P 15. DATA COMMUNICATION TW 50 20 15 P 06. DIGITAL LABORATORY 31 P C 25 10 16. PROCESSOR INTERFACING LABORATORY TW 50 20 38 P C 17. PROCESSOR INTERFACING LABORATORY OR 50 20 37 P 07. DIGITAL LABORATORY PR 08. PROGRAMMING LABORATORY TW 50 20 28 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 15 P 42 P 50 20 40 P C 50 20 09. PROGRAMMING LABORATORY PR 19. DATA STRUCTURES AND FILES LAB PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 36 P C 50 20 33 P 20. OBJECT ORIENTED PROGRAMMING LAB TW 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 32 P GRAND TOTAL = 962/1500, RESULT: FIRST CLASS ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71200838B , S80058540 , PICT , S80058540 S80058540 DHAMANE AKANKSHA ARUN KANCHAN 01. DISCRETE STRUCTURES 100 40 62 P 100 40 77 P C 11. ENG MATHS III PP 100 40 48 P C 12. COMPUTER GRAPHICS 100 40 52 P 02. COMPUTER ORGANIZATION 100 40 68 P C 100 40 62 P 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 13. PROCESSOR ARCHITECTURE & INTER. PP 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 60 P C PP 100 40 59 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 54 P C 100 40 15. DATA COMMUNICATION 50 20 43 P C 25 10 17 P 06. DIGITAL LABORATORY 16. PROCESSOR INTERFACING LABORATORY TW TW 07. DIGITAL LABORATORY 50 20 32 P C 50 20 30 P PR 17. PROCESSOR INTERFACING LABORATORY OR 08. PROGRAMMING LABORATORY 50 20 40 P C 25 10 TW 18. DATA STRUCTURES AND FILES LAB TW 22 P 09. PROGRAMMING LABORATORY 50 20 42 P C 50 20 41 P PR 19. DATA STRUCTURES AND FILES LAB PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 45 P C 50 20 40 P 20. OBJECT ORIENTED PROGRAMMING LAB TW 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 40 P GRAND TOTAL = 994/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS: , 71200845E , S80058541 , PICT , S80058541 S80058541 DISALE GITA BIBHISHAN RADHA PP 01. DISCRETE STRUCTURES 11. ENG MATHS III 100 40 43 P PP 100 40 63 P C 100 40 57 P C 100 40 02. COMPUTER ORGANIZATION PP 12. COMPUTER GRAPHICS 43 P 100 40 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 40 P 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 51 P C 14. DATA STRUCTURES AND FILES 100 40 48 P C 100 40 04. FUNDAMENTAL OF DATA STRUCTURES PP PP 46 P 100 40 100 40 05. HUMANITIES AND SOCIAL SCIENCES PP 58 P C 15. DATA COMMUNICATION 52 P 50 20 40 P C 14 P 06. DIGITAL LABORATORY 25 10 TW 16. PROCESSOR INTERFACING LABORATORY TW 50 20 36 P C 50 20 07. DIGITAL LABORATORY PR 17. PROCESSOR INTERFACING LABORATORY OR 07 F 50 20 37 P C 08. PROGRAMMING LABORATORY TW 18. DATA STRUCTURES AND FILES LAB TW 25 10 17 P 09. PROGRAMMING LABORATORY 50 20 35 P 50 20 39 P PR 19. DATA STRUCTURES AND FILES LAB PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 44 P C 50 20 36 P 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 32 P 21. OBJECT ORIENTED PROGRAMMING LAB PR GRAND TOTAL = 838/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS: , 71200846C , S80058542 , PICT , S80058542 S80058542 DIXIT PRANAV SUDHIR SHUBHANGI 01. DISCRETE STRUCTURES 100 40 63 P C 11. ENG MATHS III 100 40 65 P 02. COMPUTER ORGANIZATION PP 100 40 60 P C 12. COMPUTER GRAPHICS 100 40 58 P 100 40 68 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 48 P 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 60 P C 14. DATA STRUCTURES AND FILES 100 40 62 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 51 P C PP 100 40 61 P 15. DATA COMMUNICATION TW 50 20 17 P 06. DIGITAL LABORATORY 40 P C 25 10 16. PROCESSOR INTERFACING LABORATORY TW 50 20 45 P C 17. PROCESSOR INTERFACING LABORATORY OR 50 20 28 P 07. DIGITAL LABORATORY PR 08. PROGRAMMING LABORATORY TW 50 20 42 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 21 P 50 20 40 P C 50 20 43 P 09. PROGRAMMING LABORATORY PR 19. DATA STRUCTURES AND FILES LAB PR 50 20 44 P C 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 42 P 20. OBJECT ORIENTED PROGRAMMING LAB TW 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 44 P GRAND TOTAL = 1002/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71200850M , S80058543 , PICT , S80058543 BABITA S80058543 DURGE PRAPTI ANIL 01. DISCRETE STRUCTURES 100 40 47 P C 100 40 50 P 11. ENG MATHS III PP 100 40 43 P C 12. COMPUTER GRAPHICS 100 40 47 P 02. COMPUTER ORGANIZATION 100 40 44 P C 100 40 40 P 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 13. PROCESSOR ARCHITECTURE & INTER. PP 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 65 P C PP 100 40 43 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 59 P C 100 40 43 P 15. DATA COMMUNICATION TW 50 20 40 P C 25 10 18 P 06. DIGITAL LABORATORY 16. PROCESSOR INTERFACING LABORATORY TW 50 20 22 P 07. DIGITAL LABORATORY 50 20 20 P C PR 17. PROCESSOR INTERFACING LABORATORY OR 08. PROGRAMMING LABORATORY 50 20 38 P C 25 10 22 P TW 18. DATA STRUCTURES AND FILES LAB TW 09. PROGRAMMING LABORATORY 50 20 35 P C 50 20 32 P PR 19. DATA STRUCTURES AND FILES LAB PR 50 20 44 P C 50 20 41 P 10. COMMUNICATION AND LANGUAGE LAB. TW 20. OBJECT ORIENTED PROGRAMMING LAB TW 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 35 P GRAND TOTAL = 828/1500, RESULT: HIGHER SECOND CLASS ORDN. 1 MARKS : , 71200853F , S80058544 , PICT , S80058544 PRABHA S80058544 GAIKWAD PRINCE VIJAYKUMAR 01. DISCRETE STRUCTURES 100 40 40 P 100 40 53 P C 11. ENG MATHS III PP 100 40 100 40 02. COMPUTER ORGANIZATION 42 P C 12. COMPUTER GRAPHICS 44 P 100 40 52 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 27 F 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 49 P C 100 40 PP 46 P 100 40 43 P C 100 40 05. HUMANITIES AND SOCIAL SCIENCES PP 15. DATA COMMUNICATION 56 P TW 50 20 35 P C 15 P 06. DIGITAL LABORATORY 25 10 16. PROCESSOR INTERFACING LABORATORY TW 50 20 35 P C 50 20 07. DIGITAL LABORATORY PR 17. PROCESSOR INTERFACING LABORATORY OR 08 F 50 20 35 P C 08. PROGRAMMING LABORATORY TW 18. DATA STRUCTURES AND FILES LAB TW 25 10 13 P 09. PROGRAMMING LABORATORY 50 20 35 P PR 50 20 38 P C 19. DATA STRUCTURES AND FILES LAB PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 38 P C 50 20 29 P 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 37 P 21. OBJECT ORIENTED PROGRAMMING LAB PR GRAND TOTAL = 770/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS: , 71200854D , S80058545 , PICT , S80058545 S80058545 GAIKWAD ROHAN KIRAN LATA 01. DISCRETE STRUCTURES 100 40 40 P C 11. ENG MATHS III 100 40 40 P 02. COMPUTER ORGANIZATION PP 100 40 44 P C 12. COMPUTER GRAPHICS 100 40 44 P 100 40 45 P 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 19 F 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 40 P C 14. DATA STRUCTURES AND FILES 100 40 40 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 50 P C PP 100 40 51 P 15. DATA COMMUNICATION TW 50 20 10 P 06. DIGITAL LABORATORY 21 P C 25 10 16. PROCESSOR INTERFACING LABORATORY TW 50 20 21 P C 17. PROCESSOR INTERFACING LABORATORY OR 50 20 07. DIGITAL LABORATORY PR AA F 08. PROGRAMMING LABORATORY TW 50 20 22 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 10 P 50 20 07 F 50 20 AA F 09. PROGRAMMING LABORATORY PR 19. DATA STRUCTURES AND FILES LAB PR 50 20 20 P C 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 22 P 20. OBJECT ORIENTED PROGRAMMING LAB TW 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 AA F GRAND TOTAL = 546/1500, RESULT: FAILS ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71200855B , S80058546 , PICT , S80058546 S80058546 GAME AJAY BALASAHEB ANITA 01. DISCRETE STRUCTURES 100 40 82 P PP 100 40 80 P C 11. ENG MATHS III PP 100 40 76 P C 12. COMPUTER GRAPHICS 100 40 02. COMPUTER ORGANIZATION PP 100 40 68 P C 100 40 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 13. PROCESSOR ARCHITECTURE & INTER. PP 67 P 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 61 P C PP 100 40 63 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 64 P C 100 40 73 P 15. DATA COMMUNICATION TW 50 20 44 P C 25 10 19 P 06. DIGITAL LABORATORY 16. PROCESSOR INTERFACING LABORATORY TW 07. DIGITAL LABORATORY 50 20 27 P C 50 20 27 P PR 17. PROCESSOR INTERFACING LABORATORY OR 08. PROGRAMMING LABORATORY 50 20 45 P C 25 10 23 P TW 18. DATA STRUCTURES AND FILES LAB TW 09. PROGRAMMING LABORATORY 50 20 32 P C 50 20 39 P PR 19. DATA STRUCTURES AND FILES LAB PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 46 P C 50 20 45 P 20. OBJECT ORIENTED PROGRAMMING LAB TW 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 43 P GRAND TOTAL = 1089/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS: , 71200856L , S80058547 , PICT , S80058547 S80058547 GANDHI KAMLESH ISHWARLAL SUVARNA PP 01. DISCRETE STRUCTURES PP 100 40 11. ENG MATHS III 100 40 64 P 48 P C 100 40 100 40 02. COMPUTER ORGANIZATION PP 67 P C 12. COMPUTER GRAPHICS 47 P 59 P C 100 40 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 60 P 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 53 P C 100 40 PP 45 P 100 40 46 P.C 100 40 05. HUMANITIES AND SOCIAL SCIENCES PP 15. DATA COMMUNICATION 64 P TW 50 20 06. DIGITAL LABORATORY 43 P C 25 10 21 P 16. PROCESSOR INTERFACING LABORATORY TW 50 20 40 P C 50 20 30 P 07. DIGITAL LABORATORY PR 17. PROCESSOR INTERFACING LABORATORY OR 50 20 08. PROGRAMMING LABORATORY TW 44 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 22 P 09. PROGRAMMING LABORATORY 50 20 37 P PR 50 20 36 P C 19. DATA STRUCTURES AND FILES LAB PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 42 P C 50 20 45 P 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 41 P 21. OBJECT ORIENTED PROGRAMMING LAB PR GRAND TOTAL = 954/1500, RESULT: FIRST CLASS ORDN. 1 MARKS: , 71200860J , S80058548 , PICT , S80058548 S80058548 GAVALI ANKITA SUNIL SUDHA 01. DISCRETE STRUCTURES PP 100 40 49 P C 11. ENG MATHS III 100 40 40 P 02. COMPUTER ORGANIZATION PP 100 40 60 P C 12. COMPUTER GRAPHICS 100 40 52 P 100 40 100 40 54 P 13. PROCESSOR ARCHITECTURE & INTER. PP 40 P 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 44 P C 14. DATA STRUCTURES AND FILES 100 40 54 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 52 P C PP 100 40 52 P 15. DATA COMMUNICATION TW 50 20 39 P C 17 P 06. DIGITAL LABORATORY 25 10 16. PROCESSOR INTERFACING LABORATORY TW 50 20 39 P 17. PROCESSOR INTERFACING LABORATORY OR 50 20 08 F 07. DIGITAL LABORATORY PR 08. PROGRAMMING LABORATORY TW 50 20 37 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 17 P 09. PROGRAMMING LABORATORY 50 20 40 P C 50 20 38 P PR 19. DATA STRUCTURES AND FILES LAB PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 38 P C 50 20 30 P 20. OBJECT ORIENTED PROGRAMMING LAB TW 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 35 P GRAND TOTAL = 835/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER KUNDA , 71200864M , S80058549 , PICT , S80058549 S80058549 GHATE ABHIJIT SURESHRAO 01. DISCRETE STRUCTURES 100 40 44 P 100 40 53 P C 11. ENG MATHS III PP PP 100 40 50 P C 12. COMPUTER GRAPHICS 100 40 46 P 02. COMPUTER ORGANIZATION 100 40 40 P C 100 40 40 P 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 13. PROCESSOR ARCHITECTURE & INTER. PP 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 46 P C PP 100 40 51 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 55 P C 100 40 40 P 15. DATA COMMUNICATION TW 50 20 32 P C 25 10 15 P 06. DIGITAL LABORATORY 16. PROCESSOR INTERFACING LABORATORY TW 37 P C 07. DIGITAL LABORATORY 50 20 50 20 20 P PR 17. PROCESSOR INTERFACING LABORATORY OR 08. PROGRAMMING LABORATORY 50 20 34 P C 25 10 15 P TW 18. DATA STRUCTURES AND FILES LAB TW 09. PROGRAMMING LABORATORY 50 20 34 P C 50 20 32 P PR 19. DATA STRUCTURES AND FILES LAB PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 36 P C 50 20 28 P 20. OBJECT ORIENTED PROGRAMMING LAB TW 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 38 P GRAND TOTAL = 786/1500, RESULT: SECOND CLASS ORDN. 1 MARKS: , 71200865к , S80058550 , РІСТ , S80058550 PRADNYA S80058550 GHODKE NIRANJAN DATTATRAYA PP O1. DISCRETE STRUCTURES PP 82 P C 11. ENG MATHS III 100 40 65 P 100 40 100 40 100 40 02. COMPUTER ORGANIZATION 78 P C 12. COMPUTER GRAPHICS 63 P 100 40 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 65 P C 62 P 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 66 P C 100 40 57 P PP 100 40 100 40 05. HUMANITIES AND SOCIAL SCIENCES PP 61 P C 15. DATA COMMUNICATION 67 P TW 50 20 20 P 06. DIGITAL LABORATORY 43 P C 25 10 16. PROCESSOR INTERFACING LABORATORY TW 50 20 41 P C 50 20 32 P 07. DIGITAL LABORATORY PR 17. PROCESSOR INTERFACING LABORATORY OR 50 20 08. PROGRAMMING LABORATORY TW 44 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 23 P 09. PROGRAMMING LABORATORY PR 50 20 43 P C 19. DATA STRUCTURES AND FILES LAB PR 50 20 40 P 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 44 P C 50 20 44 P 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 32 P 21. OBJECT ORIENTED PROGRAMMING LAB PR GRAND TOTAL = 1072/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS: , 71350921L , S80058551 , PICT , S80058551 REKHA S80058551 GOHOKAR KIRAN DADAJI 01. DISCRETE STRUCTURES PP 100 40 42 P C 11. ENG MATHS III 100 40 25 F 02. COMPUTER ORGANIZATION PP 100 40 54 P C 12. COMPUTER GRAPHICS 100 40 40 P 100 40 54 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 41 P 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 44 P C 14. DATA STRUCTURES AND FILES 100 40 50 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 61 P C 100 40 59 P 15. DATA COMMUNICATION TW 50 20 16 P 06. DIGITAL LABORATORY 33 P C 25 10 16. PROCESSOR INTERFACING LABORATORY TW 50 20 23 P C 17. PROCESSOR INTERFACING LABORATORY OR 50 20 09 F 07. DIGITAL LABORATORY PR 08. PROGRAMMING LABORATORY TW 50 20 35 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 18 P 09. PROGRAMMING LABORATORY 50 20 22 P 50 20 35 P PR 19. DATA STRUCTURES AND FILES LAB PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 37 P C 50 20 38 P 20. OBJECT ORIENTED PROGRAMMING LAB TW 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 25 P GRAND TOTAL = 761/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71200867F , S80058552 , PICT , S80058552 S80058552 GOKHALE VALLARI SHRIKANT PADMAJA 01. DISCRETE STRUCTURES 100 40 62 P PP 100 40 65 P C 11. ENG MATHS III PP 100 40 72 P C 12. COMPUTER GRAPHICS 100 40 53 P 02. COMPUTER ORGANIZATION 100 40 70 P C 100 40 63 P 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 13. PROCESSOR ARCHITECTURE & INTER. PP 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 54 P C PP 100 40 65 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 56 P C 100 40 15. DATA COMMUNICATION TW 50 20 43 P C 25 10 20 P 06. DIGITAL LABORATORY 16. PROCESSOR INTERFACING LABORATORY TW 07. DIGITAL LABORATORY 50 20 34 P C 50 20 38 P PR 17. PROCESSOR INTERFACING LABORATORY OR 08. PROGRAMMING LABORATORY 50 20 42 P C 25 10 21 P TW 18. DATA STRUCTURES AND FILES LAB TW 09. PROGRAMMING LABORATORY 50 20 36 P C 50 20 40 P PR 19. DATA STRUCTURES AND FILES LAB PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 45 P C 50 20 44 P 20. OBJECT ORIENTED PROGRAMMING LAB TW 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 38 P GRAND TOTAL = 1024/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS: , 71200868D , S80058553 , PICT , S80058553 APARNA S80058553 GOLAPKAR PIYUSH ANIL PP 01. DISCRETE STRUCTURES 11. ENG MATHS III 100 40 40 P PP 100 40 68 P C 100 40 51 P C 100 40 55 P 02. COMPUTER ORGANIZATION PP 12. COMPUTER GRAPHICS 100 40 53 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 58 P 14. DATA STRUCTURES AND FILES 100 40 49 P C 100 40 04. FUNDAMENTAL OF DATA STRUCTURES PP PP 44 P 100 40 100 40 05. HUMANITIES AND SOCIAL SCIENCES PP 56 P C 15. DATA COMMUNICATION 60 P 50 20 38 P C 06. DIGITAL LABORATORY 25 10 16 P TW 16. PROCESSOR INTERFACING LABORATORY TW 50 20 34 P C 50 20 22 P 07. DIGITAL LABORATORY PR 17. PROCESSOR INTERFACING LABORATORY OR 50 20 08. PROGRAMMING LABORATORY TW 41 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 20 P 09. PROGRAMMING LABORATORY 50 20 38 P PR 50 20 35 P C 19. DATA STRUCTURES AND FILES LAB PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 42 P C 50 20 42 P 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 41 P 21. OBJECT ORIENTED PROGRAMMING LAB PR GRAND TOTAL = 903/1500, RESULT: FIRST CLASS ORDN. 1 MARKS: , 71200869B , S80058554 , PICT , S80058554 S80058554 GOSAVI KALYANI RAJGIR KAMINI 01. DISCRETE STRUCTURES PP 100 40 60 P C 11. ENG MATHS III 100 40 40 P 02. COMPUTER ORGANIZATION PP 100 40 58 P C 12. COMPUTER GRAPHICS 100 40 60 P 100 40 52 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 62 P 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 48 P C 14. DATA STRUCTURES AND FILES 100 40 63 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 69 P C PP 100 40 68 P 15. DATA COMMUNICATION TW 50 20 19 P 06. DIGITAL LABORATORY 42 P C 25 10 16. PROCESSOR INTERFACING LABORATORY TW 50 20 27 P C 17. PROCESSOR INTERFACING LABORATORY OR 50 20 30 P 07. DIGITAL LABORATORY PR 08. PROGRAMMING LABORATORY TW 50 20 38 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 22 P 50 20 27 P C 50 20 42 P 09. PROGRAMMING LABORATORY PR 19. DATA STRUCTURES AND FILES LAB PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 43 P C 50 20 40 P 20. OBJECT ORIENTED PROGRAMMING LAB TW 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 28 P GRAND TOTAL = 938/1500, RESULT: FIRST CLASS ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71200873L , S80058555 , PICT , S80058555 S80058555 GUPTA SHRADHA RAVINDRA AMITA 01. DISCRETE STRUCTURES 100 40 74 P 100 40 73 P C 11. ENG MATHS III PP PP 100 40 60 P C 12. COMPUTER GRAPHICS 100 40 50 P 02. COMPUTER ORGANIZATION 100 40 77 P C 100 40 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 13. PROCESSOR ARCHITECTURE & INTER. PP 61 P 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 56 P C PP 100 40 61 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 68 P C 100 40 69 P 15. DATA COMMUNICATION TW 50 20 45 P C 25 10 24 P 06. DIGITAL LABORATORY 16. PROCESSOR INTERFACING LABORATORY TW 07. DIGITAL LABORATORY 50 20 39 P C 50 20 40 P PR 17. PROCESSOR INTERFACING LABORATORY OR 08. PROGRAMMING LABORATORY 50 20 47 P C 25 10 23 P TW 18. DATA STRUCTURES AND FILES LAB TW 09. PROGRAMMING LABORATORY 50 20 40 P C 50 20 44 P PR 19. DATA STRUCTURES AND FILES LAB PR 50 20 47 P C 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 46 P 20. OBJECT ORIENTED PROGRAMMING LAB TW 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 42 P GRAND TOTAL = 1086/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS: , 71200875G , S80058556 , PICT , S80058556 MANIK S80058556 HADKE ANUP BANDA PP 42 P C 11. ENG MATHS III 100 40 61 P 01. DISCRETE STRUCTURES 100 40 100 40 50 P C 100 40 02. COMPUTER ORGANIZATION PP 12. COMPUTER GRAPHICS 41 P 100 40 45 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 40 P 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 46 P C 100 40 PP 45 P 100 40 58 P C 100 40 59 P 05. HUMANITIES AND SOCIAL SCIENCES PP 15. DATA COMMUNICATION 50 20 38 P C 17 P 06. DIGITAL LABORATORY 25 10 TW 16. PROCESSOR INTERFACING LABORATORY TW 50 20 37 P 50 20 07. DIGITAL LABORATORY PR 17. PROCESSOR INTERFACING LABORATORY OR 20 P 50 20 08. PROGRAMMING LABORATORY TW 36 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 18 P 09. PROGRAMMING LABORATORY 50 20 37 P 50 20 27 P C PR 19. DATA STRUCTURES AND FILES LAB PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 37 P C 50 20 25 P 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 15# P 21. OBJECT ORIENTED PROGRAMMING LAB PR GRAND TOTAL = 794/1500, RESULT: SECOND CLASS # [0.4] ORDN. 1 MARKS: , 71200877C , S80058557 , PICT , S80058557 S80058557 HARSH BAHETI ANJANA 01. DISCRETE STRUCTURES 100 40 55 P C 11. ENG MATHS III 100 40 24 F 02. COMPUTER ORGANIZATION PP 100 40 51 P C 12. COMPUTER GRAPHICS 100 40 45 P 100 40 40 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 29 F 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 53 P C 14. DATA STRUCTURES AND FILES 100 40 43 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 55 P C PP 100 40 59 P 15. DATA COMMUNICATION TW 50 20 10 P 06. DIGITAL LABORATORY 30 P C 25 10 16. PROCESSOR INTERFACING LABORATORY TW 50 20 20 P C 17. PROCESSOR INTERFACING LABORATORY OR 50 20 30 P 07. DIGITAL LABORATORY PR 08. PROGRAMMING LABORATORY TW 50 20 31 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 15 P 10 F 50 20 26 P C 50 20 09. PROGRAMMING LABORATORY PR 19. DATA STRUCTURES AND FILES LAB PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 37 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 28 P 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 33 P GRAND TOTAL = 724/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71200878M , S80058558 , PICT , S80058558 MANJUSHA S80058558 HATOLKAR ABHIRAM PRASANNA 01. DISCRETE STRUCTURES 100 40 52 P C 100 40 46 P 11. ENG MATHS III PP PP 100 40 57 P C 12. COMPUTER GRAPHICS 100 40 54 P 02. COMPUTER ORGANIZATION 100 40 64 P C 100 40 53 P 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 13. PROCESSOR ARCHITECTURE & INTER. PP 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 53 P C PP 100 40 56 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 63 P C 100 40 15. DATA COMMUNICATION TW 50 20 39 P C 25 10 18 P 06. DIGITAL LABORATORY 16. PROCESSOR INTERFACING LABORATORY TW 07. DIGITAL LABORATORY 50 20 32 P C 50 20 32 P PR 17. PROCESSOR INTERFACING LABORATORY OR 08. PROGRAMMING LABORATORY 50 20 42 P C 25 10 23 P TW 18. DATA STRUCTURES AND FILES LAB TW 09. PROGRAMMING LABORATORY 50 20 45 P C 50 20 43 P PR 19. DATA STRUCTURES AND FILES LAB PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 42 P C 50 20 44 P 20. OBJECT ORIENTED PROGRAMMING LAB TW 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 38 P GRAND TOTAL = 964/1500, RESULT: FIRST CLASS ORDN. 1 MARKS : , 71350922J , S80058559 , PICT , S80058559 MEENA S80058559 IRNAK SWAPNIL ASHOK PP 01. DISCRETE STRUCTURES 20 F 11. ENG MATHS III 100 40 13 F PP 100 40 100 40 100 40 29 F 02. COMPUTER ORGANIZATION PP 48 P C 12. COMPUTER GRAPHICS 100 40 43 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 24 F 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 40 P C 100 40 PP 40 P 100 40 49 P.C 100 40 05. HUMANITIES AND SOCIAL SCIENCES PP 15. DATA COMMUNICATION 48 P TW 50 20 25 P C 06. DIGITAL LABORATORY 25 10 14 P 16. PROCESSOR INTERFACING LABORATORY TW 50 20 22 P 50 20 07 F 07. DIGITAL LABORATORY PR 17. PROCESSOR INTERFACING LABORATORY OR 50 20 08. PROGRAMMING LABORATORY TW 32 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 16 P 09. PROGRAMMING LABORATORY 50 20 22 P C PR 19. DATA STRUCTURES AND FILES LAB PR 50 20 25 P 50 20 33 P C 50 20 39 P 10. COMMUNICATION AND LANGUAGE LAB. TW 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 28 P 21. OBJECT ORIENTED PROGRAMMING LAB PR GRAND TOTAL = 617/1500, RESULT: FAILS ORDN. 1 MARKS : , 71200884F , S80058560 , PICT , S80058560 S80058560 IYER PRIYANKA SUBRAMANIAN USHA 01. DISCRETE STRUCTURES 100 40 50 P C 11. ENG MATHS III 100 40 62 P 02. COMPUTER ORGANIZATION PP 100 40 61 P C 12. COMPUTER GRAPHICS 100 40 61 P 100 40 62 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 48 P 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 69 P C 14. DATA STRUCTURES AND FILES 100 40 65 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 60 P C PP 100 40 64 P 15. DATA COMMUNICATION TW 50 20 06. DIGITAL LABORATORY 42 P C 25 10 16 P 16. PROCESSOR INTERFACING LABORATORY TW 50 20 35 P C 17. PROCESSOR INTERFACING LABORATORY OR 50 20 40 P 07. DIGITAL LABORATORY PR 08. PROGRAMMING LABORATORY TW 50 20 41 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 19 P 35 P 50 20 32 P C 50 20 09. PROGRAMMING LABORATORY PR 19. DATA STRUCTURES AND FILES LAB PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 43 P C 50 20 42 P 20. OBJECT ORIENTED PROGRAMMING LAB TW 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 41 P GRAND TOTAL = 988/1500, RESULT: FIRST CLASS ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71350923G , S80058562 , PICT , S80058562 BHARTI S80058562 JAGTAP MANISH KISHOR 01. DISCRETE STRUCTURES 100 40 40 P PP 100 40 48 P C 11. ENG MATHS III PP 100 40 58 P C 12. COMPUTER GRAPHICS 100 40 54 P 02. COMPUTER ORGANIZATION PP 100 40 52 P C 100 40 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 13. PROCESSOR ARCHITECTURE & INTER. PP 62 P 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 41 P C PP 100 40 56 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 63 P C 100 40 15. DATA COMMUNICATION TW 50 20 42 P C 25 10 20 P 06. DIGITAL LABORATORY 16. PROCESSOR INTERFACING LABORATORY TW 07. DIGITAL LABORATORY 50 20 26 P C 50 20 35 P PR 17. PROCESSOR INTERFACING LABORATORY OR 08. PROGRAMMING LABORATORY 50 20 42 P C 25 10 24 P TW 18. DATA STRUCTURES AND FILES LAB TW 09. PROGRAMMING LABORATORY 50 20 38 P C 50 20 45 P PR 19. DATA STRUCTURES AND FILES LAB PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 43 P C 50 20 42 P 20. OBJECT ORIENTED PROGRAMMING LAB TW 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 36 P GRAND TOTAL = 934/1500, RESULT: FIRST CLASS ORDN. 1 MARKS : , 71200889G , S80058563 , PICT , S80058563 SUNITA S80058563 JALAMKAR ABHILASH RAJENDRA PP 01. DISCRETE STRUCTURES 100 40 75 P C 11. ENG MATHS III 100 40 40 P 100 40 59 P C 100 40 02. COMPUTER ORGANIZATION 12. COMPUTER GRAPHICS 40 P 100 40 70 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 AA F 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 51 P C 100 40 AA F PP 100 40 49 P.C 100 40 05. HUMANITIES AND SOCIAL SCIENCES PP 15. DATA COMMUNICATION AA F TW 50 20 38 P C 06. DIGITAL LABORATORY 25 10 16 P 16. PROCESSOR INTERFACING LABORATORY TW 50 20 37 P C 50 20 07. DIGITAL LABORATORY PR 17. PROCESSOR INTERFACING LABORATORY OR 38 P 50 20 08. PROGRAMMING LABORATORY TW 42 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 21 P 09. PROGRAMMING LABORATORY PR 50 20 40 P C 19. DATA STRUCTURES AND FILES LAB PR 50 20 39 P 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 41 P C 50 20 44 P 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 40 P 21. OBJECT ORIENTED PROGRAMMING LAB PR GRAND TOTAL = 780/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS: , 71200890L , S80058564 , PICT , S80058564 S80058564 JARHAD PRATIBHA BHAGAWAN MANDA 01. DISCRETE STRUCTURES 100 40 58 P C 11. ENG MATHS III 100 40 49 P 02. COMPUTER ORGANIZATION PP 100 40 40 P C 12. COMPUTER GRAPHICS 100 40 44 P 100 40 52 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 46 P 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 47 P C 14. DATA STRUCTURES AND FILES 100 40 45 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 51 P C PP 100 40 48 P 15. DATA COMMUNICATION TW 50 20 06. DIGITAL LABORATORY 40 P C 25 10 17 P 16. PROCESSOR INTERFACING LABORATORY TW 50 20 23 P C 17. PROCESSOR INTERFACING LABORATORY OR 50 20 35 P 07. DIGITAL LABORATORY PR 08. PROGRAMMING LABORATORY TW 50 20 42 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 20 P 50 20 40 P C 50 20 44 P 09. PROGRAMMING LABORATORY PR 19. DATA STRUCTURES AND FILES LAB PR 50 20 43 P C 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 34 P 20. OBJECT ORIENTED PROGRAMMING LAB TW 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 30 P GRAND TOTAL = 848/1500, RESULT: HIGHER SECOND CLASS ORDN. 1 MARKS: 

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71350924E , S80058565 , PICT , S80058565 YASHODA S80058565 JATAB NIKHIL HARIDWARILAL 01. DISCRETE STRUCTURES PP 100 40 47 P C 100 40 40 P 11. ENG MATHS III PP 100 40 86 P C 12. COMPUTER GRAPHICS 100 40 61 P 02. COMPUTER ORGANIZATION 100 40 53 P C 100 40 63 P 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 13. PROCESSOR ARCHITECTURE & INTER. PP 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 59 P C PP 100 40 60 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 60 P C 100 40 71 P 15. DATA COMMUNICATION TW 50 20 35 P C 25 10 22 P 06. DIGITAL LABORATORY 16. PROCESSOR INTERFACING LABORATORY TW 07. DIGITAL LABORATORY 50 20 34 P 50 20 40 P PR 17. PROCESSOR INTERFACING LABORATORY OR 08. PROGRAMMING LABORATORY 50 20 34 P C 25 10 24 P TW 18. DATA STRUCTURES AND FILES LAB TW 09. PROGRAMMING LABORATORY 35 P C 50 20 32 P PR 50 20 19. DATA STRUCTURES AND FILES LAB PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 35 P C 50 20 38 P 20. OBJECT ORIENTED PROGRAMMING LAB TW 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 35 P GRAND TOTAL = 964/1500, RESULT: FIRST CLASS ORDN. 1 MARKS: S80058566 JEEVJYOT SUKHDEV SINGH CHHABDA SANGEET KAUR CHHABDA , 71350925C , S80058566 , PICT , S80058566 PP 01. DISCRETE STRUCTURES 11. ENG MATHS III 100 40 40 P 100 40 40 P C 100 40 59 P C 100 40 52 P 02. COMPUTER ORGANIZATION 12. COMPUTER GRAPHICS 100 40 53 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 53 P 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 58 P C 100 40 54 P PP 100 40 57 P C 100 40 05. HUMANITIES AND SOCIAL SCIENCES PP 15. DATA COMMUNICATION 63 P 50 20 39 P C 06. DIGITAL LABORATORY 25 10 21 P TW 16. PROCESSOR INTERFACING LABORATORY TW 50 20 46 P C 50 20 43 P 07. DIGITAL LABORATORY PR 17. PROCESSOR INTERFACING LABORATORY OR 50 20 43 P C 08. PROGRAMMING LABORATORY TW 18. DATA STRUCTURES AND FILES LAB TW 25 10 23 P 09. PROGRAMMING LABORATORY 50 20 32 P 50 20 22 P C PR 19. DATA STRUCTURES AND FILES LAB PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 45 P C 50 20 44 P 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 34 P 21. OBJECT ORIENTED PROGRAMMING LAB PR GRAND TOTAL = 921/1500, RESULT: FIRST CLASS ORDN. 1 MARKS: , 71200896к , S80058567 , РІСТ , S80058567 S80058567 JOSHI SWANAND ARVIND ARUNA 01. DISCRETE STRUCTURES 100 40 74 P C 11. ENG MATHS III 100 40 70 P 02. COMPUTER ORGANIZATION PP 100 40 69 P C 12. COMPUTER GRAPHICS 100 40 48 P 100 40 72 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 46 P 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 63 P C 14. DATA STRUCTURES AND FILES 100 40 53 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 49 P C 100 40 67 P 15. DATA COMMUNICATION TW 50 20 06. DIGITAL LABORATORY 37 P C 25 10 17 P 16. PROCESSOR INTERFACING LABORATORY TW 50 20 34 P C 17. PROCESSOR INTERFACING LABORATORY OR 50 20 40 P 07. DIGITAL LABORATORY PR 08. PROGRAMMING LABORATORY TW 50 20 42 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 23 P 09. PROGRAMMING LABORATORY 44 P 50 20 42 P C 50 20 PR 19. DATA STRUCTURES AND FILES LAB PR 50 20 41 P C 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 40 P 20. OBJECT ORIENTED PROGRAMMING LAB TW 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 39 P GRAND TOTAL = 1010/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71200899D , S80058568 , PICT , S80058568 S80058568 KADAM YASHASWINI VISHNU ALKA 01. DISCRETE STRUCTURES 100 40 75 P C 100 40 40 P 11. ENG MATHS III PP PP 100 40 64 P C 12. COMPUTER GRAPHICS 100 40 56 P 02. COMPUTER ORGANIZATION 100 40 67 P C 100 40 58 P 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 13. PROCESSOR ARCHITECTURE & INTER. PP 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 61 P C PP 100 40 50 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 65 P C 100 40 65 P 15. DATA COMMUNICATION TW 50 20 33 P C 25 10 12 P 06. DIGITAL LABORATORY 16. PROCESSOR INTERFACING LABORATORY TW 07. DIGITAL LABORATORY 50 20 31 P C 50 20 30 P PR 17. PROCESSOR INTERFACING LABORATORY OR 08. PROGRAMMING LABORATORY 50 20 35 P C 25 10 20 P TW 18. DATA STRUCTURES AND FILES LAB TW 09. PROGRAMMING LABORATORY 50 20 39 P C 50 20 44 P PR 19. DATA STRUCTURES AND FILES LAB PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 37 P C 50 20 32 P 20. OBJECT ORIENTED PROGRAMMING LAB TW 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 32 P GRAND TOTAL = 946/1500, RESULT: FIRST CLASS ORDN. 1 MARKS : , 71200900M , S80058569 , PICT , S80058569 MANJIRI S80058569 KADU SHRADDHA AJIT PP 01. DISCRETE STRUCTURES 11. ENG MATHS III 100 40 67 P 100 40 84 P C 100 40 74 P C 100 40 02. COMPUTER ORGANIZATION PP 12. COMPUTER GRAPHICS 62 P 100 40 79 P.C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 68 P 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 61 P C 100 40 65 P PP 100 40 69 P C 100 40 05. HUMANITIES AND SOCIAL SCIENCES PP 15. DATA COMMUNICATION 69 P 50 20 47 P C 06. DIGITAL LABORATORY 25 10 21 P TW 16. PROCESSOR INTERFACING LABORATORY TW 50 20 37 P C 50 20 42 P 07. DIGITAL LABORATORY PR 17. PROCESSOR INTERFACING LABORATORY OR 50 20 08. PROGRAMMING LABORATORY TW 47 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 24 P 09. PROGRAMMING LABORATORY PR 50 20 43 P C 19. DATA STRUCTURES AND FILES LAB PR 50 20 41 P 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 47 P C 50 20 45 P 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 40 P 21. OBJECT ORIENTED PROGRAMMING LAB PR GRAND TOTAL = 1132/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS: , 71200907J , S80058570 , PICT , S80058570 S80058570 KAMBLE NIKITA MAHADEV **VANDANA** 01. DISCRETE STRUCTURES PP 100 40 46 P C 11. ENG MATHS III 100 40 58 P 02. COMPUTER ORGANIZATION PP 100 40 65 P C 12. COMPUTER GRAPHICS 100 40 51 P 100 40 61 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 54 P 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 57 P C 14. DATA STRUCTURES AND FILES 100 40 47 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 67 P C PP 100 40 66 P 15. DATA COMMUNICATION TW 50 20 06. DIGITAL LABORATORY 44 P C 25 10 21 P 16. PROCESSOR INTERFACING LABORATORY TW 50 20 25 P C 17. PROCESSOR INTERFACING LABORATORY OR 50 20 33 P 07. DIGITAL LABORATORY PR 08. PROGRAMMING LABORATORY TW 50 20 45 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 23 P 50 20 32 P 50 20 35 P C 09. PROGRAMMING LABORATORY PR 19. DATA STRUCTURES AND FILES LAB PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 46 P C 50 20 42 P 20. OBJECT ORIENTED PROGRAMMING LAB TW 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 37 P GRAND TOTAL = 955/1500, RESULT: FIRST CLASS ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71200916н , S80058571 , РІСТ , S80058571 MANISHA S80058571 KATKE AMRUTA SURESH PP 100 40 70 P 01. DISCRETE STRUCTURES PP 100 40 65 P C 11. ENG MATHS III 100 40 72 P C 12. COMPUTER GRAPHICS 100 40 40 P 02. COMPUTER ORGANIZATION PP 100 40 61 P C 100 40 40 P 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 13. PROCESSOR ARCHITECTURE & INTER. PP 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 62 P C PP 100 40 47 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 58 P C 100 40 55 P 15. DATA COMMUNICATION TW 50 20 42 P C 25 10 19 P 06. DIGITAL LABORATORY 16. PROCESSOR INTERFACING LABORATORY TW 07. DIGITAL LABORATORY 50 20 35 P C 50 20 20 P PR 17. PROCESSOR INTERFACING LABORATORY OR 08. PROGRAMMING LABORATORY 50 20 44 P C 25 10 21 P TW 18. DATA STRUCTURES AND FILES LAB TW 09. PROGRAMMING LABORATORY 50 20 40 P C 50 20 39 P PR 19. DATA STRUCTURES AND FILES LAB PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 45 P C 50 20 42 P 20. OBJECT ORIENTED PROGRAMMING LAB TW 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 32 P GRAND TOTAL = 949/1500, RESULT: FIRST CLASS ORDN. 1 MARKS: , 71200917F , S80058572 , PICT , S80058572 RAJASHREE S80058572 KAVADE PRIYANKA DHANANJAY PP 01. DISCRETE STRUCTURES PP 100 40 68 P C 11. ENG MATHS III 100 40 69 P 100 40 100 40 50 P 02. COMPUTER ORGANIZATION 64 P C 12. COMPUTER GRAPHICS 100 40 74 P.C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 45 P 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 53 P C 100 40 PP 63 P 100 40 57 P C 100 40 59 P 05. HUMANITIES AND SOCIAL SCIENCES PP 15. DATA COMMUNICATION TW 20 P 50 20 43 P C 06. DIGITAL LABORATORY 25 10 16. PROCESSOR INTERFACING LABORATORY TW 50 20 35 P C 50 20 34 P 07. DIGITAL LABORATORY PR 17. PROCESSOR INTERFACING LABORATORY OR 50 20 08. PROGRAMMING LABORATORY TW 44 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 22 P 09. PROGRAMMING LABORATORY 50 20 42 P 50 20 35 P C PR 19. DATA STRUCTURES AND FILES LAB PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 44 P C 50 20 43 P 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 33 P 21. OBJECT ORIENTED PROGRAMMING LAB PR GRAND TOTAL = 997/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS: , 71200918D , S80058573 , PICT , S80058573 ANITA S80058573 KAVITKAR JAYASHREE GOPAL 01. DISCRETE STRUCTURES 100 40 77 P C 11. ENG MATHS III 100 40 72 P 02. COMPUTER ORGANIZATION PP 100 40 63 P C 12. COMPUTER GRAPHICS 100 40 47 P 100 40 54 P 100 40 66 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 61 P C 14. DATA STRUCTURES AND FILES 100 40 52 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 59 P C PP 100 40 54 P 15. DATA COMMUNICATION TW 50 20 06. DIGITAL LABORATORY 40 P C 25 10 18 P 16. PROCESSOR INTERFACING LABORATORY TW 50 20 33 P C 17. PROCESSOR INTERFACING LABORATORY OR 50 20 34 P 07. DIGITAL LABORATORY PR 08. PROGRAMMING LABORATORY TW 50 20 43 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 23 P 09. PROGRAMMING LABORATORY PR 50 20 44 P C 50 20 43 P 19. DATA STRUCTURES AND FILES LAB PR 50 20 43 P C 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 44 P 20. OBJECT ORIENTED PROGRAMMING LAB TW 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 41 P GRAND TOTAL = 1011/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71350926M , S80058574 , PICT , S80058574 SUREKHA S80058574 KHABIYA ASHWINI ASHOK 01. DISCRETE STRUCTURES 100 40 40 P 100 40 61 P C 11. ENG MATHS III PP 100 40 81 P C 12. COMPUTER GRAPHICS 100 40 63 P 02. COMPUTER ORGANIZATION 100 40 77 P C 100 40 66 P 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 13. PROCESSOR ARCHITECTURE & INTER. PP 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 62 P C PP 100 40 53 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 62 P C 100 40 63 P 15. DATA COMMUNICATION 50 20 37 P.C 25 10 21 P 06. DIGITAL LABORATORY 16. PROCESSOR INTERFACING LABORATORY TW TW 07. DIGITAL LABORATORY 50 20 30 P C 50 20 32 P PR 17. PROCESSOR INTERFACING LABORATORY OR 08. PROGRAMMING LABORATORY 50 20 41 P C 25 10 23 P TW 18. DATA STRUCTURES AND FILES LAB TW 09. PROGRAMMING LABORATORY 50 20 40 P C 50 20 35 P PR 19. DATA STRUCTURES AND FILES LAB PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 42 P C 50 20 42 P 20. OBJECT ORIENTED PROGRAMMING LAB TW 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 34 P GRAND TOTAL = 1005/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS: , 71200922B , S80058575 , PICT , S80058575 FATIMA S80058575 KHAN SHAHBAZ AHMAD 01. DISCRETE STRUCTURES 11. ENG MATHS III 100 40 40 P 100 40 57 P C 100 40 53 P 100 40 02. COMPUTER ORGANIZATION PP 12. COMPUTER GRAPHICS 47 P 51 P C 100 40 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 56 P 14. DATA STRUCTURES AND FILES 100 40 54 P C 100 40 52 P 04. FUNDAMENTAL OF DATA STRUCTURES PP PP 100 40 40 P C 100 40 05. HUMANITIES AND SOCIAL SCIENCES PP 15. DATA COMMUNICATION 50 P 50 20 39 P C 17 P 06. DIGITAL LABORATORY 25 10 TW 16. PROCESSOR INTERFACING LABORATORY TW 50 20 38 P C 50 20 07. DIGITAL LABORATORY PR 17. PROCESSOR INTERFACING LABORATORY OR 33 P 50 20 08. PROGRAMMING LABORATORY TW 41 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 19 P 09. PROGRAMMING LABORATORY 50 20 37 P PR 50 20 30 P C 19. DATA STRUCTURES AND FILES LAB PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 41 P C 50 20 39 P 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 37 P 21. OBJECT ORIENTED PROGRAMMING LAB PR GRAND TOTAL = 871/1500, RESULT: HIGHER SECOND CLASS ORDN. 1 MARKS: , 71200924J , S80058576 , PICT , S80058576 S80058576 KHANDELWAL ABHISHEK GOURISHANKAR SANTOSHI 01. DISCRETE STRUCTURES 100 40 77 P C 11. ENG MATHS III 100 40 74 P 02. COMPUTER ORGANIZATION PP 100 40 75 P C 12. COMPUTER GRAPHICS 100 40 61 P 100 40 80 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 64 P 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 67 P C 14. DATA STRUCTURES AND FILES 100 40 61 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 69 P C 100 40 65 P 15. DATA COMMUNICATION TW 50 20 20 P 06. DIGITAL LABORATORY 46 P C 25 10 16. PROCESSOR INTERFACING LABORATORY TW 50 20 38 P C 17. PROCESSOR INTERFACING LABORATORY OR 50 20 40 P 07. DIGITAL LABORATORY PR 08. PROGRAMMING LABORATORY TW 50 20 45 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 24 P 50 20 47 P C 50 20 47 P 09. PROGRAMMING LABORATORY PR 19. DATA STRUCTURES AND FILES LAB PR 50 20 44 P C 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 44 P 20. OBJECT ORIENTED PROGRAMMING LAB TW 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 36 P GRAND TOTAL = 1124/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71200925G , S80058577 , PICT , S80058577 MAMTA S80058577 KHANDELWAL ARPIT NARENDRA 01. DISCRETE STRUCTURES 100 40 40 P 100 40 71 P C 11. ENG MATHS III PP 100 40 55 P C 12. COMPUTER GRAPHICS 100 40 40 P 02. COMPUTER ORGANIZATION 100 40 58 P C 100 40 40 P 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 13. PROCESSOR ARCHITECTURE & INTER. PP 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 50 P C PP 100 40 40 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 40 P C 100 40 40 P 15. DATA COMMUNICATION TW 50 20 36 P C 25 10 12 P 06. DIGITAL LABORATORY 16. PROCESSOR INTERFACING LABORATORY TW 22 P 07. DIGITAL LABORATORY 50 20 36 P C 50 20 PR 17. PROCESSOR INTERFACING LABORATORY OR 08. PROGRAMMING LABORATORY 50 20 37 P C 25 10 15 P TW 18. DATA STRUCTURES AND FILES LAB TW 09. PROGRAMMING LABORATORY 50 20 39 P C 50 20 42 P PR 19. DATA STRUCTURES AND FILES LAB PR 50 20 41 P C 50 20 30 P 10. COMMUNICATION AND LANGUAGE LAB. TW 20. OBJECT ORIENTED PROGRAMMING LAB TW 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 38 P GRAND TOTAL = 822+03/1500, RESULT: HIGHER SECOND CLASS [0.2] ORDN. 1 MARKS: , 71200926E , S80058578 , PICT , S80058578 RANJANA S80058578 KHAPLI TEJAS RAVINDRA PP 01. DISCRETE STRUCTURES 11. ENG MATHS III 100 40 45 P 100 40 74 P C 100 40 55 P C 100 40 49 P 02. COMPUTER ORGANIZATION PP 12. COMPUTER GRAPHICS 100 40 74 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 52 P 14. DATA STRUCTURES AND FILES 100 40 57 P C 100 40 50 P 04. FUNDAMENTAL OF DATA STRUCTURES PP PP 100 40 60 P C 100 40 05. HUMANITIES AND SOCIAL SCIENCES PP 15. DATA COMMUNICATION 60 P 50 20 42 P C 20 P 06. DIGITAL LABORATORY 25 10 TW 16. PROCESSOR INTERFACING LABORATORY TW 50 20 38 P C 50 20 20 P 07. DIGITAL LABORATORY PR 17. PROCESSOR INTERFACING LABORATORY OR 50 20 08. PROGRAMMING LABORATORY TW 45 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 23 P 09. PROGRAMMING LABORATORY 50 20 40 P PR 50 20 45 P C 19. DATA STRUCTURES AND FILES LAB PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 45 P C 50 20 44 P 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 37 P 21. OBJECT ORIENTED PROGRAMMING LAB PR GRAND TOTAL = 975/1500, RESULT: FIRST CLASS ORDN. 1 MARKS: , 71045482B , S80058579 , PICT , S80058579 S80058579 KHARAT GANESH MAHADEO SHARDHA 01. DISCRETE STRUCTURES 100 40 40 P C 11. ENG MATHS III 100 40 40 P 02. COMPUTER ORGANIZATION PP 100 40 52 P C 12. COMPUTER GRAPHICS 100 40 40 P 100 40 50 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 44 P 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 46 P C 14. DATA STRUCTURES AND FILES 100 40 40 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 51 P C PP 100 40 44 P 15. DATA COMMUNICATION TW 50 20 06. DIGITAL LABORATORY 39 P C 25 10 17 P 16. PROCESSOR INTERFACING LABORATORY TW 50 20 22 P C 17. PROCESSOR INTERFACING LABORATORY OR 50 20 07 F 07. DIGITAL LABORATORY PR 08. PROGRAMMING LABORATORY TW 50 20 41 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 21 P 50 20 32 P C 50 20 38 P 09. PROGRAMMING LABORATORY PR 19. DATA STRUCTURES AND FILES LAB PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 44 P C 50 20 41 P 20. OBJECT ORIENTED PROGRAMMING LAB TW 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 33 P GRAND TOTAL = 782/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71200927C , S80058580 , PICT , S80058580 S80058580 KHARCHE KAUSTUBH PRAMOD NEHA 01. DISCRETE STRUCTURES 100 40 41 P 100 40 73 P C 11. ENG MATHS III PP PP 100 40 64 P C 12. COMPUTER GRAPHICS 100 40 55 P 02. COMPUTER ORGANIZATION 100 40 65 P C 100 40 65 P 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 13. PROCESSOR ARCHITECTURE & INTER. PP 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 58 P C PP 100 40 50 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 62 P C 100 40 15. DATA COMMUNICATION TW 50 20 41 P C 25 10 21 P 06. DIGITAL LABORATORY 16. PROCESSOR INTERFACING LABORATORY TW 07. DIGITAL LABORATORY 50 20 41 P C 50 20 39 P PR 17. PROCESSOR INTERFACING LABORATORY OR 08. PROGRAMMING LABORATORY 50 20 44 P C 25 10 22 P TW 18. DATA STRUCTURES AND FILES LAB TW 09. PROGRAMMING LABORATORY 50 20 42 P C 50 20 44 P PR 19. DATA STRUCTURES AND FILES LAB PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 45 P C 50 20 44 P 20. OBJECT ORIENTED PROGRAMMING LAB TW 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 45 P GRAND TOTAL = 1029/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS: , 71200928M , S80058581 , PICT , S80058581 SUNITA S80058581 KHEDKAR NITIN AJINATH PP 01. DISCRETE STRUCTURES 11. ENG MATHS III 100 40 26 F 100 40 48 P C 100 40 40 P C 100 40 02. COMPUTER ORGANIZATION PP 12. COMPUTER GRAPHICS 40 P 100 40 53 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 41 P 14. DATA STRUCTURES AND FILES 100 40 43 P C 100 40 04. FUNDAMENTAL OF DATA STRUCTURES PP PP 43 P 100 40 57 P C 100 40 05. HUMANITIES AND SOCIAL SCIENCES PP 15. DATA COMMUNICATION 60 P 50 20 37 P C 17 P 06. DIGITAL LABORATORY 25 10 TW 16. PROCESSOR INTERFACING LABORATORY TW 50 20 21 P C 50 20 20 P 07. DIGITAL LABORATORY PR 17. PROCESSOR INTERFACING LABORATORY OR 50 20 08. PROGRAMMING LABORATORY TW 36 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 20 P 09. PROGRAMMING LABORATORY 50 20 33 P 50 20 32 P C PR 19. DATA STRUCTURES AND FILES LAB PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 40 P C 50 20 37 P 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 34 P 21. OBJECT ORIENTED PROGRAMMING LAB PR GRAND TOTAL = 778/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS: , 71200930C , S80058582 , PICT , S80058582 ANAGHA S80058582 KOTHADIA RAMANI BHUPENDRA 01. DISCRETE STRUCTURES 100 40 55 P C 11. ENG MATHS III 100 40 40 P 02. COMPUTER ORGANIZATION PP 100 40 49 P C 12. COMPUTER GRAPHICS 100 40 48 P 100 40 56 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 42 P 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 60 P C 14. DATA STRUCTURES AND FILES 100 40 43 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 65 P C PP 100 40 43 P 15. DATA COMMUNICATION TW 50 20 06. DIGITAL LABORATORY 40 P C 25 10 16 P 16. PROCESSOR INTERFACING LABORATORY TW 50 20 38 P C 17. PROCESSOR INTERFACING LABORATORY OR 50 20 20 P 07. DIGITAL LABORATORY PR 08. PROGRAMMING LABORATORY TW 50 20 41 P.C 18. DATA STRUCTURES AND FILES LAB TW 25 10 21 P 50 20 34 P C 50 20 41 P 09. PROGRAMMING LABORATORY PR 19. DATA STRUCTURES AND FILES LAB PR 50 20 42 P C 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 35 P 20. OBJECT ORIENTED PROGRAMMING LAB TW 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 37 P GRAND TOTAL = 866/1500, RESULT: HIGHER SECOND CLASS ORDN. 1 MARKS: 

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71350927к , S80058583 , РІСТ , S80058583 S80058583 KULKARNI PALLAVI RAJENDRA REKHA 01. DISCRETE STRUCTURES 100 40 40 P 100 40 50 P C 11. ENG MATHS III PP PP 100 40 67 P C 12. COMPUTER GRAPHICS 100 40 54 P 02. COMPUTER ORGANIZATION 100 40 62 P C 100 40 55 P 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 13. PROCESSOR ARCHITECTURE & INTER. PP 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 59 P C PP 100 40 43 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 68 P C 100 40 15. DATA COMMUNICATION TW 50 20 36 P C 25 10 19 P 06. DIGITAL LABORATORY 16. PROCESSOR INTERFACING LABORATORY TW 32 P C 07. DIGITAL LABORATORY 50 20 50 20 27 P PR 17. PROCESSOR INTERFACING LABORATORY OR 08. PROGRAMMING LABORATORY 50 20 37 P C 25 10 20 P TW 18. DATA STRUCTURES AND FILES LAB TW 09. PROGRAMMING LABORATORY 50 20 36 P C 50 20 40 P PR 19. DATA STRUCTURES AND FILES LAB PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 39 P C 50 20 43 P 20. OBJECT ORIENTED PROGRAMMING LAB TW 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 36 P GRAND TOTAL = 926/1500, RESULT: FIRST CLASS ORDN. 1 MARKS: LATA , 71200937L , S80058584 , PICT , S80058584 S80058584 KULKARNI PRANIT KRISHNA 01. DISCRETE STRUCTURES 11. ENG MATHS III 100 40 40 P 100 40 51 P C PP 100 40 52 P C 100 40 02. COMPUTER ORGANIZATION PP 12. COMPUTER GRAPHICS 40 P 100 40 59 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 44 P 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 60 P C 100 40 40 P PP 100 40 48 P C 100 40 50 P 05. HUMANITIES AND SOCIAL SCIENCES PP 15. DATA COMMUNICATION TW 50 20 38 P C 14 P 06. DIGITAL LABORATORY 25 10 16. PROCESSOR INTERFACING LABORATORY TW 50 20 34 P C 50 20 07. DIGITAL LABORATORY PR 17. PROCESSOR INTERFACING LABORATORY OR 24 P 50 20 08. PROGRAMMING LABORATORY TW 36 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 20 P 09. PROGRAMMING LABORATORY 50 20 38 P PR 50 20 36 P C 19. DATA STRUCTURES AND FILES LAB PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 39 P C 50 20 33 P 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 40 P 21. OBJECT ORIENTED PROGRAMMING LAB PR GRAND TOTAL = 836/1500, RESULT: HIGHER SECOND CLASS ORDN. 1 MARKS: , 71200938J , S80058585 , PICT , S80058585 ALKA S80058585 KULKARNI TANMAY VIJAY 01. DISCRETE STRUCTURES PP 100 40 61 P C 11. ENG MATHS III 100 40 62 P 02. COMPUTER ORGANIZATION PP 100 40 54 P C 12. COMPUTER GRAPHICS 100 40 51 P 100 40 59 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 63 P 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 49 P C 14. DATA STRUCTURES AND FILES 100 40 59 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 59 P C PP 100 40 64 P 15. DATA COMMUNICATION TW 50 20 06. DIGITAL LABORATORY 30 P C 25 10 14 P 16. PROCESSOR INTERFACING LABORATORY TW 50 20 37 P C 17. PROCESSOR INTERFACING LABORATORY OR 50 20 28 P 07. DIGITAL LABORATORY PR 08. PROGRAMMING LABORATORY TW 50 20 31 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 18 P 42 P 50 20 30 P C 50 20 09. PROGRAMMING LABORATORY PR 19. DATA STRUCTURES AND FILES LAB PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 36 P C 50 20 32 P 20. OBJECT ORIENTED PROGRAMMING LAB TW 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 31 P GRAND TOTAL = 910/1500, RESULT: FIRST CLASS ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71200940L , S80058586 , PICT , S80058586 S80058586 KUMARGAURAV SINGH MAMATA 01. DISCRETE STRUCTURES 100 40 41 P 100 40 69 P C 11. ENG MATHS III PP 100 40 57 P C 12. COMPUTER GRAPHICS 100 40 40 P 02. COMPUTER ORGANIZATION PP 100 40 63 P C 100 40 40 P 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 13. PROCESSOR ARCHITECTURE & INTER. PP 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 52 P C PP 100 40 53 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 56 P C 100 40 47 P 15. DATA COMMUNICATION TW 50 20 46 P.C 25 10 22 P 06. DIGITAL LABORATORY 16. PROCESSOR INTERFACING LABORATORY TW 07. DIGITAL LABORATORY 50 20 34 P C 50 20 35 P PR 17. PROCESSOR INTERFACING LABORATORY OR 08. PROGRAMMING LABORATORY 50 20 43 P C 25 10 22 P TW 18. DATA STRUCTURES AND FILES LAB TW 09. PROGRAMMING LABORATORY 50 20 42 P C 50 20 42 P PR 19. DATA STRUCTURES AND FILES LAB PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 48 P C 50 20 47 P 20. OBJECT ORIENTED PROGRAMMING LAB TW 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 35 P GRAND TOTAL = 934/1500, RESULT: FIRST CLASS ORDN. 1 MARKS: , 71200941j , S80058587 , PICT , S80058587 USHA S80058587 KUNJI RAHUL SRINIVASAN PP 01. DISCRETE STRUCTURES 83 P C 11. ENG MATHS III 100 40 72 P 100 40 100 40 77 P C 100 40 70 P 02. COMPUTER ORGANIZATION PP 12. COMPUTER GRAPHICS 81 P C 100 40 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 76 P 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 14. DATA STRUCTURES AND FILES 100 40 70 P C 100 40 60 P 04. FUNDAMENTAL OF DATA STRUCTURES PP PP 100 40 100 40 73 P 05. HUMANITIES AND SOCIAL SCIENCES PP 66 P C 15. DATA COMMUNICATION 50 20 22 P 06. DIGITAL LABORATORY 45 P C 25 10 TW 16. PROCESSOR INTERFACING LABORATORY TW 50 20 42 P C 50 20 39 P 07. DIGITAL LABORATORY PR 17. PROCESSOR INTERFACING LABORATORY OR 50 20 08. PROGRAMMING LABORATORY TW 45 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 24 P 09. PROGRAMMING LABORATORY 50 20 48 P PR 50 20 45 P C 19. DATA STRUCTURES AND FILES LAB PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 45 P C 50 20 45 P 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 43 P 21. OBJECT ORIENTED PROGRAMMING LAB PR GRAND TOTAL = 1171/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS: , 71350928H , S80058588 , PICT , S80058588 S80058588 LUNKAD JAYESH PRAKASH UJJWALA 01. DISCRETE STRUCTURES PP 100 40 71 P C 11. ENG MATHS III 100 40 56 P 02. COMPUTER ORGANIZATION PP 100 40 83 P C 12. COMPUTER GRAPHICS 100 40 51 P 100 40 74 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 73 P 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 66 P C 14. DATA STRUCTURES AND FILES 100 40 63 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 66 P C PP 100 40 75 P 15. DATA COMMUNICATION TW 50 20 22 P 06. DIGITAL LABORATORY 43 P C 25 10 16. PROCESSOR INTERFACING LABORATORY TW 50 20 45 P C 17. PROCESSOR INTERFACING LABORATORY OR 50 20 36 P 07. DIGITAL LABORATORY PR 08. PROGRAMMING LABORATORY TW 50 20 44 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 24 P 50 20 43 P C 50 20 48 P 09. PROGRAMMING LABORATORY PR 19. DATA STRUCTURES AND FILES LAB PR 50 20 44 P C 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 48 P 20. OBJECT ORIENTED PROGRAMMING LAB TW 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 44 P GRAND TOTAL = 1119/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71200949D , S80058589 , PICT , S80058589 SURINDERKAUR S80058589 MAHENDRA TANVEERSINGH TEJENDRASINGH 100 40 56 РС 100 40 40 P 01. DISCRETE STRUCTURES PP 11. ENG MATHS III 100 40 50 P C 12. COMPUTER GRAPHICS 100 40 53 P 02. COMPUTER ORGANIZATION 100 40 57 P C 100 40 43 P 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 13. PROCESSOR ARCHITECTURE & INTER. PP 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 49 P C PP 100 40 40 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 52 P C 100 40 53 P 15. DATA COMMUNICATION TW 50 20 34 P C 25 10 16 P 06. DIGITAL LABORATORY 16. PROCESSOR INTERFACING LABORATORY TW 07. DIGITAL LABORATORY 50 20 33 P C 50 20 36 P PR 17. PROCESSOR INTERFACING LABORATORY OR 08. PROGRAMMING LABORATORY 50 20 34 P C 25 10 TW 18. DATA STRUCTURES AND FILES LAB TW 18 P 09. PROGRAMMING LABORATORY 50 20 38 P C 50 20 42 P PR 19. DATA STRUCTURES AND FILES LAB PR 50 20 38 P C 50 20 35 P 10. COMMUNICATION AND LANGUAGE LAB. TW 20. OBJECT ORIENTED PROGRAMMING LAB TW 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 42 P GRAND TOTAL = 859/1500, RESULT: HIGHER SECOND CLASS ORDN. 1 MARKS: , 71200952D , S80058590 , PICT , S80058590 S80058590 MANALI DESAI MONA 11. ENG MATHS III 100 40 40 P 01. DISCRETE STRUCTURES 100 40 63 P C 100 40 53 P C 100 40 51 P 02. COMPUTER ORGANIZATION PP 12. COMPUTER GRAPHICS 100 40 55 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 44 p 14. DATA STRUCTURES AND FILES 100 40 43 P C 100 40 46 P 04. FUNDAMENTAL OF DATA STRUCTURES PP PP 100 40 41 P C 100 40 40 P 05. HUMANITIES AND SOCIAL SCIENCES PP 15. DATA COMMUNICATION 50 20 33 P C 15 P 06. DIGITAL LABORATORY 25 10 TW 16. PROCESSOR INTERFACING LABORATORY TW 50 20 31 P C 50 20 20 P 07. DIGITAL LABORATORY PR 17. PROCESSOR INTERFACING LABORATORY OR 50 20 08. PROGRAMMING LABORATORY TW 36 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 17 P 09. PROGRAMMING LABORATORY PR 50 20 45 P C 19. DATA STRUCTURES AND FILES LAB PR 50 20 46 P 50 20 37 P C 50 20 29 P 10. COMMUNICATION AND LANGUAGE LAB. TW 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 46 P 21. OBJECT ORIENTED PROGRAMMING LAB PR GRAND TOTAL = 831/1500, RESULT: HIGHER SECOND CLASS ORDN. 1 MARKS: , 71200953B , S80058591 , PICT , S80058591 USHA S80058591 MANDGE SHIVSHANKAR SHIVKUMAR 01. DISCRETE STRUCTURES 100 40 40 P C 11. ENG MATHS III 100 40 40 P 02. COMPUTER ORGANIZATION PP 100 40 47 P C 12. COMPUTER GRAPHICS 100 40 44 P 100 40 48 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 48 P 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 40 P C 14. DATA STRUCTURES AND FILES 100 40 43 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 49 P C 100 40 56 P 15. DATA COMMUNICATION TW 50 20 20 P 06. DIGITAL LABORATORY 39 P C 25 10 16. PROCESSOR INTERFACING LABORATORY TW 50 20 25 P C 17. PROCESSOR INTERFACING LABORATORY OR 50 20 37 P 07. DIGITAL LABORATORY PR 08. PROGRAMMING LABORATORY TW 50 20 40 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 20 P 50 20 50 20 40 P 09. PROGRAMMING LABORATORY PR 33 P C 19. DATA STRUCTURES AND FILES LAB PR 50 20 42 P C 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 38 P 20. OBJECT ORIENTED PROGRAMMING LAB TW 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 37 P GRAND TOTAL = 826/1500, RESULT: HIGHER SECOND CLASS ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71200956G , S80058592 , PICT , S80058592 REENA S80058592 MANIK AGARWAL 100 40 40 P 01. DISCRETE STRUCTURES 100 40 58 P C 11. ENG MATHS III PP 100 40 62 P C 12. COMPUTER GRAPHICS 100 40 52 P 02. COMPUTER ORGANIZATION 100 40 61 P C 100 40 43 P 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 13. PROCESSOR ARCHITECTURE & INTER. PP 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 42 P C PP 100 40 42 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 62 P C 100 40 55 P 15. DATA COMMUNICATION TW 50 20 33 P C 25 10 14 P 06. DIGITAL LABORATORY 16. PROCESSOR INTERFACING LABORATORY TW 07. DIGITAL LABORATORY 50 20 24 P C 50 20 08 F PR 17. PROCESSOR INTERFACING LABORATORY OR 08. PROGRAMMING LABORATORY 50 20 28 P C 25 10 17 P TW 18. DATA STRUCTURES AND FILES LAB TW 09. PROGRAMMING LABORATORY 50 20 34 P C 50 20 36 P PR 19. DATA STRUCTURES AND FILES LAB PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 35 P C 50 20 33 P 20. OBJECT ORIENTED PROGRAMMING LAB TW 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 35 P GRAND TOTAL = 814/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS: , 71200959M , S80058593 , PICT , S80058593 SUMATI S80058593 MARADKAR VISHAL PRABHAKAR PP 01. DISCRETE STRUCTURES 76 P C 11. ENG MATHS III 100 40 46 P 100 40 100 40 100 40 57 P 02. COMPUTER ORGANIZATION 66 P C 12. COMPUTER GRAPHICS 100 40 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 68 P C 64 P 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 65 P C 100 40 PP 48 P 100 40 100 40 53 P 05. HUMANITIES AND SOCIAL SCIENCES PP 63 P C 15. DATA COMMUNICATION TW 50 20 40 P C 20 P 06. DIGITAL LABORATORY 25 10 16. PROCESSOR INTERFACING LABORATORY TW 50 20 50 20 07. DIGITAL LABORATORY PR 44 P C 17. PROCESSOR INTERFACING LABORATORY OR 42 P 50 20 41 P C 08. PROGRAMMING LABORATORY TW 18. DATA STRUCTURES AND FILES LAB TW 25 10 19 P 09. PROGRAMMING LABORATORY 50 20 41 P 50 20 42 P C PR 19. DATA STRUCTURES AND FILES LAB PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 43 P C 50 20 43 P 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 43 P 21. OBJECT ORIENTED PROGRAMMING LAB PR GRAND TOTAL = 1024/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS: , 71100897D , S80058594 , PICT , S80058594 S80058594 MITRA ANSHUMAN SUDEEP MALINI 01. DISCRETE STRUCTURES 100 40 54 P C 11. ENG MATHS III 100 40 08 F 02. COMPUTER ORGANIZATION PP 100 40 40 P 12. COMPUTER GRAPHICS 100 40 50 P 100 40 40 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 28 F 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 40 P 14. DATA STRUCTURES AND FILES 100 40 40 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 56 P C 100 40 28 F 15. DATA COMMUNICATION TW 50 20 22 P C 06. DIGITAL LABORATORY 25 10 10 P 16. PROCESSOR INTERFACING LABORATORY TW 50 20 20 P 17. PROCESSOR INTERFACING LABORATORY OR 50 20 05 F 07. DIGITAL LABORATORY PR 08. PROGRAMMING LABORATORY TW 50 20 21 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 10 P 50 20 50 20 22 P 09. PROGRAMMING LABORATORY PR 19. DATA STRUCTURES AND FILES LAB PR AA F 50 20 30 P C 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 20 P 20. OBJECT ORIENTED PROGRAMMING LAB TW 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 AA F GRAND TOTAL = 544/1500, RESULT: FAILS ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER SUNITA , 71200963K , S80058595 , PICT , S80058595 S80058595 MOGRA ISHITA HEMANT 01. DISCRETE STRUCTURES 100 40 55 P 100 40 79 P C 11. ENG MATHS III PP 02. COMPUTER ORGANIZATION 100 40 65 P C 12. COMPUTER GRAPHICS 100 40 57 P PP 100 40 66 P C 100 40 44 P 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 13. PROCESSOR ARCHITECTURE & INTER. PP 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 57 P C PP 100 40 40 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 51 P C 100 40 52 P 15. DATA COMMUNICATION 50 20 43 P C 25 10 14 P 06. DIGITAL LABORATORY 16. PROCESSOR INTERFACING LABORATORY TW TW 07. DIGITAL LABORATORY 50 20 36 P C 50 20 38 P PR 17. PROCESSOR INTERFACING LABORATORY OR 08. PROGRAMMING LABORATORY 50 20 42 P C 25 10 20 P TW 18. DATA STRUCTURES AND FILES LAB TW 09. PROGRAMMING LABORATORY 50 20 44 P C 50 20 43 P PR 19. DATA STRUCTURES AND FILES LAB PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 43 P C 50 20 40 P 20. OBJECT ORIENTED PROGRAMMING LAB TW 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 46 P GRAND TOTAL = 975/1500, RESULT: FIRST CLASS ORDN. 1 MARKS: , 71200964H , S80058596 , РІСТ , S80058596 SHARADA S80058596 MOKASHI SUPRIYA MANIK PP 01. DISCRETE STRUCTURES 11. ENG MATHS III 100 40 40 P PP 100 40 51 P C 100 40 100 40 51 P 02. COMPUTER ORGANIZATION PP 48 P C 12. COMPUTER GRAPHICS 100 40 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 63 P C 42 P 14. DATA STRUCTURES AND FILES 100 40 59 P C 100 40 04. FUNDAMENTAL OF DATA STRUCTURES PP PP 48 P 100 40 100 40 05. HUMANITIES AND SOCIAL SCIENCES PP 68 P C 15. DATA COMMUNICATION 52 P 50 20 35 P C 10 P 06. DIGITAL LABORATORY 25 10 TW 16. PROCESSOR INTERFACING LABORATORY TW 50 20 22 P C 50 20 20 P 07. DIGITAL LABORATORY PR 17. PROCESSOR INTERFACING LABORATORY OR 50 20 25 10 08. PROGRAMMING LABORATORY TW 32 P C 18. DATA STRUCTURES AND FILES LAB TW 12 P 09. PROGRAMMING LABORATORY 50 20 15 F 50 20 35 P C PR 19. DATA STRUCTURES AND FILES LAB PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 39 P C 50 20 31 P 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 16 F 21. OBJECT ORIENTED PROGRAMMING LAB PR GRAND TOTAL = 789/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS: , 71200968L , S80058597 , PICT , S80058597 S80058597 MULE MANJUSHA ASHOK SUVARNA 01. DISCRETE STRUCTURES 100 40 62 P C 11. ENG MATHS III 100 40 44 P 02. COMPUTER ORGANIZATION PP 100 40 59 P C 12. COMPUTER GRAPHICS 100 40 62 P 100 40 69 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 63 P 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 68 P C 14. DATA STRUCTURES AND FILES 100 40 58 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 62 P C PP 100 40 69 P 15. DATA COMMUNICATION TW 50 20 22 P 06. DIGITAL LABORATORY 43 P C 25 10 16. PROCESSOR INTERFACING LABORATORY TW 50 20 42 P C 17. PROCESSOR INTERFACING LABORATORY OR 50 20 38 P 07. DIGITAL LABORATORY PR 08. PROGRAMMING LABORATORY TW 50 20 42 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 22 P 50 20 38 P C 50 20 45 P 09. PROGRAMMING LABORATORY PR 19. DATA STRUCTURES AND FILES LAB PR 50 20 44 P C 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 44 P 20. OBJECT ORIENTED PROGRAMMING LAB TW 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 39 P GRAND TOTAL = 1035/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER KAVITA , 71200977к , S80058598 , РІСТ , S80058598 S80058598 NAIK PRIYANKA MANOJ 01. DISCRETE STRUCTURES 100 40 65 P 100 40 69 P C 11. ENG MATHS III PP 100 40 71 P C 12. COMPUTER GRAPHICS 100 40 51 P 02. COMPUTER ORGANIZATION PP 100 40 76 P C 100 40 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 13. PROCESSOR ARCHITECTURE & INTER. PP 61 P 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 60 P C PP 100 40 51 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 55 P C 100 40 63 P 15. DATA COMMUNICATION 50 20 45 P C 25 10 24 P 06. DIGITAL LABORATORY 16. PROCESSOR INTERFACING LABORATORY TW TW 07. DIGITAL LABORATORY 50 20 34 P C 50 20 39 P PR 17. PROCESSOR INTERFACING LABORATORY OR 08. PROGRAMMING LABORATORY 50 20 47 P C 25 10 22 P TW 18. DATA STRUCTURES AND FILES LAB TW 09. PROGRAMMING LABORATORY 50 20 46 P C 50 20 44 P PR 19. DATA STRUCTURES AND FILES LAB PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 47 P C 50 20 46 P 20. OBJECT ORIENTED PROGRAMMING LAB TW 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 40 P GRAND TOTAL = 1056/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS: , 71200978н , S80058599 , РІСТ , S80058599 RADHIKA S80058599 NAIK SHIVANI SHRIKANT 01. DISCRETE STRUCTURES 11. ENG MATHS III 100 40 80 P PP 100 40 62 P C 100 40 71 P C 100 40 02. COMPUTER ORGANIZATION PP 12. COMPUTER GRAPHICS 61 P 100 40 70 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 82 P 14. DATA STRUCTURES AND FILES 100 40 55 P C 100 40 57 P 04. FUNDAMENTAL OF DATA STRUCTURES PP PP 100 40 100 40 05. HUMANITIES AND SOCIAL SCIENCES PP 54 P C 15. DATA COMMUNICATION 60 P 50 20 39 P C 20 P 06. DIGITAL LABORATORY 25 10 TW 16. PROCESSOR INTERFACING LABORATORY TW 50 20 41 P C 50 20 07. DIGITAL LABORATORY PR 17. PROCESSOR INTERFACING LABORATORY OR 41 P 50 20 37 P C 08. PROGRAMMING LABORATORY TW 18. DATA STRUCTURES AND FILES LAB TW 25 10 19 P 09. PROGRAMMING LABORATORY PR 50 20 44 P C 19. DATA STRUCTURES AND FILES LAB PR 50 20 47 P 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 38 P C 50 20 38 P 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 43 P 21. OBJECT ORIENTED PROGRAMMING LAB PR GRAND TOTAL = 1059/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS: , 71200980K , S80058600 , PICT , S80058600 ASHA S80058600 NATU SWAPNIL RAVINDRA 01. DISCRETE STRUCTURES PP 100 40 74 P C 11. ENG MATHS III 100 40 73 P 02. COMPUTER ORGANIZATION PP 100 40 69 P C 12. COMPUTER GRAPHICS 100 40 56 P 100 40 64 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 65 P 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 57 P C 14. DATA STRUCTURES AND FILES 100 40 55 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 55 P C 100 40 71 P 15. DATA COMMUNICATION TW 50 20 22 P 06. DIGITAL LABORATORY 38 P C 25 10 16. PROCESSOR INTERFACING LABORATORY TW 50 20 41 P C 17. PROCESSOR INTERFACING LABORATORY OR 50 20 42 P 07. DIGITAL LABORATORY PR 08. PROGRAMMING LABORATORY TW 50 20 47 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 24 P 50 20 46 P C 50 20 48 P 09. PROGRAMMING LABORATORY PR 19. DATA STRUCTURES AND FILES LAB PR 50 20 41 P C 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 48 P 20. OBJECT ORIENTED PROGRAMMING LAB TW 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 48 P GRAND TOTAL = 1084/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER SUNETRA , 71200981H , S80058601 , PICT , S80058601 S80058601 NAVGIRE SAGAR PRADEEP PP 01. DISCRETE STRUCTURES 100 40 51 P PP 100 40 50 P C 11. ENG MATHS III 100 40 72 P C 12. COMPUTER GRAPHICS 100 40 61 P 02. COMPUTER ORGANIZATION 100 40 77 P C 100 40 59 P 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 13. PROCESSOR ARCHITECTURE & INTER. PP 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 59 P C PP 100 40 58 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 71 P C 100 40 15. DATA COMMUNICATION 50 20 39 P C 25 10 15 P 06. DIGITAL LABORATORY 16. PROCESSOR INTERFACING LABORATORY TW TW 07. DIGITAL LABORATORY 50 20 42 P C 50 20 28 P PR 17. PROCESSOR INTERFACING LABORATORY OR 08. PROGRAMMING LABORATORY 50 20 42 P C 25 10 21 P TW 18. DATA STRUCTURES AND FILES LAB TW 09. PROGRAMMING LABORATORY 50 20 39 P C 50 20 48 P PR 19. DATA STRUCTURES AND FILES LAB PR 50 20 43 P C 50 20 44 P 10. COMMUNICATION AND LANGUAGE LAB. TW 20. OBJECT ORIENTED PROGRAMMING LAB TW 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 36 P GRAND TOTAL = 1019/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS: , 71350929F , S80058602 , PICT , S80058602 SUNITA S80058602 NEHE ABHISHEK ASHOK PP 01. DISCRETE STRUCTURES 11. ENG MATHS III 100 40 40 P 100 40 56 P C 100 40 100 40 57 P 02. COMPUTER ORGANIZATION PP 44 P C 12. COMPUTER GRAPHICS 100 40 70 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 57 P 14. DATA STRUCTURES AND FILES 100 40 60 P C 100 40 04. FUNDAMENTAL OF DATA STRUCTURES PP PP 59 P 100 40 54 P C 100 40 05. HUMANITIES AND SOCIAL SCIENCES PP 15. DATA COMMUNICATION 67 P 50 20 38 P C 19 P 06. DIGITAL LABORATORY 25 10 TW 16. PROCESSOR INTERFACING LABORATORY TW 50 20 30 P C 50 20 09 F 07. DIGITAL LABORATORY PR 17. PROCESSOR INTERFACING LABORATORY OR 50 20 08. PROGRAMMING LABORATORY TW 43 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 20 P 09. PROGRAMMING LABORATORY 50 20 40 P PR 50 20 35 P C 19. DATA STRUCTURES AND FILES LAB PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 44 P C 50 20 43 P 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 33 P 21. OBJECT ORIENTED PROGRAMMING LAB PR GRAND TOTAL = 918/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS: , 71200986J , S80058603 , PICT , S80058603 S80058603 NEWALKAR REVA VIVEK SMITA 01. DISCRETE STRUCTURES PP 100 40 69 P C 11. ENG MATHS III 100 40 69 P 02. COMPUTER ORGANIZATION PP 100 40 75 P C 12. COMPUTER GRAPHICS 100 40 61 P 100 40 73 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 50 P 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 60 P C 14. DATA STRUCTURES AND FILES 100 40 63 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 66 P C 100 40 60 P 15. DATA COMMUNICATION TW 50 20 19 P 06. DIGITAL LABORATORY 41 P C 25 10 16. PROCESSOR INTERFACING LABORATORY TW 50 20 38 P C 17. PROCESSOR INTERFACING LABORATORY OR 50 20 38 P 07. DIGITAL LABORATORY PR 08. PROGRAMMING LABORATORY TW 50 20 44 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 21 P 50 20 36 P C 50 20 43 P 09. PROGRAMMING LABORATORY PR 19. DATA STRUCTURES AND FILES LAB PR 50 20 46 P C 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 43 P 20. OBJECT ORIENTED PROGRAMMING LAB TW 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 35 P GRAND TOTAL = 1050/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71200992C , S80058604 , PICT , S80058604 SUMITRA S80058604 OSWAL SAHIT SURESH 01. DISCRETE STRUCTURES 100 40 40 P 100 40 52 P C 11. ENG MATHS III PP 100 40 55 P C 12. COMPUTER GRAPHICS 100 40 49 P 02. COMPUTER ORGANIZATION PP 100 40 66 P C 100 40 53 P 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 13. PROCESSOR ARCHITECTURE & INTER. PP 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 56 P C PP 100 40 56 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 64 P C 100 40 15. DATA COMMUNICATION 66 P TW 50 20 39 P C 25 10 17 P 06. DIGITAL LABORATORY 16. PROCESSOR INTERFACING LABORATORY TW 07. DIGITAL LABORATORY 50 20 23 P C 50 20 36 P PR 17. PROCESSOR INTERFACING LABORATORY OR 08. PROGRAMMING LABORATORY 50 20 43 P C 25 10 19 P TW 18. DATA STRUCTURES AND FILES LAB TW 09. PROGRAMMING LABORATORY 50 20 38 P C 50 20 44 P PR 19. DATA STRUCTURES AND FILES LAB PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 43 P C 50 20 39 P 20. OBJECT ORIENTED PROGRAMMING LAB TW 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 36 P GRAND TOTAL = 934/1500, RESULT: FIRST CLASS ORDN. 1 MARKS: , 71200993M , S80058605 , PICT , S80058605 VIDYA S80058605 PABALKAR SARANG SHIVNATH PP 01. DISCRETE STRUCTURES 11. ENG MATHS III 100 40 40 P 100 40 58 P C 100 40 47 P C 100 40 59 P 02. COMPUTER ORGANIZATION PP 12. COMPUTER GRAPHICS 100 40 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 65 P C 47 P 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 54 P C 100 40 PP 59 P 100 40 49 P C 100 40 05. HUMANITIES AND SOCIAL SCIENCES PP 15. DATA COMMUNICATION 51 P TW 50 20 37 P C 15 P 06. DIGITAL LABORATORY 25 10 16. PROCESSOR INTERFACING LABORATORY TW 50 20 33 P C 50 20 32 P 07. DIGITAL LABORATORY PR 17. PROCESSOR INTERFACING LABORATORY OR 50 20 08. PROGRAMMING LABORATORY TW 40 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 17 P 09. PROGRAMMING LABORATORY 50 20 43 P PR 50 20 33 P C 19. DATA STRUCTURES AND FILES LAB PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 41 P C 50 20 36 P 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 46 P 21. OBJECT ORIENTED PROGRAMMING LAB PR GRAND TOTAL = 902/1500, RESULT: FIRST CLASS ORDN. 1 MARKS: , 71200994к , S80058606 , РІСТ , S80058606 S80058606 PADIA SARVESH LALIT SEEMA 01. DISCRETE STRUCTURES 100 40 72 P C 11. ENG MATHS III 100 40 57 P 02. COMPUTER ORGANIZATION PP 100 40 72 P C 12. COMPUTER GRAPHICS 100 40 51 P 100 40 71 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 61 P 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 51 P C 14. DATA STRUCTURES AND FILES 100 40 50 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 50 P C PP 100 40 57 P 15. DATA COMMUNICATION TW 50 20 20 P 06. DIGITAL LABORATORY 41 P C 25 10 16. PROCESSOR INTERFACING LABORATORY TW 50 20 39 P C 17. PROCESSOR INTERFACING LABORATORY OR 50 20 42 P 07. DIGITAL LABORATORY PR 08. PROGRAMMING LABORATORY TW 50 20 44 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 21 P 50 20 40 P C 50 20 48 P 09. PROGRAMMING LABORATORY PR 19. DATA STRUCTURES AND FILES LAB PR 50 20 43 P C 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 45 P 20. OBJECT ORIENTED PROGRAMMING LAB TW 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 45 P GRAND TOTAL = 1020/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71200996F , S80058607 , PICT , S80058607 VANDANA S80058607 PALLOD HRISHIKESH RAMNARAYAN 01. DISCRETE STRUCTURES 100 40 75 P 100 40 77 P C 11. ENG MATHS III PP PP 100 40 72 P C 12. COMPUTER GRAPHICS 100 40 61 P 02. COMPUTER ORGANIZATION 100 40 62 P C 100 40 59 P 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 13. PROCESSOR ARCHITECTURE & INTER. PP 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 63 P C PP 100 40 58 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 63 P C 100 40 15. DATA COMMUNICATION TW 50 20 41 P C 25 10 23 P 06. DIGITAL LABORATORY 16. PROCESSOR INTERFACING LABORATORY TW 07. DIGITAL LABORATORY 50 20 40 P C 50 20 45 P PR 17. PROCESSOR INTERFACING LABORATORY OR 08. PROGRAMMING LABORATORY 50 20 46 P C 25 10 23 P TW 18. DATA STRUCTURES AND FILES LAB TW 09. PROGRAMMING LABORATORY 50 20 46 P C 50 20 45 P PR 19. DATA STRUCTURES AND FILES LAB PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 42 P C 50 20 43 P 20. OBJECT ORIENTED PROGRAMMING LAB TW 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 40 P GRAND TOTAL = 1087/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS: , 71200997D , S80058608 , PICT , S80058608 LEENA S80058608 PANCHARIYA ANIKET SANJAY PP 01. DISCRETE STRUCTURES 11. ENG MATHS III 100 40 66 P 100 40 60 P C 100 40 71 P C 100 40 02. COMPUTER ORGANIZATION 12. COMPUTER GRAPHICS 58 P 100 40 74 P.C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 58 P 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 55 P C 100 40 PP 61 P 100 40 51 P C 100 40 05. HUMANITIES AND SOCIAL SCIENCES PP 15. DATA COMMUNICATION 54 P TW 50 20 40 P C 15 P 06. DIGITAL LABORATORY 25 10 16. PROCESSOR INTERFACING LABORATORY TW 50 20 37 P C 50 20 07. DIGITAL LABORATORY PR 17. PROCESSOR INTERFACING LABORATORY OR 30 P 50 20 08. PROGRAMMING LABORATORY TW 40 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 19 P 09. PROGRAMMING LABORATORY PR 50 20 41 P C 19. DATA STRUCTURES AND FILES LAB PR 50 20 47 P 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 40 P C 50 20 43 P 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 38 P 21. OBJECT ORIENTED PROGRAMMING LAB PR GRAND TOTAL = 998/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS: , 71201001н , S80058609 , РІСТ , S80058609 S80058609 PARAKH NAMAN RAJENDRA SANGEETA 01. DISCRETE STRUCTURES PP 100 40 71 P C 11. ENG MATHS III 100 40 64 P 02. COMPUTER ORGANIZATION PP 100 40 58 P C 12. COMPUTER GRAPHICS 100 40 43 P 100 40 67 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 42 P 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 58 P C 14. DATA STRUCTURES AND FILES 100 40 54 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 46 P C 100 40 60 P 15. DATA COMMUNICATION TW 50 20 06. DIGITAL LABORATORY 34 P C 25 10 14 P 16. PROCESSOR INTERFACING LABORATORY TW 50 20 38 P C 17. PROCESSOR INTERFACING LABORATORY OR 50 20 39 P 07. DIGITAL LABORATORY PR 08. PROGRAMMING LABORATORY TW 50 20 35 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 20 P 50 20 36 P C 50 20 43 P 09. PROGRAMMING LABORATORY PR 19. DATA STRUCTURES AND FILES LAB PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 36 P C 50 20 35 P 20. OBJECT ORIENTED PROGRAMMING LAB TW 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 42 P GRAND TOTAL = 935/1500, RESULT: FIRST CLASS ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71201003D , S80058610 , PICT , S80058610 RENU S80058610 PARINITA MATHARU 100 40 47 P 01. DISCRETE STRUCTURES 100 40 70 P C 11. ENG MATHS III PP 100 40 68 P C 12. COMPUTER GRAPHICS 100 40 55 P 02. COMPUTER ORGANIZATION PP 100 40 64 P C 100 40 42 P 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 13. PROCESSOR ARCHITECTURE & INTER. PP 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 51 P C PP 100 40 56 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 56 P C 100 40 54 P 15. DATA COMMUNICATION TW 50 20 36 P C 25 10 14 P 06. DIGITAL LABORATORY 16. PROCESSOR INTERFACING LABORATORY TW 07. DIGITAL LABORATORY 50 20 40 P C 50 20 23 P PR 17. PROCESSOR INTERFACING LABORATORY OR 08. PROGRAMMING LABORATORY 50 20 39 P C 25 10 TW 18. DATA STRUCTURES AND FILES LAB TW 16 P 09. PROGRAMMING LABORATORY 50 20 39 P C 50 20 44 P PR 19. DATA STRUCTURES AND FILES LAB PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 41 P C 50 20 37 P 20. OBJECT ORIENTED PROGRAMMING LAB TW 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 36 P GRAND TOTAL = 928/1500, RESULT: FIRST CLASS ORDN. 1 MARKS: , 71350930к , S80058611 , РІСТ , S80058611 SEEMA S80058611 PATADE PRATIK RAMDAS PP 01. DISCRETE STRUCTURES 60 P C 11. ENG MATHS III 100 40 42 P PP 100 40 100 40 100 40 54 P 02. COMPUTER ORGANIZATION PP 67 P C 12. COMPUTER GRAPHICS 100 40 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 61 P C 52 P 14. DATA STRUCTURES AND FILES 100 40 65 P C 100 40 56 P 04. FUNDAMENTAL OF DATA STRUCTURES PP PP 100 40 51 P C 100 40 05. HUMANITIES AND SOCIAL SCIENCES PP 15. DATA COMMUNICATION 57 P 50 20 42 P C 24 P 06. DIGITAL LABORATORY 25 10 TW 16. PROCESSOR INTERFACING LABORATORY TW 50 20 28 P C 50 20 07. DIGITAL LABORATORY PR 17. PROCESSOR INTERFACING LABORATORY OR 40 P 50 20 40 P C 08. PROGRAMMING LABORATORY TW 18. DATA STRUCTURES AND FILES LAB TW 25 10 23 P 09. PROGRAMMING LABORATORY 50 20 45 P PR 50 20 44 P C 19. DATA STRUCTURES AND FILES LAB PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 42 P C 50 20 43 P 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 38 P 21. OBJECT ORIENTED PROGRAMMING LAB PR GRAND TOTAL = 974/1500, RESULT: FIRST CLASS ORDN. 1 MARKS: , 71201005L , S80058612 , PICT , S80058612 S80058612 PATIL DEEPAK RAMESH MINAKSHEE 01. DISCRETE STRUCTURES 100 40 67 P C 11. ENG MATHS III 100 40 59 P 02. COMPUTER ORGANIZATION PP 100 40 63 P C 12. COMPUTER GRAPHICS 100 40 59 P 100 40 78 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 50 P 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 68 P C 14. DATA STRUCTURES AND FILES 100 40 57 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 67 P C PP 100 40 64 P 15. DATA COMMUNICATION TW 50 20 22 P 06. DIGITAL LABORATORY 41 P C 25 10 16. PROCESSOR INTERFACING LABORATORY TW 50 20 37 P C 17. PROCESSOR INTERFACING LABORATORY OR 50 20 40 P 07. DIGITAL LABORATORY PR 08. PROGRAMMING LABORATORY TW 50 20 41 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 23 P 50 20 41 P C 50 20 40 P 09. PROGRAMMING LABORATORY PR 19. DATA STRUCTURES AND FILES LAB PR 50 20 44 P C 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 43 P 20. OBJECT ORIENTED PROGRAMMING LAB TW 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 33 P GRAND TOTAL = 1037/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71201006J , S80058613 , PICT , S80058613 PRIYA S80058613 PATIL INDRAJEET ANIL 01. DISCRETE STRUCTURES 100 40 40 P PP 100 40 40 P C 11. ENG MATHS III PP 100 40 45 P C 12. COMPUTER GRAPHICS 100 40 47 P 02. COMPUTER ORGANIZATION PP 100 40 56 P C 100 40 18 F 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 13. PROCESSOR ARCHITECTURE & INTER. PP 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 44 P C PP 100 40 40 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 55 P C 100 40 15. DATA COMMUNICATION TW 50 20 35 P C 25 10 15 P 06. DIGITAL LABORATORY 16. PROCESSOR INTERFACING LABORATORY TW 07. DIGITAL LABORATORY 50 20 32 P C 50 20 20 P PR 17. PROCESSOR INTERFACING LABORATORY OR 08. PROGRAMMING LABORATORY 50 20 34 P C 25 10 15 P TW 18. DATA STRUCTURES AND FILES LAB TW 09. PROGRAMMING LABORATORY 50 20 34 P 50 20 29 P PR 19. DATA STRUCTURES AND FILES LAB PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 39 P C 50 20 35 P 20. OBJECT ORIENTED PROGRAMMING LAB TW 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 30 P GRAND TOTAL = 747/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS: , 71350931H , S80058614 , PICT , S80058614 S80058614 PATIL MAYUR PAVANKUMAR MANDAKINI PP 01. DISCRETE STRUCTURES 56 P C 11. ENG MATHS III 100 40 40 P PP 100 40 100 40 100 40 53 P 02. COMPUTER ORGANIZATION PP 64 P C 12. COMPUTER GRAPHICS 100 40 57 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 47 P 14. DATA STRUCTURES AND FILES 100 40 52 P C 100 40 04. FUNDAMENTAL OF DATA STRUCTURES PP PP 61 P 100 40 100 40 05. HUMANITIES AND SOCIAL SCIENCES PP 64 P C 15. DATA COMMUNICATION 62 P 50 20 39 P C 19 P 06. DIGITAL LABORATORY 25 10 TW 16. PROCESSOR INTERFACING LABORATORY TW 50 20 30 P C 50 20 07. DIGITAL LABORATORY PR 17. PROCESSOR INTERFACING LABORATORY OR 40 P 50 20 08. PROGRAMMING LABORATORY TW 41 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 23 P 09. PROGRAMMING LABORATORY 50 20 44 P PR 50 20 35 P C 19. DATA STRUCTURES AND FILES LAB PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 40 P C 50 20 40 P 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 43 P 21. OBJECT ORIENTED PROGRAMMING LAB PR GRAND TOTAL = 950/1500, RESULT: FIRST CLASS ORDN. 1 MARKS: , 71201011E , S80058615 , PICT , S80058615 S80058615 PATKE SWATI SANJAY SUNITA 01. DISCRETE STRUCTURES 100 40 61 P C 11. ENG MATHS III 100 40 27 F 02. COMPUTER ORGANIZATION PP 100 40 40 P C 12. COMPUTER GRAPHICS 100 40 40 P 100 40 63 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 43 P 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 60 P C 14. DATA STRUCTURES AND FILES 100 40 52 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 41 P C PP 100 40 40 P 15. DATA COMMUNICATION TW 50 20 06. DIGITAL LABORATORY 39 P C 25 10 18 P 16. PROCESSOR INTERFACING LABORATORY TW 50 20 35 P C 17. PROCESSOR INTERFACING LABORATORY OR 50 20 33 P 07. DIGITAL LABORATORY PR 08. PROGRAMMING LABORATORY TW 50 20 42 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 18 P 45 P 50 20 42 P C 50 20 09. PROGRAMMING LABORATORY PR 19. DATA STRUCTURES AND FILES LAB PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 40 P C 50 20 35 P 20. OBJECT ORIENTED PROGRAMMING LAB TW 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 38 P GRAND TOTAL = 852/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71350932F , S80058616 , PICT , S80058616 SHEVANTA S80058616 PAWAR KAVITA SANDIPAN PP 100 40 40 P 01. DISCRETE STRUCTURES 100 40 03 F 11. ENG MATHS III PP 100 40 47 P C 12. COMPUTER GRAPHICS 100 40 40 P 02. COMPUTER ORGANIZATION 100 40 44 P 100 40 13 F 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 13. PROCESSOR ARCHITECTURE & INTER. PP 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 25 F PP 100 40 40 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 64 P C 15. DATA COMMUNICATION 100 40 56 P TW 50 20 32 P C 25 10 16 P 06. DIGITAL LABORATORY 16. PROCESSOR INTERFACING LABORATORY TW 22 P C 07. DIGITAL LABORATORY 50 20 50 20 06 F PR 17. PROCESSOR INTERFACING LABORATORY OR 08. PROGRAMMING LABORATORY 50 20 30 P C 25 10 17 P TW 18. DATA STRUCTURES AND FILES LAB TW 09. PROGRAMMING LABORATORY 50 20 22 P 50 20 22 P PR 19. DATA STRUCTURES AND FILES LAB PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 32 P C 50 20 30 P 20. OBJECT ORIENTED PROGRAMMING LAB TW 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 17 F GRAND TOTAL = 618/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS: , 71201014к , S80058617 , рІСТ , S80058617 S80058617 PHADNIS SHARWARI SADANAND SUPRIYA PP 01. DISCRETE STRUCTURES 11. ENG MATHS III 100 40 40 P 100 40 54 P C 100 40 53 P C 100 40 51 P 02. COMPUTER ORGANIZATION 12. COMPUTER GRAPHICS 100 40 63 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 52 P 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 53 P C 100 40 PP 44 P 100 40 52 P C 100 40 05. HUMANITIES AND SOCIAL SCIENCES PP 15. DATA COMMUNICATION 48 P TW 50 20 42 P C 19 P 06. DIGITAL LABORATORY 25 10 16. PROCESSOR INTERFACING LABORATORY TW 50 20 34 P C 50 20 35 P 07. DIGITAL LABORATORY PR 17. PROCESSOR INTERFACING LABORATORY OR 50 20 40 P C 08. PROGRAMMING LABORATORY TW 18. DATA STRUCTURES AND FILES LAB TW 25 10 20 P 09. PROGRAMMING LABORATORY 50 20 38 P 50 20 32 P C PR 19. DATA STRUCTURES AND FILES LAB PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 42 P C 50 20 39 P 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 37 P 21. OBJECT ORIENTED PROGRAMMING LAB PR GRAND TOTAL = 888/1500, RESULT: HIGHER SECOND CLASS ORDN. 1 MARKS: , 71201016F , S80058618 , PICT , S80058618 S80058618 PILAJI EKTA BALAJI ANUJA 01. DISCRETE STRUCTURES PP 100 40 65 P C 11. ENG MATHS III 100 40 45 P 02. COMPUTER ORGANIZATION PP 100 40 50 P C 12. COMPUTER GRAPHICS 100 40 45 P 100 40 59 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 47 P 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 60 P C 14. DATA STRUCTURES AND FILES 100 40 54 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 54 P C PP 100 40 47 P 15. DATA COMMUNICATION TW 50 20 18 P 06. DIGITAL LABORATORY 40 P C 25 10 16. PROCESSOR INTERFACING LABORATORY TW 50 20 36 P C 17. PROCESSOR INTERFACING LABORATORY OR 50 20 34 P 07. DIGITAL LABORATORY PR 08. PROGRAMMING LABORATORY TW 50 20 40 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 21 P 50 20 40 P C 50 20 42 P 09. PROGRAMMING LABORATORY PR 19. DATA STRUCTURES AND FILES LAB PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 43 P C 50 20 39 P 20. OBJECT ORIENTED PROGRAMMING LAB TW 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 41 P GRAND TOTAL = 920/1500, RESULT: FIRST CLASS ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71201017D , \$80058619 , PICT , \$80058619 S80058619 POHANKAR RASIKA AVINASH ANJALI 01. DISCRETE STRUCTURES PP 100 40 75 P C 100 40 89 P 11. ENG MATHS III PP 100 40 79 P C 12. COMPUTER GRAPHICS 100 40 50 P 02. COMPUTER ORGANIZATION 100 40 80 P C 100 40 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 13. PROCESSOR ARCHITECTURE & INTER. PP 61 P 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 73 P C PP 100 40 65 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 65 P C 100 40 62 P 15. DATA COMMUNICATION TW 50 20 47 P.C 25 10 23 P 06. DIGITAL LABORATORY 16. PROCESSOR INTERFACING LABORATORY TW 07. DIGITAL LABORATORY 50 20 46 P C 50 20 42 P PR 17. PROCESSOR INTERFACING LABORATORY OR 08. PROGRAMMING LABORATORY 50 20 48 P C 25 10 24 P TW 18. DATA STRUCTURES AND FILES LAB TW 09. PROGRAMMING LABORATORY 50 20 46 P C 50 20 48 P PR 19. DATA STRUCTURES AND FILES LAB PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 48 P C 50 20 46 P 20. OBJECT ORIENTED PROGRAMMING LAB TW 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 48 P GRAND TOTAL = 1165/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS: , 71201018B , S80058620 , PICT , S80058620 S80058620 POTDUKHE VAISHNAVI NILKANTH SANGEETA PP 11. ENG MATHS III 100 40 40 P 01. DISCRETE STRUCTURES 100 40 52 P C 100 40 54 P C 100 40 02. COMPUTER ORGANIZATION 12. COMPUTER GRAPHICS 40 P 100 40 62 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 40 P 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 51 P C 100 40 48 P PP 100 40 41 P C 100 40 40 P 05. HUMANITIES AND SOCIAL SCIENCES PP 15. DATA COMMUNICATION 50 20 39 P C 17 P 06. DIGITAL LABORATORY 25 10 TW 16. PROCESSOR INTERFACING LABORATORY TW 50 20 35 P C 50 20 20 P 07. DIGITAL LABORATORY PR 17. PROCESSOR INTERFACING LABORATORY OR 50 20 08. PROGRAMMING LABORATORY TW 38 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 19 P 09. PROGRAMMING LABORATORY 50 20 40 P PR 50 20 41 P C 19. DATA STRUCTURES AND FILES LAB PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 42 P C 50 20 36 P 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 37 P 21. OBJECT ORIENTED PROGRAMMING LAB PR GRAND TOTAL = 832/1500, RESULT: HIGHER SECOND CLASS ORDN. 1 MARKS: , 71201019L , S80058621 , PICT , S80058621 S80058621 POTNIS ISHA RAHUL SUVARNA 01. DISCRETE STRUCTURES 100 40 79 P C 11. ENG MATHS III 100 40 45 P 02. COMPUTER ORGANIZATION PP 100 40 70 P C 12. COMPUTER GRAPHICS 100 40 52 P 100 40 77 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 49 P 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 67 P C 14. DATA STRUCTURES AND FILES 100 40 52 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 54 P C PP 100 40 65 P 15. DATA COMMUNICATION TW 50 20 20 P 06. DIGITAL LABORATORY 46 P C 25 10 16. PROCESSOR INTERFACING LABORATORY TW 50 20 40 P C 17. PROCESSOR INTERFACING LABORATORY OR 50 20 36 P 07. DIGITAL LABORATORY PR 08. PROGRAMMING LABORATORY TW 50 20 47 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 22 P 50 20 43 P C 50 20 44 P 09. PROGRAMMING LABORATORY PR 19. DATA STRUCTURES AND FILES LAB PR 50 20 47 P C 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 45 P 20. OBJECT ORIENTED PROGRAMMING LAB TW 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 37 P GRAND TOTAL = 1037/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71201022L , S80058622 , PICT , S80058622 S80058622 PRASHUL SINGH ARCHANA PP 100 40 73 P 01. DISCRETE STRUCTURES 100 40 57 P C 11. ENG MATHS III 100 40 65 P C 12. COMPUTER GRAPHICS 100 40 02. COMPUTER ORGANIZATION 100 40 61 P C 100 40 45 P 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 13. PROCESSOR ARCHITECTURE & INTER. PP 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 62 P C PP 100 40 57 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 55 P C 100 40 54 P 15. DATA COMMUNICATION TW 50 20 39 P C 25 10 21 P 06. DIGITAL LABORATORY 16. PROCESSOR INTERFACING LABORATORY TW 07. DIGITAL LABORATORY 50 20 42 P C 50 20 43 P PR 17. PROCESSOR INTERFACING LABORATORY OR 08. PROGRAMMING LABORATORY 50 20 38 P C 25 10 20 P TW 18. DATA STRUCTURES AND FILES LAB TW 09. PROGRAMMING LABORATORY 50 20 43 P C 50 20 43 P PR 19. DATA STRUCTURES AND FILES LAB PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 39 P C 50 20 39 P 20. OBJECT ORIENTED PROGRAMMING LAB TW 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 39 P GRAND TOTAL = 979/1500, RESULT: FIRST CLASS ORDN. 1 MARKS: , 71201023J , S80058623 , PICT , S80058623 S80058623 PRERIT VILAS AUTI INDU PP 71 P C 11. ENG MATHS III 100 40 60 P 01. DISCRETE STRUCTURES 100 40 100 40 80 P C 100 40 51 P 02. COMPUTER ORGANIZATION PP 12. COMPUTER GRAPHICS 100 40 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 66 P C 54 p 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 66 P C 100 40 56 P PP 100 40 54 P C 100 40 05. HUMANITIES AND SOCIAL SCIENCES PP 15. DATA COMMUNICATION 60 P TW 50 20 46 P C 06. DIGITAL LABORATORY 25 10 21 P 16. PROCESSOR INTERFACING LABORATORY TW 50 20 40 P C 50 20 34 P 07. DIGITAL LABORATORY PR 17. PROCESSOR INTERFACING LABORATORY OR 50 20 47 P C 08. PROGRAMMING LABORATORY TW 18. DATA STRUCTURES AND FILES LAB TW 25 10 21 P 09. PROGRAMMING LABORATORY 50 20 45 P 50 20 44 P C PR 19. DATA STRUCTURES AND FILES LAB PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 46 P C 50 20 44 P 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 41 P 21. OBJECT ORIENTED PROGRAMMING LAB PR GRAND TOTAL = 1047/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS: , 71201029H , S80058624 , РІСТ , S80058624 S80058624 RAHUL KUMAR USHA 01. DISCRETE STRUCTURES 100 40 65 P C 11. ENG MATHS III 100 40 45 P 02. COMPUTER ORGANIZATION PP 100 40 63 P C 12. COMPUTER GRAPHICS 100 40 42 P 100 40 64 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 26 F 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 47 P C 14. DATA STRUCTURES AND FILES 100 40 46 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 43 P C PP 100 40 57 P 15. DATA COMMUNICATION TW 50 20 15 P 06. DIGITAL LABORATORY 35 P C 25 10 16. PROCESSOR INTERFACING LABORATORY TW 50 20 35 P C 17. PROCESSOR INTERFACING LABORATORY OR 50 20 28 P 07. DIGITAL LABORATORY PR 08. PROGRAMMING LABORATORY TW 50 20 32 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 19 P 50 20 44 P C 50 20 40 P 09. PROGRAMMING LABORATORY PR 19. DATA STRUCTURES AND FILES LAB PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 37 P C 50 20 35 P 20. OBJECT ORIENTED PROGRAMMING LAB TW 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 43 P GRAND TOTAL = 861/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71201035B , S80058629 , PICT , S80058629 S80058629 RATHOD NEHA SANJAY NALINI 01. DISCRETE STRUCTURES 100 40 78 P 100 40 64 P C 11. ENG MATHS III PP 100 40 81 P C 12. COMPUTER GRAPHICS 100 40 61 P 02. COMPUTER ORGANIZATION 100 40 73 P C 100 40 53 P 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 13. PROCESSOR ARCHITECTURE & INTER. PP 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 66 P C PP 100 40 70 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 65 P C 100 40 61 P 15. DATA COMMUNICATION TW 50 20 44 P C 25 10 23 P 06. DIGITAL LABORATORY 16. PROCESSOR INTERFACING LABORATORY TW 07. DIGITAL LABORATORY 50 20 40 P C 50 20 44 P PR 17. PROCESSOR INTERFACING LABORATORY OR 08. PROGRAMMING LABORATORY 50 20 39 P C 25 10 23 P TW 18. DATA STRUCTURES AND FILES LAB TW 09. PROGRAMMING LABORATORY 50 20 42 P C 50 20 47 P PR 19. DATA STRUCTURES AND FILES LAB PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 46 P C 50 20 44 P 20. OBJECT ORIENTED PROGRAMMING LAB TW 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 40 P GRAND TOTAL = 1104/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS: , 71201040J , S80058630 , PICT , S80058630 AMRUTA S80058630 RITESH RAJIV POREY PP 01. DISCRETE STRUCTURES 11. ENG MATHS III 100 40 40 P 100 40 58 P C 100 40 100 40 52 P 02. COMPUTER ORGANIZATION PP 62 P C 12. COMPUTER GRAPHICS 100 40 70 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 52 P 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 53 P C 100 40 54 P PP 100 40 71 P C 100 40 05. HUMANITIES AND SOCIAL SCIENCES PP 15. DATA COMMUNICATION 62 P TW 50 20 42 P C 18 P 06. DIGITAL LABORATORY 25 10 16. PROCESSOR INTERFACING LABORATORY TW 50 20 36 P C 50 20 07. DIGITAL LABORATORY PR 17. PROCESSOR INTERFACING LABORATORY OR 34 P 50 20 08. PROGRAMMING LABORATORY TW 37 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 23 P 09. PROGRAMMING LABORATORY 50 20 PR 50 20 41 P C 19. DATA STRUCTURES AND FILES LAB PR 46 P 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 44 P C 50 20 43 P 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 40 P 21. OBJECT ORIENTED PROGRAMMING LAB PR GRAND TOTAL = 978/1500, RESULT: FIRST CLASS ORDN. 1 MARKS: , 71201046H , S80058631 , РІСТ , S80058631 S80058631 SAHARSH BHATIA KIRAN 01. DISCRETE STRUCTURES 100 40 50 P C 11. ENG MATHS III 100 40 11 F 02. COMPUTER ORGANIZATION PP 100 40 53 P C 12. COMPUTER GRAPHICS 100 40 41 P 100 40 44 P 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 18 F 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 42 P C 14. DATA STRUCTURES AND FILES 100 40 53 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 48 P C PP 100 40 45 P 15. DATA COMMUNICATION TW 50 20 10 P 06. DIGITAL LABORATORY 28 P C 25 10 16. PROCESSOR INTERFACING LABORATORY TW 50 20 27 P C 17. PROCESSOR INTERFACING LABORATORY OR 50 20 07 F 07. DIGITAL LABORATORY PR 08. PROGRAMMING LABORATORY TW 50 20 29 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 13 P 50 20 38 P C 50 20 30 P 09. PROGRAMMING LABORATORY PR 19. DATA STRUCTURES AND FILES LAB PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 32 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 23 P 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 39 P GRAND TOTAL = 681/1500, RESULT: FAILS A.T.K.T. RESULT RESERVED FOR BKLG ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71201048D , S80058632 , PICT , S80058632 S80058632 SAKSHI SIRPAL ANUPAMA 100 40 54 P 01. DISCRETE STRUCTURES 100 40 65 P C 11. ENG MATHS III PP 100 40 61 P C 12. COMPUTER GRAPHICS 100 40 56 P 02. COMPUTER ORGANIZATION 100 40 65 P C 100 40 53 P 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 13. PROCESSOR ARCHITECTURE & INTER. PP 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 60 P C PP 100 40 54 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 67 P C 100 40 59 P 15. DATA COMMUNICATION TW 50 20 40 P C 25 10 21 P 06. DIGITAL LABORATORY 16. PROCESSOR INTERFACING LABORATORY TW 07. DIGITAL LABORATORY 50 20 39 P C 50 20 40 P PR 17. PROCESSOR INTERFACING LABORATORY OR 08. PROGRAMMING LABORATORY 50 20 40 P C 25 10 21 P TW 18. DATA STRUCTURES AND FILES LAB TW 09. PROGRAMMING LABORATORY 50 20 33 P C 50 20 40 P PR 19. DATA STRUCTURES AND FILES LAB PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 43 P C 50 20 41 P 20. OBJECT ORIENTED PROGRAMMING LAB TW 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 42 P GRAND TOTAL = 994/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS: , 71201049B , S80058633 , PICT , S80058633 S80058633 SALUNKE GAURI RANGNATH ANITA PP 01. DISCRETE STRUCTURES 76 P C 11. ENG MATHS III 100 40 77 P 100 40 100 40 78 P C 100 40 51 P 02. COMPUTER ORGANIZATION PP 12. COMPUTER GRAPHICS 100 40 80 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 51 P 14. DATA STRUCTURES AND FILES 100 40 49 P C 100 40 61 P 04. FUNDAMENTAL OF DATA STRUCTURES PP PP 100 40 100 40 05. HUMANITIES AND SOCIAL SCIENCES PP 68 P C 15. DATA COMMUNICATION 67 P 50 20 47 P C 23 P 06. DIGITAL LABORATORY 25 10 TW 16. PROCESSOR INTERFACING LABORATORY TW 50 20 42 P C 50 20 37 P 07. DIGITAL LABORATORY PR 17. PROCESSOR INTERFACING LABORATORY OR 50 20 08. PROGRAMMING LABORATORY TW 48 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 24 P 09. PROGRAMMING LABORATORY 50 20 45 P PR 50 20 44 P C 19. DATA STRUCTURES AND FILES LAB PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 48 P C 50 20 45 P 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 48 P 21. OBJECT ORIENTED PROGRAMMING LAB PR GRAND TOTAL = 1109/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS: , 71201052B , S80058634 , PICT , S80058634 S80058634 SAPKALE SNEHAL SURESH USHA 01. DISCRETE STRUCTURES PP 100 40 40 P C 11. ENG MATHS III 100 40 11 F 02. COMPUTER ORGANIZATION PP 100 40 48 P C 12. COMPUTER GRAPHICS 100 40 28 F 100 40 40 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 29 F 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 45 P C 14. DATA STRUCTURES AND FILES 100 40 40 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 40 P C 100 40 40 P 15. DATA COMMUNICATION TW 50 20 06. DIGITAL LABORATORY 31 P C 25 10 12 P 16. PROCESSOR INTERFACING LABORATORY TW 50 20 35 P C 17. PROCESSOR INTERFACING LABORATORY OR 50 20 25 P 07. DIGITAL LABORATORY PR 08. PROGRAMMING LABORATORY TW 50 20 25 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 17 P 50 20 25 P 50 20 30 P C 09. PROGRAMMING LABORATORY PR 19. DATA STRUCTURES AND FILES LAB PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 34 P C 50 20 34 P 20. OBJECT ORIENTED PROGRAMMING LAB TW 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 28 P GRAND TOTAL = 657/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71201053L , S80058635 , PICT , S80058635 SANGITA S80058635 SARODE ASHWINI RAJENDRA 01. DISCRETE STRUCTURES 100 40 52 P PP 100 40 69 P C 11. ENG MATHS III PP 100 40 73 P C 12. COMPUTER GRAPHICS 100 40 51 P 02. COMPUTER ORGANIZATION 100 40 63 P C 100 40 55 P 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 13. PROCESSOR ARCHITECTURE & INTER. PP 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 69 P C PP 100 40 63 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 67 P C 100 40 71 P 15. DATA COMMUNICATION TW 50 20 45 P C 25 10 21 P 06. DIGITAL LABORATORY 16. PROCESSOR INTERFACING LABORATORY TW 07. DIGITAL LABORATORY 50 20 30 P C 50 20 36 P PR 17. PROCESSOR INTERFACING LABORATORY OR 08. PROGRAMMING LABORATORY 50 20 46 P C 25 10 22 P TW 18. DATA STRUCTURES AND FILES LAB TW 09. PROGRAMMING LABORATORY 50 20 42 P C 50 20 41 P PR 19. DATA STRUCTURES AND FILES LAB PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 45 P C 50 20 45 P 20. OBJECT ORIENTED PROGRAMMING LAB TW 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 42 P GRAND TOTAL = 1048/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS: , 71201058M , S80058636 , PICT , S80058636 S80058636 SAWANT ARUNDHATI ULHAS MADHAVI PP 01. DISCRETE STRUCTURES 11. ENG MATHS III 100 40 44 P 100 40 64 P C 100 40 100 40 02. COMPUTER ORGANIZATION PP 60 P C 12. COMPUTER GRAPHICS 48 P 100 40 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 40 P 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 71 P C 14. DATA STRUCTURES AND FILES 100 40 57 P C 100 40 44 P 04. FUNDAMENTAL OF DATA STRUCTURES PP PP 100 40 100 40 59 P 05. HUMANITIES AND SOCIAL SCIENCES PP 64 P C 15. DATA COMMUNICATION 50 20 39 P C 06. DIGITAL LABORATORY 25 10 21 P TW 16. PROCESSOR INTERFACING LABORATORY TW 50 20 37 P C 50 20 35 P 07. DIGITAL LABORATORY PR 17. PROCESSOR INTERFACING LABORATORY OR 50 20 39 P C 08. PROGRAMMING LABORATORY TW 18. DATA STRUCTURES AND FILES LAB TW 25 10 22 P 09. PROGRAMMING LABORATORY 50 20 45 P 40 P C PR 50 20 19. DATA STRUCTURES AND FILES LAB PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 39 P C 50 20 42 P 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 41 P 21. OBJECT ORIENTED PROGRAMMING LAB PR GRAND TOTAL = 951/1500, RESULT: FIRST CLASS ORDN. 1 MARKS: , 71201059к , S80058637 , РІСТ , S80058637 S80058637 SHAH DISHANT UMESH SHILPA 01. DISCRETE STRUCTURES 100 40 40 P C 11. ENG MATHS III 100 40 40 P 02. COMPUTER ORGANIZATION PP 100 40 59 P C 12. COMPUTER GRAPHICS 100 40 50 P 100 40 43 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 40 P 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 59 P C 14. DATA STRUCTURES AND FILES 100 40 40 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 68 P C PP 100 40 44 P 15. DATA COMMUNICATION TW 50 20 22 P C 06. DIGITAL LABORATORY 25 10 10 P 16. PROCESSOR INTERFACING LABORATORY TW 50 20 31 P 17. PROCESSOR INTERFACING LABORATORY OR 50 20 06 F 07. DIGITAL LABORATORY PR 08. PROGRAMMING LABORATORY TW 50 20 26 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 10 P 50 20 30 P C 50 20 09. PROGRAMMING LABORATORY PR 19. DATA STRUCTURES AND FILES LAB PR AA F 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 31 P C 50 20 20 P 20. OBJECT ORIENTED PROGRAMMING LAB TW 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 AA F GRAND TOTAL = 669/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71201061M , S80058638 , PICT , S80058638 BEENA S80058638 SHAH RUTUJA RAJESH 01. DISCRETE STRUCTURES 100 40 82 P 100 40 73 P C 11. ENG MATHS III PP 100 40 78 P C 12. COMPUTER GRAPHICS 100 40 57 P 02. COMPUTER ORGANIZATION PP 100 40 77 P C 100 40 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 13. PROCESSOR ARCHITECTURE & INTER. PP 64 P 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 73 P C PP 100 40 71 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 70 P C 100 40 69 P 15. DATA COMMUNICATION TW 50 20 45 P C 25 10 23 P 06. DIGITAL LABORATORY 16. PROCESSOR INTERFACING LABORATORY TW 07. DIGITAL LABORATORY 50 20 43 P C 50 20 45 P PR 17. PROCESSOR INTERFACING LABORATORY OR 08. PROGRAMMING LABORATORY 50 20 45 P C 25 10 24 P TW 18. DATA STRUCTURES AND FILES LAB TW 09. PROGRAMMING LABORATORY 50 20 43 P C 50 20 43 P PR 19. DATA STRUCTURES AND FILES LAB PR 50 20 45 P C 50 20 45 P 10. COMMUNICATION AND LANGUAGE LAB. TW 20. OBJECT ORIENTED PROGRAMMING LAB TW 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 48 P GRAND TOTAL = 1163/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS: , 71201064F , S80058639 , PICT , S80058639 S80058639 SHARIKA KHURANA RASHMT PP 62 P C 100 40 16 F 01. DISCRETE STRUCTURES 100 40 11. ENG MATHS III 100 40 56 P C 100 40 02. COMPUTER ORGANIZATION PP 12. COMPUTER GRAPHICS 46 P 100 40 64 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 23 F 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 61 P C 100 40 PP 40 P 100 40 100 40 05. HUMANITIES AND SOCIAL SCIENCES PP 62 P C 15. DATA COMMUNICATION 56 P TW 50 20 33 P C 14 P 06. DIGITAL LABORATORY 25 10 16. PROCESSOR INTERFACING LABORATORY TW 50 20 32 P C 50 20 07. DIGITAL LABORATORY PR 17. PROCESSOR INTERFACING LABORATORY OR 08 F 50 20 08. PROGRAMMING LABORATORY TW 37 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 14 P 09. PROGRAMMING LABORATORY PR 50 20 38 P C 19. DATA STRUCTURES AND FILES LAB PR 50 20 12 F 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 39 P C 50 20 33 P 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 40 P 21. OBJECT ORIENTED PROGRAMMING LAB PR GRAND TOTAL = 786/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS: , 71101005G , S80058640 , PICT , S80058640 S80058640 SHIKHAR SHANKAR KHANDELWAL KANTA 01. DISCRETE STRUCTURES 100 40 AA F 11. ENG MATHS III 100 40 AA F 02. COMPUTER ORGANIZATION PP 100 40 AA F 12. COMPUTER GRAPHICS 100 40 AA F 100 40 43 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 AA F 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 25 F 14. DATA STRUCTURES AND FILES 100 40 AA F 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 AA F PP 100 40 15. DATA COMMUNICATION AA F TW 50 20 06. DIGITAL LABORATORY 20 P C 25 10 10 P 16. PROCESSOR INTERFACING LABORATORY TW 50 20 31 P C 17. PROCESSOR INTERFACING LABORATORY OR 50 20 07. DIGITAL LABORATORY PR AA F 08. PROGRAMMING LABORATORY TW 50 20 22 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 10 P PR 50 20 AA F 50 20 28 P C 09. PROGRAMMING LABORATORY 19. DATA STRUCTURES AND FILES LAB PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 22 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 20 P 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 AA F GRAND TOTAL = 231/1500, RESULT: FAILS RESULT RESERVED FOR BKLG ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71350935L , S80058641 , PICT , S80058641 S80058641 SHINDE ANIKET NAVNATH BABYNANDA 01. DISCRETE STRUCTURES PP 100 40 40 P C PP 100 40 17 F 11. ENG MATHS III 100 40 62 P C 12. COMPUTER GRAPHICS 100 40 46 P 02. COMPUTER ORGANIZATION 100 40 69 P C 100 40 45 P 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 13. PROCESSOR ARCHITECTURE & INTER. PP 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 66 P C PP 100 40 56 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 57 P C 100 40 62 P 15. DATA COMMUNICATION 50 20 38 P C 25 10 14 P 06. DIGITAL LABORATORY 16. PROCESSOR INTERFACING LABORATORY TW TW 07. DIGITAL LABORATORY 50 20 20 P C 50 20 31 P PR 17. PROCESSOR INTERFACING LABORATORY OR 08. PROGRAMMING LABORATORY 50 20 40 P C 25 10 19 P TW 18. DATA STRUCTURES AND FILES LAB TW 09. PROGRAMMING LABORATORY 50 20 42 P C 50 20 47 P PR 19. DATA STRUCTURES AND FILES LAB PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 38 P C 50 20 40 P 20. OBJECT ORIENTED PROGRAMMING LAB TW 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 35 P GRAND TOTAL = 884/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS: , 71201073E , S80058642 , PICT , S80058642 S80058642 SHUBHAM BOHRA MONA PP 57 P C 11. ENG MATHS III 100 40 60 P 01. DISCRETE STRUCTURES 100 40 100 40 100 40 02. COMPUTER ORGANIZATION PP 42 P C 12. COMPUTER GRAPHICS 44 P 100 40 41 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 03. DIGITAL ELECTRONICS & LOGIC DESIGPP AA F 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 46 P C 100 40 42 P PP 100 40 41 P C 100 40 05. HUMANITIES AND SOCIAL SCIENCES PP 15. DATA COMMUNICATION TW 50 20 24 P C 06. DIGITAL LABORATORY 25 10 11 P 16. PROCESSOR INTERFACING LABORATORY TW 50 20 25 P C 50 20 06 F 07. DIGITAL LABORATORY PR 17. PROCESSOR INTERFACING LABORATORY OR 50 20 29 P C 08. PROGRAMMING LABORATORY TW 18. DATA STRUCTURES AND FILES LAB TW 25 10 11 P 09. PROGRAMMING LABORATORY 50 20 32 P C 50 20 PR 19. DATA STRUCTURES AND FILES LAB PR 25 P 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 31 P C 50 20 28 P 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 37 P 21. OBJECT ORIENTED PROGRAMMING LAB PR GRAND TOTAL = 676/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS: , 71201075M , S80058643 , PICT , S80058643 S80058643 SIDDHARTH DALAL REKHA 01. DISCRETE STRUCTURES 100 40 64 P C 11. ENG MATHS III 100 40 79 P 02. COMPUTER ORGANIZATION PP 100 40 68 P C 12. COMPUTER GRAPHICS 100 40 50 P 100 40 76 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 43 P 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 70 P C 14. DATA STRUCTURES AND FILES 100 40 59 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 55 P C PP 100 40 58 P 15. DATA COMMUNICATION TW 50 20 21 P 06. DIGITAL LABORATORY 38 P C 25 10 16. PROCESSOR INTERFACING LABORATORY TW 50 20 44 P C 17. PROCESSOR INTERFACING LABORATORY OR 50 20 32 P 07. DIGITAL LABORATORY PR 08. PROGRAMMING LABORATORY TW 50 20 39 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 21 P 50 20 43 P C 50 20 41 P 09. PROGRAMMING LABORATORY PR 19. DATA STRUCTURES AND FILES LAB PR 50 20 41 P C 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 43 P 20. OBJECT ORIENTED PROGRAMMING LAB TW 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 36 P GRAND TOTAL = 1021/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71201077н , S80058644 , РІСТ , S80058644 S80058644 SIDHESH BADRINARAYAN KAUSALYA 01. DISCRETE STRUCTURES PP 100 40 60 P C 11. ENG MATHS III PP 100 40 61 P 100 40 83 P C 12. COMPUTER GRAPHICS 100 40 62 P 02. COMPUTER ORGANIZATION PP 100 40 80 P C 100 40 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 13. PROCESSOR ARCHITECTURE & INTER. PP 67 P 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 71 P C PP 100 40 69 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 73 P C 100 40 62 P 15. DATA COMMUNICATION 50 20 43 P C 25 10 20 P 06. DIGITAL LABORATORY 16. PROCESSOR INTERFACING LABORATORY TW TW 07. DIGITAL LABORATORY 50 20 38 P C 50 20 42 P PR 17. PROCESSOR INTERFACING LABORATORY OR 08. PROGRAMMING LABORATORY 50 20 44 P C 25 10 24 P TW 18. DATA STRUCTURES AND FILES LAB TW 09. PROGRAMMING LABORATORY 50 20 45 P C 50 20 48 P PR 19. DATA STRUCTURES AND FILES LAB PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 42 P C 50 20 42 P 20. OBJECT ORIENTED PROGRAMMING LAB TW 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 46 P GRAND TOTAL = 1122/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS: SYEDA JABEEN FATEMA , 71201088C , S80058645 , PICT , S80058645 S80058645 SYED JUNAID ALI MASOOD ALI PP 01. DISCRETE STRUCTURES PP 100 40 46 P 11. ENG MATHS III 100 40 40 P 100 40 100 40 02. COMPUTER ORGANIZATION 63 P C 12. COMPUTER GRAPHICS 43 P 100 40 40 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 40 P 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 100 40 40 P C PP 40 P 100 40 63 P C 100 40 52 P 05. HUMANITIES AND SOCIAL SCIENCES PP 15. DATA COMMUNICATION 50 20 39 P C 17 P 06. DIGITAL LABORATORY 25 10 TW 16. PROCESSOR INTERFACING LABORATORY TW 50 20 24 P C 50 20 21 P 07. DIGITAL LABORATORY PR 17. PROCESSOR INTERFACING LABORATORY OR 50 20 35 P C 08. PROGRAMMING LABORATORY TW 18. DATA STRUCTURES AND FILES LAB TW 25 10 19 P 09. PROGRAMMING LABORATORY 50 20 40 P PR 50 20 35 P C 19. DATA STRUCTURES AND FILES LAB PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 39 P C 50 20 35 P 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 36 P 21. OBJECT ORIENTED PROGRAMMING LAB PR GRAND TOTAL = 807/1500, RESULT: SECOND CLASS ORDN. 1 MARKS: , 71201089M , S80058646 , PICT , S80058646 JYOTI S80058646 TAKALKAR TANMAYEE SUHAS 01. DISCRETE STRUCTURES 100 40 74 P C 11. ENG MATHS III 100 40 40 P 02. COMPUTER ORGANIZATION PP 100 40 66 P C 12. COMPUTER GRAPHICS 100 40 64 P 100 40 69 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 73 P 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 62 P C 14. DATA STRUCTURES AND FILES 100 40 59 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 64 P C 100 40 63 P 15. DATA COMMUNICATION TW 50 20 06. DIGITAL LABORATORY 35 P C 25 10 18 P 16. PROCESSOR INTERFACING LABORATORY TW 50 20 39 P C 17. PROCESSOR INTERFACING LABORATORY OR 50 20 41 P 07. DIGITAL LABORATORY PR 08. PROGRAMMING LABORATORY TW 50 20 38 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 21 P 09. PROGRAMMING LABORATORY PR 50 20 43 P C 50 20 43 P 19. DATA STRUCTURES AND FILES LAB PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 35 P C 50 20 36 P 20. OBJECT ORIENTED PROGRAMMING LAB TW 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 35 P GRAND TOTAL = 1018/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71201095F , S80058647 , PICT , S80058647 S80058647 THORAT SAYALI SURESH SULABHA 01. DISCRETE STRUCTURES 100 40 40 P PP 100 40 44 P C 11. ENG MATHS III PP 100 40 40 P C 12. COMPUTER GRAPHICS 100 40 40 P 02. COMPUTER ORGANIZATION 100 40 40 P C 100 40 40 P 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 13. PROCESSOR ARCHITECTURE & INTER. PP 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 40 P C PP 100 40 54 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 41 P C 100 40 42 P 15. DATA COMMUNICATION TW 50 20 35 P C 25 10 16 P 06. DIGITAL LABORATORY 16. PROCESSOR INTERFACING LABORATORY TW 22 P 07. DIGITAL LABORATORY 50 20 33 P C 50 20 PR 17. PROCESSOR INTERFACING LABORATORY OR 08. PROGRAMMING LABORATORY 50 20 34 P C 25 10 TW 18. DATA STRUCTURES AND FILES LAB TW 16 P 09. PROGRAMMING LABORATORY 50 20 26 P C 50 20 30 P PR 19. DATA STRUCTURES AND FILES LAB PR 50 20 39 P C 50 20 32 P 10. COMMUNICATION AND LANGUAGE LAB. TW 20. OBJECT ORIENTED PROGRAMMING LAB TW 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 37 P GRAND TOTAL = 741+09/1500, RESULT: SECOND CLASS [0.2] ORDN. 1 MARKS: , 71350936J , S80058648 , PICT , S80058648 SUNITA S80058648 TIKONE SNEHAL SHAM PP 01. DISCRETE STRUCTURES 45 P C 11. ENG MATHS III 100 40 40 P PP 100 40 100 40 44 P C 100 40 02. COMPUTER ORGANIZATION PP 12. COMPUTER GRAPHICS 42 P 100 40 41 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 46 P 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 56 P C 100 40 PP 56 P 100 40 100 40 50 P 05. HUMANITIES AND SOCIAL SCIENCES PP 58 P C 15. DATA COMMUNICATION TW 50 20 37 P C 22 P 06. DIGITAL LABORATORY 25 10 16. PROCESSOR INTERFACING LABORATORY TW 25 P C 50 20 20 P 07. DIGITAL LABORATORY PR 50 20 17. PROCESSOR INTERFACING LABORATORY OR 50 20 08. PROGRAMMING LABORATORY TW 41 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 21 P 09. PROGRAMMING LABORATORY 50 20 15# P 50 20 30 P C PR 19. DATA STRUCTURES AND FILES LAB PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 42 P C 50 20 44 P 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 45 P 21. OBJECT ORIENTED PROGRAMMING LAB PR GRAND TOTAL = 820/1500, RESULT: SECOND CLASS # [0.4] ORDN. 1 MARKS: , 71201114F , S80058649 , PICT , S80058649 S80058649 WADKE RUCHA VIJAY ANURADHA 25 F 01. DISCRETE STRUCTURES 100 40 11. ENG MATHS III 100 40 16 F 02. COMPUTER ORGANIZATION PP 100 40 55 P 12. COMPUTER GRAPHICS 100 40 40 P 100 40 50 P 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 40 P 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 42 P 14. DATA STRUCTURES AND FILES 100 40 40 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 67 P PP 100 40 41 P 15. DATA COMMUNICATION TW 50 20 17 P 06. DIGITAL LABORATORY 35 P C 25 10 16. PROCESSOR INTERFACING LABORATORY TW 50 20 05 F 17. PROCESSOR INTERFACING LABORATORY OR 50 20 09 F 07. DIGITAL LABORATORY PR 08. PROGRAMMING LABORATORY TW 50 20 33 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 19 P 50 20 27 P 50 20 10 F 09. PROGRAMMING LABORATORY PR 19. DATA STRUCTURES AND FILES LAB PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 40 P C 50 20 38 P 20. OBJECT ORIENTED PROGRAMMING LAB TW 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 40 P GRAND TOTAL = 689/1500, RESULT: FAILS A.T.K.T. RESULT RESERVED FOR BKLG ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71201118J , S80058650 , PICT , S80058650 MANISHA S80058650 WALZADE ROHAN SUNIL 01. DISCRETE STRUCTURES 100 40 50 P PP 100 40 46 P C 11. ENG MATHS III PP 100 40 45 P C 12. COMPUTER GRAPHICS 100 40 40 P 02. COMPUTER ORGANIZATION PP 100 40 41 P C 100 40 47 P 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 13. PROCESSOR ARCHITECTURE & INTER. PP 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 47 P C PP 100 40 46 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 68 P C 100 40 40 P 15. DATA COMMUNICATION TW 50 20 32 P C 25 10 14 P 06. DIGITAL LABORATORY 16. PROCESSOR INTERFACING LABORATORY TW 20 P C 07. DIGITAL LABORATORY 50 20 50 20 06 F PR 17. PROCESSOR INTERFACING LABORATORY OR 08. PROGRAMMING LABORATORY 50 20 30 P C 25 10 TW 18. DATA STRUCTURES AND FILES LAB TW 16 P 09. PROGRAMMING LABORATORY 50 20 26 P C 50 20 35 P PR 19. DATA STRUCTURES AND FILES LAB PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 36 P C 50 20 27 P 20. OBJECT ORIENTED PROGRAMMING LAB TW 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 36 P GRAND TOTAL = 748/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS: , 71350937G , S80058651 , PICT , S80058651 S80058651 YADAV AKASH VINOD VIJAYA PP 01. DISCRETE STRUCTURES 11. ENG MATHS III 100 40 20 F 100 40 50 P 100 40 100 40 02. COMPUTER ORGANIZATION PP 52 P C 12. COMPUTER GRAPHICS 40 P 100 40 63 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 44 P 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 49 P C 100 40 PP 60 P 100 40 55 P C 100 40 49 P 05. HUMANITIES AND SOCIAL SCIENCES PP 15. DATA COMMUNICATION TW 50 20 33 P C 13 P 06. DIGITAL LABORATORY 25 10 16. PROCESSOR INTERFACING LABORATORY TW 50 20 20 P C 50 20 23 P 07. DIGITAL LABORATORY PR 17. PROCESSOR INTERFACING LABORATORY OR 50 20 08. PROGRAMMING LABORATORY TW 33 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 15 P 09. PROGRAMMING LABORATORY 50 20 40 P PR 50 20 34 P C 19. DATA STRUCTURES AND FILES LAB PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 34 P C 50 20 35 P 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 33 P 21. OBJECT ORIENTED PROGRAMMING LAB PR GRAND TOTAL = 795/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS: , 71201121J , S80058652 , PICT , S80058652 S80058652 YADAV GAURAV RAJENDRA RASHMI 01. DISCRETE STRUCTURES PP 100 40 43 P C 11. ENG MATHS III 100 40 16 F 02. COMPUTER ORGANIZATION PP 100 40 65 P C 12. COMPUTER GRAPHICS 100 40 54 P 100 40 62 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 55 P 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 62 P C 14. DATA STRUCTURES AND FILES 100 40 69 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 63 P C PP 100 40 58 P 15. DATA COMMUNICATION TW 50 20 17 P 06. DIGITAL LABORATORY 41 P C 25 10 16. PROCESSOR INTERFACING LABORATORY TW 50 20 42 P C 17. PROCESSOR INTERFACING LABORATORY OR 50 20 30 P 07. DIGITAL LABORATORY PR 08. PROGRAMMING LABORATORY TW 50 20 39 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 20 P 50 20 30 P C 50 20 41 P 09. PROGRAMMING LABORATORY PR 19. DATA STRUCTURES AND FILES LAB PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 42 P C 50 20 38 P 20. OBJECT ORIENTED PROGRAMMING LAB TW 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 35 P GRAND TOTAL = 922/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71201125M , S80058653 , PICT , S80058653 PRAMILA S80058653 ZANJARE SHRIDATTA GOVIND 01. DISCRETE STRUCTURES 100 40 48 P 100 40 27 F 11. ENG MATHS III PP PP 100 40 43 P C 12. COMPUTER GRAPHICS 100 40 51 P 02. COMPUTER ORGANIZATION 100 40 49 P C 100 40 40 P 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 13. PROCESSOR ARCHITECTURE & INTER. PP 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 46 P C PP 100 40 40 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 68 P C 100 40 54 P 15. DATA COMMUNICATION TW 50 20 42 P C 25 10 18 P 06. DIGITAL LABORATORY 16. PROCESSOR INTERFACING LABORATORY TW 27 P 07. DIGITAL LABORATORY 50 20 50 20 08 F PR 17. PROCESSOR INTERFACING LABORATORY OR 08. PROGRAMMING LABORATORY 50 20 42 P C 25 10 20 P TW 18. DATA STRUCTURES AND FILES LAB TW 09. PROGRAMMING LABORATORY 50 20 25 P C 50 20 28 P PR 19. DATA STRUCTURES AND FILES LAB PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 46 P C 50 20 40 P 20. OBJECT ORIENTED PROGRAMMING LAB TW 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 35 P GRAND TOTAL = 797/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS: , 71100717к , S80058654 , РІСТ , S80058654 KALPANA S80058654 ADEP AKASH RAJENDRA 01. DISCRETE STRUCTURES 40 P C 11. ENG MATHS III 100 40 40 P 100 40 PP 100 40 40 P C 100 40 57 P C 02. COMPUTER ORGANIZATION PP 12. COMPUTER GRAPHICS 100 40 40 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 40 P C 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 40 P C 100 40 PP 40 P C 100 40 40 P C 100 40 55 P C 05. HUMANITIES AND SOCIAL SCIENCES PP 15. DATA COMMUNICATION 50 20 31 P C 25 10 06. DIGITAL LABORATORY 10 P C TW 16. PROCESSOR INTERFACING LABORATORY TW 50 20 23 P C 50 20 07. DIGITAL LABORATORY PR 17. PROCESSOR INTERFACING LABORATORY OR 22 P C 50 20 08. PROGRAMMING LABORATORY TW 27 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 11 PC 09. PROGRAMMING LABORATORY 50 20 38 P C PR 50 20 20 P C 19. DATA STRUCTURES AND FILES LAB PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 35 P C 50 20 33 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 32 P C 21. OBJECT ORIENTED PROGRAMMING LAB PR GRAND TOTAL = 714/1500, RESULT: PASS CLASS ORDN. 1 MARKS: , 71100719F , S80058655 , PICT , S80058655 DIMPLE S80058655 ADITYA SINGH SOLANKI 01. DISCRETE STRUCTURES 100 40 53 P C 11. ENG MATHS III 100 40 40 P C 02. COMPUTER ORGANIZATION PP 100 40 51 P C 12. COMPUTER GRAPHICS 100 40 43 P.C 40 P C 100 40 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 40 P C 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 42 P C 14. DATA STRUCTURES AND FILES 100 40 40 P.C 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 46 P C PP 100 40 41 P C 15. DATA COMMUNICATION TW 50 20 06. DIGITAL LABORATORY 30 P C 25 10 18 P C 16. PROCESSOR INTERFACING LABORATORY TW 50 20 30 P C 17. PROCESSOR INTERFACING LABORATORY OR 50 20 20 P C 07. DIGITAL LABORATORY PR 08. PROGRAMMING LABORATORY TW 50 20 34 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 17 PC 50 20 23 P C 50 20 21 P 09. PROGRAMMING LABORATORY PR 19. DATA STRUCTURES AND FILES LAB PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 36 P C 50 20 36 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 25 P GRAND TOTAL = 726/1500, RESULT: PASS CLASS ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71100764M , S80058664 , PICT , S80058664 MEENA S80058664 CHAUDHARI PRANITA RAVISHANKAR 100 40 45 P C 100 40 40 P C 01. DISCRETE STRUCTURES PP 11. ENG MATHS III PP 100 40 40 P C 12. COMPUTER GRAPHICS 100 40 45 P C 02. COMPUTER ORGANIZATION 100 40 49 P C 100 40 40 P.C 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 13. PROCESSOR ARCHITECTURE & INTER. PP 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 44 P C 100 40 52 P PP 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 40 P C 100 40 40 P 15. DATA COMMUNICATION TW 50 20 36 P C 25 10 06. DIGITAL LABORATORY 16. PROCESSOR INTERFACING LABORATORY TW 14 P C 07. DIGITAL LABORATORY 50 20 35 P C 50 20 28 P C PR 17. PROCESSOR INTERFACING LABORATORY OR 08. PROGRAMMING LABORATORY 50 20 37 P C 25 10 TW 18. DATA STRUCTURES AND FILES LAB TW 10 P C 09. PROGRAMMING LABORATORY 50 20 26 P C 50 20 20 P C PR 19. DATA STRUCTURES AND FILES LAB PR 50 20 37 P C 50 20 36 P C 10. COMMUNICATION AND LANGUAGE LAB. TW 20. OBJECT ORIENTED PROGRAMMING LAB TW 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 35 P C GRAND TOTAL = 749+01/1500, RESULT: SECOND CLASS [0.2] ORDN. 1 MARKS: , 71100770F , S80058665 , PICT , S80058665 VIMAL S80058665 CHOKHAR PRITHVIRAJ BALASAHEB PP 100 40 11 F 01. DISCRETE STRUCTURES 100 40 40 P C 11. ENG MATHS III 100 40 40 P C 100 40 20 F 02. COMPUTER ORGANIZATION 12. COMPUTER GRAPHICS 100 40 42 P 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 19 F 14. DATA STRUCTURES AND FILES 100 40 40 P C 100 40 04. FUNDAMENTAL OF DATA STRUCTURES PP PP 40 P C 100 40 40 P C 100 40 05. HUMANITIES AND SOCIAL SCIENCES PP 15. DATA COMMUNICATION TW 50 20 06. DIGITAL LABORATORY 23 P C 25 10 15 P C 16. PROCESSOR INTERFACING LABORATORY TW 22 P C 50 20 27 P C 07. DIGITAL LABORATORY PR 50 20 17. PROCESSOR INTERFACING LABORATORY OR 50 20 25 10 15 P C 08. PROGRAMMING LABORATORY TW 29 P C 18. DATA STRUCTURES AND FILES LAB TW 09. PROGRAMMING LABORATORY 50 20 30 P C 50 20 AA F PR 19. DATA STRUCTURES AND FILES LAB PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 31 P C 50 20 29 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 21 P 21. OBJECT ORIENTED PROGRAMMING LAB PR GRAND TOTAL = 574/1500, RESULT: FAILS A.T.K.T. RESULT RESERVED FOR BKLG ORDN. 1 MARKS: , 71241757F , S80058666 , PICT , S80058666 S80058666 DAHAWAD SUNIL NAMDEO VANITA 01. DISCRETE STRUCTURES 100 40 40 P 11. ENG MATHS III 100 40 24 F 02. COMPUTER ORGANIZATION PP 100 40 57 P C 12. COMPUTER GRAPHICS PP 100 40 40 P C 100 40 40 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 54 P C 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 54 P C 14. DATA STRUCTURES AND FILES 100 40 40 P.C 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 42 P C PP 100 40 43 P C 15. DATA COMMUNICATION TW 50 20 06. DIGITAL LABORATORY 35 P C 25 10 13 P C 16. PROCESSOR INTERFACING LABORATORY TW 50 20 22 P C 17. PROCESSOR INTERFACING LABORATORY OR 50 20 29 P C 07. DIGITAL LABORATORY PR 15 P C 08. PROGRAMMING LABORATORY TW 50 20 38 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 50 20 32 P C 50 20 37 P C 09. PROGRAMMING LABORATORY PR 19. DATA STRUCTURES AND FILES LAB PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 39 P.C. 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 35 P C 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 33 P GRAND TOTAL = 762/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71100800M , S80058669 , PICT , S80058669 S80058669 GAWANDE ANAGHA ARVIND MAYA 01. DISCRETE STRUCTURES 100 40 24 F PP 100 40 47 P C 11. ENG MATHS III PP 100 40 42 P C 12. COMPUTER GRAPHICS 100 40 53 P C 02. COMPUTER ORGANIZATION PP 100 40 58 P C 100 40 52 P.C 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 13. PROCESSOR ARCHITECTURE & INTER. PP 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 43 P C PP 100 40 53 P C 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 47 P C 100 40 51 P.C 15. DATA COMMUNICATION TW 50 20 33 P C 25 10 18 P C 06. DIGITAL LABORATORY 16. PROCESSOR INTERFACING LABORATORY TW 32 P C 07. DIGITAL LABORATORY 50 20 50 20 28 P C PR 17. PROCESSOR INTERFACING LABORATORY OR 08. PROGRAMMING LABORATORY 50 20 38 P C 25 10 TW 18. DATA STRUCTURES AND FILES LAB TW 16 P C 09. PROGRAMMING LABORATORY 50 20 23 P C 50 20 38 P C PR 19. DATA STRUCTURES AND FILES LAB PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 39 P C 50 20 36 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 37 P C GRAND TOTAL = 808/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS: , 71100802н , S80058670 , РІСТ , S80058670 SWATI S80058670 GHODAKE PRAJAKTA DHANANJAY PP 01. DISCRETE STRUCTURES 49 P C 11. ENG MATHS III 100 40 40 P 100 40 100 40 47 P C 100 40 02. COMPUTER ORGANIZATION 12. COMPUTER GRAPHICS 45 P C 43 P C 100 40 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 40 P C 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 40 P C 100 40 PP 43 P C 100 40 45 P C 100 40 05. HUMANITIES AND SOCIAL SCIENCES PP 15. DATA COMMUNICATION 47 P C TW 50 20 30 P C 25 10 06. DIGITAL LABORATORY 10 P C 16. PROCESSOR INTERFACING LABORATORY TW 50 20 25 P C 50 20 07. DIGITAL LABORATORY PR 17. PROCESSOR INTERFACING LABORATORY OR 20 P C 50 20 08. PROGRAMMING LABORATORY TW 24 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 13 P C 09. PROGRAMMING LABORATORY 50 20 20 P C 50 20 30 P C PR 19. DATA STRUCTURES AND FILES LAB PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 31 P C 50 20 30 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 33 P 21. OBJECT ORIENTED PROGRAMMING LAB PR GRAND TOTAL = 705/1500, RESULT: PASS CLASS ORDN. 1 MARKS: , 71045436J , S80058671 , PICT , S80058671 SINDHU S80058671 GHODE AMAR PRADEEPKUMAR 01. DISCRETE STRUCTURES 100 40 52 P C 11. ENG MATHS III 100 40 20 F 02. COMPUTER ORGANIZATION PP PP 100 40 42 P C 12. COMPUTER GRAPHICS 100 40 42 P C 100 40 44 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 40 P C 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 40 P C 14. DATA STRUCTURES AND FILES 100 40 49 P C 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 41 P C PP 100 40 45 P C 15. DATA COMMUNICATION TW 50 20 20 P C 06. DIGITAL LABORATORY 25 10 11 P C 16. PROCESSOR INTERFACING LABORATORY TW 50 20 33 P C 17. PROCESSOR INTERFACING LABORATORY OR 50 20 22 P C 07. DIGITAL LABORATORY PR 08. PROGRAMMING LABORATORY TW 50 20 30 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 12 PC 50 20 33 P C 50 20 30 P C 09. PROGRAMMING LABORATORY PR 19. DATA STRUCTURES AND FILES LAB PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 25 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 26 P C 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 20 P C GRAND TOTAL = 677/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS: 

DATE: 27 JULY 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 54 (567)

NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71100810J , S80058673 , PICT , S80058673 S80058673 GORADE TUSHAR BHAUSAHEB ALKA 01. DISCRETE STRUCTURES 100 40 23 F PP 100 40 40 P C 11. ENG MATHS III PP 100 40 40 P C 12. COMPUTER GRAPHICS 100 40 40 P C 02. COMPUTER ORGANIZATION 100 40 42 P C 100 40 40 P C 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 13. PROCESSOR ARCHITECTURE & INTER. PP 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 46 P C PP 100 40 40 P C 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 40 P C 100 40 40 P 15. DATA COMMUNICATION TW 50 20 32 P C 25 10 06. DIGITAL LABORATORY 16. PROCESSOR INTERFACING LABORATORY TW 10 P C 07. DIGITAL LABORATORY 50 20 24 P C 50 20 20 P C PR 17. PROCESSOR INTERFACING LABORATORY OR 08. PROGRAMMING LABORATORY 50 20 22 P C 25 10 13 P C TW 18. DATA STRUCTURES AND FILES LAB TW 09. PROGRAMMING LABORATORY 50 20 28 P C 50 20 34 P C PR 19. DATA STRUCTURES AND FILES LAB PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 29 P C 50 20 35 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 35 P C GRAND TOTAL = 673/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS: PARAMJEET KAUR , 71045444K , S80058675 , PICT , S80058675 S80058675 HARKIRPAL SINGH PP 16 F 01. DISCRETE STRUCTURES 11. ENG MATHS III 100 40 00 F 100 40 100 40 52 P 100 40 02. COMPUTER ORGANIZATION PP 12. COMPUTER GRAPHICS 40 P 100 40 40 P 100 40 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 13. PROCESSOR ARCHITECTURE & INTER. PP 27 F 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 26 F 100 40 PP 43 P 100 40 100 40 05. HUMANITIES AND SOCIAL SCIENCES PP 58 P 15. DATA COMMUNICATION 52 P TW 21 P C 50 20 06. DIGITAL LABORATORY 25 10 10 P C 16. PROCESSOR INTERFACING LABORATORY TW 50 20 21 P 50 20 21 P C 07. DIGITAL LABORATORY PR 17. PROCESSOR INTERFACING LABORATORY OR 50 20 25 10 10 P C 08. PROGRAMMING LABORATORY TW 20 P C 18. DATA STRUCTURES AND FILES LAB TW 09. PROGRAMMING LABORATORY 50 20 AA F PR 50 20 22 P C 19. DATA STRUCTURES AND FILES LAB PR 50 20 32 P C 50 20 22 P C 10. COMMUNICATION AND LANGUAGE LAB. TW 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 34 P 21. OBJECT ORIENTED PROGRAMMING LAB PR GRAND TOTAL = 567/1500, RESULT: FAILS ORDN. 1 MARKS: , 71100828M , S80058676 , PICT , S80058676 AMITA S80058676 JAIPURIA ROHIT SHARADKUMAR 01. DISCRETE STRUCTURES 100 40 47 P C 11. ENG MATHS III 100 40 40 P C 02. COMPUTER ORGANIZATION PP 100 40 40 P C 12. COMPUTER GRAPHICS PP 100 40 40 P.C 40 P C 100 40 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 40 P C 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 42 P C 14. DATA STRUCTURES AND FILES 100 40 43 P.C. 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 43 P C PP 100 40 40 P C 15. DATA COMMUNICATION TW 50 20 06. DIGITAL LABORATORY 33 P C 25 10 19 P C 16. PROCESSOR INTERFACING LABORATORY TW 50 20 35 P C 17. PROCESSOR INTERFACING LABORATORY OR 50 20 29 P C 07. DIGITAL LABORATORY PR 08. PROGRAMMING LABORATORY TW 50 20 25 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 15 P C 40 P 50 20 22 P C 50 20 09. PROGRAMMING LABORATORY PR 19. DATA STRUCTURES AND FILES LAB PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 33 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 39 P C 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 28 P C GRAND TOTAL = 733/1500, RESULT: PASS CLASS ORDN. 1 MARKS:

DATE: 27 JULY 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 55 ( 568)

NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71100857E , S80058677 , PICT , S80058677 PRAMILA S80058677 KHADSE MAYUR DILIP 01. DISCRETE STRUCTURES PP 100 40 22 F 100 40 40 P C 11. ENG MATHS III 100 40 40 P C 12. COMPUTER GRAPHICS 100 40 41 P C 02. COMPUTER ORGANIZATION PP 100 40 45 P C 100 40 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 13. PROCESSOR ARCHITECTURE & INTER. PP 41 P C 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 51 P C PP 100 40 42 P C 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 51 P C 100 40 40 P.C 15. DATA COMMUNICATION TW 50 20 23 P C 25 10 12 P C 06. DIGITAL LABORATORY 16. PROCESSOR INTERFACING LABORATORY TW 07. DIGITAL LABORATORY 50 20 38 P C 50 20 26 P C PR 17. PROCESSOR INTERFACING LABORATORY OR 08. PROGRAMMING LABORATORY 50 20 28 P C 25 10 11 P C TW 18. DATA STRUCTURES AND FILES LAB TW 09. PROGRAMMING LABORATORY 50 20 29 P C 50 20 27 P C PR 19. DATA STRUCTURES AND FILES LAB PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 32 P C 50 20 29 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 25 P C GRAND TOTAL = 693/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS: , 71045485G , S80058678 , PICT , S80058678 MANGAL S80058678 KOSHTI ANUJA RAMESH PP 01. DISCRETE STRUCTURES 11. ENG MATHS III 100 40 26 F PP 100 40 58 P C 100 40 100 40 02. COMPUTER ORGANIZATION PP 41 P C 12. COMPUTER GRAPHICS 53 P C 100 40 53 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 44 P C 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 50 P C 100 40 PP 49 P C 100 40 100 40 05. HUMANITIES AND SOCIAL SCIENCES PP 54 P C 15. DATA COMMUNICATION 40 P C TW 50 20 06. DIGITAL LABORATORY 34 P C 25 10 13 P C 16. PROCESSOR INTERFACING LABORATORY TW 50 20 22 P C 50 20 07. DIGITAL LABORATORY PR 17. PROCESSOR INTERFACING LABORATORY OR 23 P C 50 20 37 P C 25 10 17 P C 08. PROGRAMMING LABORATORY TW 18. DATA STRUCTURES AND FILES LAB TW 09. PROGRAMMING LABORATORY 50 20 35 P C 50 20 28 P PR 19. DATA STRUCTURES AND FILES LAB PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 37 P C 50 20 36 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 20 P C 21. OBJECT ORIENTED PROGRAMMING LAB PR GRAND TOTAL = 770/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS: , 71045491M , S80058679 , PICT , S80058679 S80058679 KULKARNI ALOK DIPAK GEETA 01. DISCRETE STRUCTURES 100 40 AA F 11. ENG MATHS III 100 40 AA F 02. COMPUTER ORGANIZATION PP 100 40 AA F 12. COMPUTER GRAPHICS PP 100 40 AA F 100 40 AA F 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 AA F 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 45 P C 14. DATA STRUCTURES AND FILES 100 40 AA F 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 40 P C PP 100 40 AA F 15. DATA COMMUNICATION TW 50 20 06. DIGITAL LABORATORY 22 P C 25 10 15 P C 16. PROCESSOR INTERFACING LABORATORY TW 50 20 33 P C 17. PROCESSOR INTERFACING LABORATORY OR 50 20 07. DIGITAL LABORATORY PR AA F 08. PROGRAMMING LABORATORY TW 50 20 35 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 15 P C 50 20 29 P C 50 20 35 P C 09. PROGRAMMING LABORATORY PR 19. DATA STRUCTURES AND FILES LAB PR 50 20 37 P C 10. COMMUNICATION AND LANGUAGE LAB. TW 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 35 P C 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 40 P C GRAND TOTAL = 381/1500, RESULT: FAILS RESULT RESERVED FOR BKLG ORDN. 1 MARKS:

DATE : 27 JULY 2013 CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 56 ( 569)

NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71100890G , S80058680 , PICT , S80058680 PREETI S80058680 MANSI MATHUR 100 40 23 F 01. DISCRETE STRUCTURES 100 40 40 P C 11. ENG MATHS III PP 100 40 48 P C 12. COMPUTER GRAPHICS 100 40 02. COMPUTER ORGANIZATION PP 46 P C 100 40 54 P C 100 40 42 P C 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 13. PROCESSOR ARCHITECTURE & INTER. PP 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 46 P C 100 40 51 P C PP 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 53 P C 100 40 46 P.C 15. DATA COMMUNICATION TW 50 20 31 P C 25 10 14 P C 06. DIGITAL LABORATORY 16. PROCESSOR INTERFACING LABORATORY TW 07. DIGITAL LABORATORY 50 20 30 P C 50 20 PR 17. PROCESSOR INTERFACING LABORATORY OR 28 P C 08. PROGRAMMING LABORATORY 50 20 30 P C 25 10 13 P C TW 18. DATA STRUCTURES AND FILES LAB TW 09. PROGRAMMING LABORATORY 50 20 20 P C 50 20 42 P PR 19. DATA STRUCTURES AND FILES LAB PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 31 P C 50 20 35 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 25 P C GRAND TOTAL = 748/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS: , 71045530F , S80058681 , PICT , S80058681 SNEHAL S80058681 MULAY AMIT YESHWANT PP 01. DISCRETE STRUCTURES 61 P C 11. ENG MATHS III 100 40 40 P 100 40 100 40 100 40 02. COMPUTER ORGANIZATION PP 44 P C 12. COMPUTER GRAPHICS 56 P C 100 40 44 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 40 P C 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 100 40 51 P C 41 P C PP 100 40 53 P C 100 40 05. HUMANITIES AND SOCIAL SCIENCES PP 15. DATA COMMUNICATION 40 P C 50 20 20 P C 06. DIGITAL LABORATORY 25 10 10 P C TW 16. PROCESSOR INTERFACING LABORATORY TW 50 20 38 P C 50 20 07. DIGITAL LABORATORY PR 17. PROCESSOR INTERFACING LABORATORY OR 20 P C 50 20 25 P C 08. PROGRAMMING LABORATORY TW 18. DATA STRUCTURES AND FILES LAB TW 25 10 12 PC 09. PROGRAMMING LABORATORY 50 20 21 P C 50 20 29 P C PR 19. DATA STRUCTURES AND FILES LAB PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 25 P C 50 20 23 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 25 P C 21. OBJECT ORIENTED PROGRAMMING LAB PR GRAND TOTAL = 718/1500, RESULT: PASS CLASS ORDN. 1 MARKS: , 71134970D , S80058682 , PICT , S80058682 S80058682 MULLA ASMA MUSA RABIYA 01. DISCRETE STRUCTURES 100 40 49 P C 11. ENG MATHS III 100 40 40 P 02. COMPUTER ORGANIZATION PP 100 40 40 P C 12. COMPUTER GRAPHICS PP 100 40 40 P C 40 P C 100 40 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 40 P C 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 48 P C 14. DATA STRUCTURES AND FILES 100 40 54 P C 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 44 P C PP 100 40 43 P C 15. DATA COMMUNICATION TW 50 20 06. DIGITAL LABORATORY 38 P C 25 10 21 P C 16. PROCESSOR INTERFACING LABORATORY TW 50 20 22 P C 17. PROCESSOR INTERFACING LABORATORY OR 50 20 29 P C 07. DIGITAL LABORATORY PR 22 P C 08. PROGRAMMING LABORATORY TW 50 20 42 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 50 20 33 P C 50 20 22 P C 09. PROGRAMMING LABORATORY PR 19. DATA STRUCTURES AND FILES LAB PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 37 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 43 P C 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 20 P C GRAND TOTAL = 767/1500, RESULT: SECOND CLASS ORDN. 1 MARKS: 

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71100904L , S80058685 , PICT , S80058685 SUREKHA S80058685 MUTHA VINITA VINOD 01. DISCRETE STRUCTURES 100 40 40 P 100 40 58 P C 11. ENG MATHS III PP 100 40 40 P C 12. COMPUTER GRAPHICS 100 40 51 P C 02. COMPUTER ORGANIZATION PP 100 40 42 P C 100 40 48 P.C 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 13. PROCESSOR ARCHITECTURE & INTER. PP 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 49 P C PP 100 40 50 P C 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 40 P C 100 40 51 P.C 15. DATA COMMUNICATION TW 50 20 32 P C 25 10 13 P C 06. DIGITAL LABORATORY 16. PROCESSOR INTERFACING LABORATORY TW 32 P C 07. DIGITAL LABORATORY 50 20 50 20 35 P C PR 17. PROCESSOR INTERFACING LABORATORY OR 08. PROGRAMMING LABORATORY 50 20 35 P C 25 10 13 P C TW 18. DATA STRUCTURES AND FILES LAB TW 09. PROGRAMMING LABORATORY 50 20 38 P C 50 20 31 P C PR 19. DATA STRUCTURES AND FILES LAB PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 34 P C 50 20 35 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 44 P C GRAND TOTAL = 811/1500, RESULT: SECOND CLASS ORDN. 1 MARKS: , 71100907E , S80058686 , PICT , S80058686 MEENA S80058686 NAIK RASHMI BHARAT PP 01. DISCRETE STRUCTURES 46 P C 11. ENG MATHS III 100 40 41 P 100 40 100 40 59 P C 100 40 02. COMPUTER ORGANIZATION PP 12. COMPUTER GRAPHICS 43 P C 100 40 40 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 40 P C 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 49 P C 100 40 46 P C PP 100 40 48 P C 100 40 05. HUMANITIES AND SOCIAL SCIENCES PP 15. DATA COMMUNICATION 48 P C TW 50 20 38 P C 06. DIGITAL LABORATORY 25 10 18 P C 16. PROCESSOR INTERFACING LABORATORY TW 50 20 22 P C 50 20 07. DIGITAL LABORATORY PR 17. PROCESSOR INTERFACING LABORATORY OR 31 P C 50 20 08. PROGRAMMING LABORATORY TW 34 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 18 P C 09. PROGRAMMING LABORATORY 50 20 38 P C 50 20 20 P C PR 19. DATA STRUCTURES AND FILES LAB PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 41 P C 50 20 40 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 32 P C 21. OBJECT ORIENTED PROGRAMMING LAB PR GRAND TOTAL = 792/1500, RESULT: SECOND CLASS ORDN. 1 MARKS: , 70925526J , S80058688 , PICT , S80058688 S80058688 NILESH D PHADTARE KUNDA 01. DISCRETE STRUCTURES 100 40 43 P C 11. ENG MATHS III 100 40 40 P C 02. COMPUTER ORGANIZATION PP 100 40 50 P C 12. COMPUTER GRAPHICS PP 100 40 43 P C 100 40 25 F 100 40 50 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 40 P C 14. DATA STRUCTURES AND FILES 100 40 72 P.C 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 40 P C PP 100 40 43 P C 15. DATA COMMUNICATION TW 50 20 06. DIGITAL LABORATORY 35 P C 25 10 20 P C 16. PROCESSOR INTERFACING LABORATORY TW 50 20 31 P C 17. PROCESSOR INTERFACING LABORATORY OR 50 20 20 P C 07. DIGITAL LABORATORY PR 23 P C 08. PROGRAMMING LABORATORY TW 50 20 43 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 50 20 32 P C 50 20 25 P C 09. PROGRAMMING LABORATORY PR 19. DATA STRUCTURES AND FILES LAB PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 36 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 44 P C 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 41 P C GRAND TOTAL = 796/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS: 

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71134973」 , S80058689 , PICT , S80058689 MAHERUNISSA S80058689 PATEL NAZNIN JAVED 01. DISCRETE STRUCTURES 100 40 40 PC 100 40 40 P 11. ENG MATHS III PP 100 40 40 P C 12. COMPUTER GRAPHICS 100 40 48 P C 02. COMPUTER ORGANIZATION PP 100 40 45 P C 100 40 40 P C 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 13. PROCESSOR ARCHITECTURE & INTER. PP 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 44 P C PP 100 40 64 P C 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 46 P C 100 40 53 P.C 15. DATA COMMUNICATION TW 50 20 35 P C 25 10 14 P C 06. DIGITAL LABORATORY 16. PROCESSOR INTERFACING LABORATORY TW 07. DIGITAL LABORATORY 50 20 40 P C 50 20 22 P C PR 17. PROCESSOR INTERFACING LABORATORY OR 08. PROGRAMMING LABORATORY 50 20 42 P C 25 10 19 P C TW 18. DATA STRUCTURES AND FILES LAB TW 09. PROGRAMMING LABORATORY 50 20 22 P C 50 20 35 P C PR 19. DATA STRUCTURES AND FILES LAB PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 43 P C 50 20 41 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 35 P C GRAND TOTAL = 808/1500, RESULT: SECOND CLASS ORDN. 1 MARKS: , 71100949L , S80058692 , PICT , S80058692 S80058692 PAWAR NEHA ANIL JAYA PP 40 P C 11. ENG MATHS III 100 40 13 F 01. DISCRETE STRUCTURES 100 40 100 40 45 P C 100 40 02. COMPUTER ORGANIZATION PP 12. COMPUTER GRAPHICS 55 P C 100 40 40 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 40 P C 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 50 P C 100 40 44 P C PP 100 40 51 P C 100 40 05. HUMANITIES AND SOCIAL SCIENCES PP 15. DATA COMMUNICATION 42 P C TW 50 20 35 P C 06. DIGITAL LABORATORY 25 10 12 P C 16. PROCESSOR INTERFACING LABORATORY TW 50 20 50 20 24 P C 07. DIGITAL LABORATORY PR 34 P C 17. PROCESSOR INTERFACING LABORATORY OR 50 20 08. PROGRAMMING LABORATORY TW 34 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 15 P C 09. PROGRAMMING LABORATORY 50 20 37 P C PR 50 20 25 P C 19. DATA STRUCTURES AND FILES LAB PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 38 P C 50 20 33 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 AA F 21. OBJECT ORIENTED PROGRAMMING LAB PR GRAND TOTAL = 707/1500, RESULT: FAILS A.T.K.T. RESULT RESERVED FOR BKLG ORDN. 1 MARKS: , 71100951B , S80058693 , PICT , S80058693 S80058693 PAWAR PRASAD GORAKSH MEENAKSHI 01. DISCRETE STRUCTURES 100 40 43 P C 11. ENG MATHS III 100 40 52 P 02. COMPUTER ORGANIZATION PP 100 40 41 P C 12. COMPUTER GRAPHICS PP 100 40 41 P C 100 40 100 40 41 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 40 P C 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 43 P C 14. DATA STRUCTURES AND FILES 100 40 45 P.C 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 53 P C PP 100 40 40 P C 15. DATA COMMUNICATION TW 50 20 06. DIGITAL LABORATORY 24 P C 25 10 10 P C 16. PROCESSOR INTERFACING LABORATORY TW 50 20 30 P C 17. PROCESSOR INTERFACING LABORATORY OR 50 20 28 P C 07. DIGITAL LABORATORY PR 08. PROGRAMMING LABORATORY TW 50 20 28 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 10 P C 50 20 22 P C 50 20 42 P C 09. PROGRAMMING LABORATORY PR 19. DATA STRUCTURES AND FILES LAB PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 34 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 24 P C 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 37 P C GRAND TOTAL = 728/1500, RESULT: PASS CLASS ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71100963F , S80058694 , PICT , S80058694 PROMILA S80058694 RAHUL SHARMA 100 40 44 P C 01. DISCRETE STRUCTURES 100 40 50 P C 11. ENG MATHS III PP 100 40 44 P C 12. COMPUTER GRAPHICS 100 40 02. COMPUTER ORGANIZATION 100 40 40 P C 100 40 40 P.C 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 13. PROCESSOR ARCHITECTURE & INTER. PP 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 52 P C PP 100 40 40 P C 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 49 P C 100 40 66 P.C 15. DATA COMMUNICATION TW 50 20 21 PC 25 10 06. DIGITAL LABORATORY 16. PROCESSOR INTERFACING LABORATORY TW 10 P C 07. DIGITAL LABORATORY 50 20 23 P C 50 20 20 P C PR 17. PROCESSOR INTERFACING LABORATORY OR 08. PROGRAMMING LABORATORY 50 20 20 P C 25 10 10 P C TW 18. DATA STRUCTURES AND FILES LAB TW 09. PROGRAMMING LABORATORY 50 20 23 P C 50 20 44 P PR 19. DATA STRUCTURES AND FILES LAB PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 31 P C 50 20 22 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 31 P C GRAND TOTAL = 720/1500, RESULT: PASS CLASS ORDN. 1 MARKS: , 71100971G , S80058695 , PICT , S80058695 BIMLA S80058695 RATTAN NAKUL VISHWAS PP 01. DISCRETE STRUCTURES 47 P C 11. ENG MATHS III 100 40 56 P 100 40 100 40 100 40 02. COMPUTER ORGANIZATION PP 41 P C 12. COMPUTER GRAPHICS 44 P C 100 40 53 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 48 P C 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 52 P C 100 40 PP 40 P C 100 40 40 P C 100 40 05. HUMANITIES AND SOCIAL SCIENCES PP 15. DATA COMMUNICATION 49 P C TW 50 20 33 P C 06. DIGITAL LABORATORY 25 10 16 P C 16. PROCESSOR INTERFACING LABORATORY TW 50 20 36 P C 50 20 07. DIGITAL LABORATORY PR 17. PROCESSOR INTERFACING LABORATORY OR 38 P C 50 20 37 P C 08. PROGRAMMING LABORATORY TW 18. DATA STRUCTURES AND FILES LAB TW 25 10 17 P C 09. PROGRAMMING LABORATORY 50 20 39 P C PR 50 20 38 P C 19. DATA STRUCTURES AND FILES LAB PR 50 20 38 P C 50 20 40 P C 10. COMMUNICATION AND LANGUAGE LAB. TW 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 39 P C 21. OBJECT ORIENTED PROGRAMMING LAB PR GRAND TOTAL = 841/1500, RESULT: HIGHER SECOND CLASS ORDN. 1 MARKS: , 71100982B , S80058696 , PICT , S80058696 S80058696 SAHARE NUPUR ARVIND MAMTA 01. DISCRETE STRUCTURES 100 40 40 P C 11. ENG MATHS III 100 40 58 P C 02. COMPUTER ORGANIZATION PP 100 40 54 P C 12. COMPUTER GRAPHICS PP 100 40 53 P C 100 40 46 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 58 P C 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 52 P C 14. DATA STRUCTURES AND FILES 100 40 46 P C 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 43 P C PP 100 40 56 P C 15. DATA COMMUNICATION TW 50 20 06. DIGITAL LABORATORY 34 P C 25 10 17 P C 16. PROCESSOR INTERFACING LABORATORY TW 50 20 22 P C 17. PROCESSOR INTERFACING LABORATORY OR 50 20 30 P C 07. DIGITAL LABORATORY PR 08. PROGRAMMING LABORATORY TW 50 20 38 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 15 P C 50 20 30 P C 50 20 41 P 09. PROGRAMMING LABORATORY PR 19. DATA STRUCTURES AND FILES LAB PR 50 20 39 P C 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 38 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 30 P GRAND TOTAL = 840/1500, RESULT: HIGHER SECOND CLASS ORDN. 1 MARKS: 

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71045598E , S80058697 , PICT , S80058697 POOJA S80058697 SARTHAK MAJITHIA 01. DISCRETE STRUCTURES 100 40 24 F 100 40 63 P C 11. ENG MATHS III PP 100 40 52 P C 12. COMPUTER GRAPHICS 100 40 57 P C 02. COMPUTER ORGANIZATION PP 100 40 62 P C 100 40 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 13. PROCESSOR ARCHITECTURE & INTER. PP 41 P C 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 63 P C PP 100 40 42 P C 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 48 P C 100 40 48 P.C 15. DATA COMMUNICATION TW 50 20 20 P C 25 10 10 P C 06. DIGITAL LABORATORY 16. PROCESSOR INTERFACING LABORATORY TW 07. DIGITAL LABORATORY 50 20 30 P C 50 20 20 P C PR 17. PROCESSOR INTERFACING LABORATORY OR 08. PROGRAMMING LABORATORY 50 20 20 P C 25 10 TW 18. DATA STRUCTURES AND FILES LAB TW 15 P C 09. PROGRAMMING LABORATORY 50 20 48 P C 50 20 43 P C PR 19. DATA STRUCTURES AND FILES LAB PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 30 P C 50 20 20 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 39 P C GRAND TOTAL = 795/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS: , 71101010C , S80058699 , PICT , S80058699 RAJSHRI S80058699 SHIRBHATE ABHILASH DNYANESHWAR PP 01. DISCRETE STRUCTURES 46 P C 11. ENG MATHS III 100 40 40 P 100 40 100 40 40 P C 100 40 02. COMPUTER ORGANIZATION 12. COMPUTER GRAPHICS 43 P C 100 40 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 63 P C 41 P C 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 100 40 48 P C PP 40 P C 100 40 40 P C 100 40 05. HUMANITIES AND SOCIAL SCIENCES PP 15. DATA COMMUNICATION 43 P C TW 50 20 33 P C 25 10 06. DIGITAL LABORATORY 10 P C 16. PROCESSOR INTERFACING LABORATORY TW 50 20 22 P C 50 20 07. DIGITAL LABORATORY PR 17. PROCESSOR INTERFACING LABORATORY OR 36 P C 50 20 08. PROGRAMMING LABORATORY TW 36 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 13 P C 09. PROGRAMMING LABORATORY 50 20 32 P C PR 50 20 27 P C 19. DATA STRUCTURES AND FILES LAB PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 35 P C 50 20 29 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 35 P C 21. OBJECT ORIENTED PROGRAMMING LAB PR GRAND TOTAL = 752/1500, RESULT: SECOND CLASS ORDN. 1 MARKS: , 71101017L , S80058700 , PICT , S80058700 S80058700 SOLANKE BHUSHAN NARENDRA REKHA 01. DISCRETE STRUCTURES 100 40 44 P C 11. ENG MATHS III 100 40 19 F 02. COMPUTER ORGANIZATION PP 100 40 43 P C 12. COMPUTER GRAPHICS 100 40 43 P.C 100 40 40 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 46 P C 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 48 P C 14. DATA STRUCTURES AND FILES 100 40 57 P.C 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 40 P C PP 100 40 46 P C 15. DATA COMMUNICATION TW 50 20 06. DIGITAL LABORATORY 33 P C 25 10 10 P C 16. PROCESSOR INTERFACING LABORATORY TW 50 20 30 P C 17. PROCESSOR INTERFACING LABORATORY OR 50 20 30 P C 07. DIGITAL LABORATORY PR 12 P C 08. PROGRAMMING LABORATORY TW 50 20 31 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 35 P C 50 20 24 P C 50 20 09. PROGRAMMING LABORATORY PR 19. DATA STRUCTURES AND FILES LAB PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 35 P C 50 20 33 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 34 P C GRAND TOTAL = 733/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS:

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71045642F , S80058703 , PICT , S80058703 S80058703 TEJASH KUMAR RITA 01. DISCRETE STRUCTURES 100 40 40 P 11. ENG MATHS III PP 100 40 AA F 100 40 44 P C 12. COMPUTER GRAPHICS 100 40 40 P 02. COMPUTER ORGANIZATION PP 100 40 40 P C 100 40 40 P 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 13. PROCESSOR ARCHITECTURE & INTER. PP 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 40 P C PP 100 40 40 P 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 40 P C 100 40 15. DATA COMMUNICATION TW 50 20 24 P C 25 10 10 P C 06. DIGITAL LABORATORY 16. PROCESSOR INTERFACING LABORATORY TW 07. DIGITAL LABORATORY 50 20 23 P C 50 20 20 P PR 17. PROCESSOR INTERFACING LABORATORY OR 08. PROGRAMMING LABORATORY 50 20 20 P C 25 10 10 P C TW 18. DATA STRUCTURES AND FILES LAB TW 09. PROGRAMMING LABORATORY 50 20 20 P C 50 20 30 P PR 19. DATA STRUCTURES AND FILES LAB PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 33 P C 50 20 27 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 28 P GRAND TOTAL = 613/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS: , 71101047B , S80058705 , PICT , S80058705 VINA S80058705 VICHARE GAURAV GIRISH PP 01. DISCRETE STRUCTURES PP 100 40 11. ENG MATHS III 100 40 59 P C 64 P C 100 40 100 40 02. COMPUTER ORGANIZATION PP 42 P C 12. COMPUTER GRAPHICS 45 P C 100 40 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 46 P C 40 P 14. DATA STRUCTURES AND FILES 100 40 51 P C 100 40 04. FUNDAMENTAL OF DATA STRUCTURES PP PP 46 P C 100 40 40 P C 100 40 05. HUMANITIES AND SOCIAL SCIENCES PP 15. DATA COMMUNICATION 47 P C 50 20 30 P C 06. DIGITAL LABORATORY 25 10 20 P C TW 16. PROCESSOR INTERFACING LABORATORY TW 50 20 30 P C 50 20 07. DIGITAL LABORATORY PR 17. PROCESSOR INTERFACING LABORATORY OR 36 P C 50 20 08. PROGRAMMING LABORATORY TW 28 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 15 P C 09. PROGRAMMING LABORATORY 50 20 30 P C PR 50 20 25 P C 19. DATA STRUCTURES AND FILES LAB PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 38 P C 50 20 41 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 24 P C 21. OBJECT ORIENTED PROGRAMMING LAB PR GRAND TOTAL = 797/1500, RESULT: SECOND CLASS ORDN. 1 MARKS: , 71101051L , S80058706 , PICT , S80058706 S80058706 VIRWANI SUNNY JAMANLAL KIRTI 01. DISCRETE STRUCTURES 100 40 40 P C 11. ENG MATHS III 100 40 21 F PP 02. COMPUTER ORGANIZATION PP 100 40 47 P C 12. COMPUTER GRAPHICS 100 40 40 P C 100 40 43 P 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 45 P 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 45 P C 14. DATA STRUCTURES AND FILES 100 40 41 P.C 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 42 P C PP 100 40 56 P C 15. DATA COMMUNICATION TW 50 20 06. DIGITAL LABORATORY 28 P C 25 10 10 P C 16. PROCESSOR INTERFACING LABORATORY TW 50 20 34 P C 17. PROCESSOR INTERFACING LABORATORY OR 50 20 30 P C 07. DIGITAL LABORATORY PR 11 P C 08. PROGRAMMING LABORATORY TW 50 20 33 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 21 P C 50 20 27 P C 50 20 09. PROGRAMMING LABORATORY PR 19. DATA STRUCTURES AND FILES LAB PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 34 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 30 P C 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 33 P C GRAND TOTAL = 711/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS: 

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71101052J , S80058707 , PICT , S80058707 S80058707 WADILE NITIN ADHAR SUMAN 01. DISCRETE STRUCTURES PP 100 40 40 P C 100 40 51 P C 11. ENG MATHS III 100 40 46 P C 12. COMPUTER GRAPHICS 100 40 41 P C 02. COMPUTER ORGANIZATION PP 100 40 40 P C 100 40 29 F 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 13. PROCESSOR ARCHITECTURE & INTER. PP 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 48 P C PP 100 40 51 P C 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 40 P C 100 40 40 P.C 15. DATA COMMUNICATION TW 50 20 35 P C 25 10 10 P C 06. DIGITAL LABORATORY 16. PROCESSOR INTERFACING LABORATORY TW 07. DIGITAL LABORATORY 50 20 20 P C 50 20 34 P C PR 17. PROCESSOR INTERFACING LABORATORY OR 08. PROGRAMMING LABORATORY 50 20 37 P C 25 10 12 P C TW 18. DATA STRUCTURES AND FILES LAB TW 09. PROGRAMMING LABORATORY 50 20 20 P C 50 20 38 P C PR 19. DATA STRUCTURES AND FILES LAB PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 37 P C 50 20 32 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 21 P C GRAND TOTAL = 722/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS: , 70925337M , S80058709 , PICT , S80058709 S80058709 ANUSHKA GHOGALE NIVEDITA PP 40 P 11. ENG MATHS III 100 40 03 F 01. DISCRETE STRUCTURES 100 40 100 40 40 P C 100 40 02. COMPUTER ORGANIZATION PP 12. COMPUTER GRAPHICS 42 P 100 40 46 P 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 40 P 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 40 P C 100 40 40 P C PP 100 40 40 P C 100 40 05. HUMANITIES AND SOCIAL SCIENCES PP 15. DATA COMMUNICATION 42 P C TW 50 20 23 P C 25 10 06. DIGITAL LABORATORY 16 P C 16. PROCESSOR INTERFACING LABORATORY TW 50 20 32 P C 50 20 25 P C 07. DIGITAL LABORATORY PR 17. PROCESSOR INTERFACING LABORATORY OR 50 20 08. PROGRAMMING LABORATORY TW 27 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 12 PC 09. PROGRAMMING LABORATORY 50 20 20 P C 50 20 20 P C PR 19. DATA STRUCTURES AND FILES LAB PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 31 P C 50 20 34 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 30 P 21. OBJECT ORIENTED PROGRAMMING LAB PR GRAND TOTAL = 643/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS: , 71100934B , S80058710 , PICT , S80058710 S80058710 PATHAK ANUJA PRAVIN UJJWALA 01. DISCRETE STRUCTURES 100 40 40 P C 11. ENG MATHS III 100 40 12 F 02. COMPUTER ORGANIZATION PP 100 40 40 P C 12. COMPUTER GRAPHICS PP 100 40 40 P C 100 40 43 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 40 P C 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 48 P C 14. DATA STRUCTURES AND FILES 100 40 47 P C 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 55 P C PP 100 40 48 P C 15. DATA COMMUNICATION TW 50 20 06. DIGITAL LABORATORY 34 P C 25 10 16 P C 16. PROCESSOR INTERFACING LABORATORY TW 50 20 30 P C 17. PROCESSOR INTERFACING LABORATORY OR 50 20 38 P C 07. DIGITAL LABORATORY PR 08. PROGRAMMING LABORATORY TW 50 20 29 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 16 PC 50 20 20 P C 50 20 26 P C 09. PROGRAMMING LABORATORY PR 19. DATA STRUCTURES AND FILES LAB PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 36 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 36 P C 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 27 P C GRAND TOTAL = 721/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS:

DATE: 27 JULY 2013 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 63 (576)

NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER , 71045622M , S80058711 , PICT , S80058711 MADHURI S80058711 SHUKLA MANISH NITYANAND 01. DISCRETE STRUCTURES 100 40 57 P C 11. ENG MATHS III PP 100 40 05 F PP 100 40 44 P C 12. COMPUTER GRAPHICS 100 40 40 P C 02. COMPUTER ORGANIZATION PP 100 40 57 P C 100 40 51 P.C 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 13. PROCESSOR ARCHITECTURE & INTER. PP 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 49 P C 100 40 40 P C PP 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 45 P C 100 40 45 P.C 15. DATA COMMUNICATION TW 50 20 28 P C 25 10 12 P C 06. DIGITAL LABORATORY 16. PROCESSOR INTERFACING LABORATORY TW 07. DIGITAL LABORATORY 50 20 30 P C 50 20 21 P C PR 17. PROCESSOR INTERFACING LABORATORY OR 08. PROGRAMMING LABORATORY 50 20 28 P C 25 10 11 P C TW 18. DATA STRUCTURES AND FILES LAB TW 09. PROGRAMMING LABORATORY 50 20 32 P C 50 20 28 P C PR 19. DATA STRUCTURES AND FILES LAB PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 31 P C 50 20 27 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 37 P C GRAND TOTAL = 718/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS: , 71101028F , S80058712 , PICT , S80058712 SHALINEE S80058712 TEKE AMRUTA PRAKASH PP 01. DISCRETE STRUCTURES 100 40 47 P C 100 40 43 P 11. ENG MATHS III 100 40 47 P C 100 40 54 P C 02. COMPUTER ORGANIZATION PP 12. COMPUTER GRAPHICS 100 40 42 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 44 P C 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 14. DATA STRUCTURES AND FILES 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 41 P C 100 40 53 P C PP 100 40 40 P C 100 40 54 P C 05. HUMANITIES AND SOCIAL SCIENCES PP 15. DATA COMMUNICATION 50 20 34 P C 06. DIGITAL LABORATORY 25 10 17 P C TW 16. PROCESSOR INTERFACING LABORATORY TW 50 20 22 P C 50 20 07. DIGITAL LABORATORY PR 17. PROCESSOR INTERFACING LABORATORY OR 28 P C 50 20 31 P C 08. PROGRAMMING LABORATORY TW 18. DATA STRUCTURES AND FILES LAB TW 25 10 14 P C 09. PROGRAMMING LABORATORY 50 20 26 P C PR 50 20 20 P C 19. DATA STRUCTURES AND FILES LAB PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 40 P C 50 20 39 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 30 P C 21. OBJECT ORIENTED PROGRAMMING LAB PR GRAND TOTAL = 766/1500, RESULT: SECOND CLASS ORDN. 1 MARKS: , 71045526н , , , РІСТ , S80058717 S80058717 MOHAMMED AAQUIB ANSARI MOHAMMED YUSUF SUFIYA 01. DISCRETE STRUCTURES 100 40 43 P C 11. ENG MATHS III 100 40 40 P C 02. COMPUTER ORGANIZATION PP 100 40 41 P C 12. COMPUTER GRAPHICS PP 100 40 50 P C 100 40 45 P C 13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 40 P C 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40 40 P C 14. DATA STRUCTURES AND FILES 100 40 73 P.C 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40 40 P C PP 100 40 45 P C 15. DATA COMMUNICATION TW 50 20 06. DIGITAL LABORATORY 20 P C 25 10 10 P C 16. PROCESSOR INTERFACING LABORATORY TW 50 20 17. PROCESSOR INTERFACING LABORATORY OR 50 20 07. DIGITAL LABORATORY PR AA F AA F 13 P C 08. PROGRAMMING LABORATORY TW 50 20 20 P C 18. DATA STRUCTURES AND FILES LAB TW 25 10 39 P C 50 20 35 P C 50 20 09. PROGRAMMING LABORATORY PR 19. DATA STRUCTURES AND FILES LAB PR 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20 P C 20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 20 P C 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 20 P C GRAND TOTAL = 654/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS:

UNIVERSITY OF PUNE ,S.E.(2008 PAT.)(INFORMATION TECHNOLOGY)

DATE : 27 JULY 2013	CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.								64	( 5	77)
NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.  OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER											
	 R			 ME	 ENAKSHI		 , PI	 CT		 58005	 8718
01. DISCRETE STRUCTURES	PP	100	40	51	РС	11. ENG MATHS III	PP	100	40	AA	F
02. COMPUTER ORGANIZATION	PP	100	40	40	РС	12. COMPUTER GRAPHICS	PP	100	40	AA	F
03. DIGITAL ELECTRONICS & LOGIC DES	IGPP	100	40	50	РС	13. PROCESSOR ARCHITECTURE & INTER	. PP	100	40	AA	F
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	45	P C	14. DATA STRUCTURES AND FILES	PP	100	40	40	P C
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	40	P C	15. DATA COMMUNICATION	PP	100	40	AA	F
06. DIGITAL LABORATORY	TW	50	20	35	P C	16. PROCESSOR INTERFACING LABORATO	RY TW	25	10	11	P C
07. DIGITAL LABORATORY	PR	50	20	20	PC	17. PROCESSOR INTERFACING LABORATO	RY OR	50	20	AA	F
08. PROGRAMMING LABORATORY	TW	50	20	33	PC	18. DATA STRUCTURES AND FILES LAB	TW	25	10	15	PC
09. PROGRAMMING LABORATORY	PR	50	20	20	PC	19. DATA STRUCTURES AND FILES LAB	PR	50	20	27	PC
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	40	PC	20. OBJECT ORIENTED PROGRAMMING LA	B TW	50	20	41	PC
						21. OBJECT ORIENTED PROGRAMMING LA	B PR	50	20	AA	F
GRAND TOTAL = 508/1500, RESULT: FAILS											
ORDN. 1 MARKS :											