UNIVERSITY OF PUNE ,T.E.(2008 PAT.)(ELECTRONICS & TELEC.) EXAMINATION NOV. 2011 DATE : 02 MAR. 2012 CENTRÉ : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 01 (370) NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER T8053001 ABDULE MONALI MUKUND SANDHYA , 71045354L , , PICT , T8053001 01. CONTROL SYSTEMS PP 100 40 48 P 02. DIGITAL COMMUNICATION PP 100 40 49 P 03. DIGITAL COMMUNICATION PR 50 20 28 P 40 53 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 Р 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 41 06. MICROCONTROLLERS & APPLICATION PP 100 40 47 07. MICROCONTROLLERS & APPLICATION PR 50 20 32 08. DIGITAL SIGNAL PROCESSING PP 100 40 42 09. DIGITAL SIGNAL PROCESSING OR 50 20 30 P 10. ELECTRONIC DESIGN PRACTICE OR 50 20 25 P FIRST TERM TOTAL = 395/750. ORDN. 1 MARKS : T8053002 ABHINIT KUMAR ASHA DEVI , 71050778L , , PICT , T8053002 01. CONTROL SYSTEMS PP 100 40 75 P 02. DIGITAL COMMUNICATION PP 100 40 47 P 03. DIGITAL COMMUNICATION PR 50 20 33 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 55 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 20 37 P 50 06. MICROCONTROLLERS & APPLICATION PP 100 40 46 P 07. MICROCONTROLLERS & APPLICATION PR 50 20 33 P 08. DIGITAL SIGNAL PROCESSING PP 100 40 63 P 09. DIGITAL SIGNAL PROCESSING OR 50 20 39 P 10. ELECTRONIC DESIGN PRACTICE OR 50 20 20 P FIRST TERM TOTAL = 448/750. ORDN. 1 MARKS : INDRANI , 71045356G , , PICT , T8053003 T8053003 ABHISHEK ANAND

 01. CONTROL SYSTEMS
 PP
 100
 40
 63
 P

 02. DIGITAL COMMUNICATION
 PP
 100
 40
 40
 P

 03. DIGITAL COMMUNICATION
 PR
 50
 20
 33
 P

 100 40 51 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 20 36 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 06. MICROCONTROLLERS & APPLICATION PP 100 40 40 P 07. MICROCONTROLLERS & APPLICATION PR 50 20 25 P 08. DIGITAL SIGNAL PROCESSING PP
09. DIGITAL SIGNAL PROCESSING OR
10. ELECTRONIC DESIGN PRACTICE OR 100 37 OR 50 20 50 20 25 P FIRST TERM TOTAL = 402/750. ORDN. 1 MARKS:

UNIVERSITY OF PUNE ,T.E.(2008 PAT.)(ELECTRONICS & TELEC.) EXAMINATION NOV. 2011 DATE : 02 MAR. 2012 CENTRÉ : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 02 (371) NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER T8053004 AGARWAL VINIT SHARAD SANTOSH , 70925321E , , PICT , T8053004 01. CONTROL SYSTEMS PP 100 40 02. DIGITAL COMMUNICATION PP 100 40 03. DIGITAL COMMUNICATION PR 50 20 55 P 20 39 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 66 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 38 06. MICROCONTROLLERS & APPLICATION PP 100 40 58 P 07. MICROCONTROLLERS & APPLICATION PR 50 20 40 P 08. DIGITAL SIGNAL PROCESSING PP 100 40 63 P 09. DIGITAL SIGNAL PROCESSING OR 50 20 38 P 10. ELECTRONIC DESIGN PRACTICE OR 50 20 28 P FIRST TERM TOTAL = 490/750. ORDN. 1 MARKS : T8053005 AMIT KUMAR RITA , 71045367B , , PICT , T8053005 01. CONTROL SYSTEMS PP 100
02. DIGITAL COMMUNICATION PP 100
03. DIGITAL COMMUNICATION PR 50 40 49 P 20 39 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 61 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 20 31 P 50 06. MICROCONTROLLERS & APPLICATION PP 100 40 07. MICROCONTROLLERS & APPLICATION PR 50 20 32 08. DIGITAL SIGNAL PROCESSING PP 100 40 09. DIGITAL SIGNAL PROCESSING OR 50 20 10. ELECTRONIC DESIGN PRACTICE OR 50 20 PP 100 40 72 20 34 FIRST TERM TOTAL = 471/750. ORDN. 1 MARKS: , 71045368L , , PICT , T8053006 T8053006 ANAGH PAL MANDIRA 01. CONTROL SYSTEMS PP 100 40 58 P 02. DIGITAL COMMUNICATION PP 100 40 50 P 03. DIGITAL COMMUNICATION PR 50 20 38 P 100 40 55 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 20 36 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 06. MICROCONTROLLERS & APPLICATION PP 100 07. MICROCONTROLLERS & APPLICATION PR 50 20 35 P 08. DIGITAL SIGNAL PROCESSING PP
09. DIGITAL SIGNAL PROCESSING OR
10. ELECTRONIC DESIGN PRACTICE OR 100 OR 50 20 34 50 20 07 F FIRST TERM TOTAL = 424/750.

ORDN. 1 MARKS:

UNIVERSITY OF PUNE ,T.E.(2008 PAT.)(ELECTRONICS & TELEC.) EXAMINATION NOV. 2011 DATE : 02 MAR. 2012 CENTRÉ : PUNE INSTITÚTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 03 (372) NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER T8053007 ANANNYA MAZUMDER NEELA , 71045369J , , PICT , T8053007 01. CONTROL SYSTEMS PP 100 40 61 P 02. DIGITAL COMMUNICATION PP 100 40 52 P 03. DIGITAL COMMUNICATION PR 50 20 40 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 75 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 41 Ρ 06. MICROCONTROLLERS & APPLICATION PP 100 40 52 07. MICROCONTROLLERS & APPLICATION PR 50 20 25 08. DIGITAL SIGNAL PROCESSING PP 100 40 60 P 09. DIGITAL SIGNAL PROCESSING OR 50 20 20 P 10. ELECTRONIC DESIGN PRACTICE OR 50 20 37 P FIRST TERM TOTAL = 463/750. ORDN. 1 MARKS : HASINA , 71129924C , , PICT , T8053008 T8053008 ATTAR BENAZIR MEHBOOB 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 62 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 20 42 50 06. MICROCONTROLLERS & APPLICATION PP 100 40 55 P 07. MICROCONTROLLERS & APPLICATION PR 50 20 42 P 08. DIGITAL SIGNAL PROCESSING PP 100 40 63 P 09. DIGITAL SIGNAL PROCESSING OR 50 20 39 P 10. ELECTRONIC DESIGN PRACTICE OR 50 20 39 P FIRST TERM TOTAL = 494/750. ORDN. 1 MARKS : URMILA , 71045379F , , PICT , T8053009 T8053009 AWASARE VRUSHALI VALMIK 01. CONTROL SYSTEMS PP 100 40 55 P 02. DIGITAL COMMUNICATION PP 100 40 43 P 03. DIGITAL COMMUNICATION PR 50 20 29 P 100 40 54 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 20 42 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 06. MICROCONTROLLERS & APPLICATION PP 100 40 40 P 07. MICROCONTROLLERS & APPLICATION PR 50 20 20 P 08. DIGITAL SIGNAL PROCESSING PP
09. DIGITAL SIGNAL PROCESSING OR
10. ELECTRONIC DESIGN PRACTICE OR 100 30 or 50 20 50 20 36 P FIRST TERM TOTAL = 389/750. ORDN. 1 MARKS:

DATE : 02 MAR. 2012 CENT	RÉ : PUNE :	INSTITUTE OF COMPUTER	•	PAGE NO. 04 (373)
NOTE: FIRST LINE : SEAT NO., NAME OF THE OTHER LINES: HEAD OF PASSING, MAX.	CANDIDATE	, MOTHER, PERMANENT IN. PASS MARKS, MARK	REG. NO., PREVIOUS SE	EAT NO., COLLEGE, SEAT NO.
T8053010 BAGMAR RITESH SURESH		ANITA	, 71045381н ,	, PICT , T8053010
01. CONTROL SYSTEMS PP 02. DIGITAL COMMUNICATION PP 03. DIGITAL COMMUNICATION PR 04. NETWORK SYNTHESIS & FILTER DESIGNPP 05. NETWORK SYNTHESIS & FILTER DESIGNTW 06. MICROCONTROLLERS & APPLICATION PP 07. MICROCONTROLLERS & APPLICATION PR 08. DIGITAL SIGNAL PROCESSING PP 09. DIGITAL SIGNAL PROCESSING OR 10. ELECTRONIC DESIGN PRACTICE OR	100 40 100 40 50 20 100 40 50 20 100 40 50 20 100 40 50 20 50 20 50 20	62 P 54 P 30 P 71 P 39 P 56 P 30 P 68 P 44 P 33 P		
FIRST TERM TOTAL = 487/750. ORDN. 1 MARKS :				
T8053011 BAHETI BHAKTI VILAS	100 10	PADMA	, 71045382F ,	, PICT , T8053011
01. CONTROL SYSTEMS PP 02. DIGITAL COMMUNICATION PP 03. DIGITAL COMMUNICATION PR 04. NETWORK SYNTHESIS & FILTER DESIGNPP 05. NETWORK SYNTHESIS & FILTER DESIGNTW 06. MICROCONTROLLERS & APPLICATION PP 07. MICROCONTROLLERS & APPLICATION PR 08. DIGITAL SIGNAL PROCESSING PP 09. DIGITAL SIGNAL PROCESSING OR 10. ELECTRONIC DESIGN PRACTICE OR	100 40 100 40 50 20 100 40 50 20 100 40 50 20 100 40 50 20 50 20	65 P 47 P 45 P 75 P 42 P 51 P 42 P 61 P 35 P 32 P		
FIRST TERM TOTAL = 495/750. ORDN. 1 MARKS :				
T8053012 BANSAL SHRUTI SANJAY		MEENA	, 71129925м ,	, PICT , T8053012
01. CONTROL SYSTEMS PP 02. DIGITAL COMMUNICATION PP 03. DIGITAL COMMUNICATION PR 04. NETWORK SYNTHESIS & FILTER DESIGNPP 05. NETWORK SYNTHESIS & FILTER DESIGNTW 06. MICROCONTROLLERS & APPLICATION PP 07. MICROCONTROLLERS & APPLICATION PR 08. DIGITAL SIGNAL PROCESSING PP 09. DIGITAL SIGNAL PROCESSING OR 10. ELECTRONIC DESIGN PRACTICE OR	100 40 100 40 50 20 100 40 50 20 100 40 50 20 100 40 50 20 20	63 P 53 P 37 P 53 P 44 P 55 P 42 P 73 P 36 P 28 P		
FIRST TERM TOTAL = 484/750. ORDN. 1 MARKS :				

UNIVERSITY OF PUNE ,T.E.(2008 PAT.)(ELECTRONICS & TELEC.) EXAMINATION NOV. 2011 DATE : 02 MAR. 2012 CENTRÉ : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 05 (374) NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER T8053013 BHAGAT ABHILASHA RAMDAS MADHURI , 71045386J , , PICT , T8053013 01. CONTROL SYSTEMS PP
02. DIGITAL COMMUNICATION PP
03. DIGITAL COMMUNICATION PR 01. CONTROL SYSTEMS PP 100 40 50 P 20 20 P 50 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 48 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 39 P 06. MICROCONTROLLERS & APPLICATION PP 100 40 41 P 07. MICROCONTROLLERS & APPLICATION PR 50 20 10 F 08. DIGITAL SIGNAL PROCESSING PP 100 40 56 P 09. DIGITAL SIGNAL PROCESSING OR 50 20 32 P 10. ELECTRONIC DESIGN PRACTICE OR 50 20 32 P FIRST TERM TOTAL = 349/750. ORDN. 1 MARKS : T8053014 BHAT TUSHAR GOVIND VEENA , 71045388E , , PICT , T8053014 01. CONTROL SYSTEMS PP 100 40 57 P 02. DIGITAL COMMUNICATION PP 100 40 50 P 03. DIGITAL COMMUNICATION PR 50 20 35 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 75 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 20 34 P 50 06. MICROCONTROLLERS & APPLICATION PP 100 40 55 P 07. MICROCONTROLLERS & APPLICATION PR 50 20 32 P 08. DIGITAL SIGNAL PROCESSING PP 100 40 72 09. DIGITAL SIGNAL PROCESSING OR 50 20 30 10. ELECTRONIC DESIGN PRACTICE OR 50 20 32 20 30 P FIRST TERM TOTAL = 472/750. ORDN. 1 MARKS: MADHAVI , 71045392C , , PICT , T8053015 T8053015 BHOITE AJINKYA CHANDRAKANT 01. CONTROL SYSTEMS PP 100 40 63 P 02. DIGITAL COMMUNICATION PP 100 40 40 P 03. DIGITAL COMMUNICATION PR 50 20 26 P 100 40 61 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 20 40 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 06. MICROCONTROLLERS & APPLICATION PP 100 07. MICROCONTROLLERS & APPLICATION PR 50 20 31 P 08. DIGITAL SIGNAL PROCESSING PP
09. DIGITAL SIGNAL PROCESSING OR
10. ELECTRONIC DESIGN PRACTICE OR 100 40 71 OR 50 20 30 50 20 30 P FIRST TERM TOTAL = 451/750. ORDN. 1 MARKS:

UNIVERSITY OF PUNE ,T.E.(2008 PAT.)(ELECTRONICS & TELEC.) EXAMINATION NOV. 2011 DATE : 02 MAR. 2012 CENTRÉ : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 06 (375) NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER T8053016 BHOSALE PRASHANT DNYANADEO RANJANA , 71045393M , , PICT , T8053016 01. CONTROL SYSTEMS PP
02. DIGITAL COMMUNICATION PP
03. DIGITAL COMMUNICATION PR 01. CONTROL SYSTEMS PP 100 40 45 P 20 32 P 50 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 61 Р 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 32 06. MICROCONTROLLERS & APPLICATION PP 100 40 46 P 07. MICROCONTROLLERS & APPLICATION PR 50 20 36 P 08. DIGITAL SIGNAL PROCESSING PP 100 40 60 P 09. DIGITAL SIGNAL PROCESSING OR 50 20 22 P 10. ELECTRONIC DESIGN PRACTICE OR 50 20 06 F FIRST TERM TOTAL = 399/750. ORDN. 1 MARKS : DHANASHREE , 71045396F , , , PICT , T8053017 T8053017 BICHU TANMAY NITIN

 01. CONTROL SYSTEMS
 PP
 100
 40
 75
 P

 02. DIGITAL COMMUNICATION
 PP
 100
 40
 58
 P

 03. DIGITAL COMMUNICATION
 PR
 50
 20
 45
 P

 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 71 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 20 41 P 50 06. MICROCONTROLLERS & APPLICATION PP 100 40 68 P 07. MICROCONTROLLERS & APPLICATION PR 50 20 41 P 08. DIGITAL SIGNAL PROCESSING PP 100 40 78 P 09. DIGITAL SIGNAL PROCESSING OR 50 20 44 P 10. ELECTRONIC DESIGN PRACTICE OR 50 20 45 P FIRST TERM TOTAL = 566/750. ORDN. 1 MARKS: , 71045406G , , PICT , T8053018 T8053018 CHAVAN TRUPTI GANESH LATA 01. CONTROL SYSTEMS PP 100 40 46 P 02. DIGITAL COMMUNICATION PP 100 40 51 P 03. DIGITAL COMMUNICATION PR 50 20 39 P 100 40 67 04. NETWORK SYNTHESIS & FILTER DESIGNPP 20 46 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 06. MICROCONTROLLERS & APPLICATION PP 100 07. MICROCONTROLLERS & APPLICATION PR 50 20 40 P 08. DIGITAL SIGNAL PROCESSING PP
09. DIGITAL SIGNAL PROCESSING OR
10. ELECTRONIC DESIGN PRACTICE OR 100 OR 50 20 35 50 20 39 P FIRST TERM TOTAL = 473/750. ORDN. 1 MARKS:

UNIVERSITY OF PUNE ,T.E.(2008 PAT.)(ELECTRONICS & TELEC.) EXAMINATION NOV. 2011 DATE : 02 MAR. 2012 CENTRÉ : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 07 (376) NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER T8053019 DABHADE ROHIDAS RAGHUNATH BEBEE , 71045409M , , PICT , T8053019 01. CONTROL SYSTEMS PP
02. DIGITAL COMMUNICATION PP
03. DIGITAL COMMUNICATION PR 01. CONTROL SYSTEMS PP 100 40 40 P 20 37 P 50 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 34 06. MICROCONTROLLERS & APPLICATION PP 100 40 50 P 07. MICROCONTROLLERS & APPLICATION PR 50 20 35 08. DIGITAL SIGNAL PROCESSING PP 100 40 61 P 09. DIGITAL SIGNAL PROCESSING OR 50 20 33 P 10. ELECTRONIC DESIGN PRACTICE OR 50 20 P FIRST TERM TOTAL = 429/750. ORDN. 1 MARKS : T8053020 DESAI SHWETA ARUN ANJANI , 71129926K , , PICT , T8053020

 01. CONTROL SYSTEMS
 PP
 100
 40
 65
 P

 02. DIGITAL COMMUNICATION
 PP
 100
 40
 46
 P

 03. DIGITAL COMMUNICATION
 PR
 50
 20
 35
 P

 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 61 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 20 44 P 50 06. MICROCONTROLLERS & APPLICATION PP 100 40 45 P 07. MICROCONTROLLERS & APPLICATION PR 50 20 34 P 08. DIGITAL SIGNAL PROCESSING PP 100 40 63 P 09. DIGITAL SIGNAL PROCESSING OR 50 20 30 P 10. ELECTRONIC DESIGN PRACTICE OR 50 20 22 P FIRST TERM TOTAL = 445/750. ORDN. 1 MARKS: MADHURI , 71129927H , , PICT , T8053021 T8053021 DHANNE ANJALI BASAVRAJ 01. CONTROL SYSTEMS PP 100 40 70 P 02. DIGITAL COMMUNICATION PP 100 40 43 P 03. DIGITAL COMMUNICATION PR 50 20 37 P 100 40 70 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 20 41 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 06. MICROCONTROLLERS & APPLICATION PP 100 07. MICROCONTROLLERS & APPLICATION PR 50 20 42 P 08. DIGITAL SIGNAL PROCESSING PP
09. DIGITAL SIGNAL PROCESSING OR
10. ELECTRONIC DESIGN PRACTICE OR 100 OR 50 20 08 50 20 22 P FIRST TERM TOTAL = 456/750. ORDN. 1 MARKS:

	.(2008 PAT.)(ELECTRONICS & Tennic Ten)11 PAGE NO. 08 (377)
NOTE: FIRST LINE : SEAT NO., NAME OF THE CAND OTHER LINES: HEAD OF PASSING, MAX. MARK	IDATE, MOTHER, PERMANENT RI	EG. NO., PREVIOUS SEAT NO.	, COLLEGE, SEAT NO.
T8053022 DHIKALE JITENDRA BABAN	USHA	, 71045414H ,	
01. CONTROL SYSTEMS PP 100 02. DIGITAL COMMUNICATION PP 100 03. DIGITAL COMMUNICATION PR 50 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 06. MICROCONTROLLERS & APPLICATION PP 100 07. MICROCONTROLLERS & APPLICATION PR 50 08. DIGITAL SIGNAL PROCESSING PP 100 09. DIGITAL SIGNAL PROCESSING OR 50 10. ELECTRONIC DESIGN PRACTICE OR 50	40 59 P 40 58 P 20 39 P 40 62 P 20 39 P 40 53 P 20 37 P 40 79 P 20 39 P 20 20 P		
FIRST TERM TOTAL = 485/750. ORDN. 1 MARKS :			
T8053023 DIGHE GANESH DATTATRAY 01. CONTROL SYSTEMS PP 100 02. DIGITAL COMMUNICATION PP 100 03. DIGITAL COMMUNICATION PR 50 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 06. MICROCONTROLLERS & APPLICATION PP 100 07. MICROCONTROLLERS & APPLICATION PR 50 08. DIGITAL SIGNAL PROCESSING PP 100 09. DIGITAL SIGNAL PROCESSING OR 50 10. ELECTRONIC DESIGN PRACTICE OR 50	SHOBHA 40 66 P 40 54 P 20 22 P 40 59 P 20 36 P 40 54 P 20 34 P 40 58 P 20 34 P 20 34 P 20 22 P	, 71045417в ,	, PICT , T8053023
FIRST TERM TOTAL = 439/750. ORDN. 1 MARKS :			
T8053024 FIRAKE PARESH RAVINDRA	LAXMI	, 71045425C ,	, PICT , T8053024
01. CONTROL SYSTEMS PP 100 02. DIGITAL COMMUNICATION PP 100 03. DIGITAL COMMUNICATION PR 50 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 06. MICROCONTROLLERS & APPLICATION PP 100 07. MICROCONTROLLERS & APPLICATION PR 50 08. DIGITAL SIGNAL PROCESSING PP 100 09. DIGITAL SIGNAL PROCESSING OR 50 10. ELECTRONIC DESIGN PRACTICE OR 50 FIRST TERM TOTAL = 508/750.	40 69 P 40 59 P 20 45 P 40 67 P 20 47 P 40 49 P 20 35 P 40 73 P 20 40 P 20 24 P		
ORDN. 1 MARKS :			

DATE: 02 MAR. 2012 CENTRE	: PUNE INSTITUTE OF C	ONICS & TELEC.) EXAMINATION NOV. OMPUTER TECHNOLOGY, PUNE.	PAGE NO. 09 (378)
NOTE: FIRST LINE : SEAT NO., NAME OF THE CA OTHER LINES: HEAD OF PASSING, MAX. MA	NDIDATE, MOTHER, PER	, MARKS OBTAINÉD, P/F:PASS/FAII	NO., COLLEGE, SEAT NO. L, C:PREVIOUS CARRY OVER
T8053025 GADHE GANESH VITTHALRAO	KEVALBAI	, 71045427к ,	, PICT , T8053025
01. CONTROL SYSTEMS PP 10 02. DIGITAL COMMUNICATION PP 10 03. DIGITAL COMMUNICATION PR 50 04. NETWORK SYNTHESIS & FILTER DESIGNPP 10 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 06. MICROCONTROLLERS & APPLICATION PP 10 07. MICROCONTROLLERS & APPLICATION PR 08. DIGITAL SIGNAL PROCESSING PP 10 09. DIGITAL SIGNAL PROCESSING OR 50 10. ELECTRONIC DESIGN PRACTICE OR 50	0 40 40 P 0 20 36 P 0 40 47 P 0 20 35 P 0 40 45 P 0 20 38 P 0 40 57 P 0 20 35 P		
FIRST TERM TOTAL = 383/750. ORDN. 1 MARKS :			
T8053026 GAVNEKAR SHREYAS SANJIV		, 71045435L ,	
01. CONTROL SYSTEMS PP 100 02. DIGITAL COMMUNICATION PP 100 03. DIGITAL COMMUNICATION PR 50 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 06. MICROCONTROLLERS & APPLICATION PP 100 07. MICROCONTROLLERS & APPLICATION PR 50 08. DIGITAL SIGNAL PROCESSING PP 100 09. DIGITAL SIGNAL PROCESSING OR 50 10. ELECTRONIC DESIGN PRACTICE OR 50	0 40 43 P 0 20 31 P 0 40 56 P 0 20 31 P 0 40 47 P 0 20 12 F 0 40 57 P 0 20 30 P		
FIRST TERM TOTAL = 378/750. ORDN. 1 MARKS :			
T8053027 GHOGARE SAYALI RAJESH	SUJATA	, 71129928F ,	, PICT , T8053027
01. CONTROL SYSTEMS PP 100 02. DIGITAL COMMUNICATION PP 100 03. DIGITAL COMMUNICATION PR 50 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 05. NETWORK SYNTHESIS & FILTER DESIGNTW 500 06. MICROCONTROLLERS & APPLICATION PP 100 07. MICROCONTROLLERS & APPLICATION PR 500 08. DIGITAL SIGNAL PROCESSING PP 100 09. DIGITAL SIGNAL PROCESSING OR 500 10. ELECTRONIC DESIGN PRACTICE OR 500 FIRST TERM TOTAL = 391/750.	0 40 46 P 0 20 30 P 0 40 56 P 0 20 40 P 0 40 40 P 0 20 43 P 0 40 52 P 0 20 20 P		
ORDN. 1 MARKS :			

DATE : 02 MAR. 2012 CEN	TRE : PUNE	INSTITUTE OF COMPUTE		PAGE NO. 10 (379)
NOTE: FIRST LINE : SEAT NO., NAME OF TH OTHER LINES: HEAD OF PASSING, MAX	E CANDIDAT . MARKS,	E, MOTHER, PERMANEN ⁻ MIN. PASS MARKS, MAI	REG. NO., PREVIOUS SEAT RKS OBTAINED, P/F:PASS/FA	NO., COLLEGE, SEAT NO. IL, C:PREVIOUS CARRY OVER
T8053028 GHONE PRASAD SHARAD		HEMLATA	, 71045437G ,	
01. CONTROL SYSTEMS PP 02. DIGITAL COMMUNICATION PP 03. DIGITAL COMMUNICATION PR 04. NETWORK SYNTHESIS & FILTER DESIGNPP 05. NETWORK SYNTHESIS & FILTER DESIGNTW 06. MICROCONTROLLERS & APPLICATION PP 07. MICROCONTROLLERS & APPLICATION PR 08. DIGITAL SIGNAL PROCESSING PP 09. DIGITAL SIGNAL PROCESSING OR 10. ELECTRONIC DESIGN PRACTICE OR	100 40 100 40 50 20 100 40 50 20 100 40 50 20 100 40 50 20 50 20	64 P 40 P 29 P 52 P 37 P 40 P 35 P 52 P 27 P 23 P		
FIRST TERM TOTAL = 399/750. ORDN. 1 MARKS :				
		ROHINI	, 71045438E ,	, PICT , T8053029
01. CONTROL SYSTEMS PP 02. DIGITAL COMMUNICATION PP 03. DIGITAL COMMUNICATION PR 04. NETWORK SYNTHESIS & FILTER DESIGNPP 05. NETWORK SYNTHESIS & FILTER DESIGNTW 06. MICROCONTROLLERS & APPLICATION PP 07. MICROCONTROLLERS & APPLICATION PR 08. DIGITAL SIGNAL PROCESSING PP 09. DIGITAL SIGNAL PROCESSING OR 10. ELECTRONIC DESIGN PRACTICE OR	100 40 100 40 50 20 100 40 50 20 100 40 50 20 100 40 50 20 50 20	45 P 41 P 27 P 50 P 32 P 48 P 36 P 52 P 20 P 07 F		
FIRST TERM TOTAL = 358/750. DRDN. 1 MARKS :				
		MEGHA	, 71045439c ,	
01. CONTROL SYSTEMS PP 02. DIGITAL COMMUNICATION PP 03. DIGITAL COMMUNICATION PR 04. NETWORK SYNTHESIS & FILTER DESIGNPP 05. NETWORK SYNTHESIS & FILTER DESIGNTW 06. MICROCONTROLLERS & APPLICATION PP 07. MICROCONTROLLERS & APPLICATION PR 08. DIGITAL SIGNAL PROCESSING PP 09. DIGITAL SIGNAL PROCESSING OR 10. ELECTRONIC DESIGN PRACTICE OR	100 40 100 40 50 20 100 40 50 20 100 40 50 20 100 40 50 20 50 20	46 P 42 P 34 P 65 P 39 P 49 P 39 P 59 P 34 P 20 P	, , , , , , , , , , , , , , , , , , , ,	, 110, 10033030
FIRST TERM TOTAL = 427/750. DRDN. 1 MARKS :				

UNIVERSITY OF PUNE ,T.E.(2008 PAT.)(ELECTRONICS & TELEC.) EXAMINATION NOV. 2011 DATE : 02 MAR. 2012 CENTRÉ : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 11 (380) NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER T8053031 GULSHEEN KAUR AHUJA BHUP INDER , 71045440G , , PICT , T8053031 01. CONTROL SYSTEMS PP 100 40 02. DIGITAL COMMUNICATION PP 100 40 03. DIGITAL COMMUNICATION PR 50 20 49 P 20 44 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 56 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 42 06. MICROCONTROLLERS & APPLICATION PP 100 40 46 P 07. MICROCONTROLLERS & APPLICATION PR 50 20 38 P 08. DIGITAL SIGNAL PROCESSING PP 100 40 69 09. DIGITAL SIGNAL PROCESSING OR 50 20 39 P 10. ELECTRONIC DESIGN PRACTICE OR 50 20 37 P FIRST TERM TOTAL = 477/750. ORDN. 1 MARKS : T8053032 GUNDI NACHIKET DINESH VINEETA , 71045441E , , PICT , T8053032 01. CONTROL SYSTEMS PP 100 40 46 P 02. DIGITAL COMMUNICATION PP 100 40 46 P 03. DIGITAL COMMUNICATION PR 50 20 30 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 51 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 20 43 P 50 06. MICROCONTROLLERS & APPLICATION PP 100 40 49 P 07. MICROCONTROLLERS & APPLICATION PR 50 20 41 P 08. DIGITAL SIGNAL PROCESSING PP 100 40 56 P 09. DIGITAL SIGNAL PROCESSING OR 50 20 30 P 10. ELECTRONIC DESIGN PRACTICE OR 50 20 29 P FIRST TERM TOTAL = 421/750. ORDN. 1 MARKS: ROOPASHRI , 71045446F , , , PICT , T8053033 T8053033 HIMANSHU KAMAT

 01. CONTROL SYSTEMS
 PP
 100
 40
 44
 P

 02. DIGITAL COMMUNICATION
 PP
 100
 40
 45
 P

 03. DIGITAL COMMUNICATION
 PR
 50
 20
 32
 P

 100 40 52 Р 04. NETWORK SYNTHESIS & FILTER DESIGNPP 20 28 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 06. MICROCONTROLLERS & APPLICATION PP 100 40 40 P 07. MICROCONTROLLERS & APPLICATION PR 50 20 34 P 08. DIGITAL SIGNAL PROCESSING PP
09. DIGITAL SIGNAL PROCESSING OR
10. ELECTRONIC DESIGN PRACTICE OR 100 35 OR 50 20 50 20 20 P FIRST TERM TOTAL = 384/750. ORDN. 1 MARKS:

	, ,	08 PAT.)(ELECTRONICS & INSTITUTE OF COMPUTER	TELEC.) EXAMINATION NOV. TECHNOLOGY, PUNE.	2011 PAGE NO. 12	(381)
NOTE: FIRST LINE : SEAT NO., NAME OF TH OTHER LINES: HEAD OF PASSING, MAX	E CANDIDAT . MARKS,	E, MOTHER, PERMANENT R MIN. PASS MARKS, MARKS	EG. NO., PREVIOUS SEAT N	O., COLLEGE, SEAT N	NO.
T8053034 HISARIYA RISHI RAMMOHAN		SANDHYA	, 71129929D ,	, PICT , T	т8053034
01. CONTROL SYSTEMS PP 02. DIGITAL COMMUNICATION PP 03. DIGITAL COMMUNICATION PR 04. NETWORK SYNTHESIS & FILTER DESIGNPP 05. NETWORK SYNTHESIS & FILTER DESIGNTW 06. MICROCONTROLLERS & APPLICATION PP 07. MICROCONTROLLERS & APPLICATION PR 08. DIGITAL SIGNAL PROCESSING PP 09. DIGITAL SIGNAL PROCESSING OR 10. ELECTRONIC DESIGN PRACTICE OR	100 40 100 40 50 20 100 40 50 20 100 40 50 20 100 40 50 20 50 20	59 P 46 P 35 P 47 P 39 P 57 P 38 P 63 P 35 P 20 P			
FIRST TERM TOTAL = 439/750. ORDN. 1 MARKS :					
T8053035 INGLE CHETAN VASUDEO					
01. CONTROL SYSTEMS PP 02. DIGITAL COMMUNICATION PP 03. DIGITAL COMMUNICATION PR 04. NETWORK SYNTHESIS & FILTER DESIGNPP 05. NETWORK SYNTHESIS & FILTER DESIGNTW 06. MICROCONTROLLERS & APPLICATION PP 07. MICROCONTROLLERS & APPLICATION PR 08. DIGITAL SIGNAL PROCESSING PP 09. DIGITAL SIGNAL PROCESSING OR 10. ELECTRONIC DESIGN PRACTICE OR	100 40 100 40 50 20 100 40 50 20 100 40 50 20 100 40 50 20 50 20	64 P 48 P 37 P 53 P 42 P 56 P 34 P 52 P 37 P 24 P	, / 1112333011 ,	, 1201	10033033
FIRST TERM TOTAL = 447/750. ORDN. 1 MARKS :					
T8053036 JADHAV ARTI GULAB		· · · · · · · · · · · · · · · · · · ·	, 71129931F ,	, PICT , ⁷	т8053036
01. CONTROL SYSTEMS PP 02. DIGITAL COMMUNICATION PP 03. DIGITAL COMMUNICATION PR 04. NETWORK SYNTHESIS & FILTER DESIGNPP 05. NETWORK SYNTHESIS & FILTER DESIGNTW 06. MICROCONTROLLERS & APPLICATION PP 07. MICROCONTROLLERS & APPLICATION PR 08. DIGITAL SIGNAL PROCESSING PP 09. DIGITAL SIGNAL PROCESSING OR 10. ELECTRONIC DESIGN PRACTICE OR FIRST TERM TOTAL = 499/750. ORDN. 1 MARKS:	100 40 100 40 50 20 100 40 50 20 100 40 50 20 100 40 50 20 50 20	64 P 56 P 33 P 59 P 42 P 78 P 40 P 64 P 37 P 26 P			

DATE : 02 MAR. 2012 CEN	TRE : PUNE	INSTITUTE OF COMPUTER		PAGE NO. 13 (
NOTE: FIRST LINE : SEAT NO., NAME OF THE OTHER LINES: HEAD OF PASSING, MAX	E CANDIDAT	E, MOTHER, PERMANENT MIN. PASS MARKS, MARK	REG. NO., PREVIOUS S	EAT NO., COLLEGE, SEAT NO	ο.
T8053037 TADHAY OMKAR EU CHAND			. 707014633 .	DTCT TS	8053037
T8053037 JADHAV OMKAR FULCHAND 01. CONTROL SYSTEMS PP 02. DIGITAL COMMUNICATION PP 03. DIGITAL COMMUNICATION PR 04. NETWORK SYNTHESIS & FILTER DESIGNPP 05. NETWORK SYNTHESIS & FILTER DESIGNTW 06. MICROCONTROLLERS & APPLICATION PP 07. MICROCONTROLLERS & APPLICATION PP 08. DIGITAL SIGNAL PROCESSING PP 09. DIGITAL SIGNAL PROCESSING OR 10. ELECTRONIC DESIGN PRACTICE OR	100 40 100 40 50 20 100 40 50 20 100 40 50 20 100 40 50 20 50 20	JYOTSNA 45 P 41 P 29 P 28 F 27 P 48 P 08 F 40 P 36 P 07 F	, 70701463J ,	, PICT , T8	8053037
FIRST TERM TOTAL = 309/750. ORDN. 1 MARKS :					
T8053038 JAIN AMEY MANOJ		NILIMA	, 71045455E ,	, PICT , T8	8053038
01. CONTROL SYSTEMS PP 02. DIGITAL COMMUNICATION PP 03. DIGITAL COMMUNICATION PR 04. NETWORK SYNTHESIS & FILTER DESIGNPP 05. NETWORK SYNTHESIS & FILTER DESIGNTW 06. MICROCONTROLLERS & APPLICATION PP 07. MICROCONTROLLERS & APPLICATION PR 08. DIGITAL SIGNAL PROCESSING PP 09. DIGITAL SIGNAL PROCESSING OR 10. ELECTRONIC DESIGN PRACTICE OR	100 40 100 40 50 20 100 40 50 20 100 40 50 20 100 40 50 20 50 20	40 P 43 P 33 P 40 P 34 P 45 P 31 P 57 P 28 P 20 P			
FIRST TERM TOTAL = 371/750. ORDN. 1 MARKS :					
T8053039 JAJOO POONAM ASHOK		JAYWANTI	, 71045459н ,	, PICT , T8	8053039
01. CONTROL SYSTEMS PP 02. DIGITAL COMMUNICATION PP 03. DIGITAL COMMUNICATION PR 04. NETWORK SYNTHESIS & FILTER DESIGNPP 05. NETWORK SYNTHESIS & FILTER DESIGNTW 06. MICROCONTROLLERS & APPLICATION PP 07. MICROCONTROLLERS & APPLICATION PR 08. DIGITAL SIGNAL PROCESSING PP 09. DIGITAL SIGNAL PROCESSING OR 10. ELECTRONIC DESIGN PRACTICE OR FIRST TERM TOTAL = 456/750. ORDN. 1 MARKS:	100 40 100 40 50 20 100 40 50 20 100 40 50 20 100 40 50 20 50 20	54 P 47 P 37 P 66 P 41 P 68 P 25 P 52 P 38 P 28 P			

UNIVERSITY OF PUNE ,T.E.(2008 PAT.)(ELECTRONICS & TELEC.) EXAMINATION NOV. 2011 DATE : 02 MAR. 2012 CENTRÉ : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 14 (383) NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER T8053040 JOSHI PURVA AVINASH AARTI , 71045463F , , , PICT , T8053040 01. CONTROL SYSTEMS PP 100 40 62 P 02. DIGITAL COMMUNICATION PP 100 40 59 P 03. DIGITAL COMMUNICATION PR 50 20 29 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 45 P 06. MICROCONTROLLERS & APPLICATION PP 100 40 64 P 07. MICROCONTROLLERS & APPLICATION PR 50 20 30 P 08. DIGITAL SIGNAL PROCESSING PP 100 40 75 P 09. DIGITAL SIGNAL PROCESSING OR 50 20 35 P 10. ELECTRONIC DESIGN PRACTICE OR 50 20 36 P FIRST TERM TOTAL = 504/750. ORDN. 1 MARKS : RINISHA , 71045464D , , PICT , T8053041 T8053041 KAINGADE SARTHAK RANJEET 01. CONTROL SYSTEMS PP 100
02. DIGITAL COMMUNICATION PP 100
03. DIGITAL COMMUNICATION PR 50 PP 100 40 72 P 40 63 P 20 45 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 67 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 20 39 P 50 06. MICROCONTROLLERS & APPLICATION PP 100 40 74 P 20 40 P 07. MICROCONTROLLERS & APPLICATION PR 50 08. DIGITAL SIGNAL PROCESSING PP 100 40 69 P 09. DIGITAL SIGNAL PROCESSING OR 50 20 40 P 10. ELECTRONIC DESIGN PRACTICE OR 50 20 32 P FIRST TERM TOTAL = 541/750. ORDN. 1 MARKS: , 71045465B , , PICT , T8053042 T8053042 KAKADE ABHIJIT SURESH VANDANA 01. CONTROL SYSTEMS PP 100 40 46 P 02. DIGITAL COMMUNICATION PP 100 40 55 P 03. DIGITAL COMMUNICATION PR 50 20 33 P 100 40 51 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 20 39 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 06. MICROCONTROLLERS & APPLICATION PP 100 07. MICROCONTROLLERS & APPLICATION PR 50 20 38 P 08. DIGITAL SIGNAL PROCESSING PP
09. DIGITAL SIGNAL PROCESSING OR
10. ELECTRONIC DESIGN PRACTICE OR 100 40 71 20 37 OR 50 50 20 27 Р FIRST TERM TOTAL = 451/750.

ORDN. 1 MARKS:

UNIVERSITY OF PUNE ,T.E.(2008 INDICED TO THE CONTROL OF THE CONTRO	PAT.)(ELECTRONICS & T STITUTE OF COMPUTER T			PAGE NO. 15 (384)
NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN	MOTHER, PERMANENT RE . PASS MARKS, MARKS	G. NO., PREVIOUS	SEAT NO., COL	LEGE, SEAT NO.
T8053043 KAMPU DHANANJAY ANANTRAO	SHUBHANGI	, 71045467J ,		PICT , T8053043
01. CONTROL SYSTEMS PP 100 40 9 02. DIGITAL COMMUNICATION PP 100 40 40 9 03. DIGITAL COMMUNICATION PR 50 20 40 9 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 9 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 9 06. MICROCONTROLLERS & APPLICATION PP 100 40 9 07. MICROCONTROLLERS & APPLICATION PR 50 20 20 8. DIGITAL SIGNAL PROCESSING PP 100 40 99. DIGITAL SIGNAL PROCESSING OR 50 20 20 20 20 20 20 20 20 20 20 20 20 20	52 P 49 P 44 P 55 P 32 P 63 P 25 P 71 P 22 P		,	,
FIRST TERM TOTAL = 446/750. ORDN. 1 MARKS :				
T8053044 KARE NACHIKET CHANDRASHEKHAR	NANDINI			PICT , T8053044
01. CONTROL SYSTEMS PP 100 40 102. DIGITAL COMMUNICATION PP 100 40 103. DIGITAL COMMUNICATION PR 50 20 104. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 105. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 106. MICROCONTROLLERS & APPLICATION PP 100 40 107. MICROCONTROLLERS & APPLICATION PR 50 20 108. DIGITAL SIGNAL PROCESSING PP 100 40 109.	53 P 55 P 38 P 62 P 45 P 71 P 38 P 63 P 37 P 35 P		,	,
FIRST TERM TOTAL = 497/750. ORDN. 1 MARKS :				
02. DIGITAL COMMUNICATION PP 100 40 40 03. DIGITAL COMMUNICATION PR 50 20 30 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 06. MICROCONTROLLERS & APPLICATION PP 100 40 70. MICROCONTROLLERS & APPLICATION PR 50 20 08. DIGITAL SIGNAL PROCESSING PP 100 40 09. DIGITAL SIGNAL PROCESSING OR 50 20 30 09.	VIDYA 68 P 48 P 35 P 69 P 42 P 72 P 25 P 74 P 38 P 30 P	, 71045490C ,	,	PICT , T8053045
ORDN. 1 MARKS :				

UNIVERSITY OF PUNE ,T.E.(2008 PAT.)(ELECTRONICS & TELEC.) EXAMINATION NOV. 2011 DATE : 02 MAR. 2012 CENTRÉ : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 16 (385) NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER T8053046 KULKARNI GAURAV PRADEEP NISHA , 71059376н , , , ріст , т8053046 01. CONTROL SYSTEMS PP
02. DIGITAL COMMUNICATION PP
03. DIGITAL COMMUNICATION PR PP 100 40 50 P 20 34 P 50 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 66 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 45 06. MICROCONTROLLERS & APPLICATION PP 100 40 62 07. MICROCONTROLLERS & APPLICATION PR 50 20 33 08. DIGITAL SIGNAL PROCESSING PP 100 40 71 P 09. DIGITAL SIGNAL PROCESSING OR 50 20 35 P 10. ELECTRONIC DESIGN PRACTICE OR 50 20 43 P FIRST TERM TOTAL = 489/750. ORDN. 1 MARKS : T8053047 KULKARNI HARSHAD RAVINDRA , 71045495D , , PICT , T8053047 SADHANA U1. CONTROL SYSTEMS PP 100 40 47 P
02. DIGITAL COMMUNICATION PP 100 40 47 P
03. DIGITAL COMMUNICATION PR 50 20 34 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 54 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 20 33 P 50 06. MICROCONTROLLERS & APPLICATION PP 100 40 59 P 07. MICROCONTROLLERS & APPLICATION PR 50 20 30 P 08. DIGITAL SIGNAL PROCESSING PP 100 40 70 P 09. DIGITAL SIGNAL PROCESSING OR 50 20 35 P 10. ELECTRONIC DESIGN PRACTICE OR 50 20 32 P FIRST TERM TOTAL = 441/750. ORDN. 1 MARKS: SWARALI , 71045497L , , PICT , T8053048 T8053048 KULKARNI SAURABH CHANDRAKANT 01. CONTROL SYSTEMS PP 100 40 57 P 02. DIGITAL COMMUNICATION PP 100 40 48 P 03. DIGITAL COMMUNICATION PR 50 20 07 F 100 40 65 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 20 39 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 06. MICROCONTROLLERS & APPLICATION PP 100 40 56 P 07. MICROCONTROLLERS & APPLICATION PR 50 20 32 08. DIGITAL SIGNAL PROCESSING PP
09. DIGITAL SIGNAL PROCESSING OR
10. ELECTRONIC DESIGN PRACTICE OR 100 OR 50 20 35 50 20 20 P FIRST TERM TOTAL = 424/750. ORDN. 1 MARKS:

UNIVERSITY OF PUNE ,T.E.(2008 PAT.)(ELECTRONICS & TELEC.) EXAMINATION NOV. 2011 DATE : 02 MAR. 2012 CENTRÉ : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 17 (386) NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER T8053049 KULKARNI YASH VIJAY VINITA , 71045499G , , PICT , T8053049 01. CONTROL SYSTEMS PP 100 40 02. DIGITAL COMMUNICATION PP 100 40 03. DIGITAL COMMUNICATION PR 50 20 47 P 20 37 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 48 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 38 06. MICROCONTROLLERS & APPLICATION PP 100 40 59 07. MICROCONTROLLERS & APPLICATION PR 50 20 25 08. DIGITAL SIGNAL PROCESSING PP 100 40 60 P 09. DIGITAL SIGNAL PROCESSING OR 50 20 28 P 10. ELECTRONIC DESIGN PRACTICE OR 50 20 25 P FIRST TERM TOTAL = 426/750. ORDN. 1 MARKS : T8053050 KUMBHAKARN MANSI ARUN SULBHA , 71045500D , , PICT , T8053050

 01. CONTROL SYSTEMS
 PP
 100
 40
 51
 P

 02. DIGITAL COMMUNICATION
 PP
 100
 40
 48
 P

 03. DIGITAL COMMUNICATION
 PR
 50
 20
 41
 P

 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 60 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 20 47 P 50 06. MICROCONTROLLERS & APPLICATION PP 100 40 65 P 20 40 P 07. MICROCONTROLLERS & APPLICATION PR 50 08. DIGITAL SIGNAL PROCESSING PP 100 40 71 P 09. DIGITAL SIGNAL PROCESSING OR 50 20 37 P 10. ELECTRONIC DESIGN PRACTICE OR 50 20 32 P FIRST TERM TOTAL = 492/750. ORDN. 1 MARKS: SANGITA , 71129932D , PICT , T8053051 T8053051 LIMBHORE NITIN SHIVAJI

 01. CONTROL SYSTEMS
 PP
 100
 40
 60
 P

 02. DIGITAL COMMUNICATION
 PP
 100
 40
 48
 P

 03. DIGITAL COMMUNICATION
 PR
 50
 20
 21
 P

 100 40 62 04. NETWORK SYNTHESIS & FILTER DESIGNPP 20 35 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 06. MICROCONTROLLERS & APPLICATION PP 100 40 73 P 07. MICROCONTROLLERS & APPLICATION PR 50 20 41 P 08. DIGITAL SIGNAL PROCESSING PP
09. DIGITAL SIGNAL PROCESSING OR
10. ELECTRONIC DESIGN PRACTICE OR 100 40 73 OR 50 20 38 50 20 30 P FIRST TERM TOTAL = 481/750. ORDN. 1 MARKS:

DATE: 02 MAR. 2012 CENT	E ,T.E.(2008 PAT.)(ELECTRONICS & TRE : PUNE INSTITUTE OF COMPUTER	TECHNOLOGY, PUNE.	PAGE NO. 18 (387)
NOTE: FIRST LINE : SEAT NO., NAME OF THE OTHER LINES: HEAD OF PASSING, MAX.	. MARKS, MIN. PASS MARKS, MARKS	EG. NO., PREVIOUS SEAT NO. OBTAINED, P/F:PASS/FAIL,	, COLLEGE, SEAT NO. C:PREVIOUS CARRY OVER
T8053052 LONDHE AARTI SURESH	NANDA	, 71045505E ,	
01. CONTROL SYSTEMS PP 02. DIGITAL COMMUNICATION PP 03. DIGITAL COMMUNICATION PR 04. NETWORK SYNTHESIS & FILTER DESIGNPP 05. NETWORK SYNTHESIS & FILTER DESIGNTW 06. MICROCONTROLLERS & APPLICATION PP 07. MICROCONTROLLERS & APPLICATION PR 08. DIGITAL SIGNAL PROCESSING PP 09. DIGITAL SIGNAL PROCESSING OR 10. ELECTRONIC DESIGN PRACTICE OR	100		
FIRST TERM TOTAL = 444/750. ORDN. 1 MARKS :			
T8053053 LUCKY KUMAR		, 71051136B ,	, PICT , T8053053
01. CONTROL SYSTEMS PP 02. DIGITAL COMMUNICATION PP 03. DIGITAL COMMUNICATION PR 04. NETWORK SYNTHESIS & FILTER DESIGNPP 05. NETWORK SYNTHESIS & FILTER DESIGNTW 06. MICROCONTROLLERS & APPLICATION PP 07. MICROCONTROLLERS & APPLICATION PR 08. DIGITAL SIGNAL PROCESSING PP 09. DIGITAL SIGNAL PROCESSING OR 10. ELECTRONIC DESIGN PRACTICE OR	100		
FIRST TERM TOTAL = 447/750. ORDN. 1 MARKS :			
T8053054 MAGDUM FAREEN JABBAR	YASMIN		
01. CONTROL SYSTEMS PP 02. DIGITAL COMMUNICATION PP 03. DIGITAL COMMUNICATION PR 04. NETWORK SYNTHESIS & FILTER DESIGNPP 05. NETWORK SYNTHESIS & FILTER DESIGNTW 06. MICROCONTROLLERS & APPLICATION PP 07. MICROCONTROLLERS & APPLICATION PR 08. DIGITAL SIGNAL PROCESSING PP 09. DIGITAL SIGNAL PROCESSING OR 10. ELECTRONIC DESIGN PRACTICE OR	100	,,	,
ORDN. 1 MARKS :			

		08 PAT.)(ELECTRONICS & 1	TELEC.) EXAMINATION NOV. 2 TECHNOLOGY, PUNE.	011 PAGE NO. 19 (388)
NOTE: FIRST LINE : SEAT NO., NAME OF TH OTHER LINES: HEAD OF PASSING, MAX	E CANDIDAT	E, MOTHER, PERMANENT R	EG. NO., PREVIOUS SEAT NO	., COLLEGE, SEAT NO.
T8053055 MAHAJAN TUSHAR GOPAL		JANABAI	, 71045508к ,	, PICT , T8053055
01. CONTROL SYSTEMS PP 02. DIGITAL COMMUNICATION PP 03. DIGITAL COMMUNICATION PR 04. NETWORK SYNTHESIS & FILTER DESIGNPP 05. NETWORK SYNTHESIS & FILTER DESIGNTW 06. MICROCONTROLLERS & APPLICATION PP 07. MICROCONTROLLERS & APPLICATION PR 08. DIGITAL SIGNAL PROCESSING PP 09. DIGITAL SIGNAL PROCESSING OR 10. ELECTRONIC DESIGN PRACTICE OR	100 40 100 40 50 20 100 40 50 20 100 40 50 20 100 40 50 20	40 P 32 P 54 P 29 P 48 P 32 P 62 P 36 P		
FIRST TERM TOTAL = 414/750. ORDN. 1 MARKS :				
T8053056 MAHENDRA CHOUDHARY		HEERA	, 71045511K ,	, PICT , T8053056
01. CONTROL SYSTEMS PP 02. DIGITAL COMMUNICATION PP 03. DIGITAL COMMUNICATION PR 04. NETWORK SYNTHESIS & FILTER DESIGNPP 05. NETWORK SYNTHESIS & FILTER DESIGNTW 06. MICROCONTROLLERS & APPLICATION PP 07. MICROCONTROLLERS & APPLICATION PR 08. DIGITAL SIGNAL PROCESSING PP 09. DIGITAL SIGNAL PROCESSING OR 10. ELECTRONIC DESIGN PRACTICE OR	100 40 100 40 50 20 100 40 50 20 100 40 50 20 100 40 50 20 50 20	62 P 40 P 63 P 37 P 62 P 30 P 57 P 36 P		
FIRST TERM TOTAL = 443/750. ORDN. 1 MARKS :				
T8053057 MAHESHWARI SNEHAL RAVINDRA		AARTI	, 71045512н ,	, PICT , T8053057
01. CONTROL SYSTEMS PP 02. DIGITAL COMMUNICATION PP 03. DIGITAL COMMUNICATION PR 04. NETWORK SYNTHESIS & FILTER DESIGNPP 05. NETWORK SYNTHESIS & FILTER DESIGNTW 06. MICROCONTROLLERS & APPLICATION PP 07. MICROCONTROLLERS & APPLICATION PR 08. DIGITAL SIGNAL PROCESSING PP 09. DIGITAL SIGNAL PROCESSING OR 10. ELECTRONIC DESIGN PRACTICE OR	100 40 100 40 50 20 100 40 50 20 100 40 50 20 100 40 50 20	31 F 38 P 45 P 38 P 49 P 34 P 41 P 32 P		
FIRST TERM TOTAL = 354/750. ORDN. 1 MARKS :				

UNIVERSITY OF PUNE ,T.E.(2008 PAT.)(ELECTRONICS & TELEC.) EXAMINATION NOV. 2011 DATE : 02 MAR. 2012 CENTRÉ : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 20 (389) NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER T8053058 MALDODE RAHUL SUDHAKAR SANGEETA , 71045514D , , PICT , T8053058 01. CONTROL SYSTEMS PP 100 40 02. DIGITAL COMMUNICATION PP 100 40 03. DIGITAL COMMUNICATION PR 50 20 01. CONTROL SYSTEMS 52 P 39 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 56 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 44 06. MICROCONTROLLERS & APPLICATION PP 100 40 64 P 07. MICROCONTROLLERS & APPLICATION PR 50 20 35 P 08. DIGITAL SIGNAL PROCESSING PP 100 40 74 P 09. DIGITAL SIGNAL PROCESSING OR 50 20 37 10. ELECTRONIC DESIGN PRACTICE OR 50 20 20 P FIRST TERM TOTAL = 466/750. ORDN. 1 MARKS : T8053059 MANE DEEPALI LIMBAJI REKHA , 71045517」 , , PICT , T8053059 01. CONTROL SYSTEMS PP 100 40 49 P 02. DIGITAL COMMUNICATION PP 100 40 43 P 03. DIGITAL COMMUNICATION PR 50 20 35 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 66 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 20 38 P 50 06. MICROCONTROLLERS & APPLICATION PP 100 40 60 P 07. MICROCONTROLLERS & APPLICATION PR 50 20 32 08. DIGITAL SIGNAL PROCESSING PP 100 40 60 P 09. DIGITAL SIGNAL PROCESSING OR 50 20 07 F 10. ELECTRONIC DESIGN PRACTICE OR 50 20 29 P FIRST TERM TOTAL = 419/750. ORDN. 1 MARKS: , 71045518G , , PICT , T8053060 T8053060 MANE GAURANGI LALASAHEB KALPANA 01. CONTROL SYSTEMS PP 100 40 40 P 02. DIGITAL COMMUNICATION PP 100 40 54 P 03. DIGITAL COMMUNICATION PR 50 20 30 P PP 100 40 100 40 44 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 20 42 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 06. MICROCONTROLLERS & APPLICATION PP 100 40 57 P 07. MICROCONTROLLERS & APPLICATION PR 50 20 39 08. DIGITAL SIGNAL PROCESSING PP
09. DIGITAL SIGNAL PROCESSING OR
10. ELECTRONIC DESIGN PRACTICE OR 100 32 OR 50 20 50 20 30 P FIRST TERM TOTAL = 420/750. ORDN. 1 MARKS:

DATE : 02 MAR. 2012 CEN	TRÉ : PÙNE	INSTITUTE OF COMPUTER		PAGE NO.	21 (390)
NOTE: FIRST LINE : SEAT NO., NAME OF TH OTHER LINES: HEAD OF PASSING, MAX	E CANDIDAT . MARKS,	E, MOTHER, PERMANENT F MIN. PASS MARKS, MARKS	REG. NO., PREVIOUS SEAT	NO., COLLEGE, SE	AT NO.
T8053061 MANTRI SHRUTI BAJRANG		SUREKHA	, 71045519E ,	, PICT	
01. CONTROL SYSTEMS PP 02. DIGITAL COMMUNICATION PP 03. DIGITAL COMMUNICATION PR 04. NETWORK SYNTHESIS & FILTER DESIGNPP 05. NETWORK SYNTHESIS & FILTER DESIGNTW 06. MICROCONTROLLERS & APPLICATION PP 07. MICROCONTROLLERS & APPLICATION PR 08. DIGITAL SIGNAL PROCESSING PP 09. DIGITAL SIGNAL PROCESSING OR 10. ELECTRONIC DESIGN PRACTICE OR	100 40 100 40 50 20 100 40 50 20 100 40 50 20 100 40 50 20 20	46 P 31 P 55 P 37 P 59 P 30 P 59 P 27 P			
FIRST TERM TOTAL = 411/750. ORDN. 1 MARKS :					
			711200225		
T8053062 MERCHANT ALIAKBAR YAHAYA 01. CONTROL SYSTEMS PP 02. DIGITAL COMMUNICATION PP 03. DIGITAL COMMUNICATION PR 04. NETWORK SYNTHESIS & FILTER DESIGNPP 05. NETWORK SYNTHESIS & FILTER DESIGNTW 06. MICROCONTROLLERS & APPLICATION PP 07. MICROCONTROLLERS & APPLICATION PR 08. DIGITAL SIGNAL PROCESSING PP 09. DIGITAL SIGNAL PROCESSING OR 10. ELECTRONIC DESIGN PRACTICE OR	100 40 100 40 50 20 100 40 50 20 100 40 50 20 100 40 50 20 50 20	50 P 30 P 65 P 37 P 66 P 32 P 63 P 20 P	, 71129933В ,	, PICT	, т8053062
FIRST TERM TOTAL = 410/750. ORDN. 1 MARKS :					
T8053063 MISHRA ABHISHEK ASHUTOSH		MALA		, PICT	
01. CONTROL SYSTEMS PP 02. DIGITAL COMMUNICATION PP 03. DIGITAL COMMUNICATION PR 04. NETWORK SYNTHESIS & FILTER DESIGNPP 05. NETWORK SYNTHESIS & FILTER DESIGNTW 06. MICROCONTROLLERS & APPLICATION PP 07. MICROCONTROLLERS & APPLICATION PR 08. DIGITAL SIGNAL PROCESSING PP 09. DIGITAL SIGNAL PROCESSING OR 10. ELECTRONIC DESIGN PRACTICE OR FIRST TERM TOTAL = 378/750. ORDN. 1 MARKS:	100 40 100 40 50 20 100 40 50 20 100 40 50 20 100 40 50 20	41 P 33 P 51 P 32 P 55 P 11 F 50 P 36 P			

		08 PAT.)(ELECTRONICS & INSTITUTE OF COMPUTER	TELEC.) EXAMINATION NOV. 2 TECHNOLOGY, PUNE.	011 PAGE NO. 22 (391)
NOTE: FIRST LINE : SEAT NO., NAME OF TH OTHER LINES: HEAD OF PASSING, MAX	E CANDIDAT	E, MOTHER, PERMANENT R	EG. NO., PREVIOUS SEAT NO	., COLLEGE, SEAT NO.
				T8053064
T8053064 MOHAMMED HABIBULLAH BAIG NAS 01. CONTROL SYSTEMS PP	100 40	G SADIYA BEGUM	, /10456/3F ,	, PICT , T8053064
02. DIGITAL COMMUNICATION PP 03. DIGITAL COMMUNICATION PR	100 40 50 20	61 P 44 P		
04. NETWORK SYNTHESIS & FILTER DESIGNPP 05. NETWORK SYNTHESIS & FILTER DESIGNTW	100 40 50 20	71 P 32 P		
06. MICROCONTROLLERS & APPLICATION PP 07. MICROCONTROLLERS & APPLICATION PR 08. DIGITAL SIGNAL PROCESSING PP	100 40 50 20 100 40	71 P 35 P 73 P		
09. DIGITAL SIGNAL PROCESSING OR 10. ELECTRONIC DESIGN PRACTICE OR	50 20 50 20	73 P 39 P 20 P		
	30 20			
FIRST TERM TOTAL = 509/750. ORDN. 1 MARKS :				
T8053065 MORE NISHANT PRAVIN		SHARADA	, 70925509」 ,	, PICT , T8053065
01. CONTROL SYSTEMS PP 02. DIGITAL COMMUNICATION PP 03. DIGITAL COMMUNICATION PR	100 40 100 40 50 20	25 F 25 F 30 P		
04. NETWORK SYNTHESIS & FILTER DESIGNPP 05. NETWORK SYNTHESIS & FILTER DESIGNTW	100 40 50 20	43 P 37 P		
06. MICROCONTROLLERS & APPLICATION PP 07. MICROCONTROLLERS & APPLICATION PR	100 40 50 20	43 P 37 P		
08. DIGITAL SIGNAL PROCESSING PP 09. DIGITAL SIGNAL PROCESSING OR	100 40 50 20	50 P 20 P		
10. ELECTRONIC DESIGN PRACTICE OR	50 20	06 F		
FIRST TERM TOTAL = 316/750. ORDN. 1 MARKS :				
T8053066 MRIDUL NAGAR		UCNAM	, 71045528D ,	, PICT , T8053066
01. CONTROL SYSTEMS PP 02. DIGITAL COMMUNICATION PP	100 40 100 40	40 P 51 P		
03. DIGITAL COMMUNICATION PR 04. NETWORK SYNTHESIS & FILTER DESIGNPP	50 20 100 40	38 P 54 P		
05. NETWORK SYNTHESIS & FILTER DESIGNTW 06. MICROCONTROLLERS & APPLICATION PP 07. MICROCONTROLLERS & APPLICATION PR	50 20 100 40 50 20	32 P 63 P 30 P		
08. DIGITAL SIGNAL PROCESSING PP 09. DIGITAL SIGNAL PROCESSING OR	100 40 50 20	59 P 36 P		
10. ELECTRONIC DESIGN PRACTICE OR	50 20	05 F		
FIRST TERM TOTAL = 408/750. ORDN. 1 MARKS :				

	E.(2008 PAT.)(ELECTRONICS & PUNE INSTITUTE OF COMPUTER		11 PAGE NO. 23 (392)
NOTE: FIRST LINE : SEAT NO., NAME OF THE CANE OTHER LINES: HEAD OF PASSING, MAX. MARK	DIDATE, MOTHER, PERMANENT R KS, MIN. PASS MARKS, MARKS	EG. NO., PREVIOUS SEAT NO.	, COLLEGE, SEAT NO.
		74045500-	
T8053067 MUJAWAR SAMIR HANIF	RUBAB	, 71045529в ,	, PICT , T8053067
01. CONTROL SYSTEMS PP 100 02. DIGITAL COMMUNICATION PP 100 03. DIGITAL COMMUNICATION PR 50 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 06. MICROCONTROLLERS & APPLICATION PP 100 07. MICROCONTROLLERS & APPLICATION PR 50 08. DIGITAL SIGNAL PROCESSING PP 100 09. DIGITAL SIGNAL PROCESSING OR 50 10. ELECTRONIC DESIGN PRACTICE OR 50	40 49 P 40 43 P 20 37 P 40 58 P 20 31 P 40 57 P 20 32 P 40 63 P 20 20 P 20 05 F		
FIRST TERM TOTAL = 395/750. ORDN. 1 MARKS :			
T8053068 MULLA SHARIQA AZAMALI	ARIFA	, 71045531D ,	, PICT , T8053068
01. CONTROL SYSTEMS PP 100 02. DIGITAL COMMUNICATION PP 100 03. DIGITAL COMMUNICATION PR 50 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 06. MICROCONTROLLERS & APPLICATION PP 100 07. MICROCONTROLLERS & APPLICATION PR 50 08. DIGITAL SIGNAL PROCESSING PP 100 09. DIGITAL SIGNAL PROCESSING OR 50 10. ELECTRONIC DESIGN PRACTICE OR 50	40 58 P 40 65 P 20 40 P 40 58 P 20 37 P 40 76 P 20 34 P 40 58 P 20 27 P 20 25 P		
FIRST TERM TOTAL = 478/750. ORDN. 1 MARKS :			
T8053069 MUNDANKAR SUSHANT SANJAY	CHATIA	, 71045532B	
T8053069 MUNDANKAR SUSHANT SANJAY 01. CONTROL SYSTEMS PP 100 02. DIGITAL COMMUNICATION PP 100 03. DIGITAL COMMUNICATION PR 50 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 06. MICROCONTROLLERS & APPLICATION PP 100 07. MICROCONTROLLERS & APPLICATION PR 50 08. DIGITAL SIGNAL PROCESSING PP 100 09. DIGITAL SIGNAL PROCESSING OR 50 10. ELECTRONIC DESIGN PRACTICE OR 50 FIRST TERM TOTAL = 452/750. ORDN. 1 MARKS :	SHAILA 40 52 P 40 51 P 20 32 P 40 57 P 20 35 P 40 63 P 20 35 P 40 75 P 20 32 P 20 32 P 20 32 P 20 20 P	, /10455328 ,	, PICT , T8053069

	,T.E.(2008 PAT.)(ELECTRONICS & T		11 PAGE NO. 24 (393)
NOTE: FIRST LINE: SEAT NO., NAME OF THE OTHER LINES: HEAD OF PASSING, MAX.	CANDIDATE, MOTHER, PERMANENT REMARKS, MIN. PASS MARKS, MARKS	G. NO., PREVIOUS SEAT NO.	, COLLEGE, SEAT NO.
T8053070 NAGHATE ANKIT VIJAY	MADULIBT	710455376	
01. CONTROL SYSTEMS PP 02. DIGITAL COMMUNICATION PP 03. DIGITAL COMMUNICATION PR 04. NETWORK SYNTHESIS & FILTER DESIGNPP 05. NETWORK SYNTHESIS & FILTER DESIGNTW 06. MICROCONTROLLERS & APPLICATION PP 07. MICROCONTROLLERS & APPLICATION PR	MADHURI 100	, 71045537C ,	, PICT , T8053070
FIRST TERM TOTAL = 410/750. ORDN. 1 MARKS :			
T8053072 NEVASE PRAJAKTA MARUTI	NANDA	, 71129934L ,	, PICT , T8053072
02. DIGITAL COMMUNICATION PP 03. DIGITAL COMMUNICATION PR 04. NETWORK SYNTHESIS & FILTER DESIGNPP 05. NETWORK SYNTHESIS & FILTER DESIGNTW 06. MICROCONTROLLERS & APPLICATION PP 07. MICROCONTROLLERS & APPLICATION PR	100		
FIRST TERM TOTAL = 398/750. ORDN. 1 MARKS :			
T8053073 NIRMAL MAYUR VINAYAKRAO	SUNITA	, 71045548J ,	, PICT , T8053073
02. DIGITAL COMMUNICATION PP 03. DIGITAL COMMUNICATION PR 04. NETWORK SYNTHESIS & FILTER DESIGNPP 05. NETWORK SYNTHESIS & FILTER DESIGNTW 06. MICROCONTROLLERS & APPLICATION PP 07. MICROCONTROLLERS & APPLICATION PR	100		
FIRST TERM TOTAL = 479/750. ORDN. 1 MARKS :			

DATE : 02 MAR. 2012 CEN	TRÉ : PUNE	08 PAT.)(ELECTRONICS & INSTITUTE OF COMPUTER	TECHNOLOGY, PUNE.	PAGE NO.	25 (394)
NOTE: FIRST LINE : SEAT NO., NAME OF TH OTHER LINES: HEAD OF PASSING, MAX	E CANDIDAT	E, MOTHER, PERMANENT	REG. NO., PREVIOUS S	EAT NO., COLLEGE, S	EAT NO.
T8053074 PACHANGE ASHISH NARAYAN		KALPANA	, 71045551) ,	, PICT	, т8053074
O1. CONTROL SYSTEMS PP O2. DIGITAL COMMUNICATION PP O3. DIGITAL COMMUNICATION PR O4. NETWORK SYNTHESIS & FILTER DESIGNPP O5. NETWORK SYNTHESIS & FILTER DESIGNTW O6. MICROCONTROLLERS & APPLICATION PP O7. MICROCONTROLLERS & APPLICATION PR O8. DIGITAL SIGNAL PROCESSING PP O9. DIGITAL SIGNAL PROCESSING OR 10. ELECTRONIC DESIGN PRACTICE OR	100 40 100 40 50 20 100 40 50 20 100 40 50 20 100 40 50 20 50 20	55 P 50 P 36 P 63 P 35 P 40 P 31 P 59 P 33 P 32 P			
FIRST TERM TOTAL = 434/750. ORDN. 1 MARKS :					
T8053075 PARODE ASHWINI SANJAY	100 10	JAISHREE	, 71045559D ,	, PICT	, т8053075
01. CONTROL SYSTEMS PP 02. DIGITAL COMMUNICATION PP 03. DIGITAL COMMUNICATION PR 04. NETWORK SYNTHESIS & FILTER DESIGNPP 05. NETWORK SYNTHESIS & FILTER DESIGNTW 06. MICROCONTROLLERS & APPLICATION PP 07. MICROCONTROLLERS & APPLICATION PR 08. DIGITAL SIGNAL PROCESSING PP 09. DIGITAL SIGNAL PROCESSING OR 10. ELECTRONIC DESIGN PRACTICE OR	100 40 100 40 50 20 100 40 50 20 100 40 50 20 100 40 50 20	50 P 50 P 34 P 49 P 43 P 40 P 36 P 57 P 35 P			
FIRST TERM TOTAL = 429/750. ORDN. 1 MARKS :					
T8053076 PATIL GAURAV VISHRAM 01. CONTROL SYSTEMS PP 02. DIGITAL COMMUNICATION PP 03. DIGITAL COMMUNICATION PR 04. NETWORK SYNTHESIS & FILTER DESIGNPP 05. NETWORK SYNTHESIS & FILTER DESIGNTW 06. MICROCONTROLLERS & APPLICATION PP 07. MICROCONTROLLERS & APPLICATION PR 08. DIGITAL SIGNAL PROCESSING PP 09. DIGITAL SIGNAL PROCESSING OR 10. ELECTRONIC DESIGN PRACTICE OR FIRST TERM TOTAL = 329/750. ORDN. 1 MARKS :	100 40 100 40 50 20 100 40 50 20 100 40 50 20 100 40 50 20 50 20	PRAMILA 49 P 49 P 08 F 40 P 33 P 43 P 26 P 42 P 32 P 07 F	, 71045564L ,	, РІСТ	, т8053076

DATE: 02 MAR. 2012 CENTR	RÉ : PUNE	8 PAT.)(ELECTRONICS & INSTITUTE OF COMPUTER	TECHNOLOGY, PUNE.		PAGE NO.	26 (395)
NOTE: FIRST LINE : SEAT NO., NAME OF THE OTHER LINES: HEAD OF PASSING, MAX.	CANDIDATE MARKS, M		REG. NO., PREVIOU S OBTAINED, P/F:P	IS SEAT NO., PASS/FAIL, C	COLLEGE, SI PREVIOUS CAR	EAT NO.
T8053077 PATIL OMKAR VISHWAS		VANITA	74045566-	,	, PICT	, т8053077
02. DIGITAL COMMUNICATION PP 03. DIGITAL COMMUNICATION PR 04. NETWORK SYNTHESIS & FILTER DESIGNPP 05. NETWORK SYNTHESIS & FILTER DESIGNTW 06. MICROCONTROLLERS & APPLICATION PP 07. MICROCONTROLLERS & APPLICATION PR	100 40 100 40 50 20 100 40 50 20 100 40 50 20 100 40 50 20 50 20	60 P 49 P 07 F 51 P 38 P 40 P 31 P 55 P 38 P 25 P				
FIRST TERM TOTAL = 394/750. ORDN. 1 MARKS :						
T8053078 PATIL SONAM SHASHIKANT						
01. CONTROL SYSTEMS PP 02. DIGITAL COMMUNICATION PP 03. DIGITAL COMMUNICATION PR 04. NETWORK SYNTHESIS & FILTER DESIGNPP 05. NETWORK SYNTHESIS & FILTER DESIGNTW 06. MICROCONTROLLERS & APPLICATION PP 07. MICROCONTROLLERS & APPLICATION PR	100 40 100 40 50 20 100 40 50 20 100 40 50 20 100 40 50 20 50 20	62 P 50 P 20 P 55 P 40 P 54 P 25 P 66 P 28 P 06 F	,	,	,	,
FIRST TERM TOTAL = 406/750. ORDN. 1 MARKS :						
T8053079 PATIL TEJAS LAXMAN		SUREKHA	, 71045568C			
02. DIGITAL COMMUNICATION PP 03. DIGITAL COMMUNICATION PR 04. NETWORK SYNTHESIS & FILTER DESIGNPP 05. NETWORK SYNTHESIS & FILTER DESIGNTW 06. MICROCONTROLLERS & APPLICATION PP 07. MICROCONTROLLERS & APPLICATION PR	100 40 100 40 50 20 100 40 50 20 100 40 50 20 100 40 50 20 50 20	59 P 51 P 27 P 58 P 47 P 40 P 34 P 58 P 39 P 28 P				
FIRST TERM TOTAL = 441/750. ORDN. 1 MARKS :						

UNIVERSITY OF PUNE ,T.E.(2008 PAT.)(ELECTRONICS & TELEC.) EXAMINATION NOV. 2011 DATE : 02 MAR. 2012 CENTRÉ : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 27 (396) NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER T8053080 PAWADE NITIN NARAYAN RENUKA , 71045569M , , PICT , T8053080 01. CONTROL SYSTEMS PP 100 40 02. DIGITAL COMMUNICATION PP 100 40 03. DIGITAL COMMUNICATION PR 50 20 49 P 20 33 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 56 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 41 06. MICROCONTROLLERS & APPLICATION PP 100 40 40 P 07. MICROCONTROLLERS & APPLICATION PR 50 20 36 P 08. DIGITAL SIGNAL PROCESSING PP 100 40 43 P 09. DIGITAL SIGNAL PROCESSING OR 50 20 34 P 10. ELECTRONIC DESIGN PRACTICE OR 50 20 21 P FIRST TERM TOTAL = 409/750. ORDN. 1 MARKS : T8053081 PAWAR PRADIPKUMAR RAOSAHEB SANGITA , 71045572M , , PICT , T8053081 01. CONTROL SYSTEMS PP 100
02. DIGITAL COMMUNICATION PP 100
03. DIGITAL COMMUNICATION PR 50 PP 100 40 59 P 40 58 P 20 32 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 65 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 20 36 P 50 06. MICROCONTROLLERS & APPLICATION PP 100 40 40 P 07. MICROCONTROLLERS & APPLICATION PR 50 20 28 P 08. DIGITAL SIGNAL PROCESSING PP 100 40 55 09. DIGITAL SIGNAL PROCESSING OR 50 20 38 10. ELECTRONIC DESIGN PRACTICE OR 50 20 07 20 38 P FIRST TERM TOTAL = 418/750. ORDN. 1 MARKS: , 71045573K , , PICT , T8053082 T8053082 PAWAR SURAJ SUBHASH VIDYA

 01. CONTROL SYSTEMS
 PP
 100
 40
 46
 P

 02. DIGITAL COMMUNICATION
 PP
 100
 40
 40
 P

 03. DIGITAL COMMUNICATION
 PR
 50
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 100 40 26 F 04. NETWORK SYNTHESIS & FILTER DESIGNPP 20 31 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 06. MICROCONTROLLERS & APPLICATION PP 100 40 25 F 07. MICROCONTROLLERS & APPLICATION PR 50 20 10 F 08. DIGITAL SIGNAL PROCESSING PP
09. DIGITAL SIGNAL PROCESSING OR
10. ELECTRONIC DESIGN PRACTICE OR 100 OR 50 20 20 50 20 06 F FIRST TERM TOTAL = 256/750. ORDN. 1 MARKS:

DATE : 02 MAR. 2012 CEN	ITRE : PUNE	INSTITUTE OF COMPUTER		PAGE NO. 28 (397)
NOTE: FIRST LINE : SEAT NO., NAME OF THE OTHER LINES: HEAD OF PASSING, MAX	IE CANDIDATE (. MARKS, M	, MOTHER, PERMANENT IN. PASS MARKS, MARK	REG. NO., PREVIOUS SEAT (S OBTAINED, P/F:PASS/FA	NO., COLLEGE, SEAT NO. IL, C:PREVIOUS CARRY OVER
T8053083 PAWAR VISHAL ALIAS DIGVIJAY		VIJAYANTI	, 71070203F ,	
01. CONTROL SYSTEMS PP 02. DIGITAL COMMUNICATION PP 03. DIGITAL COMMUNICATION PR 04. NETWORK SYNTHESIS & FILTER DESIGNPP 05. NETWORK SYNTHESIS & FILTER DESIGNTW 06. MICROCONTROLLERS & APPLICATION PP 07. MICROCONTROLLERS & APPLICATION PR 08. DIGITAL SIGNAL PROCESSING PP 09. DIGITAL SIGNAL PROCESSING OR 10. ELECTRONIC DESIGN PRACTICE OR	100 40 100 40 50 20 100 40 50 20 100 40 50 20 100 40 50 20 50 20 50 20	55 P 26 F 44 P AA F 27 P 40 P 30 P AA F 20 P 21 P		
FIRST TERM TOTAL = 263/750. DRDN. 1 MARKS :				
T8053084 PITRODA UTSAV RAJENDRA		KAILASH		, PICT , T8053084
01. CONTROL SYSTEMS PP 02. DIGITAL COMMUNICATION PP 03. DIGITAL COMMUNICATION PR 04. NETWORK SYNTHESIS & FILTER DESIGNPP 05. NETWORK SYNTHESIS & FILTER DESIGNTW 06. MICROCONTROLLERS & APPLICATION PP 07. MICROCONTROLLERS & APPLICATION PR 08. DIGITAL SIGNAL PROCESSING PP 09. DIGITAL SIGNAL PROCESSING OR 10. ELECTRONIC DESIGN PRACTICE OR	100 40 100 40 50 20 100 40 50 20 100 40 50 20 100 40 50 20 50 20	57 P 40 P 30 P 40 P 44 P 46 P 44 P 57 P 30 P 27 P		
FIRST TERM TOTAL = 415/750. DRDN. 1 MARKS :				
		DAYA	, 71045575F ,	
01. CONTROL SYSTEMS PP 02. DIGITAL COMMUNICATION PP 03. DIGITAL COMMUNICATION PR 04. NETWORK SYNTHESIS & FILTER DESIGNPP 05. NETWORK SYNTHESIS & FILTER DESIGNTW 06. MICROCONTROLLERS & APPLICATION PP 07. MICROCONTROLLERS & APPLICATION PR 08. DIGITAL SIGNAL PROCESSING PP 09. DIGITAL SIGNAL PROCESSING OR 10. ELECTRONIC DESIGN PRACTICE OR	100 40 100 40 50 20 100 40 50 20 100 40 50 20 100 40 50 20 50 20	49 P 52 P 34 P 55 P 42 P 48 P 32 P 56 P 32 P 22 P	,,	, .==. ,
FIRST TERM TOTAL = 422/750. DRDN. 1 MARKS :				

	TRÉ : PUNE E CANDIDATE . MARKS, M	INSTITUTE OF COMPUTER	EG. NO., PREVIOUS SEAT NO. OBTAINED, P/F:PASS/FAIL,	PAGE NO	EAT NO. RY OVER
T8053086 PRABHUDESAI PRATHAMESH PRAKA		ADITI	, 71045579」 ,		, т8053086
01. CONTROL SYSTEMS PP 02. DIGITAL COMMUNICATION PP 03. DIGITAL COMMUNICATION PR 04. NETWORK SYNTHESIS & FILTER DESIGNPP 05. NETWORK SYNTHESIS & FILTER DESIGNTW 06. MICROCONTROLLERS & APPLICATION PP 07. MICROCONTROLLERS & APPLICATION PR 08. DIGITAL SIGNAL PROCESSING PP 09. DIGITAL SIGNAL PROCESSING OR 10. ELECTRONIC DESIGN PRACTICE OR	100 40 100 40 50 20 100 40 50 20 100 40 50 20 100 40 50 20 50 20 50 20	60 P 49 P 40 P 53 P 37 P 40 P 44 P 64 P 38 P 30 P			
FIRST TERM TOTAL = 455/750. ORDN. 1 MARKS :					
T8053087 PRIYANKA TOMAR		SHASHI	, 71045583G ,		
01. CONTROL SYSTEMS PP 02. DIGITAL COMMUNICATION PP 03. DIGITAL COMMUNICATION PR 04. NETWORK SYNTHESIS & FILTER DESIGNTW 05. NETWORK SYNTHESIS & FILTER DESIGNTW 06. MICROCONTROLLERS & APPLICATION PP 07. MICROCONTROLLERS & APPLICATION PR 08. DIGITAL SIGNAL PROCESSING PP 09. DIGITAL SIGNAL PROCESSING OR 10. ELECTRONIC DESIGN PRACTICE OR	100 40 100 40 50 20 100 40 50 20 100 40 50 20 100 40 50 20 50 20	53 P 56 P 07 F 55 P 31 P 49 P 34 P 57 P 28 P 22 P			
FIRST TERM TOTAL = 392/750. ORDN. 1 MARKS :					
T8053088 ROOPAL OBEROI		AMARJEET	, 71045591н ,	, PICT	, т8053088
01. CONTROL SYSTEMS PP 02. DIGITAL COMMUNICATION PP 03. DIGITAL COMMUNICATION PR 04. NETWORK SYNTHESIS & FILTER DESIGNPP 05. NETWORK SYNTHESIS & FILTER DESIGNTW 06. MICROCONTROLLERS & APPLICATION PP 07. MICROCONTROLLERS & APPLICATION PR 08. DIGITAL SIGNAL PROCESSING PP 09. DIGITAL SIGNAL PROCESSING OR 10. ELECTRONIC DESIGN PRACTICE OR FIRST TERM TOTAL = 389/750. ORDN. 1 MARKS:	100 40 100 40 50 20 100 40 50 20 100 40 50 20 100 40 50 20 50 20	48 P 41 P 34 P 49 P 35 P 40 P 27 P 50 P 35 P 30 P			

	E ,T.E.(2008 PAT.)(ELECTRONICS & TRE : PUNE INSTITUTE OF COMPUTER		D11 PAGE NO. 30 (399)
NOTE: FIRST LINE : SEAT NO., NAME OF THE OTHER LINES: HEAD OF PASSING, MAX.	E CANDIDATE, MOTHER, PERMANENT . MARKS, MIN. PASS MARKS, MARK	REG. NO., PREVIOUS SEAT NO	., COLLEGE, SEAT NO.
T8053089 SAURABH SANJAY MANE	PALLAVI	, 71045601J ,	, PICT , T8053089
01. CONTROL SYSTEMS PP 02. DIGITAL COMMUNICATION PP 03. DIGITAL COMMUNICATION PR 04. NETWORK SYNTHESIS & FILTER DESIGNPP 05. NETWORK SYNTHESIS & FILTER DESIGNTW 06. MICROCONTROLLERS & APPLICATION PP 07. MICROCONTROLLERS & APPLICATION PR 08. DIGITAL SIGNAL PROCESSING PP 09. DIGITAL SIGNAL PROCESSING OR 10. ELECTRONIC DESIGN PRACTICE OR	100		
FIRST TERM TOTAL = 420/750. ORDN. 1 MARKS :			
T8053090 SAYED SAIF IFTEKAR	SHAKILA	, 71045604C ,	, PICT , T8053090
01. CONTROL SYSTEMS PP 02. DIGITAL COMMUNICATION PP 03. DIGITAL COMMUNICATION PR 04. NETWORK SYNTHESIS & FILTER DESIGNPP 05. NETWORK SYNTHESIS & FILTER DESIGNTW 06. MICROCONTROLLERS & APPLICATION PP 07. MICROCONTROLLERS & APPLICATION PR 08. DIGITAL SIGNAL PROCESSING PP 09. DIGITAL SIGNAL PROCESSING OR 10. ELECTRONIC DESIGN PRACTICE OR	100		
FIRST TERM TOTAL = 320/750. ORDN. 1 MARKS :			
T8053091 SHAIKH SHOAIB IQBAL	REHANA	, 71129937E ,	, PICT , T8053091
01. CONTROL SYSTEMS PP 02. DIGITAL COMMUNICATION PP 03. DIGITAL COMMUNICATION PR 04. NETWORK SYNTHESIS & FILTER DESIGNPP 05. NETWORK SYNTHESIS & FILTER DESIGNTW 06. MICROCONTROLLERS & APPLICATION PP 07. MICROCONTROLLERS & APPLICATION PR 08. DIGITAL SIGNAL PROCESSING PP 09. DIGITAL SIGNAL PROCESSING OR 10. ELECTRONIC DESIGN PRACTICE OR	100		
FIRST TERM TOTAL = 443/750. ORDN. 1 MARKS :			

DATE: 02 MAR. 2012 CENTRÉ:	PUNE INSTITUTE OF COMPUTER		PAGE NO. 31 (400)
NOTE: FIRST LINE : SEAT NO., NAME OF THE CAND OTHER LINES: HEAD OF PASSING, MAX. MARK	IDATE, MOTHER, PERMANENT R	OBTAINED, P/F:PASS/FAIL,	, COLLEGE, SEAT NO.
T8053092 SHELKE MINAXI BHARAT	PUTULA	, 71129938c ,	, PICT , T8053092
01. CONTROL SYSTEMS PP 100 02. DIGITAL COMMUNICATION PP 100 03. DIGITAL COMMUNICATION PR 50 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 06. MICROCONTROLLERS & APPLICATION PP 100 07. MICROCONTROLLERS & APPLICATION PR 50 08. DIGITAL SIGNAL PROCESSING PP 100 09. DIGITAL SIGNAL PROCESSING OR 50 10. ELECTRONIC DESIGN PRACTICE OR 50	40 54 P 40 44 P 20 20 P 40 51 P 20 44 P 40 46 P 20 36 P 40 51 P 20 26 P 20 33 P		
FIRST TERM TOTAL = 405/750. ORDN. 1 MARKS :			
T8053093 SHENDKAR SUSHMA HANUMANT			, PICT , T8053093
01. CONTROL SYSTEMS PP 100 02. DIGITAL COMMUNICATION PP 100 03. DIGITAL COMMUNICATION PR 50 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 06. MICROCONTROLLERS & APPLICATION PP 100 07. MICROCONTROLLERS & APPLICATION PR 50 08. DIGITAL SIGNAL PROCESSING PP 100 09. DIGITAL SIGNAL PROCESSING OR 50 10. ELECTRONIC DESIGN PRACTICE OR 50	40 47 P 40 31 F 20 24 P 40 40 P 20 45 P 40 43 P 20 30 P 40 57 P 20 30 P 20 27 P	, . 	, . <u></u>
FIRST TERM TOTAL = 374/750. ORDN. 1 MARKS :			
T8053094 SHINDE MANOJ SURESH		, 71045615J ,	
01. CONTROL SYSTEMS PP 100 02. DIGITAL COMMUNICATION PP 100 03. DIGITAL COMMUNICATION PR 50 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 06. MICROCONTROLLERS & APPLICATION PP 100 07. MICROCONTROLLERS & APPLICATION PR 50 08. DIGITAL SIGNAL PROCESSING PP 100 09. DIGITAL SIGNAL PROCESSING OR 50 10. ELECTRONIC DESIGN PRACTICE OR 50 FIRST TERM TOTAL = 447/750. ORDN. 1 MARKS :	40 59 P 40 43 P 20 39 P 40 50 P 20 35 P 40 42 P 20 35 P 40 75 P 20 39 P 20 30 P		

		8 PAT.)(ELECTRONICS & INSTITUTE OF COMPUTER	TELEC.) EXAMINATION NOV. TECHNOLOGY, PUNE.	2011 PAGE NO.	32 (401)
NOTE: FIRST LINE : SEAT NO., NAME OF THE OTHER LINES: HEAD OF PASSING, MAX	E CANDIDATE . MARKS, M	, MOTHER, PERMANENT R IIN. PASS MARKS, MARKS	EG. NO., PREVIOUS SEAT N	O., COLLEGE, SE	AT NO.
T8053095 SHITOLE JAYESH DASHRATH		SULOCHANA	, 71129940E ,	, PICT	, т8053095
01. CONTROL SYSTEMS PP 02. DIGITAL COMMUNICATION PP 03. DIGITAL COMMUNICATION PR 04. NETWORK SYNTHESIS & FILTER DESIGNPP 05. NETWORK SYNTHESIS & FILTER DESIGNTW 06. MICROCONTROLLERS & APPLICATION PP 07. MICROCONTROLLERS & APPLICATION PR 08. DIGITAL SIGNAL PROCESSING PP 09. DIGITAL SIGNAL PROCESSING OR 10. ELECTRONIC DESIGN PRACTICE OR	100 40 100 40 50 20 100 40 50 20 100 40 50 20 100 40 50 20 50 20	46 P 40 P 26 P 42 P 41 P 40 P 29 P 43 P 25 P 37 P			
FIRST TERM TOTAL = 369/750. ORDN. 1 MARKS :					
T8053096 SIDDHARTHA BANGA		SUMEDHA RANI	, 71045625F ,	, PICT	, т8053096
01. CONTROL SYSTEMS PP 02. DIGITAL COMMUNICATION PP 03. DIGITAL COMMUNICATION PR 04. NETWORK SYNTHESIS & FILTER DESIGNPP 05. NETWORK SYNTHESIS & FILTER DESIGNTW 06. MICROCONTROLLERS & APPLICATION PP 07. MICROCONTROLLERS & APPLICATION PR 08. DIGITAL SIGNAL PROCESSING PP 09. DIGITAL SIGNAL PROCESSING OR 10. ELECTRONIC DESIGN PRACTICE OR	100 40 100 40 50 20 100 40 50 20 100 40 50 20 100 40 50 20 50 20	40 P 31 F 26 P 28 F 30 P 28 F 35 P 40 P 06 F 21 P			
FIRST TERM TOTAL = 285/750. ORDN. 1 MARKS :					
T8053098 SUBAGHYA MAHAJAN		VIJAY	, 71045635C ,	, PICT	, т8053098
01. CONTROL SYSTEMS PP 02. DIGITAL COMMUNICATION PP 03. DIGITAL COMMUNICATION PR 04. NETWORK SYNTHESIS & FILTER DESIGNPP 05. NETWORK SYNTHESIS & FILTER DESIGNTW 06. MICROCONTROLLERS & APPLICATION PP 07. MICROCONTROLLERS & APPLICATION PR 08. DIGITAL SIGNAL PROCESSING PP 09. DIGITAL SIGNAL PROCESSING OR 10. ELECTRONIC DESIGN PRACTICE OR	100 40 100 40 50 20 100 40 50 20 100 40 50 20 100 40 50 20 50 20	45 P 20 F 30 P 40 P 26 P 31 F 32 P 49 P 33 P 22 P			
FIRST TERM TOTAL = 328/750. ORDN. 1 MARKS :					

	T.E.(2008 PAT.)(ELECTRONICS & T,		11 PAGE NO. 33 (402)
NOTE: FIRST LINE : SEAT NO., NAME OF THE OTHER LINES: HEAD OF PASSING, MAX.	CANDIDATE, MOTHER, PERMANENT REMARKS, MIN. PASS MARKS, MARKS	G. NO., PREVIOUS SEAT NO.	, COLLEGE, SEAT NO.
T8053099 SURVE SMIT BALASAHEB	PUSHPA	, 71045637K	
01. CONTROL SYSTEMS PP 02. DIGITAL COMMUNICATION PP 03. DIGITAL COMMUNICATION PR 04. NETWORK SYNTHESIS & FILTER DESIGNPP 05. NETWORK SYNTHESIS & FILTER DESIGNTW 06. MICROCONTROLLERS & APPLICATION PP 07. MICROCONTROLLERS & APPLICATION PR 08. DIGITAL SIGNAL PROCESSING PP 09. DIGITAL SIGNAL PROCESSING OR 10. ELECTRONIC DESIGN PRACTICE OR	100		
FIRST TERM TOTAL = 396/750. ORDN. 1 MARKS :			
-0053100		710456425	
T8053100 TELI REKHA MALLAPPA 01. CONTROL SYSTEMS PP 02. DIGITAL COMMUNICATION PP 03. DIGITAL COMMUNICATION PR 04. NETWORK SYNTHESIS & FILTER DESIGNPP 05. NETWORK SYNTHESIS & FILTER DESIGNTW 06. MICROCONTROLLERS & APPLICATION PP 07. MICROCONTROLLERS & APPLICATION PR 08. DIGITAL SIGNAL PROCESSING PP 09. DIGITAL SIGNAL PROCESSING OR 10. ELECTRONIC DESIGN PRACTICE OR	KASTURI 100	, 71045643D ,	, PICT , T8053100
FIRST TERM TOTAL = 413/750. ORDN. 1 MARKS :			
T8053101 TEMGIRE PRASAD SANTU	TAI	, 71045644в ,	
01. CONTROL SYSTEMS PP 02. DIGITAL COMMUNICATION PP 03. DIGITAL COMMUNICATION PR 04. NETWORK SYNTHESIS & FILTER DESIGNPP 05. NETWORK SYNTHESIS & FILTER DESIGNTW 06. MICROCONTROLLERS & APPLICATION PP 07. MICROCONTROLLERS & APPLICATION PR 08. DIGITAL SIGNAL PROCESSING PP 09. DIGITAL SIGNAL PROCESSING OR 10. ELECTRONIC DESIGN PRACTICE OR	100		
ORDN. 1 MARKS :			

DATE: 02 MAR. 2012 CENTRE:	PUNE INSTITUTE OF COMPUTER	•	PAGE NO. 34 (403)
NOTE: FIRST LINE : SEAT NO., NAME OF THE CAN OTHER LINES: HEAD OF PASSING, MAX. MAR	DIDATE, MOTHER, PERMANENT F KS, MIN. PASS MARKS, MARKS	REG. NO., PREVIOUS SEAT NO.	, COLLEGE, SEAT NO. C:PREVIOUS CARRY OVER
T8053102 THORAT TANVI TATYASAHEB	SULOCHANA	, 71045650G ,	
01. CONTROL SYSTEMS PP 100 02. DIGITAL COMMUNICATION PP 100 03. DIGITAL COMMUNICATION PR 50 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 06. MICROCONTROLLERS & APPLICATION PP 100 07. MICROCONTROLLERS & APPLICATION PR 50 08. DIGITAL SIGNAL PROCESSING PP 100 09. DIGITAL SIGNAL PROCESSING OR 50 10. ELECTRONIC DESIGN PRACTICE OR 50	40 45 P 20 39 P 40 45 P 20 37 P 40 40 P 20 23 P 40 45 P 20 38 P		
FIRST TERM TOTAL = 384/750. ORDN. 1 MARKS :			
T8053103 THOSAR TRUSHA MILIND	SUNANDA	, 70801652к ,	
01. CONTROL SYSTEMS PP 100 02. DIGITAL COMMUNICATION PP 100 03. DIGITAL COMMUNICATION PR 50 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 06. MICROCONTROLLERS & APPLICATION PP 100 07. MICROCONTROLLERS & APPLICATION PR 50 08. DIGITAL SIGNAL PROCESSING PP 100 09. DIGITAL SIGNAL PROCESSING OR 50 10. ELECTRONIC DESIGN PRACTICE OR 50	40 28 F 20 06 F 40 25 F 20 26 P 40 31 F 20 32 P 40 22 F 20 27 P		
FIRST TERM TOTAL = 232/750. ORDN. 1 MARKS :			
T0053104 NAME WARTINGSW PRINTED		710455515	
T8053104 VYAS HARIKESH PRADEEP 01. CONTROL SYSTEMS PP 100 02. DIGITAL COMMUNICATION PP 100 03. DIGITAL COMMUNICATION PR 50 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 05. NETWORK SYNTHESIS & FILTER DESIGNTW 500 06. MICROCONTROLLERS & APPLICATION PP 100 07. MICROCONTROLLERS & APPLICATION PR 50 08. DIGITAL SIGNAL PROCESSING PP 100 09. DIGITAL SIGNAL PROCESSING OR 50 10. ELECTRONIC DESIGN PRACTICE OR 50 FIRST TERM TOTAL = 387/750.	40 44 P 20 34 P 40 47 P 20 34 P 40 43 P 20 28 P 40 46 P 20 34 P	, 71045661B ,	, PICT , T8053104
ORDN. 1 MARKS :			

UNIVERSITY OF PUNE ,T.E.(2008 PAT.)(ELECTRONICS & TELEC.) EXAMINATION NOV. 2011 DATE : 02 MAR. 2012 CENTRÉ : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 35 (404) NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER T8053105 WAGHMARE ABHISHEK SUBHASH VIDYA , 71045662L , , PICT , T8053105 01. CONTROL SYSTEMS PP 100 40 41 P 02. DIGITAL COMMUNICATION PP 100 40 40 P 03. DIGITAL COMMUNICATION PR 50 20 36 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 40 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 42 06. MICROCONTROLLERS & APPLICATION PP 100 40 30 F 07. MICROCONTROLLERS & APPLICATION PR 50 20 32 08. DIGITAL SIGNAL PROCESSING PP 100 40 47 P 09. DIGITAL SIGNAL PROCESSING OR 50 20 35 P 10. ELECTRONIC DESIGN PRACTICE OR 50 20 20 P FIRST TERM TOTAL = 363/750. ORDN. 1 MARKS : T8053106 WATHORE SANKET UTTAMRAO KAVITA , 71045665E , , PICT , T8053106 01. CONTROL SYSTEMS PP 100
02. DIGITAL COMMUNICATION PP 100
03. DIGITAL COMMUNICATION PR 50 PP 100 40 30 F 40 23 F 20 05 F 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 47 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 20 32 50 06. MICROCONTROLLERS & APPLICATION PP 100 40 41 P 07. MICROCONTROLLERS & APPLICATION PR 50 20 10 F 08. DIGITAL SIGNAL PROCESSING PP 100 40 30 F 09. DIGITAL SIGNAL PROCESSING OR 50 20 10 F 10. ELECTRONIC DESIGN PRACTICE OR 50 20 25 P FIRST TERM TOTAL = 253/750. ORDN. 1 MARKS: , 71045667M , , PICT , T8053107 T8053107 YADAV VIKAS PRABHURAO SUREKHA 01. CONTROL SYSTEMS PP 100 40 56 P 02. DIGITAL COMMUNICATION PP 100 40 41 P 03. DIGITAL COMMUNICATION PR 50 20 42 P 100 40 63 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 20 28 P 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 06. MICROCONTROLLERS & APPLICATION PP 100 40 47 P 07. MICROCONTROLLERS & APPLICATION PR 50 20 33 P 08. DIGITAL SIGNAL PROCESSING PP
09. DIGITAL SIGNAL PROCESSING OR
10. ELECTRONIC DESIGN PRACTICE OR 100 OR 50 20 38 50 20 22 P FIRST TERM TOTAL = 410/750.

ORDN. 1 MARKS:

UNIVERSITY OF PUNE ,T.E.(2008 PAT.)(ELECTRONICS & TELEC.) EXAMINATION NOV. 2011 DATE : 02 MAR. 2012 CENTRÉ : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 36 (405) NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER KRANTI , 71045668K , , PICT , T8053108 T8053108 YARDI NIRANJAN DHANANJAY 01. CONTROL SYSTEMS PP 100 40 52 P 02. DIGITAL COMMUNICATION PP 100 40 40 P 03. DIGITAL COMMUNICATION PR 50 20 34 P 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 67 Р 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 32 06. MICROCONTROLLERS & APPLICATION PP 100 40 58 P 07. MICROCONTROLLERS & APPLICATION PR 50 20 32 08. DIGITAL SIGNAL PROCESSING PP 100 40 62 P 09. DIGITAL SIGNAL PROCESSING OR 50 20 37 P 10. ELECTRONIC DESIGN PRACTICE OR 50 20 38 P FIRST TERM TOTAL = 452/750. ORDN. 1 MARKS : VEENA KUMARI , 70801330K , T8053003 , PICT , T8053110 T8053110 AMIT KUMAR BHASKAR 01. CONTROL SYSTEMS PP 100 40 47 P C 11. SIGNAL CODING & ESTIMATION THEORYPP 100 40 46 P C 02. DIGITAL COMMUNICATION PP 100 40 20 F 12. SIGNAL CODING & ESTIMATION THEORYPP 50 20 25 P C 03. DIGITAL COMMUNICATION PR 50 20 21 P C 13. SYSTEM PROGRA.& OPERATING SYS. PP 100 40 41 P C 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 40 P C 14. SYSTEM PROGRA.& OPERATING SYS. TW 50 20 39 P C 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 35 P C 15. COMPUTER ORGANIZATION & ARCHITEC PP 100 40 49 P C 06. MICROCONTROLLERS & APPLICATION PP 100 40 40 P C 16. INDUSTRIAL MANAGEMENT PP 100 40 52 P C 07. MICROCONTROLLERS & APPLICATION PR 50 20 29 P C 17. WAVE THEORY & ANTENNA PP 100 40 53 P C 08. DIGITAL SIGNAL PROCESSING PP 100 40 41 P C 18. WAVE THEORY & ANTENNA PR 50 20 37 P C 09. DIGITAL SIGNAL PROCESSING OR 50 20 24 P C 19. MINI PROJECT & SEMINAR OR 50 20 35 P C 10. ELECTRONIC DESIGN PRACTICE OR 50 20 24 P C 20. TEST & MEASUREMENT TECHNIQUES OR 50 20 35 P C GRAND TOTAL = 738/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS: , 70925339H , T8053006 , PICT , T8053111 T8053111 ARPITA CHAKRABORTY RITA

GRAND TOTAL = 935/1500, RESULT: FIRST CLASS ORDN. 1 MARKS:

UNIVERSITY OF PUNE ,T.E.(2008 PAT.)(ELECTRONICS & TELEC.) EXAMINATION NOV. 2011 DATE : 02 MAR. 2012 CENTRÉ : PÙNE INSTITÚTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 37 (406) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER T8053112 AVIJEET DUBEY MEERA , 70801347D , T8053009 , PICT , T8053112 01. CONTROL SYSTEMS PP 100 40 49 P C 11. SIGNAL CODING & ESTIMATION THEORYPP 100 40 40 P C 02. DIGITAL COMMUNICATION PP 100 40 23 F 12. SIGNAL CODING & ESTIMATION THEORYPP 50 20 20 P C 03. DIGITAL COMMUNICATION PR 50 20 21 P C 13. SYSTEM PROGRA. & OPERATING SYS. PP 100 40 43 P C 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 48 P C 14. SYSTEM PROGRA. & OPERATING SYS. TW 50 20 36 P C 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 20 P C 15. COMPUTER ORGANIZATION & ARCHITEC PP 100 40 02 F 06. MICROCONTROLLERS & APPLICATION PP 100 40 AA F 16. INDUSTRIAL MANAGEMENT PP 100 40 41 P C 07. MICROCONTROLLERS & APPLICATION PR 50 20 22 P C 17. WAVE THEORY & ANTENNA PP 100 40 40 P C 08. DIGITAL SIGNAL PROCESSING PP 100 40 40 P C 18. WAVE THEORY & ANTENNA PR 50 20 34 P C 09. DIGITAL SIGNAL PROCESSING OR 50 20 24 P C 19. MINI PROJECT & SEMINAR OR 50 20 29 P C 10. ELECTRONIC DESIGN PRACTICE OR 50 20 31 P C 20. TEST & MEASUREMENT TECHNIQUES OR 50 20 30 P C GRAND TOTAL = 593/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS : SINDHU , 71070194C , T8053010 , PICT , T8053113 T8053113 BANKAR DHANESH BAPURAO 01. CONTROL SYSTEMS PP 100 40 75 P C 11. SIGNAL CODING & ESTIMATION THEORYPP 100 40 46 P C 02. DIGITAL COMMUNICATION PP 100 40 63 P C 12. SIGNAL CODING & ESTIMATION THEORYPP 50 20 22 P C 03. DIGITAL COMMUNICATION PR 50 20 38 P C 13. SYSTEM PROGRA. OPERATING SYS. PP 100 40 74 P C 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 78 P C 14. SYSTEM PROGRA. OPERATING SYS. TW 50 20 43 P C 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 45 P C 15. COMPUTER ORGANIZATION & ARCHITEC PP 100 40 62 P C 06. MICROCONTROLLERS & APPLICATION PP 100 40 88 P C 16. INDUSTRIAL MANAGEMENT PP 100 40 64 P C 07. MICROCONTROLLERS & APPLICATION PR 50 20 35 P C 17. WAVE THEORY & ANTENNA PP 100 40 61 P C 08. DIGITAL SIGNAL PROCESSING PP 100 40 47 P C 18. WAVE THEORY & ANTENNA PR 50 20 36 P C 09. DIGITAL SIGNAL PROCESSING OR 50 20 35 P C 19. MINI PROJECT & SEMINAR OR 50 20 32 P C 10. ELECTRONIC DESIGN PRACTICE OR 50 20 29 P C 20. TEST & MEASUREMENT TECHNIQUES OR 50 20 38 P GRAND TOTAL = 1011/1500, RESULT: FIRST CLASS WITH DISTINCTION ORDN. 1 MARKS: , 70801366L , T8053014 , PICT , T8053114 T8053114 BHATIA DHAWAL RAMESHCHAND REENA

01. CONTROL SYSTEMS PP 100 40 49 P 11. SIGNAL CODING & ESTIMATION THEORYPP 100 40 27 F 02. DIGITAL COMMUNICATION PP 100 40 18 F 12. SIGNAL CODING & ESTIMATION THEORYPR 50 20 22 P 03. DIGITAL COMMUNICATION PR 50 20 40 P C 13. SYSTEM PROGRA. & OPERATING SYS. PP 100 40 49 P C 04. NETWORK SYNTHESIS & FILTER DESIGNTP 100 40 40 P C 14. SYSTEM PROGRA. & OPERATING SYS. TW 50 20 38 P C 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 39 P C 15. COMPUTER ORGANIZATION & ARCHITEC PP 100 40 40 P C 16. INDUSTRIAL MANAGEMENT PP 100 40 44 P C 17. MICROCONTROLLERS & APPLICATION PR 50 20 35 P C 17. WAVE THEORY & ANTENNA PP 100 40 10 F 08. DIGITAL SIGNAL PROCESSING PP 100 40 44 P C 18. WAVE THEORY & ANTENNA PR 50 20 38 P C 19. DIGITAL SIGNAL PROCESSING OR 50 20 22 P C 19. MINI PROJECT & SEMINAR OR 50 20 39 P C 10. ELECTRONIC DESIGN PRACTICE OR 50 20 25 P C 20. TEST & MEASUREMENT TECHNIQUES OR 50 20 39 P C

GRAND TOTAL = 698/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS :

UNIVERSITY OF PUNE ,T.E.(2008 PAT.)(ELECTRONICS & TELEC.) EXAMINATION NOV. 2011 DATE: 02 MAR. 2012 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 38 (407) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MÍN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER T8053115 BHOIR PRASAD SHIVAJI USHA , 70925368M , T8053016 , PICT , T8053115 100 40 P 01. CONTROL SYSTEMS 11. SIGNAL CODING & ESTIMATION THEORYPP 100 100 30 P 02. DIGITAL COMMUNICATION PP 40 40 P 12. SIGNAL CODING & ESTIMATION THEORYPR 50 20 PR 20 50 23 P C 100 03. DIGITAL COMMUNICATION 13. SYSTEM PROGRA. & OPERATING SYS. PP 40 40 P C 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 40 P C 14. SYSTEM PROGRA. & OPERATING SYS. TW 50 20 34 P C 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 38 P C 15. COMPUTER ORGANIZATION & ARCHITEC PP 100 40 09 F

100

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100

OR

50

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20

40

43 P.C

20 P C

40 P C

20 26 P C

50 20 21 P C

PP

PP

PR

OR

100

100

50

50

40

20

50 20 28 P

40 P.C

35 P.C

40 10 F

20 32 P C

16. INDUSTRIAL MANAGEMENT

19. MINI PROJECT & SEMINAR

18. WAVE THEORY & ANTENNA
19. MINI PROJECT & SEMINAR

20. TEST & MEASUREMENT TECHNIQUES OR

17. WAVE THEORY & ANTENNA

GRAND TOTAL = 629/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS :

06. MICROCONTROLLERS & APPLICATION PP

07. MICROCONTROLLERS & APPLICATION PR

10. ELECTRONIC DESIGN PRACTICE

08. DIGITAL SIGNAL PROCESSING 09. DIGITAL SIGNAL PROCESSING

Т8053116	BORAWAKE MANOJ VILAS				NI	RMALA		, 709253	72K ,	Т8053019	,	PICT	,	т8053	116
01. CONTROL		PP	100	40	75	РС		SIGNAL CODI					40	44	РС
02. DIGITAL	COMMUNICATION	PP	100	40	42	РС	12.	SIGNAL CODI	NG & ESTI	MATION THE	ORYPR	. 50	20	42	Р
03. DIGITAL	COMMUNICATION	PR	50	20	35	РС	13.	SYSTEM PROGI	RA.& OPER	ATING SYS.	PP	100	40	62	РС
<pre>04. NETWORK</pre>	SYNTHESIS & FILTER DESIGN	NPP	100	40	46	РС	14.	SYSTEM PROGI	RA.& OPER	ATING SYS.	TW	50	20	40	РС
<pre>05. NETWORK</pre>	SYNTHESIS & FILTER DESIGN	NTW	50	20	42	РС	15.	COMPUTER OR	GANIZATIO	N & ARCHIT	EC PP	100	40	40	РС
06. MICROCO	NTROLLERS & APPLICATION	PP	100	40	45	РС	16.	INDUSTRIAL	MANAGEMEN	Т	PP	100	40	45	РС
07. MICROCO	NTROLLERS & APPLICATION	PR	50	20	30	РС	17.	WAVE THEORY	& ANTENN	Α	PP	100	40	54	РС
08. DIGITAL	SIGNAL PROCESSING	PP	100	40	57	РС	18.	WAVE THEORY	& ANTENN	Α	PR	50	20	42	РС
09. DIGITAL	SIGNAL PROCESSING	OR	50	20	34	РС	19.	MINI PROJECT	T & SEMIN	AR	OR	50	20	33	РС
10. ELECTRO	NIC DESIGN PRACTICE	OR	50	20	26	PС	20.	TEST & MEAS	UREMENT T	ECHNIQUES	OR	50	20	40	РС

GRAND TOTAL = 874/1500, RESULT: HIGHER SECOND CLASS ORDN. 1 MARKS:

01. CONTROL SYSTEMS PP 100 40 42 P C 11. SIGNAL CODING & ESTIMATION THEORYPP 100 40 02. DIGITAL COMMUNICATION PP 100 40 26 F 12. SIGNAL CODING & ESTIMATION THEORYPR 50 20 03. DIGITAL COMMUNICATION PR 50 20 22 P C 13. SYSTEM PROGRA. & OPERATING SYS. PP 100 40 4. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 45 P C 14. SYSTEM PROGRA. & OPERATING SYS. TW 50 20 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 40 P C 15. COMPUTER ORGANIZATION & ARCHITEC PP 100 40 06. MICROCCONTROLLERS & APPLICATION PP 100 40 40 P C 16. INDUSTRIAL MANAGEMENT PP 100 40	
07. MICROCONTROLLERS & APPLICATION PR 50 20 30 P C 17. WAVE THEORY & ANTENNA PP 100 40 08. DIGITAL SIGNAL PROCESSING PP 100 40 45 P C 18. WAVE THEORY & ANTENNA PR 50 20 09. DIGITAL SIGNAL PROCESSING OR 50 20 32 P C 19. MINI PROJECT & SEMINAR OR 50 20 10. ELECTRONIC DESIGN PRACTICE OR 50 20 22 P C 20. TEST & MEASUREMENT TECHNIQUES OR 50 20	0 40 P C 0 42 P C 0 43 P C 0 41 P C 0 40 P C 0 39 P C 0 42 P C

GRAND TOTAL = 725/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS:

UNIVERSITY OF PUNE ,T.E.(2008 PAT.)(ELECTRONICS & TELEC.) EXAMINATION NOV. 2011 DATE: 02 MAR. 2012 CENTRÉ: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 39 (408) NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER T8053118 JAGTAP SAUDAMINI DHANANJAY PRERANA , 71070199D , T8053045 , PICT , T8053118 43 P C 40 P C 01. CONTROL SYSTEMS 11. SIGNAL CODING & ESTIMATION THEORYPP 100 PP 100 52 P C 38 P 02. DIGITAL COMMUNICATION 40 12. SIGNAL CODING & ESTIMATION THEORYPR 50 20 PP PR 20 38 P C 100 03. DIGITAL COMMUNICATION 50 13. SYSTEM PROGRA. & OPERATING SYS. PP 40 40 P C 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 41 P C 14. SYSTEM PROGRA. & OPERATING SYS. TW 50 20 40 P C

15. COMPUTER ORGANIZATION & ARCHITEC PP

16. INDUSTRIAL MANAGEMENT PP

18. WAVE THEORY & ANTENNA PR 19. MINI PROJECT & SEMINAR OR

20. TEST & MEASUREMENT TECHNIQUES OR

17. WAVE THEORY & ANTENNA

100

100

100

50

PP

40

40

20

50 20 42 P C

50 20 40 P C

51 P C

49 P.C

40 P C

40 53 P C

50

100

50

100

OR

20

40

41 P C

48 P C

20 37 P C

40 48 P C

50 20 34 P C

50 20 35 P C

GRAND TOTAL = 850/1500, RESULT: HIGHER SECOND CLASS ORDN. 1 MARKS:

05. NETWORK SYNTHESIS & FILTER DESIGNTW

06. MICROCONTROLLERS & APPLICATION PP

07. MICROCONTROLLERS & APPLICATION PR

10. ELECTRONIC DESIGN PRACTICE

08. DIGITAL SIGNAL PROCESSING 09. DIGITAL SIGNAL PROCESSING

T8053119	JITESH MAHESH KUMAR ADN	NANI			ВН	IAVNA		, 709	25453к	, т8053047	7,	PICT	,	г8053:	119
01. CONTR	OL SYSTEMS	PP	100	40	53	РС	11.	SIGNAL C	ODING &	ESTIMATION TH	HEORYPF	100	40	45	РС
02. DIGIT	AL COMMUNICATION	PP	100	40	40	РС	12.	SIGNAL C	ODING &	ESTIMATION TH	HEORYPR	₹ 50	20	20	РС
03. DIGIT	AL COMMUNICATION	PR	50	20	37	РС	13.	SYSTEM P	ROGRA.&	OPERATING SYS	S. PF	100	40	43	РС
04. NETWO	RK SYNTHESIS & FILTER DESI	IGNPP	100	40	40	РС	14.	SYSTEM P	ROGRA.&	OPERATING SYS	5. TW	<i>i</i> 50	20	43	РС
05. NETWO	RK SYNTHESIS & FILTER DESI	IGNTW	50	20	38	РС	15.	COMPUTER	ORGANIZ	ATION & ARCH	ITEC PF	100	40	45	РС
06. MICRO	CONTROLLERS & APPLICATION	PP	100	40	47	РС	16.	INDUSTRI	AL MANAG	SEMENT	PF	100	40	40	РС
07. MICRO	CONTROLLERS & APPLICATION	PR	50	20	30	РС	17.	WAVE THE	ORY & AN	ITENNA	PF	100	40	40	РС
08. DIGIT	AL SIGNAL PROCESSING	PP	100	40	50	РС	18.	WAVE THE	ORY & AN	ITENNA	PR	₹ 50	20	36	РС
09. DIGIT	AL SIGNAL PROCESSING	OR	50	20	36	РС	19.	MINI PRO	JECT & S	SEMINAR	OR	₹ 50	20	43	РС
10. FLECT	RONTC DESTGN PRACTICE	OR	50	20	32	PC	20.	TFST & M	IFASUREME	NT TECHNIQUE	S OR	50	20	38	Р

GRAND TOTAL = 796/1500, RESULT: SECOND CLASS ORDN. 1 MARKS:

T8053121 MAUSKAR CHAITANYA UMESH			SUNITA	, 70925501c , T8053058 , PICT	, т8053121
01. CONTROL SYSTEMS PI 02. DIGITAL COMMUNICATION PI 03. DIGITAL COMMUNICATION PI 04. NETWORK SYNTHESIS & FILTER DESIGNTI 05. NETWORK SYNTHESIS & FILTER DESIGNTI 06. MICROCONTROLLERS & APPLICATION PI 07. MICROCONTROLLERS & APPLICATION PI	100 50 100 50 100 100 100	40 40 20 40 20 40 20	55 P 48 P C 26 P C 46 P 20 P C 61 P C 25 P C	11. SIGNAL CODING & ESTIMATION THEORYPP 100 12. SIGNAL CODING & ESTIMATION THEORYPR 50 13. SYSTEM PROGRA.& OPERATING SYS. PP 100 14. SYSTEM PROGRA.& OPERATING SYS. TW 50 15. COMPUTER ORGANIZATION & ARCHITEC PP 100 16. INDUSTRIAL MANAGEMENT PP 100 17. WAVE THEORY & ANTENNA PP 100	40 68 P C 20 33 P C 40 54 P C 20 25 P C 40 55 P C 40 59 P C 40 58 P C
08. DIGITAL SIGNAL PROCESSING PI 09. DIGITAL SIGNAL PROCESSING O	100	40 20	46 P C 25 P C	18. WAVE THEORY & ANTENNA PR 50 19. MINI PROJECT & SEMINAR OR 50	20 39 P C 20 33 P C
10. ELECTRONIC DESIGN PRACTICE OF		20	20 P C	20. TEST & MEASUREMENT TECHNIQUES OR 50	20 33 P C 20 38 P C

GRAND TOTAL = 834/1500, RESULT: HIGHER SECOND CLASS ORDN. 1 MARKS:

UNIVERSITY OF PUNE ,T.E.(2008 PAT.)(ELECTRONICS & TELEC.) EXAMINATION NOV. 2011

DATE: 02 MAR. 2012 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 40 (409)

NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8053122 MOHIT M. HARDIKAR ANJALI , 70925507B , T8053060 , PICT , T8053122

PP 100 50 P C 53 P C 01. CONTROL SYSTEMS 11. SIGNAL CODING & ESTIMATION THEORYPP 100 100 02. DIGITAL COMMUNICATION PP 40 40 P C 12. SIGNAL CODING & ESTIMATION THEORYPR 50 20 34 P 03. DIGITAL COMMUNICATION PR 20 50 34 P C 100 61 P C 13. SYSTEM PROGRA. & OPERATING SYS. PP 40 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 40 P C 14. SYSTEM PROGRA. & OPERATING SYS. TW 50 20 20 P C 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 20 P C 15. COMPUTER ORGANIZATION & ARCHITEC PP 100 40 50 P C 06. MICROCONTROLLERS & APPLICATION PP 100 40 44 P C 16. INDUSTRIAL MANAGEMENT PP 100 40 71 PC 07. MICROCONTROLLERS & APPLICATION PR 50 20 22 P C 17. WAVE THEORY & ANTENNA PP 100 40 60 P C 18. WAVE THEORY & ANTENNA 19. MINI PROJECT & SEMINAR 40 40 P C 100 PR 50 20 25 P C 08. DIGITAL SIGNAL PROCESSING 09. DIGITAL SIGNAL PROCESSING OR 50 20 25 P C OR 50 20 35 P C 10. ELECTRONIC DESIGN PRACTICE 50 20 24 P C 20. TEST & MEASUREMENT TECHNIQUES OR 50 20 32 P C OR

GRAND TOTAL = 780/1500, RESULT: SECOND CLASS ORDN. 1 MARKS :

3123	MOTWANI NIKITA SUDAMA				KR	ISHNA	,	71070201K	, т8053061	, F	ICT	, -	г8053	123
		PP	100	40	63	Р					100	40	46	РС
DIGITAL	COMMUNICATION	PP	100	40	40	РС	12. SIGNA	AL CODING &	ESTIMATION THEO	RYPR	50	20	30	РС
DIGITAL	COMMUNICATION	PR	50	20	30	РС	13. SYSTE	M PROGRA.&	OPERATING SYS.	PP	100	40	43	РС
NETWORK	SYNTHESIS & FILTER DESIG	GNPP	100	40	AA	F	14. SYSTE	M PROGRA.&	OPERATING SYS.	TW	50	20	40	РС
NETWORK	SYNTHESIS & FILTER DESIG	GNTW	50	20	40	РС	15. COMPL	JTER ORGANI	ZATION & ARCHITE	C PP	100	40	47	РС
MICROCON	NTROLLERS & APPLICATION	PP	100	40	42	РС	16. INDUS	STRIAL MANA	GEMENT	PP	100	40	62	РС
MICROCON	NTROLLERS & APPLICATION	PR	50	20	26	РС	17. WAVE	THEORY & A	NTENNA	PP	100	40	40	РС
DIGITAL	SIGNAL PROCESSING	PP	100	40	40	РС	18. WAVE	THEORY & A	NTENNA	PR	50	20	32	Р
DIGITAL	SIGNAL PROCESSING	OR	50	20	30	РС	19. MINI	PROJECT &	SEMINAR	OR	50	20	36	РС
ELECTRON	NIC DESIGN PRACTICE	OR	50	20	27	РС	20. TEST	& MEASUREM	ENT TECHNIQUES	OR	50	20	35	РС
	CONTROL DIGITAL DIGITAL NETWORK NETWORK MICROCON MICROCON DIGITAL DIGITAL	CONTROL SYSTEMS DIGITAL COMMUNICATION DIGITAL COMMUNICATION NETWORK SYNTHESIS & FILTER DESI	CONTROL SYSTEMS PP DIGITAL COMMUNICATION PP DIGITAL COMMUNICATION PR NETWORK SYNTHESIS & FILTER DESIGNTP NETWORK SYNTHESIS & FILTER DESIGNTW MICROCONTROLLERS & APPLICATION PP MICROCONTROLLERS & APPLICATION PR DIGITAL SIGNAL PROCESSING PP DIGITAL SIGNAL PROCESSING OR	CONTROL SYSTEMS PP 100 DIGITAL COMMUNICATION PP 100 DIGITAL COMMUNICATION PR 50 NETWORK SYNTHESIS & FILTER DESIGNPP 100 NETWORK SYNTHESIS & FILTER DESIGNTW 50 MICROCONTROLLERS & APPLICATION PP 100 MICROCONTROLLERS & APPLICATION PR 50 DIGITAL SIGNAL PROCESSING PP 100 DIGITAL SIGNAL PROCESSING OR 50	CONTROL SYSTEMS PP 100 40 DIGITAL COMMUNICATION PP 100 40 DIGITAL COMMUNICATION PR 50 20 NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 MICROCONTROLLERS & APPLICATION PP 100 40 MICROCONTROLLERS & APPLICATION PR 50 20 DIGITAL SIGNAL PROCESSING PP 100 40 DIGITAL SIGNAL PROCESSING OR 50 20	CONTROL SYSTEMS PP 100 40 63 DIGITAL COMMUNICATION PP 100 40 40 DIGITAL COMMUNICATION PR 50 20 30 NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 AA NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 40 MICROCONTROLLERS & APPLICATION PP 100 40 42 MICROCONTROLLERS & APPLICATION PR 50 20 26 DIGITAL SIGNAL PROCESSING PP 100 40 40 DIGITAL SIGNAL PROCESSING OR 50 20 30	CONTROL SYSTEMS PP 100 40 63 P DIGITAL COMMUNICATION PP 100 40 40 P C DIGITAL COMMUNICATION PR 50 20 30 P C NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 AA F NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 40 P C MICROCONTROLLERS & APPLICATION PP 100 40 42 P C MICROCONTROLLERS & APPLICATION PR 50 20 26 P C DIGITAL SIGNAL PROCESSING PP 100 40 40 P C DIGITAL SIGNAL PROCESSING OR 50 20 30 P C	CONTROL SYSTEMS PP 100 40 63 P 11. SIGNADIGITAL COMMUNICATION PP 100 40 40 P C 12. SIGNADIGITAL COMMUNICATION PR 50 20 30 P C 13. SYSTENT NETWORK SYNTHESIS & FILTER DESIGNAPP 100 40 AA F 14. SYSTENT NETWORK SYNTHESIS & FILTER DESIGNAPP 100 40 P C 15. COMPUNICROCONTROLLERS & APPLICATION PP 100 40 42 P C 16. INDUSTRICT NETWORK SYNTHESIS & FILTER DESIGNATION PR 50 20 26 P C 17. WAVE DIGITAL SIGNAL PROCESSING PP 100 40 40 P C 18. WAVE DIGITAL SIGNAL PROCESSING OR 50 20 30 P C 19. MINI	CONTROL SYSTEMS PP 100 40 63 P 11. SIGNAL CODING & DIGITAL COMMUNICATION PP 100 40 40 P C 12. SIGNAL CODING & DIGITAL COMMUNICATION PR 50 20 30 P C 13. SYSTEM PROGRA.& NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 AA F 14. SYSTEM PROGRA.& NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 40 P C 15. COMPUTER ORGANI MICROCONTROLLERS & APPLICATION PP 100 40 42 P C 16. INDUSTRIAL MANA MICROCONTROLLERS & APPLICATION PR 50 20 26 P C 17. WAVE THEORY & A DIGITAL SIGNAL PROCESSING PP 100 40 40 P C 18. WAVE THEORY & A DIGITAL SIGNAL PROCESSING OR 50 20 30 P C 19. MINI PROJECT &	CONTROL SYSTEMS PP 100 40 63 P 11. SIGNAL CODING & ESTIMATION THEO DIGITAL COMMUNICATION PP 100 40 40 P C 12. SIGNAL CODING & ESTIMATION THEO DIGITAL COMMUNICATION PR 50 20 30 P C 13. SYSTEM PROGRA. & OPERATING SYS. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 AA F 14. SYSTEM PROGRA. & OPERATING SYS. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 40 P C 15. COMPUTER ORGANIZATION & ARCHITE MICROCONTROLLERS & APPLICATION PP 100 40 42 P C 16. INDUSTRIAL MANAGEMENT MICROCONTROLLERS & APPLICATION PR 50 20 26 P C 17. WAVE THEORY & ANTENNA DIGITAL SIGNAL PROCESSING PP 100 40 40 P C 18. WAVE THEORY & ANTENNA DIGITAL SIGNAL PROCESSING OR 50 20 30 P C 19. MINI PROJECT & SEMINAR	CONTROL SYSTEMS PP 100 40 63 P 11. SIGNAL CODING & ESTIMATION THEORYPP DIGITAL COMMUNICATION PP 100 40 40 P C 12. SIGNAL CODING & ESTIMATION THEORYPR DIGITAL COMMUNICATION PR 50 20 30 P C 13. SYSTEM PROGRA. OPERATING SYS. PP NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 AA F 14. SYSTEM PROGRA. OPERATING SYS. TW NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 40 P C 15. COMPUTER ORGANIZATION & ARCHITEC PP MICROCONTROLLERS & APPLICATION PP 100 40 42 P C 16. INDUSTRIAL MANAGEMENT PP MICROCONTROLLERS & APPLICATION PR 50 20 26 P C 17. WAVE THEORY & ANTENNA PP DIGITAL SIGNAL PROCESSING PP 100 40 40 P C 18. WAVE THEORY & ANTENNA PR DIGITAL SIGNAL PROCESSING OR 50 20 30 P C 19. MINI PROJECT & SEMINAR OR	CONTROL SYSTEMS PP 100 40 63 P 11. SIGNAL CODING & ESTIMATION THEORYPP 100 DIGITAL COMMUNICATION PP 100 40 40 P C 12. SIGNAL CODING & ESTIMATION THEORYPR 50 DIGITAL COMMUNICATION PR 50 20 30 P C 13. SYSTEM PROGRA. & OPERATING SYS. PP 100 NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 AA F 14. SYSTEM PROGRA. & OPERATING SYS. TW 50 NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 40 P C 15. COMPUTER ORGANIZATION & ARCHITEC PP 100 MICROCONTROLLERS & APPLICATION PP 100 40 42 P C 16. INDUSTRIAL MANAGEMENT PP 100 MICROCONTROLLERS & APPLICATION PR 50 20 26 P C 17. WAVE THEORY & ANTENNA PP 100 DIGITAL SIGNAL PROCESSING PP 100 40 40 P C 18. WAVE THEORY & ANTENNA PR 50 DIGITAL SIGNAL PROCESSING OR 50 20 30 P C 19. MINI PROJECT & SEMINAR OR 50	CONTROL SYSTEMS PP 100 40 63 P 11. SIGNAL CODING & ESTIMATION THEORYPP 100 40 DIGITAL COMMUNICATION PP 100 40 40 P C 12. SIGNAL CODING & ESTIMATION THEORYPR 50 20 DIGITAL COMMUNICATION PR 50 20 30 P C 13. SYSTEM PROGRA. & OPERATING SYS. PP 100 40 NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 AA F 14. SYSTEM PROGRA. & OPERATING SYS. TW 50 20 NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 40 P C 15. COMPUTER ORGANIZATION & ARCHITEC PP 100 40 MICROCONTROLLERS & APPLICATION PP 100 40 42 P C 16. INDUSTRIAL MANAGEMENT PP 100 40 MICROCONTROLLERS & APPLICATION PR 50 20 26 P C 17. WAVE THEORY & ANTENNA PP 100 40 DIGITAL SIGNAL PROCESSING PP 100 40 40 P C 18. WAVE THEORY & ANTENNA PR 50 20 DIGITAL SIGNAL PROCESSING OR 50 20 30 P C 19. MINI PROJECT & SEMINAR OR 50 20	CONTROL SYSTEMS PP 100 40 63 P 11. SIGNAL CODING & ESTIMATION THEORYPP 100 40 46 DIGITAL COMMUNICATION PP 100 40 40 P C 12. SIGNAL CODING & ESTIMATION THEORYPR 50 20 30 DIGITAL COMMUNICATION PR 50 20 30 P C 13. SYSTEM PROGRA. & OPERATING SYS. PP 100 40 43 NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 AA F 14. SYSTEM PROGRA. & OPERATING SYS. TW 50 20 40 NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 40 P C 15. COMPUTER ORGANIZATION & ARCHITEC PP 100 40 47 MICROCONTROLLERS & APPLICATION PP 100 40 42 P C 16. INDUSTRIAL MANAGEMENT PP 100 40 62 MICROCONTROLLERS & APPLICATION PR 50 20 26 P C 17. WAVE THEORY & ANTENNA PP 100 40 40 DIGITAL SIGNAL PROCESSING PP 100 40 40 P C 18. WAVE THEORY & ANTENNA PR 50 20 32 DIGITAL SIGNAL PROCESSING OR 50 20 30 P C 19. MINI PROJECT & SEMINAR OR 50 20 36

GRAND TOTAL = 749/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS:

02. DIGITAL COMMUNICATION PP 100 40 52 P C 12. SIGNAL CODING & ESTIMATION THEORYPR 50 20 32 F 03. DIGITAL COMMUNICATION PR 50 20 38 P C 13. SYSTEM PROGRA. & OPERATING SYS. PP 100 40 51 F 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 70 P C 14. SYSTEM PROGRA. & OPERATING SYS. TW 50 20 41 F 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 40 P C 15. COMPUTER ORGANIZATION & ARCHITEC PP 100 40 62 F 06. MICROCONTROLLERS & APPLICATION PP 100 40 66 P C 16. INDUSTRIAL MANAGEMENT PP 100 40 67 F 07. MICROCONTROLLERS & APPLICATION PR 50 20 30 P C 17. WAVE THEORY & ANTENNA PP 100 40 54 F 08. DIGITAL SIGNAL PROCESSING PP 100 40 54 P C 18. WAVE THEORY & ANTENNA PR 50 20 35 F	T8053124 MULGE VAIJNATH SIDD	NNA			INDU	, 70925514E , T8053063 , PICT	, т8053124
***	01. CONTROL SYSTEMS 02. DIGITAL COMMUNICATION 03. DIGITAL COMMUNICATION 04. NETWORK SYNTHESIS & FILTER I 05. NETWORK SYNTHESIS & FILTER I 06. MICROCONTROLLERS & APPLICAT: 07. MICROCONTROLLERS & APPLICAT:	PP PP PR ESIGNPP ESIGNTW ON PP ON PR	100 50 100 50 100 50	40 20 40 20 40 20	59 P C 52 P C 38 P C 70 P C 40 P C 66 P C 30 P C	11. SIGNAL CODING & ESTIMATION THEORYPP 100 12. SIGNAL CODING & ESTIMATION THEORYPR 50 13. SYSTEM PROGRA.& OPERATING SYS. PP 100 14. SYSTEM PROGRA.& OPERATING SYS. TW 50 15. COMPUTER ORGANIZATION & ARCHITEC PP 100 16. INDUSTRIAL MANAGEMENT PP 100 17. WAVE THEORY & ANTENNA PP 100	40 60 P C 20 32 P C 40 51 P C 20 41 P C 40 62 P C 40 67 P C 40 54 P C
					<u> </u>		
	09. DIGITAL SIGNAL PROCESSING	OR	50 50	20	35 P C	19. MINI PROJECT & SEMINAR OR 50	20 41 P C 20 45 P C

GRAND TOTAL = 962/1500, RESULT: FIRST CLASS ORDN. 1 MARKS :

UNIVERSITY OF PUNE ,T.E.(2008 PAT.)(ELECTRONICS & TELEC.) EXAMINATION NOV. 2011 DATE : 02 MAR. 2012 CENTRÉ : PÙNE INSTITÚTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 41 (410) NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER T8053125 NITESH KR SINGH RITA , 70801532J , T8053065 , PICT , T8053125 01. CONTROL SYSTEMS PP 100 40 50 P 11. SIGNAL CODING & ESTIMATION THEORYPP 100 40 AA F 02. DIGITAL COMMUNICATION PP 100 40 AA F 12. SIGNAL CODING & ESTIMATION THEORYPR 50 20 AA F 03. DIGITAL COMMUNICATION PR 50 20 23 P C 13. SYSTEM PROGRA. & OPERATING SYS. PP 100 40 AA F 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 44 P C 14. SYSTEM PROGRA. & OPERATING SYS. TW 50 20 26 P C 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 22 P C 15. COMPUTER ORGANIZATION & ARCHITEC PP 100 40 46 P C 06. MICROCONTROLLERS & APPLICATION PP 100 40 44 P C 16. INDUSTRIAL MANAGEMENT PP 100 40 49 P C 07. MICROCONTROLLERS & APPLICATION PR 50 20 28 P C 17. WAVE THEORY & ANTENNA PP 100 40 AA F 08. DIGITAL SIGNAL PROCESSING PP 100 40 AA F 18. WAVE THEORY & ANTENNA PR 50 20 25 P C 09. DIGITAL SIGNAL PROCESSING OR 50 20 26 P C 19. MINI PROJECT & SEMINAR OR 50 20 30 P C GRAND TOTAL = 441/1500, RESULT: FAILS ORDN. 1 MARKS : T8053126 PATIL JAYA PANDHARINATH KAMAL , 70925548K , T8053071 , PICT , T8053126 GRAND TOTAL = 837/1500, RESULT: HIGHER SECOND CLASS ORDN. 1 MARKS: , 70925552H , Т8053073 , РІСТ , Т8053127 T8053127 PATIL SAYALI ANIL SUMATI 01. CONTROL SYSTEMS PP 100 40 56 P C 02. DIGITAL COMMUNICATION PP 100 40 58 P C 03. DIGITAL COMMUNICATION PR 50 20 41 P C GRAND TOTAL = 948/1500, RESULT: FIRST CLASS ORDN. 1 MARKS :

UNIVERSITY OF PUNE ,T.E.(2008 PAT.)(ELECTRONICS & TELEC.) EXAMINATION NOV. 2011 DATE: 02 MAR. 2012 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 42 (411) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MÍN. PASS MARKS, MARKS OBTAINÉD, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER T8053128 PRASHANT POPAT JAGTAP MUKTA , 70801568к , Т8053078 , РІСТ , Т8053128 PP 100 40 11. SIGNAL CODING & ESTIMATION THEORYPP 100 40 40 P 01. CONTROL SYSTEMS 46 P C 02. DIGITAL COMMUNICATION PP 100 40 19 F 03. DIGITAL COMMUNICATION PR 50 20 22 P C 12. SIGNAL CODING & ESTIMATION THEORYPR 50 20 24 P GRAND TOTAL = 601/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS : MADHURI , 71070206L , T8053084 , PICT , T8053129 T8053129 SAKHALE ANJALI ANAND 01. CONTROL SYSTEMS PP 100 40 46 P C 02. DIGITAL COMMUNICATION PP 100 40 40 P C 03. DIGITAL COMMUNICATION PR 50 20 34 P C 11. SIGNAL CODING & ESTIMATION THEORYPP 100 40 P C 12. STGNAL CODING & ESTIMATION THEORYPR 50 20 37 P 13. SYSTEM PROGRA. & OPERATING SYS. PP 100 40 43 P.C 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 62 P C 14. SYSTEM PROGRA.& OPERATING SYS. TW 50 20 42 P C 20 42 P C 15. COMPUTER ORGANIZATION & ARCHITEC PP 100 40 54 P C 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 06. MICROCONTROLLERS & APPLICATION PP 100 40 50 P C 16. INDUSTRIAL MANAGEMENT PP 100 40 49 P C 07. MICROCONTROLLERS & APPLICATION PR 50 20 33 P C 17. WAVE THEORY & ANTENNA PP 100 40 40 P C 08. DIGITAL SIGNAL PROCESSING PP 100 40 43 P C 18. WAVE THEORY & ANTENNA PR 50 20 28 P C 19. DIGITAL SIGNAL PROCESSING OR 50 20 34 P C 19. MINI PROJECT & SEMINAR OR 50 20 32 P C 10. ELECTRONIC DESIGN PRACTICE OR 50 20 27 P C 20. TEST & MEASUREMENT TECHNIQUES OR 50 20 28 P C GRAND TOTAL = 804/1500, RESULT: SECOND CLASS ORDN. 1 MARKS:

т8053130	SHELAR SONALI SUBHASH				SU	SHMA		, 70925599D	, т8053088 ,	Р	ICT	,	т8053	130
02. DIGITAL 03. DIGITAL 04. NETWORK 05. NETWORK 06. MICROCO 07. MICROCO 08. DIGITAL	SYSTEMS COMMUNICATION COMMUNICATION SYNTHESIS & FILTER DESIGNERS SYNTHESIS & APPLICATION NTROLLERS & APPLICATION SIGNAL PROCESSING SIGNAL PROCESSING		100 100 50 100 50 100 50 100 50	40 40 20 40 20 40 20 40 20	40 36 62 39 53 34		14 15 16 17 18	SIGNAL CODING OF SYSTEM PROGRA. SYSTEM PROGRA. COMPUTER ORGAN	ANTENNA ANTENNA	YPR PP TW	100 50 100 50 100 100 100 50 50	40 20 40 20 40 40 40 20 20	42 39 44 41 44 46 40 37 32	P C P C P C P C P C
ELECTRO	NIC DESIGN PRACTICE	OR	50	20	29	РС	20	. TEST & MEASURE	MENT TECHNIQUES	OR	50	20	35	РС

GRAND TOTAL = 828/1500, RESULT: HIGHER SECOND CLASS ORDN. 1 MARKS:

UNIVERSITY OF PUNE ,T.E.(2008 PAT.)(ELECTRONICS & TELEC.) EXAMINATION NOV. 2011 DATE: 02 MAR. 2012 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 43 (412) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MÍN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER T8053131 SNEHAL SOHANLAL CHHAJED UJWALA , 70925612E , T8053092 , PICT , T8053131 68 P C 59 P C 01. CONTROL SYSTEMS 11. SIGNAL CODING & ESTIMATION THEORYPP 100 100 59 P C 20 02. DIGITAL COMMUNICATION PP 40 12. SIGNAL CODING & ESTIMATION THEORYPR 50 40 P C F F PR 20 36 P C 50 100 61 P C 03. DIGITAL COMMUNICATION 13. SYSTEM PROGRA. & OPERATING SYS. PP 40 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 86 P C 14. SYSTEM PROGRA. & OPERATING SYS. TW 50 20 43 P C 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 43 P.C 15. COMPUTER ORGANIZATION & ARCHITEC PP 100 40 56 P C

16. INDUSTRIAL MANAGEMENT PP

20. TEST & MEASUREMENT TECHNIQUES OR

17. WAVE THEORY & ANTENNA

18. WAVE THEORY & ANTENNA 19. MINI PROJECT & SEMINAR 40

40

20

20

50 20 38 P C

58 P C

52 P C

35 P C

30 P

100

100

50

50

PP

PR

OR

100

50

100

OR

40

66 P C

20 32 P C

40 47 P C

50 20 36 P C

50 20 31 P C

GRAND TOTAL = 976/1500, RESULT: FIRST CLASS ORDN. 1 MARKS :

06. MICROCONTROLLERS & APPLICATION PP

07. MICROCONTROLLERS & APPLICATION PR

08. DIGITAL SIGNAL PROCESSING

09. DIGITAL SIGNAL PROCESSING
10. ELECTRONIC DESIGN PRACTICE

T8053132 VATSLA KANIKA	, 70801664С , Т8053101 , РІСТ	, т8053132
01. CONTROL SYSTEMS PP 100 40 40 P C 02. DIGITAL COMMUNICATION PP 100 40 40 P C 03. DIGITAL COMMUNICATION PR 50 20 25 P C 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 43 P C 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 37 P C 06. MICROCONTROLLERS & APPLICATION PP 100 40 55 P C 07. MICROCONTROLLERS & APPLICATION PR 50 20 26 P C 08. DIGITAL SIGNAL PROCESSING PP 100 40 31* P 09. DIGITAL SIGNAL PROCESSING OR 50 20 25 P C 10. ELECTRONIC DESIGN PRACTICE OR 50 20 21 P C	11. SIGNAL CODING & ESTIMATION THEORYPP 100 12. SIGNAL CODING & ESTIMATION THEORYPR 50 13. SYSTEM PROGRA.& OPERATING SYS. PP 100 14. SYSTEM PROGRA.& OPERATING SYS. TW 50 15. COMPUTER ORGANIZATION & ARCHITEC PP 100 16. INDUSTRIAL MANAGEMENT PP 100 17. WAVE THEORY & ANTENNA PP 100 18. WAVE THEORY & ANTENNA PR 50 19. MINI PROJECT & SEMINAR OR 50 20. TEST & MEASUREMENT TECHNIQUES OR 50	40 50 P C 20 34 P C 40 41 P C 20 38 P C 40 43 P C 40 52 P C 40 46 P C 20 23 P C 20 38 P C 20 38 P C

GRAND TOTAL = 738/1500, RESULT: PASS CLASS * [0.4] ORDN. 1 MARKS:

GRAND TOTAL = 798/1500, RESULT: SECOND CLASS ORDN. 1 MARKS :

UNIVERSITY OF PUNE ,T.E.(2008 PAT.)(ELECTRONICS & TELEC.) EXAMINATION NOV. 2011

DATE : 02 MAR. 2012 CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 44 (413)

NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8053134 WALKE RAHUL VASANTRAO MANDAKINI , 70701698D , T8053104 , PICT , T8053134

O1. CONTROL SOURCE AND ADDRESS OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
TROUBLE SEAT NO., COLLEGE, SEAT NO., COLLEG

02. DIGITAL COMMUNICATION PP 03. DIGITAL COMMUNICATION PR 02. DIGITAL COMMUNICATION PP 100 52 P C 34 P C 40 12. SIGNAL CODING & ESTIMATION THEORYPR 50 20 20 24 P C 100 50 13. SYSTEM PROGRA. & OPERATING SYS. PP 40 40 P C 04. NETWORK SYNTHESIS & FILTER DESIGNPP 100 40 59 P C 14. SYSTEM PROGRA. & OPERATING SYS. TW 50 20 40 P C 39 P.C 05. NETWORK SYNTHESIS & FILTER DESIGNTW 50 20 15. COMPUTER ORGANIZATION & ARCHITEC PP 100 40 52 P C 06. MICROCONTROLLERS & APPLICATION PP 100 40 49 P C 16. INDUSTRIAL MANAGEMENT PP 40 44 P C 100 07. MICROCONTROLLERS & APPLICATION PR 50

 50
 20
 28
 P C
 17. WAVE THEORY & ANTENNA
 PP

 100
 40
 50
 P C
 18. WAVE THEORY & ANTENNA
 PR

 50
 20
 24
 P C
 19. MINI PROJECT & SEMINAR
 OR

 50
 20
 23
 P C
 20. TEST & MEASUREMENT TECHNIQUES
 OR

 20 28 P C 17. WAVE THEORY & ANTENNA PP 100 40 47 P C 100 50 20 37 P C 08. DIGITAL SIGNAL PROCESSING 09. DIGITAL SIGNAL PROCESSING 50 20 24 PC 50 20 35 P C 10. ELECTRONIC DESIGN PRACTICE 50 20 35 P OR

GRAND TOTAL = 794/1500, RESULT: SECOND CLASS ORDN. 1 MARKS:

	E ,T.E.(2008 PAT.)(COMPUTER) PUNE INSTITUTE OF COMPUTER		PAGE NO. 01 (414)
NOTE: FIRST LINE : SEAT NO., NAME OF THE CAND OTHER LINES: HEAD OF PASSING, MAX. MARK		EG. NO., PREVIOUS SEAT NO.	, COLLEGE, SEAT NO.
T8054201 ABHIMANYU BHOSALE	SUJATA	, 71045355) ,	, PICT , T8054201
01. DATABASE MANAGEMENT SYSTEMS PP 100 02. DATA COMMUNICATION PP 100 03. MICROPROCESSORS & MICROCONTROLLERPP 100 04. DIGITAL SIGNAL PROCESSING PP 100 05. THEORY OF COMPUTATION PP 100 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 08. SIGNAL PROCESSING LABORATORY TW 25 09. SIGNAL PROCESSING LABORATORY OR 50 10. HARDWARE LABORATORY TW 25 11. HARDWARE LABORATORY PR 50	40 50 P 40 40 P 40 56 P 40 45 P 40 22 F 20 32 P 20 36 P 10 15 P 20 10 F 10 15 P 20 32 P		
FIRST TERM TOTAL = 353/750. ORDN. 1 MARKS :			
T8054202 ADHAV ASHISH DNYANDEO	SUJATA	, 71045359м ,	, PICT , T8054202
01. DATABASE MANAGEMENT SYSTEMS PP 100 02. DATA COMMUNICATION PP 100 03. MICROPROCESSORS & MICROCONTROLLERPP 100 04. DIGITAL SIGNAL PROCESSING PP 100 05. THEORY OF COMPUTATION PP 100 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 08. SIGNAL PROCESSING LABORATORY TW 25 09. SIGNAL PROCESSING LABORATORY OR 50 10. HARDWARE LABORATORY TW 25	40 60 P 40 52 P 40 57 P 40 48 P 40 40 P 20 45 P 20 41 P 10 21 P 20 34 P 10 23 P 20 42 P		
FIRST TERM TOTAL = 463/750. ORDN. 1 MARKS :			
TOPATO	VAVIITA	710452605	
T8054203 ADHIKARI DHANASHREE HEMCHANDRA 01. DATABASE MANAGEMENT SYSTEMS PP 100 02. DATA COMMUNICATION PP 100 03. MICROPROCESSORS & MICROCONTROLLERPP 100 04. DIGITAL SIGNAL PROCESSING PP 100 05. THEORY OF COMPUTATION PP 100 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 08. SIGNAL PROCESSING LABORATORY TW 25 09. SIGNAL PROCESSING LABORATORY OR 50 10. HARDWARE LABORATORY TW 25 11. HARDWARE LABORATORY PR 50 FIRST TERM TOTAL = 504/750.	YAYUTA 40 60 P 40 62 P 40 67 P 40 58 P 40 40 P 20 45 P 20 46 P 10 23 P 20 38 P 10 22 P 20 43 P	, 71045360E ,	, PICT , T8054203
ORDN. 1 MARKS :			

	E ,T.E.(2008 PAT.)(COMPUTER PUNE INSTITUTE OF COMPUTER		PAGE NO. 02 (415)
NOTE: FIRST LINE : SEAT NO., NAME OF THE CAND OTHER LINES: HEAD OF PASSING, MAX. MARK	IDATE, MOTHER, PERMANENT R		, COLLEGE, SEAT NO.
T8054204 ADITI JOSHI	SURUCHI	, 71045361C ,	, PICT , T8054204
01. DATABASE MANAGEMENT SYSTEMS PP 100 02. DATA COMMUNICATION PP 100 03. MICROPROCESSORS & MICROCONTROLLERPP 100 04. DIGITAL SIGNAL PROCESSING PP 100 05. THEORY OF COMPUTATION PP 100 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 08. SIGNAL PROCESSING LABORATORY TW 25 09. SIGNAL PROCESSING LABORATORY OR 50 10. HARDWARE LABORATORY TW 25 11. HARDWARE LABORATORY PR 50	40 60 P 40 52 P 40 61 P 40 55 P 40 40 P 20 35 P 20 38 P 10 15 P 20 34 P 10 20 P 20 20 P		
FIRST TERM TOTAL = 430/750. ORDN. 1 MARKS :			
T8054205 ADTANI VASHISHTHA MOHAN	BHAWANA	, 71045362м ,	
01. DATABASE MANAGEMENT SYSTEMS PP 100 02. DATA COMMUNICATION PP 100 03. MICROPROCESSORS & MICROCONTROLLERPP 100 04. DIGITAL SIGNAL PROCESSING PP 100 05. THEORY OF COMPUTATION PP 100 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 08. SIGNAL PROCESSING LABORATORY TW 25 09. SIGNAL PROCESSING LABORATORY OR 10. HARDWARE LABORATORY TW 25 11. HARDWARE LABORATORY PR 50	40 59 P 40 63 P 40 53 P 40 48 P 40 41 P 20 40 P 20 42 P 10 20 P 20 35 P 10 19 P 20 33 P		
FIRST TERM TOTAL = 453/750. ORDN. 1 MARKS :			
T8054206 AGRAWAL ANKIT UMESHKUMAR	SHAMADEVI	, 71045364н ,	, PICT , T8054206
01. DATABASE MANAGEMENT SYSTEMS PP 100 02. DATA COMMUNICATION PP 100 03. MICROPROCESSORS & MICROCONTROLLERPP 100 04. DIGITAL SIGNAL PROCESSING PP 100 05. THEORY OF COMPUTATION PP 100 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 08. SIGNAL PROCESSING LABORATORY TW 25 09. SIGNAL PROCESSING LABORATORY OR 50 10. HARDWARE LABORATORY TW 25 11. HARDWARE LABORATORY PR 50 FIRST TERM TOTAL = 464/750. ORDN. 1 MARKS:	40 64 P 40 55 P 40 66 P 40 48 P 40 55 P 20 38 P 20 36 P 10 16 P 20 33 P 10 19 P 20 34 P		

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UNIVERSITY OF PUNE ,T.E. (2008 PAT.) (COMPUTER) EXAMINATION NOV. 2011
   DATE: 02 MAR. 2012 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.
                                                                        PAGE NO. 03 ( 416)
NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
    OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
T8054207 AGRAWAL ANNU ARUN
                                      MEENA
                                                    , 71045365F , , , PICT , T8054207
 01. DATABASE MANAGEMENT SYSTEMS PP 100 02. DATA COMMUNICATION PP 100
                                     57 P
                                 40
                                     62 P
 03. MICROPROCESSORS & MICROCONTROLLERPP
                              100
                                  40
 04. DIGITAL SIGNAL PROCESSING PP
                              100
                                  40 56
 05. THEORY OF COMPUTATION
                              100
                                  40 43
 06. RDBMS & VISUAL PROGRAMMING LAB. TW
                                  20 42
                              50
 07. RDBMS & VISUAL PROGRAMMING LAB. PR
                              50
                                  20 45
 08. SIGNAL PROCESSING LABORATORY TW
                              25 10 17
 09. SIGNAL PROCESSING LABORATORY OR
                              50 20 35 P
 10. HARDWARE LABORATORY TW 25 10 21 P
 11. HARDWARE LABORATORY
                        PR 50 20 20 P
 FIRST TERM TOTAL = 457/750.
ORDN. 1 MARKS :
T8054208 AKASH DEEP AGRAWAL
                                      KAUSALYA , 71045366D , , PICT , T8054208
                                 40 62 P
                        PP 100
 O1. DATABASE MANAGEMENT SYSTEMS
 02. DATA COMMUNICATION
                  PP 100
                                  40
                                     67 P
 03. MICROPROCESSORS & MICROCONTROLLERPP 100
                                  40 74 P
 04. DIGITAL SIGNAL PROCESSING PP 100
                                  40 59 P
 05. THEORY OF COMPUTATION
                              100
                                  40
                                     51 P
                          PP
 06. RDBMS & VISUAL PROGRAMMING LAB. TW
                              50
                                  20 40 P
 07. RDBMS & VISUAL PROGRAMMING LAB. PR
                              50
                                  20 38
 08. SIGNAL PROCESSING LABORATORY TW
                              25
                                  10
 09. SIGNAL PROCESSING LABORATORY
                              50
                                  20 33
                          OR
 10. HARDWARE LABORATORY TW
                              25 10 22 P
 11. HARDWARE LABORATORY
                        PR 50 20 38 P
 FIRST TERM TOTAL = 505/750.
ORDN. 1 MARKS:
, 71045370B , , PICT , T8054209
 T8054209 ANIRUDH JODHA
                                      ANJU
 01. DATABASE MANAGEMENT SYSTEMS PP 100 40
                          PP 100 40 42
 02. DATA COMMUNICATION
 03. MICROPROCESSORS & MICROCONTROLLERPP 100
                                 40 47
 04. DIGITAL SIGNAL PROCESSING PP 100
                                 40 22
                                 40 29
                              100
 05. THEORY OF COMPUTATION
 06. RDBMS & VISUAL PROGRAMMING LAB. TW
                              50
 07. RDBMS & VISUAL PROGRAMMING LAB. PR
                              50
 08. SIGNAL PROCESSING LABORATORY TW
                                  10 16
 10. HARDWARE LABORATORY TW
                        OR
                                  20 22
                              50
                              25
                                  10 19
                                        Р
                              50 20
                                     36 P
 FIRST TERM TOTAL = 362/750.
ORDN. 1 MARKS:
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DATE: 02 MAR. 2012 CENTRE	NE ,T.E.(2008 PAT.)(COMPUTER PUNE INSTITUTE OF COMPUTER	TECHNOLOGY, PUNE.	PAGE NO. 04 (417)
NOTE: FIRST LINE : SEAT NO., NAME OF THE CAN OTHER LINES: HEAD OF PASSING, MAX. MAR		EG. NO., PREVIOUS SEAT NO.	, COLLEGE, SEAT NO.
T8054210 AVHAD PRATIK GULAB	МАМТА	, 71045377к ,	, PICT , T8054210
01. DATABASE MANAGEMENT SYSTEMS PP 100 02. DATA COMMUNICATION PP 100 03. MICROPROCESSORS & MICROCONTROLLERPP 100 04. DIGITAL SIGNAL PROCESSING PP 100 05. THEORY OF COMPUTATION PP 100 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 08. SIGNAL PROCESSING LABORATORY TW 25 09. SIGNAL PROCESSING LABORATORY OR 50 10. HARDWARE LABORATORY TW 25 11. HARDWARE LABORATORY PR 50	40 40 P 40 40 P 40 42 P 40 40 P 20 38 P 20 10 F 10 17 P 20 23 P 10 20 P		
FIRST TERM TOTAL = 330/750. ORDN. 1 MARKS :			
T8054211 AYACHIT DUSHYANT PRADEEPRAO	PRATIBHA		
01. DATABASE MANAGEMENT SYSTEMS PP 100 02. DATA COMMUNICATION PP 100 03. MICROPROCESSORS & MICROCONTROLLERPP 100 04. DIGITAL SIGNAL PROCESSING PP 100 05. THEORY OF COMPUTATION PP 100 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 08. SIGNAL PROCESSING LABORATORY TW 29 09. SIGNAL PROCESSING LABORATORY OR 50 10. HARDWARE LABORATORY TW 29 11. HARDWARE LABORATORY PR 50	40 53 P 40 75 P 40 40 P 40 41 P 20 43 P 20 42 P 10 18 P 20 30 P 10 20 P		
FIRST TERM TOTAL = 467/750. ORDN. 1 MARKS :			
T8054212 BAHULIKAR CHAITALI SANJAY	SNEHA		, PICT , T8054212
01. DATABASE MANAGEMENT SYSTEMS PP 100 02. DATA COMMUNICATION PP 100 03. MICROPROCESSORS & MICROCONTROLLERPP 100 04. DIGITAL SIGNAL PROCESSING PP 100 05. THEORY OF COMPUTATION PP 100 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 08. SIGNAL PROCESSING LABORATORY TW 29 09. SIGNAL PROCESSING LABORATORY OR 10. HARDWARE LABORATORY TW 21 11. HARDWARE LABORATORY PR 50 FIRST TERM TOTAL = 527/750. ORDN. 1 MARKS :	40 62 P 40 68 P 40 66 P 40 58 P 40 55 P 20 45 P 20 46 P 10 24 P 20 40 P		

DATE: 02 MAR. 2012 CENTRE:	E ,T.E.(2008 PAT.)(COMPUTER PUNE INSTITUTE OF COMPUTER		PAGE NO. 05 (418)
NOTE: FIRST LINE : SEAT NO., NAME OF THE CANE OTHER LINES: HEAD OF PASSING, MAX. MARK	IDATE, MOTHER, PERMANENT R S, MIN. PASS MARKS, MARKS		, COLLEGE, SEAT NO.
T8054213 BANG GAURAV SUNIL	SUNANDA	, 71045384в ,	, PICT , T8054213
01. DATABASE MANAGEMENT SYSTEMS PP 100 02. DATA COMMUNICATION PP 100 03. MICROPROCESSORS & MICROCONTROLLERPP 100 04. DIGITAL SIGNAL PROCESSING PP 100 05. THEORY OF COMPUTATION PP 100 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 08. SIGNAL PROCESSING LABORATORY TW 25 09. SIGNAL PROCESSING LABORATORY OR 50 10. HARDWARE LABORATORY TW 25 11. HARDWARE LABORATORY PR 50	40 54 P 40 51 P 40 66 P 40 54 P 40 43 P 20 40 P 20 44 P 10 22 P 20 41 P 10 18 P 20 43 P		
FIRST TERM TOTAL = 476/750. ORDN. 1 MARKS :			
T8054214 BHADADE MOHIT DNYANESH	SHOBHA	, 71045385L ,	
01. DATABASE MANAGEMENT SYSTEMS PP 100 02. DATA COMMUNICATION PP 100 03. MICROPROCESSORS & MICROCONTROLLERPP 100 04. DIGITAL SIGNAL PROCESSING PP 100 05. THEORY OF COMPUTATION PP 100 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 08. SIGNAL PROCESSING LABORATORY TW 25 09. SIGNAL PROCESSING LABORATORY OR 50 10. HARDWARE LABORATORY TW 25 11. HARDWARE LABORATORY PR 50	40 66 P 40 58 P 40 71 P 40 50 P 40 52 P 20 39 P 20 42 P 10 21 P 20 40 P 10 22 P 20 37 P		
FIRST TERM TOTAL = 498/750. ORDN. 1 MARKS :			
T8054215 BHANDARI MUKESH DHANPAL	DEEPA	, 70801363F ,	, PICT , T8054215
01. DATABASE MANAGEMENT SYSTEMS PP 100 02. DATA COMMUNICATION PP 100 03. MICROPROCESSORS & MICROCONTROLLERPP 100 04. DIGITAL SIGNAL PROCESSING PP 100 05. THEORY OF COMPUTATION PP 100 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 08. SIGNAL PROCESSING LABORATORY TW 25 09. SIGNAL PROCESSING LABORATORY OR 50 10. HARDWARE LABORATORY TW 25 11. HARDWARE LABORATORY PR 50 FIRST TERM TOTAL = 130/750.	40 40 P 40 AA F 40 07 F 40 14 F 40 AA F 20 20 P 20 AA F 10 14 P 20 20 P 10 15 P 20 AA F		
ORDN. 1 MARKS :			

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UNIVERSITY OF PUNE ,T.E. (2008 PAT.) (COMPUTER) EXAMINATION NOV. 2011
   DATE: 02 MAR. 2012 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.
                                                                         PAGE NO. 06 ( 419)
NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
    OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
T8054216 BHANDARKAR PRATIK PRAVIN
                                      KALPANA , 71132424H , , PICT , T8054216
 01. DATABASE MANAGEMENT SYSTEMS
 02. DATA COMMUNICATION
                          PP 100
                                 40 54 P
                                  40 68 P
 03. MICROPROCESSORS & MICROCONTROLLERPP
                              100
 04. DIGITAL SIGNAL PROCESSING PP
                              100
                                  40 41
                                        Р
 05. THEORY OF COMPUTATION
                              100
                                  40 40
 06. RDBMS & VISUAL PROGRAMMING LAB. TW
                                  20 39
                              50
 07. RDBMS & VISUAL PROGRAMMING LAB. PR
                               50
                                  20 42
 08. SIGNAL PROCESSING LABORATORY TW
                               25 10 21
 09. SIGNAL PROCESSING LABORATORY OR
                               50 20 38 P
 10. HARDWARE LABORATORY TW 25 10 22 P
11. HARDWARE LABORATORY PR 50 20 39 P
 FIRST TERM TOTAL = 466/750.
ORDN. 1 MARKS :
T8054217 BHANGALE PRATIK DIGAMBAR
                                      MEENAKSHI , 71132425F , , PICT , T8054217
                        PP 100 40 58 P
 01. DATABASE MANAGEMENT SYSTEMS
 02. DATA COMMUNICATION
                  PP 100
                                  40
                                    59 P
 03. MICROPROCESSORS & MICROCONTROLLERPP 100
                                  40 71 P
 04. DIGITAL SIGNAL PROCESSING PP 100
                                  40 40 P
                                  40 45 P
 05. THEORY OF COMPUTATION
                              100
                          PP
 06. RDBMS & VISUAL PROGRAMMING LAB. TW
                              50
                                  20 42
 07. RDBMS & VISUAL PROGRAMMING LAB. PR
                               50
                                  20 35 P
 08. SIGNAL PROCESSING LABORATORY TW
                                  10 18
 09. SIGNAL PROCESSING LABORATORY OR
                               50
                                  20 29 P
 10. HARDWARE LABORATORY TW
11. HARDWARE LABORATORY
                              25 10 21 P
 11. HARDWARE LABORATORY
                         PR 50 20 44 P
 FIRST TERM TOTAL = 462/750.
ORDN. 1 MARKS :
BHARATI , 71045389C , , PICT , T8054218
 T8054218 BHAVSAR CHAITANYA SHYAM
                          PP 100 40
 01. DATABASE MANAGEMENT SYSTEMS
                                     59 P
                          PP 100 40 41 P
 02. DATA COMMUNICATION
 03. MICROPROCESSORS & MICROCONTROLLERPP 100
                                 40 55 P
 04. DIGITAL SIGNAL PROCESSING PP 100
                                 40 44 P
                              100
                                 40 40 P
 05. THEORY OF COMPUTATION
 06. RDBMS & VISUAL PROGRAMMING LAB. TW
                               50
 07. RDBMS & VISUAL PROGRAMMING LAB. PR
                               50
 08. SIGNAL PROCESSING LABORATORY TW
                                  10 17
 10. HARDWARE LABORATORY TW
                        OR
                               50
                                  20 39
                               25
                                  10 19
                                        Р
                             50 20 40 P
 FIRST TERM TOTAL = 425/750.
ORDN. 1 MARKS :
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UNIVERSITY OF PUNE ,T.E. (2008 PAT.) (COMPUTER) EXAMINATION NOV. 2011
   DATE: 02 MAR. 2012 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.
                                                                        PAGE NO. 07 ( 420)
NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
    OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
T8054219 BHOIR SARIKA DAMU
                                      ANUSAYA , 71045391E , , PICT , T8054219
 02. DATA COMMUNICATION PP 100
03. MTCROPPOCESSORS 9
                                 40 40 P
                                  40 44 P
 04. DIGITAL SIGNAL PROCESSING PP
                              100
                                  40
                                     24 F
 05. THEORY OF COMPUTATION
                              100
                                  40 22
 06. RDBMS & VISUAL PROGRAMMING LAB. TW
                                  20 40
                              50
 07. RDBMS & VISUAL PROGRAMMING LAB. PR
                              50
                                  20 06 F
 08. SIGNAL PROCESSING LABORATORY TW
                              25 10 19
 09. SIGNAL PROCESSING LABORATORY OR
                              50 20 18 F
 10. HARDWARE LABORATORY TW 25 10 21 P
11. HARDWARE LABORATORY PR 50 20 08 F
 FIRST TERM TOTAL = 282/750.
ORDN. 1 MARKS :
T8054220 BHOSALE SHITAL MANOHAR
                                               , 71132426D , , PICT , T8054220
                                      JAYA
 01. DATABASE MANAGEMENT SYSTEMS PP 100 40 45 P
 02. DATA COMMUNICATION
                   PP 100
                                 40 27 F
 03. MICROPROCESSORS & MICROCONTROLLERPP 100
                                  40 30 F
 04. DIGITAL SIGNAL PROCESSING PP 100
                                  40 17 F
 05. THEORY OF COMPUTATION
                              100
                          PP
 06. RDBMS & VISUAL PROGRAMMING LAB. TW
                              50
                                  20 34
 07. RDBMS & VISUAL PROGRAMMING LAB. PR
                              50
                                  20 16 F
 08. SIGNAL PROCESSING LABORATORY TW
                              25
                                  10 15
 09. SIGNAL PROCESSING LABORATORY OR
                              50
                                  20 10 F
 10. HARDWARE LABORATORY TW
                              25 10 18 P
 11. HARDWARE LABORATORY
                        PR 50 20 10 F
 FIRST TERM TOTAL = 231/750.
ORDN. 1 MARKS:
GEETANJALI , 71045395H , , PICT , T8054221
 T8054221 BHURKE POOJA MILIND
                        PP 100 40 54 P
 O1. DATABASE MANAGEMENT SYSTEMS
                        PP 100 40 49 P
 02. DATA COMMUNICATION
 03. MICROPROCESSORS & MICROCONTROLLERPP 100
                                 40 55 P
 04. DIGITAL SIGNAL PROCESSING PP 100
                                 40 47
                                        Р
                              100
                                 40 40
 05. THEORY OF COMPUTATION
 06. RDBMS & VISUAL PROGRAMMING LAB. TW
                              50
 07. RDBMS & VISUAL PROGRAMMING LAB. PR
                              50
 08. SIGNAL PROCESSING LABORATORY TW
                                  10 16
 10. HARDWARE LABORATORY TW
                        OR
                              50
                                  20 18
                              25
                                  10
                                     20
                                        Р
                             50 20 42
 FIRST TERM TOTAL = 407/750.
ORDN. 1 MARKS :
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UNIVERSITY OF PUNE ,T.E. (2008 PAT.) (COMPUTER) EXAMINATION NOV. 2011
   DATE: 02 MAR. 2012 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.
                                                                         PAGE NO. 08 ( 421)
NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
    OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
T8054222 BISWAJIT DEY
                                      TAPATI RANI , 71045397D , , PICT , T8054222
 02. DATA COMMUNICATION
03. MTCROPPOCESSOR
                          PP 100
                                      55 P
                                  40
                                      62 P
 03. MICROPROCESSORS & MICROCONTROLLERPP
                              100
                                  40
 04. DIGITAL SIGNAL PROCESSING PP
                              100
                                  40 44
                                         Р
                                  40 49
 05. THEORY OF COMPUTATION
                              100
 06. RDBMS & VISUAL PROGRAMMING LAB. TW
                                  20 43
                               50
 07. RDBMS & VISUAL PROGRAMMING LAB. PR
                               50
                                  20 47
 08. SIGNAL PROCESSING LABORATORY TW
                               25 10 20 P
 09. SIGNAL PROCESSING LABORATORY OR
                               50 20 38 P
 10. HARDWARE LABORATORY TW 25 10 21 P
11. HARDWARE LABORATORY PR 50 20 41 P
 FIRST TERM TOTAL = 477/750.
ORDN. 1 MARKS :
T8054223 CHANDORKAR ANUJA ANIRUDDHA
                                      ARUNA
                                                 , 71045401F , , PICT , T8054223
                          PP 100 40 62 P
 O1. DATABASE MANAGEMENT SYSTEMS
 02. DATA COMMUNICATION
                          PP 100
                                  40 57 P
 03. MICROPROCESSORS & MICROCONTROLLERPP 100
                                  40 49 P
 04. DIGITAL SIGNAL PROCESSING PP 100
                                  40 47 P
 05. THEORY OF COMPUTATION
                              100
                                  40 40 P
                          PP
 06. RDBMS & VISUAL PROGRAMMING LAB. TW
                              50
                                  20 41 P
 07. RDBMS & VISUAL PROGRAMMING LAB. PR
                               50
                                  20 44 P
 08. SIGNAL PROCESSING LABORATORY TW
                               25
                                  10 16
 09. SIGNAL PROCESSING LABORATORY
                               50
                                  20 38
                          OR
 10. HARDWARE LABORATORY TW
                               25 10 22
 11. HARDWARE LABORATORY
                         PR 50 20 40
 FIRST TERM TOTAL = 456/750.
ORDN. 1 MARKS :
, 71045404L , , PICT , T8054224
 T8054224 CHAUDHARI DIPTI KRISHNA
                                       SHARDA
                          PP 100 40 42
 O1. DATABASE MANAGEMENT SYSTEMS
                          PP 100 40 46 P
 02. DATA COMMUNICATION
 03. MICROPROCESSORS & MICROCONTROLLERPP 100
                                  40 43 P
 04. DIGITAL SIGNAL PROCESSING PP 100
                                  40 40 P
                              100
                                  40 42
 05. THEORY OF COMPUTATION
 06. RDBMS & VISUAL PROGRAMMING LAB. TW
                               50
                                  20 33 P
 07. RDBMS & VISUAL PROGRAMMING LAB. PR
                               50
                                  20 28
 08. SIGNAL PROCESSING LABORATORY TW
                                  10 15
 10. HARDWARE LABORATORY TW
                        OR
                               50
                                  20 24
                                      20
                               25
                                  10
                                         Р
                               50 20
                                     23
 FIRST TERM TOTAL = 356/750.
ORDN. 1 MARKS :
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DATE: 02 MAR. 2012 CENTRE:	IE ,T.E.(2008 PAT.)(COMPUTER PUNE INSTITUTE OF COMPUTER	TECHNOLOGY, PUNE.	PAGE NO. 09 (422)
NOTE: FIRST LINE : SEAT NO., NAME OF THE CAND OTHER LINES: HEAD OF PASSING, MAX. MARK		EG. NO., PREVIOUS SEAT NO. OBTAINED, P/F:PASS/FAIL,	., COLLEGE, SEAT NO. C:PREVIOUS CARRY OVER
T8054225 CHAUDHARI RASHMI CHANDRAKANT	KARUNA	, 71045405) ,	, PICT , T8054225
01. DATABASE MANAGEMENT SYSTEMS PP 100 02. DATA COMMUNICATION PP 100 03. MICROPROCESSORS & MICROCONTROLLERPP 100 04. DIGITAL SIGNAL PROCESSING PP 100 05. THEORY OF COMPUTATION PP 100 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 08. SIGNAL PROCESSING LABORATORY TW 25 09. SIGNAL PROCESSING LABORATORY OR 50 10. HARDWARE LABORATORY TW 25 11. HARDWARE LABORATORY PR 50	40 52 P 40 53 P 40 63 P 40 54 P 40 49 P 20 44 P 20 36 P 10 19 P 20 35 P 10 23 P 20 42 P		
FIRST TERM TOTAL = 470/750. ORDN. 1 MARKS :			
T8054226 CHAVAN ASHISH VIJAYKUMAR	RAJESHREE		, PICT , T8054226
01. DATABASE MANAGEMENT SYSTEMS PP 100 02. DATA COMMUNICATION PP 100 03. MICROPROCESSORS & MICROCONTROLLERPP 100 04. DIGITAL SIGNAL PROCESSING PP 100 05. THEORY OF COMPUTATION PP 100 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 08. SIGNAL PROCESSING LABORATORY TW 25 09. SIGNAL PROCESSING LABORATORY OR 50 10. HARDWARE LABORATORY TW 25 11. HARDWARE LABORATORY PR 50	40 60 P 40 49 P 40 49 P 40 48 P 40 22 F 20 34 P 20 32 P 10 15 P 20 10 F 10 16 P 20 20 P		
FIRST TERM TOTAL = 355/750. ORDN. 1 MARKS :			
T8054227 CHHAJED DARSHAN RAJKUMAR	KUSUM	, 71045407E ,	, PICT , T8054227
01. DATABASE MANAGEMENT SYSTEMS PP 100 02. DATA COMMUNICATION PP 100 03. MICROPROCESSORS & MICROCONTROLLERPP 100 04. DIGITAL SIGNAL PROCESSING PP 100 05. THEORY OF COMPUTATION PP 100 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 08. SIGNAL PROCESSING LABORATORY TW 25 09. SIGNAL PROCESSING LABORATORY OR 50 10. HARDWARE LABORATORY TW 25 11. HARDWARE LABORATORY PR 50	40 62 P 40 50 P 40 65 P 40 51 P 40 52 P 20 40 P 20 42 P 10 20 P 20 35 P 10 21 P 20 20 P		
FIRST TERM TOTAL = 458/750. ORDN. 1 MARKS :			

DATE: 02 MAR. 2012 CENTRE	PUNE ,T.E.(2008 PAT.)(COMPUTER) : PUNE INSTITUTE OF COMPUTER T	ECHNOLOGY, PUNE.	PAGE NO. 10 (423)
NOTE: FIRST LINE : SEAT NO., NAME OF THE C OTHER LINES: HEAD OF PASSING, MAX. M		G. NO., PREVIOUS SEAT NO., OBTAINED, P/F:PASS/FAIL,	COLLEGE, SEAT NO. C:PREVIOUS CARRY OVER
T8054228 CHHAJED NIKHIL VINOD	MANISHA	, 71045408C ,	, PICT , T8054228
02. DATA COMMUNICATION PP 103. MICROPROCESSORS & MICROCONTROLLERPP 104. DIGITAL SIGNAL PROCESSING PP 105. THEORY OF COMPUTATION PP 106. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 08. SIGNAL PROCESSING LABORATORY TW 09. SIGNAL PROCESSING LABORATORY OR 10. HARDWARE LABORATORY TW	00		
FIRST TERM TOTAL = 442/750. ORDN. 1 MARKS :			
	ANURADHA		
02. DATA COMMUNICATION PP 103. MICROPROCESSORS & MICROCONTROLLERPP 104. DIGITAL SIGNAL PROCESSING PP 105. THEORY OF COMPUTATION PP 106. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 08. SIGNAL PROCESSING LABORATORY TW 09. SIGNAL PROCESSING LABORATORY OR 10. HARDWARE LABORATORY TW	00		
FIRST TERM TOTAL = 303/750. ORDN. 1 MARKS :			
T8054230 DESHMUKH SMITA ARUN	ASHLESHA		
01. DATABASE MANAGEMENT SYSTEMS PP 102. DATA COMMUNICATION PP 1103. MICROPROCESSORS & MICROCONTROLLERPP 1104. DIGITAL SIGNAL PROCESSING PP 1105. THEORY OF COMPUTATION PP 1106. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 08. SIGNAL PROCESSING LABORATORY TW 09. SIGNAL PROCESSING LABORATORY OR 10. HARDWARE LABORATORY TW	00		,

DATE : 02 MAR. 2012 C	ENTRI	E : P	UNE I	INSTI	TUTE OF COMPUTE	ER) EXAMINATION NOV R TECHNOLOGY, PUNE.			11 (424)
NOTE: FIRST LINE : SEAT NO., NAME OF OTHER LINES: HEAD OF PASSING, MA	THE (CANDI MARKS	DATE,	, MO	THER, PERMANENT ASS MARKS, MARI	KS OBTAINED, P/F:P/	S SEAT NO., CO ASS/FAIL, C:PF	DLLEGE, S REVIOUS CAR	SEAT NO. RY OVER
T8054231 DISHA DAWANI					ENA	, 71045419〕	, , , , ,		. т8054231
01. DATABASE MANAGEMENT SYSTEMS POR CONTROLLER POR	PPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPP	100 100 100 100 100 50 50 25 50 25	40 40 40 40 20 20 10 20 10 20	56 44 63 45 52 41 47 19 28 21 35	P P P P P P P P				, and the second
FIRST TERM TOTAL = 451/750. RDN. 1 MARKS :									
T8054232 DUBEY AAKRITI MANOJ								PICT	
01. DATABASE MANAGEMENT SYSTEMS POR CONTROLLER POR	PPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPP	100 100 100 100 100 50 50 25 50 25	40 40 40 40 40 20 20 10 20 10 20	57 56 60 46 29 45 47 21 28 21 43	P P P F P P				
FIRST TERM TOTAL = 453/750. RDN. 1 MARKS :									
T8054233 DUBEY SHREYA CHANDRAMA				TA	RA	, 71045422J	,	PICT	, т8054233
01. DATABASE MANAGEMENT SYSTEMS POR CONTROLLER POR	PPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPP	100 100 100 100 100 50 50 25 50 25	40 40 40 40 20 20 10 20 10 20	55 46 64 48 50 40 42 19 30 20	P P P P P P P				
10. HARDWARE LABORATORY TO 11. HARDWARE LABORATORY PI	'R								

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UNIVERSITY OF PUNE ,T.E. (2008 PAT.) (COMPUTER) EXAMINATION NOV. 2011
   DATE: 02 MAR. 2012 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.
                                                                       PAGE NO. 12 ( 425)
NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
    OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
T8054234 DURGESH CHAPORKAR
                                     SUMEDHA
                                            01. DATABASE MANAGEMENT SYSTEMS PP 100 02. DATA COMMUNICATION PP 100
                                 40
                                     46 P
                                 40 56 P
 03. MICROPROCESSORS & MICROCONTROLLERPP
                             100
 04. DIGITAL SIGNAL PROCESSING PP
                             100
                                 40 43
                                       Р
 05. THEORY OF COMPUTATION
                             100
                                 40 40
 06. RDBMS & VISUAL PROGRAMMING LAB. TW
                                 20 28 P
                              50
 07. RDBMS & VISUAL PROGRAMMING LAB. PR
                              50
                                 20 30
 08. SIGNAL PROCESSING LABORATORY TW
                              25 10 10 P
 09. SIGNAL PROCESSING LABORATORY OR
                              50 20 34 P
 10. HARDWARE LABORATORY TW 25 10 10 P
 11. HARDWARE LABORATORY
                        PR 50 20 08 F
 FIRST TERM TOTAL = 363/750.
ORDN. 1 MARKS :
T8054236 GADE ABHIJEET BHARAT
                                           , 71132427B , , PICT , T8054236
                                     MANGAL
                        PP 100 40 70 P
 01. DATABASE MANAGEMENT SYSTEMS
                                 40 58 P
 02. DATA COMMUNICATION
                  PP 100
 03. MICROPROCESSORS & MICROCONTROLLERPP 100
                                 40 69 P
 04. DIGITAL SIGNAL PROCESSING PP 100
                                 40 49 P
                                 40 53 P
 05. THEORY OF COMPUTATION
                             100
                          PP
 06. RDBMS & VISUAL PROGRAMMING LAB. TW
                             50
                                 20 43 P
 07. RDBMS & VISUAL PROGRAMMING LAB. PR
                              50
                                 20 38
 08. SIGNAL PROCESSING LABORATORY TW
                              25
                                 10 17
 09. SIGNAL PROCESSING LABORATORY OR
                              50
                                 20 34
 10. HARDWARE LABORATORY TW
                              25 10 21 P
 11. HARDWARE LABORATORY
                        PR 50 20 25 P
 FIRST TERM TOTAL = 477/750.
ORDN. 1 MARKS :
SUNITA , 71045428H , , , PICT , T8054237
 T8054237 GADIA SAURABH PRASHANT
                          PP 100 40 46
 01. DATABASE MANAGEMENT SYSTEMS
                         PP 100 40 67 P
 02. DATA COMMUNICATION
 03. MICROPROCESSORS & MICROCONTROLLERPP 100
                                 40 68 P
 04. DIGITAL SIGNAL PROCESSING PP 100
                                 40 56
                                       Р
                                 40 53
                             100
 05. THEORY OF COMPUTATION
 06. RDBMS & VISUAL PROGRAMMING LAB. TW
                              50
                                 20 43 P
 07. RDBMS & VISUAL PROGRAMMING LAB. PR
                              50
                                 20 38 P
 08. SIGNAL PROCESSING LABORATORY TW
                                 10 17
 10. HARDWARE LABORATORY TW
                        OR
                              50
                                 20 40
                              25
                                 10
                                    21
                                       Р
                             50 20 40
 FIRST TERM TOTAL = 489/750.
ORDN. 1 MARKS :
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UNIVERSITY OF PUNE ,T.E. (2008 PAT.) (COMPUTER) EXAMINATION NOV. 2011
   DATE: 02 MAR. 2012 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.
                                                                        PAGE NO. 13 ( 426)
NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
    OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
T8054238 GADSING MAYURI LAXMAN
                                      SAVITA , 71045429F , , PICT , T8054238
 01. DATABASE MANAGEMENT SYSTEMS
 02. DATA COMMUNICATION
                          PP 100
                                  40
                                     47 P
                                  40 55 P
 03. MICROPROCESSORS & MICROCONTROLLERPP
                              100
 04. DIGITAL SIGNAL PROCESSING PP
                              100
                                  40 49 P
 05. THEORY OF COMPUTATION
                              100
                                  40
                                     57
 06. RDBMS & VISUAL PROGRAMMING LAB. TW
                                  20 38
                              50
 07. RDBMS & VISUAL PROGRAMMING LAB. PR
                              50
                                  20 28 P
 08. SIGNAL PROCESSING LABORATORY TW
                              25 10 16 P
 09. SIGNAL PROCESSING LABORATORY OR
                              50 20 41 P
 10. HARDWARE LABORATORY TW 25 10 20 P 11. HARDWARE LABORATORY PR 50 20 35 P
 FIRST TERM TOTAL = 437/750.
ORDN. 1 MARKS :
T8054239 GAIKWAD MAHESH EKNATH
                                      LATA
                                               01. DATABASE MANAGEMENT SYSTEMS PP 100 40 P
                                 40 42 P
 02. DATA COMMUNICATION
                   PP 100
 03. MICROPROCESSORS & MICROCONTROLLERPP 100
                                 40 42 P
 04. DIGITAL SIGNAL PROCESSING PP 100
                                  40 45 P
 05. THEORY OF COMPUTATION
                              100
                                  40 41 P
                          PP
 06. RDBMS & VISUAL PROGRAMMING LAB. TW
                             50
                                  20 34 P
 07. RDBMS & VISUAL PROGRAMMING LAB. PR
                              50
                                  20 08 F
 08. SIGNAL PROCESSING LABORATORY TW
                              25
                                  10 14
 09. SIGNAL PROCESSING LABORATORY OR
                              50
                                  20 12
 10. HARDWARE LABORATORY TW
11. HARDWARE LABORATORY
                              25 10 13 P
 11. HARDWARE LABORATORY
                        PR 50 20 05 F
 FIRST TERM TOTAL = 296/750.
ORDN. 1 MARKS :
VAISHALI , 71045432F , , PICT , T8054240
 T8054240 GAIKWAD PUSHAKAR KISHOR
                          PP 100 40
 O1. DATABASE MANAGEMENT SYSTEMS
                                     61 P
                          PP 100 40 62 P
 02. DATA COMMUNICATION
 03. MICROPROCESSORS & MICROCONTROLLERPP 100
                                 40 61 P
 04. DIGITAL SIGNAL PROCESSING PP 100
                                 40 53
                                        Р
                                 40 57
                              100
                                        Р
 05. THEORY OF COMPUTATION
 06. RDBMS & VISUAL PROGRAMMING LAB. TW
                              50
                                  20 42 P
 07. RDBMS & VISUAL PROGRAMMING LAB. PR
                              50
 08. SIGNAL PROCESSING LABORATORY TW
                                 10 18
 10. HARDWARE LABORATORY TW
                        OR
                                  20 20
                              50
                              25
                                     20
                                 10
                                        Р
                             50 20
                                     35 P
 FIRST TERM TOTAL = 473/750.
ORDN. 1 MARKS :
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UNIVERSITY OF PUNE ,T.E. (2008 PAT.) (COMPUTER) EXAMINATION NOV. 2011
   DATE : 02 MAR. 2012 CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.
                                                                        PAGE NO. 14 ( 427)
NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
    OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
T8054241 GANDHI ANUJ VIDYUT
                                      SHARMILA , 71045433D , , PICT , T8054241
 01. DATABASE MANAGEMENT SYSTEMS PP 100 02. DATA COMMUNICATION PP 100
                                  40 51 P
                                     69 P
 03. MICROPROCESSORS & MICROCONTROLLERPP
                              100
                                  40
 04. DIGITAL SIGNAL PROCESSING PP
                              100
                                  40
                                     53 P
 05. THEORY OF COMPUTATION
                              100
                                  40 60
 06. RDBMS & VISUAL PROGRAMMING LAB. TW
                                  20 40 P
                              50
                                  20 40 P
 07. RDBMS & VISUAL PROGRAMMING LAB. PR
                              50
 08. SIGNAL PROCESSING LABORATORY TW
                              25 10 17
 09. SIGNAL PROCESSING LABORATORY OR
                              50 20 20 P
 10. HARDWARE LABORATORY TW 25 10 20 P
 11. HARDWARE LABORATORY
                        PR 50 20 28 P
 FIRST TERM TOTAL = 462/750.
ORDN. 1 MARKS :
BALA DEVI , 71045434B , , PICT , T8054242
 T8054242 GAUTAM KUMAR
 01. DATABASE MANAGEMENT SYSTEMS PP 100
                                 40 63 P
 02. DATA COMMUNICATION
                  PP 100
                                  40
                                     59 P
 03. MICROPROCESSORS & MICROCONTROLLERPP 100
                                  40 75 P
 04. DIGITAL SIGNAL PROCESSING PP 100
                                  40 59 P
                                     59 P
 05. THEORY OF COMPUTATION
                              100
                                  40
                          PP
 06. RDBMS & VISUAL PROGRAMMING LAB. TW
                              50
                                  20
                                     34 P
 07. RDBMS & VISUAL PROGRAMMING LAB. PR
                               50
                                  20 30 P
 08. SIGNAL PROCESSING LABORATORY TW
                               25
                                  10 16
 09. SIGNAL PROCESSING LABORATORY OR
                              50 20 34 P
25 10 18 P
                              50
 10. HARDWARE LABORATORY TW
 11. HARDWARE LABORATORY
                         PR 50 20 34
 FIRST TERM TOTAL = 481/750.
ORDN. 1 MARKS :
, 71132428L , , PICT , T8054243
 T8054243 GOKHALE GAURAV SUHAS
                                      SWATI
                        PP 100 40
 01. DATABASE MANAGEMENT SYSTEMS
                        PP 100 40 50 P
 02. DATA COMMUNICATION
                                 40 66 P
 03. MICROPROCESSORS & MICROCONTROLLERPP 100
 04. DIGITAL SIGNAL PROCESSING PP 100
                                 40 45 P
                              100
                                 40 46 P
 05. THEORY OF COMPUTATION
 06. RDBMS & VISUAL PROGRAMMING LAB. TW
                              50
                                  20 28 P
 07. RDBMS & VISUAL PROGRAMMING LAB. PR
                               50
                                  20 21
 08. SIGNAL PROCESSING LABORATORY TW
                                  10 14
 10. HARDWARE LABORATORY TW
                        OR
                              50
                                  20 12
                              25
                                  10 13
                                        Р
                             50 20 05 F
 FIRST TERM TOTAL = 363/750.
ORDN. 1 MARKS :
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				AT.)(COMPUTER) OF COMPUTER 1			PAGE N	0. 15 (428)
NOTE: FIRST LINE : SEAT NO., NAME OF THE OTHER LINES: HEAD OF PASSING, MAX	 IE CANDID	ATE,	 MOTHER				., COLLEGE,	SEAT NO.
T8054244 GUPTA TARUN SATYABHUSHAN			KANAK		, 71045442C	,	, PICT	, т8054244
01. DATABASE MANAGEMENT SYSTEMS PP 02. DATA COMMUNICATION PP 03. MICROPROCESSORS & MICROCONTROLLERPP 04. DIGITAL SIGNAL PROCESSING PP 05. THEORY OF COMPUTATION PP 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 08. SIGNAL PROCESSING LABORATORY TW 09. SIGNAL PROCESSING LABORATORY OR 10. HARDWARE LABORATORY TW 11. HARDWARE LABORATORY PR	100 100 100 100 50 50 25 50 25	40 6 40 5 40 7 40 5 40 5 20 3 20 3 10 1 20 2 10 1 20 2	8 P 5 P 4 P 6 P 8 P 6 P 8 P 7 P					
FIRST TERM TOTAL = 469/750. ORDN. 1 MARKS :								
T8054245 HARSHIT SINGHANIA			SEEMA		, 71045445H	,	, PICT	, т8054245
01. DATABASE MANAGEMENT SYSTEMS PP 02. DATA COMMUNICATION PP 03. MICROPROCESSORS & MICROCONTROLLERPP 04. DIGITAL SIGNAL PROCESSING PP 05. THEORY OF COMPUTATION PP 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 08. SIGNAL PROCESSING LABORATORY TW 09. SIGNAL PROCESSING LABORATORY OR 10. HARDWARE LABORATORY PR	100 100 100 100 50 50 25 50 25	40 4 40 5 40 4 40 4 40 4 20 3 20 4 10 1 20 1 10 2 20 2	P P P P P P P P P P P P P P P P P P P					
FIRST TERM TOTAL = 364/750. ORDN. 1 MARKS :								
T8054246 JADHAV PRASHANT UTTAM			ANITA		, 71045454G	,	, PICT	, т8054246
01. DATABASE MANAGEMENT SYSTEMS PP 02. DATA COMMUNICATION PP 03. MICROPROCESSORS & MICROCONTROLLERPP 04. DIGITAL SIGNAL PROCESSING PP 05. THEORY OF COMPUTATION PP 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 08. SIGNAL PROCESSING LABORATORY TW 09. SIGNAL PROCESSING LABORATORY OR 10. HARDWARE LABORATORY TW 11. HARDWARE LABORATORY PR FIRST TERM TOTAL = 160/750. ORDN. 1 MARKS :	100 100 100 100 50 50 25 50 25	40 3 40 1 40 2 40 1 40 A 20 2 20 0 10 1 20 2 10 1 20 A	F F F F F F F F F F F F F F F F F F F					

		E.(2008 PAT.)(COMPUTE			PAGE NO. 1	L6 (429)
	CANDIDATE		REG. NO., PREVIOU	S SEAT NO.,	COLLEGE, SEA	AT NO.
T8054247 JAIN PUJAN PRAFULLAKUMAR		JYOTI	, 71045456C	,	, PICT	, т8054247
02. DATA COMMUNICATION PP 03. MICROPROCESSORS & MICROCONTROLLERPP 04. DIGITAL SIGNAL PROCESSING PP	100 40 100 40 100 40 100 40 100 40 50 20 50 20 25 10 50 20 25 10 50 20	57 P 43 P 51 P 45 P 42 P 35 P 32 P 17 P 24 P 18 P 26 P				
FIRST TERM TOTAL = 390/750. ORDN. 1 MARKS :						
T8054248 JAIN SAKSHI PARAS		JYOTI	, 71045457м	,	, PICT	, т8054248
02. DATA COMMUNICATION PP 03. MICROPROCESSORS & MICROCONTROLLERPP 04. DIGITAL SIGNAL PROCESSING PP	100 40 100 40 100 40 100 40 100 40 50 20 50 20 25 10 50 20 25 10 50 20	67 P 57 P 56 P 46 P 58 P 41 P 19 P 33 P 21 P 39 P				
FIRST TERM TOTAL = 479/750. ORDN. 1 MARKS :						
T8054249 JAISWAL PRATIK PRAKASH		KALPANA	, 71045458K	,	, PICT	, т8054249
02. DATA COMMUNICATION PP 03. MICROPROCESSORS & MICROCONTROLLERPP	100 40 100 40 100 40 100 40 100 40 50 20 55 20 25 10 50 20 25 10 50 20	68 P 56 P 69 P 40 P 49 P 34 P 35 P 17 P 28 P 15 P 20 P				

DATE : 02 MAR. 2012 CEN	TRE : P	UNE I	NSTITUTE OF COMPL	PUTER) EXAMINATION NO ITER TECHNOLOGY, PUNE			. 17 (430)
NOTE: FIRST LINE : SEAT NO., NAME OF TH OTHER LINES: HEAD OF PASSING, MAX	E CANDI . MARKS	DATE, , MI	MOTHER, PERMANE N. PASS MARKS, M		OUS SEAT NO PASS/FAIL,	., COLLEGE, C:PREVIOUS CA	SEAT NO.
T8054250 JAMES DADO			ANIA	710454600	,	, PICT	, т8054250
01. DATABASE MANAGEMENT SYSTEMS PP 02. DATA COMMUNICATION PP 03. MICROPROCESSORS & MICROCONTROLLERPP 04. DIGITAL SIGNAL PROCESSING PP 05. THEORY OF COMPUTATION PP 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 08. SIGNAL PROCESSING LABORATORY TW 09. SIGNAL PROCESSING LABORATORY OR 10. HARDWARE LABORATORY TW 11. HARDWARE LABORATORY PR	100 100 100 100 100 50 50 25 50 25	40 40 40 40 20 20 10 20 10	40 P 48 P 47 P 30 F 42 P 28 P 15 F 11 P 34 P 13 P 05 F				
FIRST TERM TOTAL = 313/750. ORDN. 1 MARKS :							
T8054251 JASPREET SINGH HORA			BHUPENDER	, 71045461K	,		
01. DATABASE MANAGEMENT SYSTEMS PP 02. DATA COMMUNICATION PP 03. MICROPROCESSORS & MICROCONTROLLERPP 04. DIGITAL SIGNAL PROCESSING PP 05. THEORY OF COMPUTATION PP 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 08. SIGNAL PROCESSING LABORATORY TW 09. SIGNAL PROCESSING LABORATORY OR 10. HARDWARE LABORATORY TW 11. HARDWARE LABORATORY PR	100 100 100 100 100 50 50 25 50 25	40 40 40 40 20 20 10 20 10 20	65 P 55 P 56 P 40 P 47 P 40 P 10 F 19 P 30 P 17 P 05 F				
FIRST TERM TOTAL = 384/750. ORDN. 1 MARKS :							
T8054252 JONDHALE KANCHAN BABUNATH			ALKA	, 71132429〕	,	, PICT	, т8054252
01. DATABASE MANAGEMENT SYSTEMS PP 02. DATA COMMUNICATION PP 03. MICROPROCESSORS & MICROCONTROLLERPP 04. DIGITAL SIGNAL PROCESSING PP 05. THEORY OF COMPUTATION PP 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 08. SIGNAL PROCESSING LABORATORY TW 09. SIGNAL PROCESSING LABORATORY OR 10. HARDWARE LABORATORY PR	100 100 100 100 100 50 50 25 50 25 50	40 40 40 40 20 20 10 20 10 20	61 P 53 P 63 P 45 P 40 P 41 P 46 P 19 P 35 P 21 P 20 P				
FIRST TERM TOTAL = 444/750. ORDN. 1 MARKS :							

DATE: 02 MAR. 2012 CENTE	RE : PUNE	E.(2008 PAT.)(COMPUTER) INSTITUTE OF COMPUTER T	ECHNOLOGY, PUNE			18 (431)
NOTE: FIRST LINE : SEAT NO., NAME OF THE OTHER LINES: HEAD OF PASSING, MAX.	CANDIDATE MARKS, N	E, MOTHER, PERMANENT RE MIN. PASS MARKS, MARKS	G. NO., PREVIO OBTAINED, P/F:	US SEAT NO., PASS/FAIL, C:	COLLEGE, SE PREVIOUS CARE	EAT NO.
T8054253 JOSHI DHANANJAY SHRINIVAS		SAVITA	71122420-	,	, PICT	, т8054253
01. DATABASE MANAGEMENT SYSTEMS PP 02. DATA COMMUNICATION PP 03. MICROPROCESSORS & MICROCONTROLLERPP 04. DIGITAL SIGNAL PROCESSING PP 05. THEORY OF COMPUTATION PP 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 08. SIGNAL PROCESSING LABORATORY TW 09. SIGNAL PROCESSING LABORATORY OR 10. HARDWARE LABORATORY TW 11. HARDWARE LABORATORY PR	100 40 100 40 100 40 100 40 100 40 50 20 55 20 55 20 55 20 25 10 50 20	67 P 61 P 71 P 49 P 57 P 46 P 23 P 31 P 22 P 39 P				
FIRST TERM TOTAL = 511/750. ORDN. 1 MARKS :						
T8054254 KANADE ROHIT RAJARAM		VIDYA	, 71132431L	,	, PICT	, т8054254
01. DATABASE MANAGEMENT SYSTEMS PP 02. DATA COMMUNICATION PP 03. MICROPROCESSORS & MICROCONTROLLERPP 04. DIGITAL SIGNAL PROCESSING PP 05. THEORY OF COMPUTATION PP 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 08. SIGNAL PROCESSING LABORATORY TW 09. SIGNAL PROCESSING LABORATORY OR 10. HARDWARE LABORATORY TW 11. HARDWARE LABORATORY PR	100 40 100 40 100 40 100 40 100 40 50 20 50 20 25 10 50 20 25 10 50 20	69 P 57 P 71 P 51 P 62 P 42 P 44 P 18 P 35 P 21 P 42 P				
FIRST TERM TOTAL = 512/750. ORDN. 1 MARKS :						
T8054255 KANTROD SUKESHNI ANIL		SUNITA	, 71045468G	,	, PICT	, т8054255
01. DATABASE MANAGEMENT SYSTEMS PP 02. DATA COMMUNICATION PP 03. MICROPROCESSORS & MICROCONTROLLERPP 04. DIGITAL SIGNAL PROCESSING PP 05. THEORY OF COMPUTATION PP 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 08. SIGNAL PROCESSING LABORATORY TW 09. SIGNAL PROCESSING LABORATORY OR 10. HARDWARE LABORATORY TW 11. HARDWARE LABORATORY PR	100 40 100 40 100 40 100 40 100 40 50 20 55 20 25 10 50 20 25 10 50 20	41 P 40 P 61 P 42 P 47 P 38 P 35 P 16 P 30 P 18 P 25 P				
FIRST TERM TOTAL = 393/750. ORDN. 1 MARKS :						

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UNIVERSITY OF PUNE ,T.E. (2008 PAT.) (COMPUTER) EXAMINATION NOV. 2011
   DATE: 02 MAR. 2012 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.
                                                                       PAGE NO. 19 ( 432)
NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
    OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
T8054256 KAPLE MADHURA MUKESH
                                     VANITA
                                                    , 71045469E , , PICT , T8054256
 02. DATA COMMUNICATION
03. MTCROPPOCESSOR
                         PP 100
                                 40
                                     63 P
 03. MICROPROCESSORS & MICROCONTROLLERPP
                             100
                                 40 67 P
 04. DIGITAL SIGNAL PROCESSING PP
                             100
                                 40 56
                                        Р
 05. THEORY OF COMPUTATION
                             100
                                  40 46
 06. RDBMS & VISUAL PROGRAMMING LAB. TW
                                  20 43
                              50
 07. RDBMS & VISUAL PROGRAMMING LAB. PR
                              50
                                 20 45 P
 08. SIGNAL PROCESSING LABORATORY TW
                              25 10 20 P
 09. SIGNAL PROCESSING LABORATORY OR
                              50 20 37
 10. HARDWARE LABORATORY TW 25 10 20 P
 11. HARDWARE LABORATORY
                        PR 50 20 41 P
 FIRST TERM TOTAL = 498/750.
ORDN. 1 MARKS :
T8054257 KARAD NEHA DASHRATH
                                     USHA
                                                    , 71045471G , , PICT , T8054257
                        PP 100 40 47 P
 01. DATABASE MANAGEMENT SYSTEMS
 02. DATA COMMUNICATION
                  PP 100
                                 40 45 P
 03. MICROPROCESSORS & MICROCONTROLLERPP 100
                                 40 56 P
 04. DIGITAL SIGNAL PROCESSING PP 100
                                 40 40 P
                                  40 40 P
 05. THEORY OF COMPUTATION
                             100
                          PP
 06. RDBMS & VISUAL PROGRAMMING LAB. TW
                             50
                                 20 37
 07. RDBMS & VISUAL PROGRAMMING LAB. PR
                              50
                                 20
                                     28
 08. SIGNAL PROCESSING LABORATORY TW
                              25
                                 10 16
 09. SIGNAL PROCESSING LABORATORY OR
                              50
                                 20 20
 10. HARDWARE LABORATORY TW
                              25 10 17
                                        Р
 11. HARDWARE LABORATORY
                        PR 50 20 28 P
 FIRST TERM TOTAL = 374/750.
ORDN. 1 MARKS :
BHARATI , 71045476H , , PICT , T8054259
 T8054259 KATARIYA RAHUL PADAMKUMAR
                        PP 100 40
 O1. DATABASE MANAGEMENT SYSTEMS
                          PP 100 40 52 P
 02. DATA COMMUNICATION
 03. MICROPROCESSORS & MICROCONTROLLERPP 100
                                 40 70 P
 04. DIGITAL SIGNAL PROCESSING PP 100
                                 40 51 P
                             100
                                 40 58
 05. THEORY OF COMPUTATION
 06. RDBMS & VISUAL PROGRAMMING LAB. TW
                              50
 07. RDBMS & VISUAL PROGRAMMING LAB. PR
                              50
 08. SIGNAL PROCESSING LABORATORY TW
                                 10 20
 10. HARDWARE LABORATORY TW
                        OR
                                 20 22
                              50
                              25
                                 10
                                     20
                                        Р
                              50 20
                                     37
 FIRST TERM TOTAL = 473/750.
ORDN. 1 MARKS :
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DATE: 02 MAR. 2012 CENTR	RE : PUNE INS	2008 PAT.)(COMPUTER		. 2011	PAGE NO. 20 (433)
NOTE: FIRST LINE : SEAT NO., NAME OF THE OTHER LINES: HEAD OF PASSING, MAX.	CANDIDATE,	PASS MARKS, MARKS	EG. NO., PREVIOU	S SEAT NO., C	
T8054260 KAWADE SARANG JAIRAJ		KARUNA	, 71045477F	, , ,	PICT , T8054260
02. DATA COMMUNICATION PP 03. MICROPROCESSORS & MICROCONTROLLERPP 04. DIGITAL SIGNAL PROCESSING PP		1 P 2 P 4 P 2 P 3 P 6 P 1 P			
FIRST TERM TOTAL = 502/750. ORDN. 1 MARKS :					
T8054261 KAZI SABIYA FARUK		SHAHEDA		, , , , ,	PICT , T8054261
02. DATA COMMUNICATION PP 03. MICROPROCESSORS & MICROCONTROLLERPP 04. DIGITAL SIGNAL PROCESSING PP	100 40 77 100 40 55 100 40 66 100 40 66 50 20 44 50 20 44 50 20 3 25 10 20 50 20 44 50 20 3 25 10 20	6 P 1 P 2 P 6 P 4 P 0 P 4 P 1 P			
FIRST TERM TOTAL = 517/750. ORDN. 1 MARKS :					
T8054262 KHADE PRERANA BALASAHEB					PICT , T8054262
01. DATABASE MANAGEMENT SYSTEMS PP 02. DATA COMMUNICATION PP 03. MICROPROCESSORS & MICROCONTROLLERPP 04. DIGITAL SIGNAL PROCESSING PP	100 40 63	1 P 0 P 1 P 7 P 2 P 2 P 0 P 8 P 0 P 1 P	,	,	,

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CAND OTHER LINES: HEAD OF PASSING, MAX. MARK		EG. NO., PREVIOUS SEAT NO. OBTAINED, P/F:PASS/FAIL,	, COLLEGE, SEAT NO. C:PREVIOUS CARRY OVER
T8054264 KHARDE ABHIJIT DIGAMBER	JYOTI	, 71045483L ,	
01. DATABASE MANAGEMENT SYSTEMS PP 100 02. DATA COMMUNICATION PP 100 03. MICROPROCESSORS & MICROCONTROLLERPP 100 04. DIGITAL SIGNAL PROCESSING PP 100 05. THEORY OF COMPUTATION PP 100 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 08. SIGNAL PROCESSING LABORATORY TW 25 09. SIGNAL PROCESSING LABORATORY OR 50 10. HARDWARE LABORATORY TW 25 11. HARDWARE LABORATORY PR 50	40 58 P 40 60 P 40 70 P 40 45 P 40 51 P 20 44 P 20 41 P 10 22 P 20 31 P 10 20 P 20 42 P		
FIRST TERM TOTAL = 484/750. ORDN. 1 MARKS :			
T8054265 KHEDKAR PRITAM KISHOR	SUREKHA	, 71045484J ,	, PICT , T8054265
01. DATABASE MANAGEMENT SYSTEMS PP 100 02. DATA COMMUNICATION PP 100 03. MICROPROCESSORS & MICROCONTROLLERPP 100 04. DIGITAL SIGNAL PROCESSING PP 100 05. THEORY OF COMPUTATION PP 100 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 08. SIGNAL PROCESSING LABORATORY TW 25 09. SIGNAL PROCESSING LABORATORY OR 50 10. HARDWARE LABORATORY PR 50	40 58 P 40 41 P 40 49 P 40 40 P 40 48 P 20 35 P 20 42 P 10 14 P 20 29 P 10 16 P 20 36 P		
FIRST TERM TOTAL = 408/750. ORDN. 1 MARKS :			
T8054266 KONDHAWALE PRASHANT MHATARBA 01. DATABASE MANAGEMENT SYSTEMS PP 100	MANGAL 40 51 P	, 70925487D ,	, PICT , T8054266
02. DATA COMMUNICATION PP 100 03. MICROPROCESSORS & MICROCONTROLLERPP 100 04. DIGITAL SIGNAL PROCESSING PP 100 05. THEORY OF COMPUTATION PP 100 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 07. RDBMS & VISUAL PROGRAMMING LAB PR 50 08. SIGNAL PROCESSING LABORATORY TW 25 09. SIGNAL PROCESSING LABORATORY OR 50 10. HARDWARE LABORATORY TW 25 11. HARDWARE LABORATORY PR 50	40 43 P 40 52 P 40 27 F 40 41 P 20 38 P 20 21 P 10 18 P 20 20 P 10 21 P 20 32 P		
FIRST TERM TOTAL = 364/750. ORDN. 1 MARKS :			

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   DATE: 02 MAR. 2012 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.
                                                                         PAGE NO. 22 ( 435)
NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
    OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
T8054267 KOTE UDAY ARUN
                                      SUNITA , 70925488B , , PICT , T8054267
 01. DATABASE MANAGEMENT SYSTEMS PP 100 02. DATA COMMUNICATION PP 100
                                  40 49 P
                                  40 47 P
 03. MICROPROCESSORS & MICROCONTROLLERPP
                              100
 04. DIGITAL SIGNAL PROCESSING PP
                              100
                                  40 44
 05. THEORY OF COMPUTATION
                              100
                                  40 40 P
 06. RDBMS & VISUAL PROGRAMMING LAB. TW
                                  20
                                      34 P
                               50
 07. RDBMS & VISUAL PROGRAMMING LAB. PR
                               50
                                  20 22
 08. SIGNAL PROCESSING LABORATORY TW
                               25 10 15
 09. SIGNAL PROCESSING LABORATORY OR
                               50 20 20 P
 10. HARDWARE LABORATORY TW 25 10 17 P
11. HARDWARE LABORATORY PR 50 20 36 P
 FIRST TERM TOTAL = 364/750.
ORDN. 1 MARKS :
T8054268 KSHATRIYA PURVA HEMANT
                                      RAJESHRI , 71045487C , , PICT , T8054268
 01. DATABASE MANAGEMENT SYSTEMS PP 100 40 52 P
 02. DATA COMMUNICATION
                   PP 100
                                  40
                                     54 P
 03. MICROPROCESSORS & MICROCONTROLLERPP 100
                                  40 61 P
 04. DIGITAL SIGNAL PROCESSING PP 100
                                  40 43 P
                                  40 59 P
 05. THEORY OF COMPUTATION
                              100
                          PP
 06. RDBMS & VISUAL PROGRAMMING LAB. TW
                              50
                                  20 42
 07. RDBMS & VISUAL PROGRAMMING LAB. PR
                               50
                                  20 32
 08. SIGNAL PROCESSING LABORATORY TW
                               25
                                  10 19
 09. SIGNAL PROCESSING LABORATORY
                               50
                                  20 24 P
                          OR
 10. HARDWARE LABORATORY TW
                               25 10 20 P
 11. HARDWARE LABORATORY
                         PR 50 20 39
 FIRST TERM TOTAL = 445/750.
ORDN. 1 MARKS:
REKHA RANI , 71045488M , , PICT , T8054269
 T8054269 KSHITIZ DANGE
 01. DATABASE MANAGEMENT SYSTEMS PP 100 40
                                      53 P
 02. DATA COMMUNICATION PP 100 40
                                      60 P
 03. MICROPROCESSORS & MICROCONTROLLERPP 100
                                  40 59 P
 04. DIGITAL SIGNAL PROCESSING PP 100
                                  40 44 P
                              100
                                  40 58
 05. THEORY OF COMPUTATION
 06. RDBMS & VISUAL PROGRAMMING LAB. TW
                               50
                                  20 40
 07. RDBMS & VISUAL PROGRAMMING LAB. PR
                               50
                                  20 39
 08. SIGNAL PROCESSING LABORATORY TW
                                  10 16
 10. HARDWARE LABORATORY TW
                        OR
                               50
                                  20 35
                               25
                                  10
                                      21
                                         Р
                              50 20
                                     30
 FIRST TERM TOTAL = 455/750.
ORDN. 1 MARKS :
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   DATE : 02 MAR. 2012 CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.
                                                                         PAGE NO. 23 ( 436)
NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
    OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
ASHWINI , 71045489K , , PICT , T8054270
 T8054270 KULKARNI AKASH ABHAY
 02. DATA COMMUNICATION
03. MTCROPPOCESSOR
                          PP 100
                                  40
                                      72 P
                                      72 P
 03. MICROPROCESSORS & MICROCONTROLLERPP
                              100
                                  40
 04. DIGITAL SIGNAL PROCESSING PP
                              100
                                  40
                                      54
                                         Р
 05. THEORY OF COMPUTATION
                              100
                                  40 51
                                         Р
 06. RDBMS & VISUAL PROGRAMMING LAB. TW
                                  20 43 P
                               50
                                  20 40 P
 07. RDBMS & VISUAL PROGRAMMING LAB. PR
                               50
 08. SIGNAL PROCESSING LABORATORY TW
                               25 10 22
 09. SIGNAL PROCESSING LABORATORY OR
                               50 20 36 P
 10. HARDWARE LABORATORY TW 25 10 20 P 11. HARDWARE LABORATORY PR 50 20 41 P
 FIRST TERM TOTAL = 517/750.
ORDN. 1 MARKS :
T8054271 KULKARNI CHINMAYI AVINASH
                                      SHOBHA
                                                , 71045493н , , , РІСТ , Т8054271
                          PP 100 40 58 P
 O1. DATABASE MANAGEMENT SYSTEMS
 02. DATA COMMUNICATION
                   PP 100
                                  40 40 P
 03. MICROPROCESSORS & MICROCONTROLLERPP 100
                                  40 40 P
 04. DIGITAL SIGNAL PROCESSING PP 100
                                  40 40 P
                                  40 40 P
 05. THEORY OF COMPUTATION
                              100
                          PP
 06. RDBMS & VISUAL PROGRAMMING LAB. TW
                              50
                                  20 36 P
 07. RDBMS & VISUAL PROGRAMMING LAB. PR
                               50
                                  20 42 P
 08. SIGNAL PROCESSING LABORATORY TW
                               25
                                  10 16
                                  20
 09. SIGNAL PROCESSING LABORATORY OR
                               50
                                      30 P
 10. HARDWARE LABORATORY TW
                               25 10 20 P
 11. HARDWARE LABORATORY
                         PR 50 20 20 P
 FIRST TERM TOTAL = 382/750.
ORDN. 1 MARKS:
, 71045494F , , PICT , T8054272
 T8054272 KULKARNI DNYANESH VIJAYRAO
                                      MANGAL
                          PP 100 40
 O1. DATABASE MANAGEMENT SYSTEMS
                                      71 P
                          PP 100 40 55 P
 02. DATA COMMUNICATION
 03. MICROPROCESSORS & MICROCONTROLLERPP 100
                                  40 59 P
                              100
                                  40 58
                                         Р
 04. DIGITAL SIGNAL PROCESSING PP
                              100
                                  40 47
 05. THEORY OF COMPUTATION
 06. RDBMS & VISUAL PROGRAMMING LAB. TW
                               50
                                  20 43 P
 07. RDBMS & VISUAL PROGRAMMING LAB. PR
                               50
 08. SIGNAL PROCESSING LABORATORY TW
                                  10 21
 10. HARDWARE LABORATORY TW
                        OR
                               50
                                  20
                                      37
                               25
                                  10
                                      21
                                         Р
                              50 20
                                     21 P
 FIRST TERM TOTAL = 471/750.
ORDN. 1 MARKS :
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	PUNE ,T.E.(2008 PAT.)(COMPUTER) : PUNE INSTITUTE OF COMPUTER T		PAGE NO. 24 (437)
			COLLEGE, SEAT NO.
T8054273 KULKARNI MANASI MILIND	JYOTI	, 71045496в ,	, PICT , T8054273
07. RDBMS & VISUAL PROGRAMMING LAB. PR 08. SIGNAL PROCESSING LABORATORY TW 09. SIGNAL PROCESSING LABORATORY OR 10. HARDWARE LABORATORY TW 2	00 40 45 P 00 40 44 P 00 40 40 P		
FIRST TERM TOTAL = 402/750. ORDN. 1 MARKS :			
T8054274 KULKARNI PRATHAMESH DEEPAK	VIDYA	, 71132433G ,	, PICT , T8054274
07. RDBMS & VISUAL PROGRAMMING LAB. PR 08. SIGNAL PROCESSING LABORATORY TW 09. SIGNAL PROCESSING LABORATORY OR 10. HARDWARE LABORATORY TW 2	00 40 48 P 00 40 52 P 00 40 50 P		
FIRST TERM TOTAL = 471/750. ORDN. 1 MARKS :			
T8054275 KULKARNI SAURABH RAVINDRA	SWATI	, 71045498」 ,	, PICT , T8054275
07. RDBMS & VISUAL PROGRAMMING LAB. PR 08. SIGNAL PROCESSING LABORATORY TW 09. SIGNAL PROCESSING LABORATORY OR 10. HARDWARE LABORATORY TW 2	00 40 53 P 00 40 55 P 00 40 70 P		

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CAND OTHER LINES: HEAD OF PASSING, MAX. MARKS		EG. NO., PREVIOUS SEAT NO. OBTAINED, P/F:PASS/FAIL,	, COLLEGE, SEAT NO. C:PREVIOUS CARRY OVER
T8054276 KUNTALWAD GNYANESH GAJJARAM	SARASWATHI	, 71045501B ,	, PICT , T8054276
01. DATABASE MANAGEMENT SYSTEMS PP 100 02. DATA COMMUNICATION PP 100 03. MICROPROCESSORS & MICROCONTROLLERPP 100 04. DIGITAL SIGNAL PROCESSING PP 100 05. THEORY OF COMPUTATION PP 100 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 08. SIGNAL PROCESSING LABORATORY TW 25 09. SIGNAL PROCESSING LABORATORY OR 50 10. HARDWARE LABORATORY TW 25 11. HARDWARE LABORATORY PR 50	40		
FIRST TERM TOTAL = 323/750. ORDN. 1 MARKS :			
T8054277 LATIFI AAMASH IMTIYAZ AHMED	SHAHERA		, PICT , T8054277
01. DATABASE MANAGEMENT SYSTEMS PP 100 02. DATA COMMUNICATION PP 100 03. MICROPROCESSORS & MICROCONTROLLERPP 100 04. DIGITAL SIGNAL PROCESSING PP 100 05. THEORY OF COMPUTATION PP 100 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 08. SIGNAL PROCESSING LABORATORY TW 25 09. SIGNAL PROCESSING LABORATORY OR 50 10. HARDWARE LABORATORY TW 25 11. HARDWARE LABORATORY PR 50	40 30 F 40 40 P 40 30 F 40 26 F 40 28 F 20 26 P 20 22 P 10 11 P 20 25 P 10 13 P 20 05 F		
FIRST TERM TOTAL = 256/750. ORDN. 1 MARKS :			
T8054278 LOKHANDE VINAYA VISHWANATHRAO			
01. DATABASE MANAGEMENT SYSTEMS PP 100 02. DATA COMMUNICATION PP 100 03. MICROPROCESSORS & MICROCONTROLLERPP 100 04. DIGITAL SIGNAL PROCESSING PP 100 05. THEORY OF COMPUTATION PP 100 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 08. SIGNAL PROCESSING LABORATORY TW 25 09. SIGNAL PROCESSING LABORATORY OR 50 10. HARDWARE LABORATORY TW 25 11. HARDWARE LABORATORY PR 50 FIRST TERM TOTAL = 445/750. ORDN. 1 MARKS:	40 57 P 40 52 P 40 60 P 40 69 P 40 55 P 20 39 P 20 34 P 10 17 P 20 30 P 10 22 P 20 10 F		

DATE : 02 MAR. 2012 CENTR	E : PUŃE I	E.(2008 PAT.)(COMPUTER INSTITUTE OF COMPUTER	TECHNOLOGY, PUNE.		PAGE NO. 26 (439)
NOTE: FIRST LINE : SEAT NO., NAME OF THE OTHER LINES: HEAD OF PASSING, MAX.	CANDIDATE, MARKS, MI		EG. NO., PREVIOUS OBTAINED, P/F:PA	S SEAT NO., CO SS/FAIL, C:PR	LLEGE, SEAT NO.
T8054279 MAHARANWAR NAMITA ANILRAO		RENUKA	, 71045510M	, , ,	PICT , T8054279
02. DATA COMMUNICATION PP 03. MICROPROCESSORS & MICROCONTROLLERPP 04. DIGITAL SIGNAL PROCESSING PP	100 40 100 40 100 40 100 40 100 40 50 20 25 10 50 20 25 10 50 20	57 P 47 P 57 P 46 P 61 P 40 P 08 F 19 P 22 P 20 P 26 P			
FIRST TERM TOTAL = 403/750. ORDN. 1 MARKS :					
T8054280 MALANI MUKUND PARMANAND		SUDHA		, , , , , ,	PICT , T8054280
02. DATA COMMUNICATION PP 03. MICROPROCESSORS & MICROCONTROLLERPP 04. DIGITAL SIGNAL PROCESSING PP	100 40 100 40 100 40 100 40 100 40 50 20 50 20 25 10 50 20 25 10 50 20	59 P 50 P 54 P 61 P 55 P 39 P 35 P 17 P 30 P 18 P 30 P			
FIRST TERM TOTAL = 448/750. ORDN. 1 MARKS :					
					PICT , T8054281
01. DATABASE MANAGEMENT SYSTEMS PP 02. DATA COMMUNICATION PP 03. MICROPROCESSORS & MICROCONTROLLERPP 04. DIGITAL SIGNAL PROCESSING PP	100 40 100 40 100 40 100 40 100 40 50 20 50 20 25 10 50 20 25 10 50 20	61 P 56 P 56 P 56 P 53 P 36 P 42 P 19 P 37 P 16 P 41 P	, .10.33135	, ,	, 10031201

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   DATE: 02 MAR. 2012 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.
                                                                       PAGE NO. 27 ( 440)
NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
    OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
T8054282 MANDLIWALA NAFISA OANALI
                                     RASHIDA , 71045516L , , , PICT , T8054282
 01. DATABASE MANAGEMENT SYSTEMS
                          PP 100
                                     52 P
 02. DATA COMMUNICATION
                                 40
                                 40 59 P
 03. MICROPROCESSORS & MICROCONTROLLERPP
                             100
 04. DIGITAL SIGNAL PROCESSING PP
                             100
                                 40
                                     64
                                       Р
 05. THEORY OF COMPUTATION
                             100
                                 40 51
 06. RDBMS & VISUAL PROGRAMMING LAB. TW
                                 20 42
                              50
                                 20 40 P
 07. RDBMS & VISUAL PROGRAMMING LAB. PR
                              50
 08. SIGNAL PROCESSING LABORATORY TW
                              25 10 16 P
 09. SIGNAL PROCESSING LABORATORY OR
                              50 20 38 P
 10. HARDWARE LABORATORY TW 25 10 21 P
 11. HARDWARE LABORATORY
                        PR 50 20 34 P
 FIRST TERM TOTAL = 481/750.
ORDN. 1 MARKS :
RAJANI , 70925499H , , PICT , T8054283
 T8054283 MANE OM ARUN
 01. DATABASE MANAGEMENT SYSTEMS PP 100
                                 40
 02. DATA COMMUNICATION
                  PP 100
                                 40 40 P
 03. MICROPROCESSORS & MICROCONTROLLERPP 100
                                 40 40 P
 04. DIGITAL SIGNAL PROCESSING PP 100
                                 40 31 F
 05. THEORY OF COMPUTATION
                             100
                                 40 19 F
                          PP
 06. RDBMS & VISUAL PROGRAMMING LAB. TW
                              50
                                 20
                                     24
 07. RDBMS & VISUAL PROGRAMMING LAB. PR
                              50
                                 20
                                    22
 08. SIGNAL PROCESSING LABORATORY TW
                              25
                                 10 11
 09. SIGNAL PROCESSING LABORATORY
                              50
                                 20 10 F
                          OR
 10. HARDWARE LABORATORY TW
                              25 10 14 P
 11. HARDWARE LABORATORY
                        PR 50 20 05 F
 FIRST TERM TOTAL = 256/750.
ORDN. 1 MARKS:
, 71132434E , , , PICT , T8054284
 T8054284 MANE POOJA VASANT
                                     VARSHA
                       PP 100 40
 01. DATABASE MANAGEMENT SYSTEMS
                         PP 100 40 58 P
 02. DATA COMMUNICATION
 03. MICROPROCESSORS & MICROCONTROLLERPP 100
                                 40 65 P
 04. DIGITAL SIGNAL PROCESSING PP 100
                                 40 60
                                       Р
                             100
                                 40 61
 05. THEORY OF COMPUTATION
 06. RDBMS & VISUAL PROGRAMMING LAB. TW
                              50
                                 20 43 P
 07. RDBMS & VISUAL PROGRAMMING LAB. PR
                              50
                                 20 36 P
 08. SIGNAL PROCESSING LABORATORY TW
                                 10 17
 10. HARDWARE LABORATORY TW
                        OR
                              50
                                 20 38
                                 10
                              25
                                     21
                                        Р
                              50 20 20 P
 FIRST TERM TOTAL = 485/750.
ORDN. 1 MARKS :
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UNIVERSITY OF PUNE ,T.E. (2008 PAT.) (COMPUTER) EXAMINATION NOV. 2011
   DATE: 02 MAR. 2012 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.
                                                                         PAGE NO. 28 ( 441)
NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
    OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
T8054285 MANVI BHATIA
                                       ANJU
                                                     , 71045520J , , PICT , T8054285
 01. DATABASE MANAGEMENT SYSTEMS PP 100 02. DATA COMMUNICATION PP 100
                                      53 P
                                  40
                                  40 58 P
 03. MICROPROCESSORS & MICROCONTROLLERPP
                              100
 04. DIGITAL SIGNAL PROCESSING PP
                              100
                                  40
                                      58
 05. THEORY OF COMPUTATION
                              100
                                   40 65
 06. RDBMS & VISUAL PROGRAMMING LAB. TW
                                   20 40
                               50
 07. RDBMS & VISUAL PROGRAMMING LAB. PR
                               50
                                  20 45
 08. SIGNAL PROCESSING LABORATORY TW
                               25 10 16 P
 09. SIGNAL PROCESSING LABORATORY OR
                               50 20 39 P
 10. HARDWARE LABORATORY TW 25 10 21 P
11. HARDWARE LABORATORY PR 50 20 38 P
 FIRST TERM TOTAL = 481/750.
ORDN. 1 MARKS :
T8054286 MASKE SHRUTI HIRALAL
                                       SANGEETA , 71045523C , , PICT , T8054286
                        PP 100 40 40 P
 01. DATABASE MANAGEMENT SYSTEMS
 02. DATA COMMUNICATION
                   PP 100
                                  40 44 P
                                  40 47 P
 03. MICROPROCESSORS & MICROCONTROLLERPP 100
 04. DIGITAL SIGNAL PROCESSING PP 100
                                  40 42 P
 05. THEORY OF COMPUTATION
                              100
                                   40 48 P
                           PP
 06. RDBMS & VISUAL PROGRAMMING LAB. TW
                              50
                                  20 40 P
 07. RDBMS & VISUAL PROGRAMMING LAB. PR
                               50
                                  20 15 F
 08. SIGNAL PROCESSING LABORATORY TW
                               25
                                  10 19
                               50 20 36 P
25 10 19 P
 09. SIGNAL PROCESSING LABORATORY OR
 10. HARDWARE LABORATORY TW
 11. HARDWARE LABORATORY
                         PR 50 20 27
 FIRST TERM TOTAL = 377/750.
ORDN. 1 MARKS :
, 71045533L , , PICT , T8054287
 T8054287 MUNESHWAR PRANALI PRAKASH
                                       PRAMILA
                        PP 100 40 49 P
 O1. DATABASE MANAGEMENT SYSTEMS
                          PP 100 40 44 P
 02. DATA COMMUNICATION
 03. MICROPROCESSORS & MICROCONTROLLERPP 100
                                  40 45 P
 04. DIGITAL SIGNAL PROCESSING PP 100
                                  40 57
                                         Р
                                  40 52
                              100
 05. THEORY OF COMPUTATION
 06. RDBMS & VISUAL PROGRAMMING LAB. TW
                               50
                                  20 40 P
 07. RDBMS & VISUAL PROGRAMMING LAB. PR
                               50
 08. SIGNAL PROCESSING LABORATORY TW
                                  10 18
 10. HARDWARE LABORATORY TW
                        OR
                               50
                                  20 36
                               25
                                  10 17
                                         Р
                              50 20
                                      08
 FIRST TERM TOTAL = 408/750.
ORDN. 1 MARKS :
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DATE: 02 MAR. 2012 CENTRE:	NE ,T.E.(2008 PAT.)(COMPUTER PUNE INSTITUTE OF COMPUTER	TECHNOLOGY, PUNE.	PAGE NO. 29 (442)
NOTE: FIRST LINE : SEAT NO., NAME OF THE CAND OTHER LINES: HEAD OF PASSING, MAX. MARK		EG. NO., PREVIOUS SEAT NO OBTAINED, P/F:PASS/FAIL,	., COLLEGE, SEAT NO. C:PREVIOUS CARRY OVER
T8054288 MUSALE SAGAR RAJENDRA	KALPANA	, 71045534) ,	, PICT , T8054288
01. DATABASE MANAGEMENT SYSTEMS PP 100 02. DATA COMMUNICATION PP 100 03. MICROPROCESSORS & MICROCONTROLLERPP 100 04. DIGITAL SIGNAL PROCESSING PP 100 05. THEORY OF COMPUTATION PP 100 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 08. SIGNAL PROCESSING LABORATORY TW 25 09. SIGNAL PROCESSING LABORATORY OR 50 10. HARDWARE LABORATORY TW 25	40 52 P 40 41 P 40 52 P 40 46 P 40 40 P 20 39 P 10 17 P 20 34 P 10 19 P 20 38 P		
FIRST TERM TOTAL = 418/750. ORDN. 1 MARKS :			
T8054289 MUTALIK MADHUMITRA MADHAV			
01. DATABASE MANAGEMENT SYSTEMS PP 100 02. DATA COMMUNICATION PP 100 03. MICROPROCESSORS & MICROCONTROLLERPP 100 04. DIGITAL SIGNAL PROCESSING PP 100 05. THEORY OF COMPUTATION PP 100 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 08. SIGNAL PROCESSING LABORATORY TW 25 09. SIGNAL PROCESSING LABORATORY OR 50 10. HARDWARE LABORATORY TW 25 11. HARDWARE LABORATORY PR 50	40 67 P 40 53 P 40 65 P 40 71 P 40 61 P 20 40 P 20 44 P 10 18 P 20 32 P 10 21 P 20 41 P	, ,	, ,
FIRST TERM TOTAL = 513/750. ORDN. 1 MARKS :			
T8054290 MUTHA SURABHI SHASHIKUMAR	UJJWALA	, 71045536E ,	, PICT , T8054290
01. DATABASE MANAGEMENT SYSTEMS PP 100 02. DATA COMMUNICATION PP 100 03. MICROPROCESSORS & MICROCONTROLLERPP 100 04. DIGITAL SIGNAL PROCESSING PP 100 05. THEORY OF COMPUTATION PP 100 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 08. SIGNAL PROCESSING LABORATORY TW 25 09. SIGNAL PROCESSING LABORATORY TW 25 10. HARDWARE LABORATORY TW 25 11. HARDWARE LABORATORY PR 50	40 65 P 40 50 P 40 62 P 40 65 P 20 42 P 20 42 P 10 20 P 20 35 P 10 22 P 20 42 P		
FIRST TERM TOTAL = 516/750. ORDN. 1 MARKS :			

UNIVERSITY OF PUNE ,T.E. (2008 PAT.) (COMPUTER) EXAMINATION NOV. 2011 DATE: 02 MAR. 2012 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 30 (443) NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER T8054291 NAIR AJAY RADHAKRISHNAN GEETHA , 71045539K , , PICT , T8054291 01. DATABASE MANAGEMENT SYSTEMS 02. DATA COMMUNICATION PP 100 40 53 P 40 49 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 04. DIGITAL SIGNAL PROCESSING PP 100 40 62 Р 05. THEORY OF COMPUTATION 100 40 48 06. RDBMS & VISUAL PROGRAMMING LAB. TW 20 36 50 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 38 08. SIGNAL PROCESSING LABORATORY TW 25 10 18 09. SIGNAL PROCESSING LABORATORY OR 50 20 29 P 10. HARDWARE LABORATORY TW 25 10 20 P 11. HARDWARE LABORATORY PR 50 20 40 P FIRST TERM TOTAL = 455/750. ORDN. 1 MARKS : T8054292 NAJAN KAUSTUBH GORAKSHANATH MEERA , 71045540C , , PICT , T8054292 PP 100 40 40 P 01. DATABASE MANAGEMENT SYSTEMS 02. DATA COMMUNICATION PP 100 40 42 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 40 P 04. DIGITAL SIGNAL PROCESSING PP 100 40 40 P 40 30 F 05. THEORY OF COMPUTATION 100 PP 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 20 45 P 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 34 08. SIGNAL PROCESSING LABORATORY TW 25 10 17 09. SIGNAL PROCESSING LABORATORY OR 50 20 20 10. HARDWARE LABORATORY TW 25 10 20 P 11. HARDWARE LABORATORY PR 50 20 35 P FIRST TERM TOTAL = 363/750. ORDN. 1 MARKS: , 70925523D , , PICT , T8054293 T8054293 NANDKAR NEHA VIJAY DIPALI PP 100 40 O1. DATABASE MANAGEMENT SYSTEMS PP 100 40 49 P 02. DATA COMMUNICATION 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 53 P 04. DIGITAL SIGNAL PROCESSING PP 100 40 53 Р 100 40 61 P 05. THEORY OF COMPUTATION 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 15 F 08. SIGNAL PROCESSING LABORATORY TW 10 15 P 10. HARDWARE LABORATORY TW OR 20 20 50 25 10 18 Р 50 20 30 P FIRST TERM TOTAL = 408/750. ORDN. 1 MARKS :

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UNIVERSITY OF PUNE ,T.E. (2008 PAT.) (COMPUTER) EXAMINATION NOV. 2011
   DATE: 02 MAR. 2012 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.
                                                                       PAGE NO. 31 ( 444)
NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
    OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
T8054294 NANWANI SHRUTI RAMESH
                                     KAMLESH , 71045541M , , PICT , T8054294
 01. DATABASE MANAGEMENT SYSTEMS
 02. DATA COMMUNICATION
                          PP 100
                                 40 45 P
 03. MICROPROCESSORS & MICROCONTROLLERPP
                             100
                                 40 62 P
 04. DIGITAL SIGNAL PROCESSING PP
                             100
                                 40 61
                                       Р
 05. THEORY OF COMPUTATION
                             100
                                 40 62
 06. RDBMS & VISUAL PROGRAMMING LAB. TW
                                 20 40
                              50
                                 20 39
 07. RDBMS & VISUAL PROGRAMMING LAB. PR
                              50
 08. SIGNAL PROCESSING LABORATORY TW
                              25 10 18 P
 09. SIGNAL PROCESSING LABORATORY OR
                              50 20 18 F
 10. HARDWARE LABORATORY TW 25 10 20 P
 11. HARDWARE LABORATORY
                        PR 50 20 31 P
 FIRST TERM TOTAL = 463/750.
ORDN. 1 MARKS :
T8054295 NARENDRA PAL SINGH
                                                   , 71045542к , , , , РІСТ , Т8054295
                                     KADAM
                        PP 100 40
 O1. DATABASE MANAGEMENT SYSTEMS
 02. DATA COMMUNICATION
                  PP 100
                                 40 51 P
 03. MICROPROCESSORS & MICROCONTROLLERPP 100
                                 40 54 P
 04. DIGITAL SIGNAL PROCESSING PP 100
                                 40 64 P
 05. THEORY OF COMPUTATION
                             100
                                 40
                                     56
                          PP
 06. RDBMS & VISUAL PROGRAMMING LAB. TW
                             50
                                 20 44
 07. RDBMS & VISUAL PROGRAMMING LAB. PR
                              50
                                 20 35
 08. SIGNAL PROCESSING LABORATORY TW
                              25
                                 10 23
 09. SIGNAL PROCESSING LABORATORY OR
                              50
                                 20 37
 10. HARDWARE LABORATORY TW
                              25 10 23 P
 11. HARDWARE LABORATORY
                        PR 50 20 38 P
 FIRST TERM TOTAL = 485/750.
ORDN. 1 MARKS:
, 71045543H , , PICT , T8054296
 T8054296 NEHA RANA
                                      SUDHA
 01. DATABASE MANAGEMENT SYSTEMS PP 100 40
 02. DATA COMMUNICATION PP 100 40 62
 03. MICROPROCESSORS & MICROCONTROLLERPP 100
                                 40 57
 04. DIGITAL SIGNAL PROCESSING PP 100
                                 40 66
                                       Р
                             100
                                 40 59
 05. THEORY OF COMPUTATION
 06. RDBMS & VISUAL PROGRAMMING LAB. TW
                              50
                                 20 42 P
 07. RDBMS & VISUAL PROGRAMMING LAB. PR
                              50
 08. SIGNAL PROCESSING LABORATORY TW
                                 10 20
 10. HARDWARE LABORATORY TW
                        OR
                              50
                                 20
                                     30
                              25
                                 10
                                     20
                                        Р
                              50 20
                                     33
 FIRST TERM TOTAL = 495/750.
ORDN. 1 MARKS :
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	E.(2008 PAT.)(COMPUTER) EXAMINATION NOV. 2011 TECHNOLOGY. PUNE.	PAGE NO. 32 (445)
NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE OTHER LINES: HEAD OF PASSING, MAX. MARKS, M		EG. NO., PREVIOUS SEAT	NO., COLLEGE, SEAT NO.	
T8054297 NEMADE AJAY VILAS	MANJUSHA	, 71045544F ,	, PICT , T805	4297
01. DATABASE MANAGEMENT SYSTEMS PP 100 40 02. DATA COMMUNICATION PP 100 40 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 04. DIGITAL SIGNAL PROCESSING PP 100 40 05. THEORY OF COMPUTATION PP 100 40 06. ROBMS & VISUAL PROGRAMMING LAB. TW 50 20 07. ROBMS & VISUAL PROGRAMMING LAB. PR 50 20 08. SIGNAL PROCESSING LABORATORY TW 25 10 09. SIGNAL PROCESSING LABORATORY OR 50 20 10. HARDWARE LABORATORY TW 25 10 11. HARDWARE LABORATORY PR 50 20	62 P 49 P 54 P 55 P 68 P 40 P 43 P 18 P 36 P 19 P			
FIRST TERM TOTAL = 475/750. ORDN. 1 MARKS :				
T8054298 NERKAR NIKITA SUNIL	AMITA	, 71045545D ,	, PICT , T805	4298
01. DATABASE MANAGEMENT SYSTEMS PP 100 40 02. DATA COMMUNICATION PP 100 40 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 04. DIGITAL SIGNAL PROCESSING PP 100 40 05. THEORY OF COMPUTATION PP 100 40 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 20 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 08. SIGNAL PROCESSING LABORATORY TW 25 10 09. SIGNAL PROCESSING LABORATORY OR 50 20 10. HARDWARE LABORATORY TW 25 10 11. HARDWARE LABORATORY PR 50 20	68 P 51 P 68 P 68 P 51 P 45 P 45 P 22 P 41 P 23 P 43 P			
FIRST TERM TOTAL = 525/750. ORDN. 1 MARKS :				
T8054299 NITYA RAJ	SARASWATHY	, 71045550L ,	, PICT , T805	4299
01. DATABASE MANAGEMENT SYSTEMS PP 100 40 02. DATA COMMUNICATION PP 100 40 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 04. DIGITAL SIGNAL PROCESSING PP 100 40 05. THEORY OF COMPUTATION PP 100 40 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 20 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 08. SIGNAL PROCESSING LABORATORY TW 25 10 09. SIGNAL PROCESSING LABORATORY OR 50 20 10. HARDWARE LABORATORY TW 25 10 11. HARDWARE LABORATORY PR 50 20 FIRST TERM TOTAL = 436/750.	59 P 40 P 44 P 60 P 51 P 38 P 43 P 20 P 26 P 20 P			

DATE : 02 MAR. 2012 CENT	RE : PUNE	E.(2008 PAT.)(COMPUTER) INSTITUTE OF COMPUTER T	ECHNOLOGY, PUNE.	PAGE NO. 33 (446)
NOTE: FIRST LINE : SEAT NO., NAME OF THE OTHER LINES: HEAD OF PASSING, MAX.	CANDIDATE MARKS, M	E, MOTHER, PERMANENT RE MIN. PASS MARKS, MARKS	OBTAINED, P/F:PASS/FAIL,	., COLLEGE, SEAT NO. C:PREVIOUS CARRY OVER
T8054300 PANDYA PARTH VIPUL		ASHA	, 71045553E ,	
01. DATABASE MANAGEMENT SYSTEMS PP 02. DATA COMMUNICATION PP 03. MICROPROCESSORS & MICROCONTROLLERPP 04. DIGITAL SIGNAL PROCESSING PP 05. THEORY OF COMPUTATION PP 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 08. SIGNAL PROCESSING LABORATORY TW 09. SIGNAL PROCESSING LABORATORY OR 10. HARDWARE LABORATORY TW 11. HARDWARE LABORATORY PR	100 40 100 40 100 40 100 40 100 40 50 20 50 20 25 10 50 20 50 20	50 P 41 P 48 P 40 P 60 P 28 P 33 P 14 P 33 P 17 P 25 P	, , , , , , , , , , , , , , , , , , , ,	, 1201 , 10031300
FIRST TERM TOTAL = 389/750. ORDN. 1 MARKS :				
T8054301 PATE MAYUR SATISH 01. DATABASE MANAGEMENT SYSTEMS PP 02. DATA COMMUNICATION PP 03. MICROPROCESSORS & MICROCONTROLLERPP 04. DIGITAL SIGNAL PROCESSING PP 05. THEORY OF COMPUTATION PP 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 08. SIGNAL PROCESSING LABORATORY TW 09. SIGNAL PROCESSING LABORATORY OR 10. HARDWARE LABORATORY TW 11. HARDWARE LABORATORY PR	100 40 100 40 100 40 100 40 100 40 50 20 50 20 25 10 50 20 25 10 50 20	MADHURI 65 P 50 P 65 P 63 P 62 P 40 P 43 P 18 P 40 P 20 P 40 P	, 71132435C ,	, PICT , T8054301
FIRST TERM TOTAL = 506/750. ORDN. 1 MARKS :				
T8054302 PATHAK AVINASH NARAYAN		NALINI	, 71132436M ,	, PICT , T8054302
01. DATABASE MANAGEMENT SYSTEMS PP 02. DATA COMMUNICATION PP 03. MICROPROCESSORS & MICROCONTROLLERPP 04. DIGITAL SIGNAL PROCESSING PP 05. THEORY OF COMPUTATION PP 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 08. SIGNAL PROCESSING LABORATORY TW 09. SIGNAL PROCESSING LABORATORY OR 10. HARDWARE LABORATORY TW 11. HARDWARE LABORATORY PR	100 40 100 40 100 40 100 40 100 40 50 20 50 20 25 10 50 20 25 10 50 20	67 P 53 P 67 P 55 P 67 P 45 P 42 P 22 P 39 P 23 P 42 P		
FIRST TERM TOTAL = 522/750. ORDN. 1 MARKS :				

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UNIVERSITY OF PUNE ,T.E. (2008 PAT.) (COMPUTER) EXAMINATION NOV. 2011
   DATE: 02 MAR. 2012 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.
                                                                        PAGE NO. 34 ( 447)
NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
     OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
PRATIBHA , 71045561F , , , PICT , T8054303
 T8054303 PATHAK VISHVAJIT HARISHCHANDRA
 01. DATABASE MANAGEMENT SYSTEMS
                          PP
                              100
 02. DATA COMMUNICATION
                                  40 46 P
                                  40 40 P
                              100
 03. MICROPROCESSORS & MICROCONTROLLERPP
 04. DIGITAL SIGNAL PROCESSING PP
                              100
                                  40 71
                                        Р
 05. THEORY OF COMPUTATION
                              100
                                  40 45
 06. RDBMS & VISUAL PROGRAMMING LAB. TW
                                  20 41 P
                              50
 07. RDBMS & VISUAL PROGRAMMING LAB. PR
                              50
                                  20 42
 08. SIGNAL PROCESSING LABORATORY TW
                              25 10 18
 09. SIGNAL PROCESSING LABORATORY
                              50 20 35 P
                          OR
 10. HARDWARE LABORATORY TW 25 10 20 P
 11. HARDWARE LABORATORY
                        PR 50 20 35 P
 FIRST TERM TOTAL = 438/750.
ORDN. 1 MARKS :
T8054304 PATIL AKSHAY JAYANT
                                      POOJA
                                                    , 71045562D , , PICT , T8054304
                        PP 100 40
 O1. DATABASE MANAGEMENT SYSTEMS
 02. DATA COMMUNICATION
                  PP 100
                                 40 59 P
 03. MICROPROCESSORS & MICROCONTROLLERPP 100
                                  40 53 P
 04. DIGITAL SIGNAL PROCESSING PP 100
                                  40 67 P
 05. THEORY OF COMPUTATION
                              100
                                  40 43 P
                          PP
 06. RDBMS & VISUAL PROGRAMMING LAB. TW
                              50
                                  20 40 P
 07. RDBMS & VISUAL PROGRAMMING LAB. PR
                              50
                                  20 38 P
 08. SIGNAL PROCESSING LABORATORY TW
                              25
                                  10 20
 09. SIGNAL PROCESSING LABORATORY
                              50
                                  20 40 P
                          OR
 10. HARDWARE LABORATORY TW
                              25 10 20 P
 11. HARDWARE LABORATORY
                        PR 50 20 40 P
 FIRST TERM TOTAL = 476/750.
ORDN. 1 MARKS:
, 71045567E , , PICT , T8054305
 T8054305 PATIL SAGAR RAJESH
                                      USHA
                          PP 100 40
 01. DATABASE MANAGEMENT SYSTEMS
                          PP 100 40 50 P
 02. DATA COMMUNICATION
 03. MICROPROCESSORS & MICROCONTROLLERPP 100
                                 40 57
 04. DIGITAL SIGNAL PROCESSING PP 100
                                 40 67
                              100
                                 40 59
 05. THEORY OF COMPUTATION
 06. RDBMS & VISUAL PROGRAMMING LAB. TW
                              50
 07. RDBMS & VISUAL PROGRAMMING LAB. PR
                              50
 08. SIGNAL PROCESSING LABORATORY TW
 10. HARDWARE LABORATORY TW
                              50
                                  20
                                     40
                              25
                                     22
                                  10
                                        Р
                              50 20
                                    43
 FIRST TERM TOTAL = 511/750.
ORDN. 1 MARKS:
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UNIVERSITY OF PUNE ,T.E. (2008 PAT.) (COMPUTER) EXAMINATION NOV. 2011
   DATE: 02 MAR. 2012 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.
                                                                        PAGE NO. 35 ( 448)
NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
    OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
T8054306 PATIL SHALAKA VIJAY
                                      SHAILAJA , 71132437K , , PICT , T8054306
 02. DATA COMMUNICATION
03. MTCROPPOCESSORS
                          PP 100
                                 40
                                     66 P
 03. MICROPROCESSORS & MICROCONTROLLERPP
                             100
                                  40 63 P
 04. DIGITAL SIGNAL PROCESSING PP
                             100
                                  40 67
                                        Р
 05. THEORY OF COMPUTATION
                             100
                                  40 76
 06. RDBMS & VISUAL PROGRAMMING LAB. TW
                                  20 45 P
                              50
 07. RDBMS & VISUAL PROGRAMMING LAB. PR
                              50
                                  20 44 P
 08. SIGNAL PROCESSING LABORATORY TW
                              25 10 21
 09. SIGNAL PROCESSING LABORATORY
                              50 20 33 P
                          OR
 10. HARDWARE LABORATORY TW 25 10 21 P
 11. HARDWARE LABORATORY
                        PR 50 20 44 P
 FIRST TERM TOTAL = 546/750.
ORDN. 1 MARKS :
T8054307 PAWAR AJINKYA NANDU
                                      VIDYA
                                                    , 71045570E , , PICT , T8054307
                        PP 100 40 45 P
 01. DATABASE MANAGEMENT SYSTEMS
 02. DATA COMMUNICATION
                  PP 100
                                 40 51 P
 03. MICROPROCESSORS & MICROCONTROLLERPP 100
                                 40 41 P
 04. DIGITAL SIGNAL PROCESSING PP 100
                                  40 31 F
 05. THEORY OF COMPUTATION
                             100
                                  40 26 F
                          PP
 06. RDBMS & VISUAL PROGRAMMING LAB. TW
                             50
                                  20
                                     24 P
 07. RDBMS & VISUAL PROGRAMMING LAB. PR
                              50
                                  20
                                     28
 08. SIGNAL PROCESSING LABORATORY TW
                                  10 11
 09. SIGNAL PROCESSING LABORATORY
                              50
                                  20 10 F
                          OR
 10. HARDWARE LABORATORY TW
                              25
                                 10 11 P
 11. HARDWARE LABORATORY
                        PR 50 20 22 P
 FIRST TERM TOTAL = 300/750.
ORDN. 1 MARKS:
, 71132438н , , , рІСТ , Т8054308
 T8054308 PAWAR HARISH MANGESH
                                      RENUKA
                          PP 100 40
 01. DATABASE MANAGEMENT SYSTEMS
                          PP 100 40 49 P
 02. DATA COMMUNICATION
 03. MICROPROCESSORS & MICROCONTROLLERPP 100
                                 40 52 P
 04. DIGITAL SIGNAL PROCESSING PP 100
                                 40 28 F
                             100
                                 40 40
 05. THEORY OF COMPUTATION
 06. RDBMS & VISUAL PROGRAMMING LAB. TW
                              50
                                  20 35 P
 07. RDBMS & VISUAL PROGRAMMING LAB. PR
                              50
 08. SIGNAL PROCESSING LABORATORY TW
 10. HARDWARE LABORATORY TW
                        OR
                                 20 25
                              50
                              25
                                 10 16
                                        Р
                             50 20
                                     05 F
 FIRST TERM TOTAL = 352/750.
ORDN. 1 MARKS:
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DATE: 02 MAR. 2012 CENTE	RE : PUNE :	E.(2008 PAT.)(COMPUTER	TECHNOLOGY, PUNE.	PAGE NO. 36	(449)
NOTE: FIRST LINE : SEAT NO., NAME OF THE OTHER LINES: HEAD OF PASSING, MAX.	${\tt CANDIDATE}$, MOTHER, PERMANENT R IN. PASS MARKS, MARKS		AT NO., COLLEGE, SEAT N	
T8054309 PILAJI KEDARNATH BALAJI		ACUNA	, 71045574н ,	, PICT , T	8054309
01. DATABASE MANAGEMENT SYSTEMS PP 02. DATA COMMUNICATION PP 03. MICROPROCESSORS & MICROCONTROLLERPP 04. DIGITAL SIGNAL PROCESSING PP 05. THEORY OF COMPUTATION PP 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 08. SIGNAL PROCESSING LABORATORY TW 09. SIGNAL PROCESSING LABORATORY OR 10. HARDWARE LABORATORY TW 11. HARDWARE LABORATORY PR FIRST TERM TOTAL = 447/750.	100 40 100 40 100 40 100 40 100 40 50 20 50 20 25 10 50 20 25 10 50 20	62 P 59 P 44 P 49 P 56 P 34 P 43 P 15 P 35 P 17 P 33 P			
ORDN. 1 MARKS :					
T8054310 POORVI ARVIND DHARWAD		ROHINI	, 70925566н ,	, PICT , T	8054310
01. DATABASE MANAGEMENT SYSTEMS PP 02. DATA COMMUNICATION PP 03. MICROPROCESSORS & MICROCONTROLLERPP 04. DIGITAL SIGNAL PROCESSING PP 05. THEORY OF COMPUTATION PP 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 08. SIGNAL PROCESSING LABORATORY TW 09. SIGNAL PROCESSING LABORATORY OR 10. HARDWARE LABORATORY TW 11. HARDWARE LABORATORY PR	100 40 100 40 100 40 100 40 100 40 50 20 50 20 25 10 50 20 25 10 50 20	26 F 40 P 27 F 22 F 11 F 32 P 26 P 20 P 10 F 21 P 05 F			
FIRST TERM TOTAL = 240/750. ORDN. 1 MARKS :					
T8054311 PORE DIVYA KISHOR		KALPANA	, 71132439F ,	, PICT , T	8054311
01. DATABASE MANAGEMENT SYSTEMS PP 02. DATA COMMUNICATION PP 03. MICROPROCESSORS & MICROCONTROLLERPP 04. DIGITAL SIGNAL PROCESSING PP 05. THEORY OF COMPUTATION PP 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 08. SIGNAL PROCESSING LABORATORY TW 09. SIGNAL PROCESSING LABORATORY OR 10. HARDWARE LABORATORY TW 11. HARDWARE LABORATORY PR	100 40 100 40 100 40 100 40 50 20 50 20 25 10 50 20 25 10 50 20	62 P 59 P 58 P 51 P 55 P 42 P 44 P 21 P 34 P 21 P 30 P			
FIRST TERM TOTAL = 477/750. ORDN. 1 MARKS :					

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UNIVERSITY OF PUNE ,T.E. (2008 PAT.) (COMPUTER) EXAMINATION NOV. 2011
   DATE: 02 MAR. 2012 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.
                                                                        PAGE NO. 37 ( 450)
NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
    OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
T8054312 POTDAR SHANTANU RATNAKAR
                                      ANITA
                                                    , 71132440K , , PICT , T8054312
 01. DATABASE MANAGEMENT SYSTEMS
                          PP 100
 02. DATA COMMUNICATION
                                 40 68 P
 03. MICROPROCESSORS & MICROCONTROLLERPP
                              100
                                  40
                                      68 P
 04. DIGITAL SIGNAL PROCESSING PP
                              100
                                  40 73
                                        Р
 05. THEORY OF COMPUTATION
                              100
                                  40 68
 06. RDBMS & VISUAL PROGRAMMING LAB. TW
                                  20 45
                              50
 07. RDBMS & VISUAL PROGRAMMING LAB. PR
                               50
                                  20 47
 08. SIGNAL PROCESSING LABORATORY TW
                               25 10 24
 09. SIGNAL PROCESSING LABORATORY OR
                               50 20 40 P
 10. HARDWARE LABORATORY TW 25 10 23 P 11. HARDWARE LABORATORY PR 50 20 46 P
 FIRST TERM TOTAL = 570/750.
ORDN. 1 MARKS :
T8054313 PRANIDHYA KHANDELWAL
                                      NISHA
                                                    , 71045580B , , PICT , T8054313
                        PP 100 40 60 P
 01. DATABASE MANAGEMENT SYSTEMS
 02. DATA COMMUNICATION
                  PP 100
                                 40 56 P
 03. MICROPROCESSORS & MICROCONTROLLERPP 100
                                  40 48 P
 04. DIGITAL SIGNAL PROCESSING PP 100
                                  40 46 P
 05. THEORY OF COMPUTATION
                              100
                                  40 44 P
                          PP
 06. RDBMS & VISUAL PROGRAMMING LAB. TW
                              50
                                  20 38
 07. RDBMS & VISUAL PROGRAMMING LAB. PR
                               50
                                  20 43 P
 08. SIGNAL PROCESSING LABORATORY TW
                               25
                                  10 16
 09. SIGNAL PROCESSING LABORATORY
                               50
                                  20 25
                          OR
 10. HARDWARE LABORATORY TW
                               25 10 17
                                        Р
 11. HARDWARE LABORATORY
                         PR 50 20 34
 FIRST TERM TOTAL = 427/750.
ORDN. 1 MARKS:
, 71045581L , , PICT , T8054314
 T8054314 PRATIVINDHYA MISHRA
                                      ANURAG
                        PP 100 40
 01. DATABASE MANAGEMENT SYSTEMS
                        PP 100 40 58 P
 02. DATA COMMUNICATION
 03. MICROPROCESSORS & MICROCONTROLLERPP 100
                                 40 56 P
 04. DIGITAL SIGNAL PROCESSING PP 100
                                 40 67
                                        Р
                              100
                                  40 60
 05. THEORY OF COMPUTATION
 06. RDBMS & VISUAL PROGRAMMING LAB. TW
                               50
                                  20 44 P
 07. RDBMS & VISUAL PROGRAMMING LAB. PR
                               50
                                  20 43 P
 08. SIGNAL PROCESSING LABORATORY TW
                                  10 24
 10. HARDWARE LABORATORY TW
                        OR
                               50
                                  20 42
                               25
                                  10
                                     22
                                        Р
                              50 20 42
 FIRST TERM TOTAL = 520/750.
ORDN. 1 MARKS :
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UNIVERSITY OF PUNE ,T.E. (2008 PAT.) (COMPUTER) EXAMINATION NOV. 2011
   DATE: 02 MAR. 2012 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.
                                                                       PAGE NO. 38 ( 451)
NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
    OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
T8054315 RAHEJA ROHAN VINOD
                                     POOJA
                                                    , 71045585C , , PICT , T8054315
 02. DATA COMMUNICATION PP 100
03. MTCROPPOCESSORS 9
                                 40 42 P
                                 40 31 F
 04. DIGITAL SIGNAL PROCESSING PP
                             100
                                  40 40 P
 05. THEORY OF COMPUTATION
                             100
                                  40 43
 06. RDBMS & VISUAL PROGRAMMING LAB. TW
                                  20 36
                              50
 07. RDBMS & VISUAL PROGRAMMING LAB. PR
                              50
                                 20 42
 08. SIGNAL PROCESSING LABORATORY TW
                              25 10 16
 09. SIGNAL PROCESSING LABORATORY OR
                              50 20 23
 10. HARDWARE LABORATORY TW 25 10 17 P
 11. HARDWARE LABORATORY
                        PR 50 20 35 P
 FIRST TERM TOTAL = 373/750.
ORDN. 1 MARKS :
T8054316 RANE RAHUL DILIP
                                     VIDYA
                                              , 71045586M , , PICT , T8054316
 01. DATABASE MANAGEMENT SYSTEMS PP 100
 02. DATA COMMUNICATION
                  PP 100
                                 40 53 P
 03. MICROPROCESSORS & MICROCONTROLLERPP 100
                                 40 58 P
 04. DIGITAL SIGNAL PROCESSING PP 100
                                  40 40 P
 05. THEORY OF COMPUTATION
                             100
                                  40 65 P
                          PP
 06. RDBMS & VISUAL PROGRAMMING LAB. TW
                             50
                                 20 33
 07. RDBMS & VISUAL PROGRAMMING LAB. PR
                              50
                                  20 43 P
 08. SIGNAL PROCESSING LABORATORY TW
                              25
                                 10 16
 09. SIGNAL PROCESSING LABORATORY
                              50
                                  20 23
                          OR
 10. HARDWARE LABORATORY TW
                              25 10 19 P
 11. HARDWARE LABORATORY
                        PR 50 20 34
 FIRST TERM TOTAL = 451/750.
ORDN. 1 MARKS :
RATNMALA , 71045588H , , , PICT , T8054317
 T8054317 RAWALE SUHAS SUDAMRAO
                          PP 100 40 40 P
 01. DATABASE MANAGEMENT SYSTEMS
                         PP 100 40 43 P
 02. DATA COMMUNICATION
 03. MICROPROCESSORS & MICROCONTROLLERPP 100
                                 40 40 P
                             100
                                 40 26 F
 04. DIGITAL SIGNAL PROCESSING PP
                             100
                                 40 40
 05. THEORY OF COMPUTATION
 06. RDBMS & VISUAL PROGRAMMING LAB. TW
                              50
                                 20 35 P
 07. RDBMS & VISUAL PROGRAMMING LAB. PR
                              50
                                 20 12 F
 08. SIGNAL PROCESSING LABORATORY TW
                                 10 14
 10. HARDWARE LABORATORY TW
                        OR
                              50
                                 20 15
                              25
                                 10 13
                                        Р
                             50 20
                                     08 F
 FIRST TERM TOTAL = 286/750.
ORDN. 1 MARKS :
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	NE ,T.E.(2008 PAT.)(COMPUTER) PUNE INSTITUTE OF COMPUTER T		PAGE NO. 39 (452)
NOTE: FIRST LINE : SEAT NO., NAME OF THE CAN OTHER LINES: HEAD OF PASSING, MAX. MAR	IDIDATE, MOTHER, PERMANENT RE	G. NO., PREVIOUS SEAT NO.,	, COLLEGE, SEAT NO.
T8054318 SAKORE MITHILA RAMNATH	MADHURI	, 71045593D ,	, PICT , T8054318
01. DATABASE MANAGEMENT SYSTEMS PP 100 02. DATA COMMUNICATION PP 100 03. MICROPROCESSORS & MICROCONTROLLERPP 100 04. DIGITAL SIGNAL PROCESSING PP 100 05. THEORY OF COMPUTATION PP 100 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 08. SIGNAL PROCESSING LABORATORY TW 25 09. SIGNAL PROCESSING LABORATORY OR 50 10. HARDWARE LABORATORY TW 25 11. HARDWARE LABORATORY PR 50 50 50 50 50 50 50 50	0 40 63 P 0 40 49 P 0 40 40 P 0 40 65 P 0 20 39 P 0 20 28 P 10 18 P 0 20 24 P 10 19 P		
FIRST TERM TOTAL = 437/750. ORDN. 1 MARKS :			
T8054319 SANDEEP AGARWAL	RITA	, 70925587L ,	, PICT , T8054319
01. DATABASE MANAGEMENT SYSTEMS PP 100 02. DATA COMMUNICATION PP 100 03. MICROPROCESSORS & MICROCONTROLLERPP 100 04. DIGITAL SIGNAL PROCESSING PP 100 05. THEORY OF COMPUTATION PP 100 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 08. SIGNAL PROCESSING LABORATORY TW 25 09. SIGNAL PROCESSING LABORATORY OR 50 10. HARDWARE LABORATORY TW 25 11. HARDWARE LABORATORY PR 50 50 50 50 50 50 50 50	40 40 P 40 11 F 40 15 F 40 50 P 20 34 P 20 12 F 5 10 15 P 20 22 P 5 10 17 P		
FIRST TERM TOTAL = 293/750. ORDN. 1 MARKS :			
T8054320 SANGHVI PALAK RAJESH	NEHA	, 71045595L ,	, РІСТ , Т8054320
01. DATABASE MANAGEMENT SYSTEMS PP 100 02. DATA COMMUNICATION PP 100 03. MICROPROCESSORS & MICROCONTROLLERPP 100 04. DIGITAL SIGNAL PROCESSING PP 100 05. THEORY OF COMPUTATION PP 100 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 07. RDBMS & VISUAL PROGRAMMING LAB. PR 608. SIGNAL PROCESSING LABORATORY TW 25 09. SIGNAL PROCESSING LABORATORY OR 50 10. HARDWARE LABORATORY TW 25 11. HARDWARE LABORATORY PR 50 50 50 50 50 50 50 50	0 40 54 P 0 40 52 P 0 40 57 P 0 40 54 P 0 20 45 P 0 20 38 P 10 23 P 10 20 22 P 10 23 P		
FIRST TERM TOTAL = 467/750. ORDN. 1 MARKS :			

UNIVERSITY DATE : 02 MAR. 2012 CEN				AT.)(COMF OF COMPU							PAGE N	10. 40) (453)
NOTE: FIRST LINE : SEAT NO., NAME OF THE OTHER LINES: HEAD OF PASSING, MAX	IE CANDIE	DATE,	MOTHER	, PERMANE	ENT REG	i. NO.,	PREVIO	JS SEAT	ΝΟ.,	COL	LEGE,	SEA	Γ NO.
										• •			
T8054321 SANGLIKAR PRATIK SANTOSH			ANJALI			, 710455	97G	,		,	PICT	!	, т8054321
01. DATABASE MANAGEMENT SYSTEMS PP 02. DATA COMMUNICATION PP 03. MICROPROCESSORS & MICROCONTROLLERPP 04. DIGITAL SIGNAL PROCESSING PP 05. THEORY OF COMPUTATION PP 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 08. SIGNAL PROCESSING LABORATORY TW 09. SIGNAL PROCESSING LABORATORY OR 10. HARDWARE LABORATORY TW 11. HARDWARE LABORATORY PR	100 100 100 100 50 50 25 50 25	40 40 440 440 420 420 420 420 10 120 120 120 120 120 120 120 120 12	6 P 6 P 0 P 0 P 0 P 0 P 2 P 6 P 2 P										
FIRST TERM TOTAL = 391/750. ORDN. 1 MARKS :													
T8054322 SARNAIK ABHISHEK MANOJ			KAVITA			, 709255	592G	,		,	PICT	:	, т8054322
01. DATABASE MANAGEMENT SYSTEMS PP 02. DATA COMMUNICATION PP 03. MICROPROCESSORS & MICROCONTROLLERPP 04. DIGITAL SIGNAL PROCESSING PP 05. THEORY OF COMPUTATION PP 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 08. SIGNAL PROCESSING LABORATORY TW 09. SIGNAL PROCESSING LABORATORY OR 10. HARDWARE LABORATORY TW 11. HARDWARE LABORATORY PR	100 100 100 100 100 50 50 25 50 25	40 40 40 40 20 20 20 10 20 10	5 P 7 P 6 P 7 F 4 P 0 P 5 F 5 P 8 F 7 P										
FIRST TERM TOTAL = 341/750. ORDN. 1 MARKS :													
T8054323 SAVLA SANKET HARISH			JYOTI			, 710456	502G	,		,	PICT		, т8054323
01. DATABASE MANAGEMENT SYSTEMS PP 02. DATA COMMUNICATION PP 03. MICROPROCESSORS & MICROCONTROLLERPP 04. DIGITAL SIGNAL PROCESSING PP 05. THEORY OF COMPUTATION PP 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 08. SIGNAL PROCESSING LABORATORY TW 09. SIGNAL PROCESSING LABORATORY OR 10. HARDWARE LABORATORY TW 11. HARDWARE LABORATORY PR	100 100 100 100 100 50 50 25 50 25	40 40 40 40 20 20 10 20 10	O P 2 P 1 F 4 P 6 P 0 P 5 P 3 P										
FIRST TERM TOTAL = 379/750. ORDN. 1 MARKS :													

DATE: 02 MAR. 2012 CENTRE:	E ,T.E.(2008 PAT.)(COMPUTER PUNE INSTITUTE OF COMPUTER	TECHNOLOGY, PUNE.	PAGE NO. 41 (454)
NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDO OTHER LINES: HEAD OF PASSING, MAX. MARK	IDATE, MOTHER, PERMANENT R		., COLLEGE, SEAT NO.
T8054324 SHAH DIMPLE PIYUSH	BINAL	, 71045606к ,	, PICT , T8054324
01. DATABASE MANAGEMENT SYSTEMS PP 100 02. DATA COMMUNICATION PP 100 03. MICROPROCESSORS & MICROCONTROLLERPP 100 04. DIGITAL SIGNAL PROCESSING PP 100 05. THEORY OF COMPUTATION PP 100 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 08. SIGNAL PROCESSING LABORATORY TW 25 09. SIGNAL PROCESSING LABORATORY OR 50 10. HARDWARE LABORATORY TW 25 11. HARDWARE LABORATORY PR 50	40 41 P 40 61 P 40 40 P 40 48 P 40 55 P 20 36 P 20 32 P 10 17 P 20 39 P 10 19 P 20 40 P		
FIRST TERM TOTAL = 428/750. ORDN. 1 MARKS :			
T8054325 SHAH YASH MANESH	RAJASHRI	, 71045608F ,	
01. DATABASE MANAGEMENT SYSTEMS PP 100 02. DATA COMMUNICATION PP 100 03. MICROPROCESSORS & MICROCONTROLLERPP 100 04. DIGITAL SIGNAL PROCESSING PP 100 05. THEORY OF COMPUTATION PP 100 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 08. SIGNAL PROCESSING LABORATORY TW 25 09. SIGNAL PROCESSING LABORATORY OR 50 10. HARDWARE LABORATORY PR 50	40 55 P 40 53 P 40 59 P 40 45 P 40 54 P 20 40 P 20 40 P 10 19 P 20 36 P 10 19 P 20 34 P		
FIRST TERM TOTAL = 454/750. ORDN. 1 MARKS :			
T8054326 SHASHANK KUMAR GUPTA	ANJANI	, 70925598F ,	, PICT , T8054326
01. DATABASE MANAGEMENT SYSTEMS PP 100 02. DATA COMMUNICATION PP 100 03. MICROPROCESSORS & MICROCONTROLLERPP 100 04. DIGITAL SIGNAL PROCESSING PP 100 05. THEORY OF COMPUTATION PP 100 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 08. SIGNAL PROCESSING LABORATORY TW 25 09. SIGNAL PROCESSING LABORATORY OR 50 10. HARDWARE LABORATORY TW 25 11. HARDWARE LABORATORY PR 50	40 44 P 40 44 P 40 40 P 40 AA F 40 45 P 20 25 P 20 33 P 10 12 P 20 20 P 10 16 P 20 05 F		
FIRST TERM TOTAL = 284/750. ORDN. 1 MARKS :			

DATE : 02 MAR. 2012 CEN	TRE : P	UŃE I	NSTITUTE OF COMPUTER		PAGE NO. 42	•
NOTE: FIRST LINE : SEAT NO., NAME OF TH OTHER LINES: HEAD OF PASSING, MAX	E CANDI . MARKS	DATE, , MI	MOTHER, PERMANENT N. PASS MARKS, MARK	REG. NO., PREVIOUS SEA'S OBTAINED, P/F:PASS/F	AIL, C:PREVIOUS CARRY ON	NO.
T8054327 SHEKHAR SAMIR SHAILESH KUMAR			PUSHPA RANI GUNJAN			т8054327
01. DATABASE MANAGEMENT SYSTEMS PP 02. DATA COMMUNICATION PP 03. MICROPROCESSORS & MICROCONTROLLERPP 04. DIGITAL SIGNAL PROCESSING PP 05. THEORY OF COMPUTATION PP 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 08. SIGNAL PROCESSING LABORATORY TW 09. SIGNAL PROCESSING LABORATORY OR 10. HARDWARE LABORATORY TW 11. HARDWARE LABORATORY PR	100 100 100 100 100 50 50 25 50 25	40 40 40 40 20 20 10 20 10 20	54 P 49 P 46 P 52 P 40 P 34 P 39 P 15 P 21 P 19 P 36 P			
FIRST TERM TOTAL = 405/750. ORDN. 1 MARKS :						
T8054328 SHERKAR ANIRUDH UMAKANT			SMITA	, 70925601к ,	, PICT , 1	т8054328
01. DATABASE MANAGEMENT SYSTEMS PP 02. DATA COMMUNICATION PP 03. MICROPROCESSORS & MICROCONTROLLERPP 04. DIGITAL SIGNAL PROCESSING PP 05. THEORY OF COMPUTATION PP 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 08. SIGNAL PROCESSING LABORATORY TW 09. SIGNAL PROCESSING LABORATORY OR 10. HARDWARE LABORATORY TW 11. HARDWARE LABORATORY PR	100 100 100 100 100 50 50 25 50 25	40 40 40 40 20 20 10 20 10 20	40 P 40 P 40 P 28 F 42 P 40 P 42 P 19 P 32 P 21 P 33 P			
FIRST TERM TOTAL = 377/750. ORDN. 1 MARKS :						
T8054329 SHINDE AMIT KEDARNATH			SANGEETA	, 71045614L ,	, PICT , 1	т8054329
01. DATABASE MANAGEMENT SYSTEMS PP 02. DATA COMMUNICATION PP 03. MICROPROCESSORS & MICROCONTROLLERPP 04. DIGITAL SIGNAL PROCESSING PP 05. THEORY OF COMPUTATION PP 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 08. SIGNAL PROCESSING LABORATORY TW 09. SIGNAL PROCESSING LABORATORY OR 10. HARDWARE LABORATORY PR	100 100 100 100 100 50 50 25 50 25	40 40 40 40 20 20 10 20 10 20	52 P 42 P 29 F 26 F 45 P 38 P 28 P 15 P 20 P 17 P 36 P			
FIRST TERM TOTAL = 348/750. ORDN. 1 MARKS :	- -	-				

DATE: 02 MAR. 2012 CENTRE: P	T.E.(2008 PAT.)(COMPUTER) UNE INSTITUTE OF COMPUTER T	ECHNOLOGY, PUNE.	PAGE NO. 43 (456)
NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDI OTHER LINES: HEAD OF PASSING, MAX. MARKS	, MÍN. PASS MARKS, MARKS	G. NO., PREVIOUS SEAT NO. OBTAINED, P/F:PASS/FAIL,	, COLLEGE, SEAT NO. C:PREVIOUS CARRY OVER
T8054330 SHREYAS KUMAR PANIGRAHI	MINATI	, 71045618C ,	
01. DATABASE MANAGEMENT SYSTEMS PP 100 02. DATA COMMUNICATION PP 100 03. MICROPROCESSORS & MICROCONTROLLERPP 100 04. DIGITAL SIGNAL PROCESSING PP 100 05. THEORY OF COMPUTATION PP 100 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 08. SIGNAL PROCESSING LABORATORY TW 25 09. SIGNAL PROCESSING LABORATORY OR 50 10. HARDWARE LABORATORY TW 25 11. HARDWARE LABORATORY PR 50	40 48 P 40 55 P 40 43 P 40 40 P 40 41 P 20 28 P 20 38 P 10 16 P 20 27 P 10 17 P 20 34 P		
FIRST TERM TOTAL = 387/750. ORDN. 1 MARKS :			
T8054331 SHRUTI SWAGATIKA	SUJATA	, 71045619м ,	, PICT , T8054331
01. DATABASE MANAGEMENT SYSTEMS PP 100 02. DATA COMMUNICATION PP 100 03. MICROPROCESSORS & MICROCONTROLLERPP 100 04. DIGITAL SIGNAL PROCESSING PP 100 05. THEORY OF COMPUTATION PP 100 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 08. SIGNAL PROCESSING LABORATORY TW 25 09. SIGNAL PROCESSING LABORATORY OR 50 10. HARDWARE LABORATORY TW 25 11. HARDWARE LABORATORY PR 50	40 50 P 40 50 P 40 55 P 40 55 P 40 46 P 20 40 P 20 39 P 10 19 P 20 29 P 10 19 P 20 23 P		
FIRST TERM TOTAL = 425/750. ORDN. 1 MARKS :			
T8054332 SIDDHA MONIKA SUNIL	JAYASHREE	, 71045623к ,	, PICT , T8054332
01. DATABASE MANAGEMENT SYSTEMS PP 100 02. DATA COMMUNICATION PP 100 03. MICROPROCESSORS & MICROCONTROLLERPP 100 04. DIGITAL SIGNAL PROCESSING PP 100 05. THEORY OF COMPUTATION PP 100 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 08. SIGNAL PROCESSING LABORATORY TW 25 09. SIGNAL PROCESSING LABORATORY OR 50 10. HARDWARE LABORATORY TW 25 11. HARDWARE LABORATORY PR 50	40 64 P 40 63 P 40 63 P 40 53 P 40 60 P 20 41 P 20 38 P 10 21 P 20 30 P 10 21 P 20 37 P		
FIRST TERM TOTAL = 491/750. ORDN. 1 MARKS :			

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UNIVERSITY OF PUNE ,T.E. (2008 PAT.) (COMPUTER) EXAMINATION NOV. 2011
   DATE : 02 MAR. 2012 CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.
                                                                         PAGE NO. 44 ( 457)
NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
    OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
T8054333 SINARE NUPOOR SANJAY
                                      KALPANA , 71045626D , , PICT , T8054333
 02. DATA COMMUNICATION
03. MTCROPPOCESSOR
                          PP 100
                                  40
                                      54 P
                                      62 P
 03. MICROPROCESSORS & MICROCONTROLLERPP
                              100
                                  40
 04. DIGITAL SIGNAL PROCESSING PP
                              100
                                  40
                                      52
                                         Р
 05. THEORY OF COMPUTATION
                              100
                                  40 56
 06. RDBMS & VISUAL PROGRAMMING LAB. TW
                                  20 40
                               50
                                  20 34
 07. RDBMS & VISUAL PROGRAMMING LAB. PR
                               50
 08. SIGNAL PROCESSING LABORATORY TW
                               25 10 20
 09. SIGNAL PROCESSING LABORATORY OR
                               50 20 33 P
 10. HARDWARE LABORATORY TW 25 10 20 P
11. HARDWARE LABORATORY PR 50 20 30 P
 FIRST TERM TOTAL = 462/750.
ORDN. 1 MARKS :
T8054334 SINGHAL PANKAJ SHRIBHAGWAN
                                      ANITA
                                                , 71045627B , , PICT , T8054334
                          PP 100 40 69 P
 01. DATABASE MANAGEMENT SYSTEMS
 02. DATA COMMUNICATION
                          PP 100
                                  40 56 P
 03. MICROPROCESSORS & MICROCONTROLLERPP 100
                                  40 58 P
 04. DIGITAL SIGNAL PROCESSING PP 100
                                  40 54 P
 05. THEORY OF COMPUTATION
                              100
                                  40
                                      65 P
                          PP
 06. RDBMS & VISUAL PROGRAMMING LAB. TW
                               50
                                  20
                                      36
 07. RDBMS & VISUAL PROGRAMMING LAB. PR
                               50
                                  20 40 P
 08. SIGNAL PROCESSING LABORATORY TW
                               25
                                  10
                                      20
 09. SIGNAL PROCESSING LABORATORY OR
                               50
                                  20 34
 10. HARDWARE LABORATORY TW
                               25 10 18 P
 11. HARDWARE LABORATORY
                         PR 50 20 35 P
 FIRST TERM TOTAL = 485/750.
ORDN. 1 MARKS :
, 71045631L , , , PICT , T8054335
 T8054335 SONAWANE KALINDI DATTATRAYA
                                      MAYA
                          PP 100 40
 01. DATABASE MANAGEMENT SYSTEMS
                          PP 100 40 55 P
 02. DATA COMMUNICATION
 03. MICROPROCESSORS & MICROCONTROLLERPP 100
                                  40 44 P
                              100
                                  40 45
 04. DIGITAL SIGNAL PROCESSING PP
                              100
                                  40 43
 05. THEORY OF COMPUTATION
 06. RDBMS & VISUAL PROGRAMMING LAB. TW
                               50
 07. RDBMS & VISUAL PROGRAMMING LAB. PR
                               50
 08. SIGNAL PROCESSING LABORATORY TW
                                  10 16
 10. HARDWARE LABORATORY TW
                        OR
                               50
                                  20 31
                               25
                                  10 19
                                         Р
                               50 20
                                      33
 FIRST TERM TOTAL = 382/750.
ORDN. 1 MARKS:
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DATE: 02 MAR. 2012 CENTRI	E : PUNE INSTITUTE	AT.)(COMPUTER) EXAMINAT OF COMPUTER TECHNOLOGY	, PUNE.	PAGE NO. 45 (458)
NOTE: FIRST LINE : SEAT NO., NAME OF THE O OTHER LINES: HEAD OF PASSING, MAX.!	CANDIDATE, MOTHER, MARKS, MIN. PASS N		PREVIOUS SEAT NO.,	
T8054336 SOUNDANKAR KOMAL RAJENDRA	KALPANA	A , 710456	34E ,	, PICT , T8054336
02. DATA COMMUNICATION PP 03. MICROPROCESSORS & MICROCONTROLLERPP 04. DIGITAL SIGNAL PROCESSING PP	100 40 22 F 100 40 42 P 100 40 42 P 100 40 40 P 100 40 40 P 50 20 34 P 50 20 24 P 25 10 15 P 50 20 21 P 25 10 19 P 50 20 25 P			
ORDN. 1 MARKS :				
T8054337 SUKHANI KUMAR DAYARAM	ARTI	, 710456	 36м ,	
02. DATA COMMUNICATION PP 03. MICROPROCESSORS & MICROCONTROLLERPP 04. DIGITAL SIGNAL PROCESSING PP	100 40 46 P 100 40 56 P 100 40 54 P 100 40 48 P 100 40 51 P 50 20 40 P 50 20 45 P 25 10 16 P 25 10 19 P 50 20 39 P			
FIRST TERM TOTAL = 435/750. ORDN. 1 MARKS :				
T8054338 TAHAKIK AVINASH ABASAHEB	SHAKUNT	TALA , 710456	38н ,	, PICT , T8054338
02. DATA COMMUNICATION PP 03. MICROPROCESSORS & MICROCONTROLLERPP 04. DIGITAL SIGNAL PROCESSING PP 05. THEORY OF COMPUTATION PP 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 08. SIGNAL PROCESSING LABORATORY TW 09. SIGNAL PROCESSING LABORATORY OR 10. HARDWARE LABORATORY TW 11. HARDWARE LABORATORY PR	LOO 40 53 P LOO 40 55 P LOO 40 59 P LOO 40 57 P LOO 40 62 P 50 20 32 P 50 20 15 F 25 10 15 P 50 20 32 P 50 20 32 P 50 20 40 P			
FIRST TERM TOTAL = 437/750. ORDN. 1 MARKS :				

DATE: 02 MAR. 2012 CENTRE: F	T.E.(2008 PAT.)(COMPUTER OF COMPUTER	TECHNOLOGY, PUNE.	PAGE NO. 46 (459)
NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDI OTHER LINES: HEAD OF PASSING, MAX. MARKS	, MIN. PASS MARKS, MARKS	EG. NO., PREVIOUS SEAT NO.	, COLLEGE, SEAT NO.
T8054339 TANPURE AJAY PRAKASH	SUNITA	, 71045639F ,	, PICT , T8054339
01. DATABASE MANAGEMENT SYSTEMS PP 100 02. DATA COMMUNICATION PP 100 03. MICROPROCESSORS & MICROCONTROLLERPP 100 04. DIGITAL SIGNAL PROCESSING PP 100 05. THEORY OF COMPUTATION PP 100 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 08. SIGNAL PROCESSING LABORATORY TW 25 09. SIGNAL PROCESSING LABORATORY OR 50 10. HARDWARE LABORATORY TW 25 11. HARDWARE LABORATORY PR 50	40 52 P 40 54 P 40 54 P 40 52 P 40 46 P 20 37 P 20 37 P 10 15 P 20 33 P 10 18 P 20 26 P		
FIRST TERM TOTAL = 424/750. ORDN. 1 MARKS :			
T8054340 TARGE SOURABH SURESH	MANISHA	, 71132441н ,	
01. DATABASE MANAGEMENT SYSTEMS PP 100 02. DATA COMMUNICATION PP 100 03. MICROPROCESSORS & MICROCONTROLLERPP 100 04. DIGITAL SIGNAL PROCESSING PP 100 05. THEORY OF COMPUTATION PP 100 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 08. SIGNAL PROCESSING LABORATORY TW 25 09. SIGNAL PROCESSING LABORATORY OR 50 10. HARDWARE LABORATORY TW 25 11. HARDWARE LABORATORY PR 50	40 60 P 40 60 P 40 66 P 40 45 P 40 58 P 20 45 P 20 42 P 10 14 P 20 25 P 10 20 P 20 35 P		
FIRST TERM TOTAL = 470/750. ORDN. 1 MARKS :			
T8054341 TATTI ADITI ANIL	ASHWINI	, 71045641н ,	, PICT , T8054341
01. DATABASE MANAGEMENT SYSTEMS PP 100 02. DATA COMMUNICATION PP 100 03. MICROPROCESSORS & MICROCONTROLLERPP 100 04. DIGITAL SIGNAL PROCESSING PP 100 05. THEORY OF COMPUTATION PP 100 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 08. SIGNAL PROCESSING LABORATORY TW 25 09. SIGNAL PROCESSING LABORATORY OR 50 10. HARDWARE LABORATORY TW 25 11. HARDWARE LABORATORY PR 50 FIRST TERM TOTAL = 452/750.	40 54 P 40 50 P 40 57 P 40 53 P 40 54 P 20 42 P 20 45 P 10 19 P 20 31 P 10 19 P 20 28 P		
ORDN. 1 MARKS :			

DATE : 02 MAR. 2012 CENT	OF PUNE ,T.E.(2008 PAT.)(COMPUTER) TRE : PUNE INSTITUTE OF COMPUTER 1	ECHNOLOGY, PUNE.	PAGE NO. 47 (460)
NOTE: FIRST LINE : SEAT NO., NAME OF THE OTHER LINES: HEAD OF PASSING, MAX.	E CANDIDATE, MOTHER, PERMANENT RE		, COLLEGE, SEAT NO.
T8054342 THAKARE DHANRAJSINGH SURESH	ЈАҮА	, 71045646J ,	, PICT , T8054342
01. DATABASE MANAGEMENT SYSTEMS PP 02. DATA COMMUNICATION PP 03. MICROPROCESSORS & MICROCONTROLLERPP 04. DIGITAL SIGNAL PROCESSING PP 05. THEORY OF COMPUTATION PP 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 08. SIGNAL PROCESSING LABORATORY TW 09. SIGNAL PROCESSING LABORATORY OR 10. HARDWARE LABORATORY TW	100		
FIRST TERM TOTAL = 390/750. ORDN. 1 MARKS :			
T8054343 THAKARE SAGAR SATISH	JAYASHREE		
01. DATABASE MANAGEMENT SYSTEMS PP 02. DATA COMMUNICATION PP 03. MICROPROCESSORS & MICROCONTROLLERPP 04. DIGITAL SIGNAL PROCESSING PP 05. THEORY OF COMPUTATION PP 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 08. SIGNAL PROCESSING LABORATORY TW 09. SIGNAL PROCESSING LABORATORY OR 10. HARDWARE LABORATORY TW	100		
FIRST TERM TOTAL = 212/750. ORDN. 1 MARKS :			
	VARSHA		
01. DATABASE MANAGEMENT SYSTEMS PP 02. DATA COMMUNICATION PP 03. MICROPROCESSORS & MICROCONTROLLERPP 04. DIGITAL SIGNAL PROCESSING PP 05. THEORY OF COMPUTATION PP 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 08. SIGNAL PROCESSING LABORATORY TW 09. SIGNAL PROCESSING LABORATORY OR 10. HARDWARE LABORATORY TW 11. HARDWARE LABORATORY PR FIRST TERM TOTAL = 417/750. ORDN. 1 MARKS :	100		

DATE : 02 MAR. 2012 CEN	TRE : PUNE	E.(2008 PAT.)(COMPUTER)	TECHNOLOGY, PUNE.	P.A	AGE NO. 48 (461)
NOTE: FIRST LINE : SEAT NO., NAME OF TH OTHER LINES: HEAD OF PASSING, MAX		, MOTHER, PERMANENT RE IN. PASS MARKS, MARKS	EG. NO., PREVIOUS	SEAT NO., COLLE	EGE, SEAT NO.
T8054346 UTKARSH MISHRA		мамта	, 71045654K ,	, , ,	PICT , T8054346
01. DATABASE MANAGEMENT SYSTEMS PP 02. DATA COMMUNICATION PP 03. MICROPROCESSORS & MICROCONTROLLERPP 04. DIGITAL SIGNAL PROCESSING PP 05. THEORY OF COMPUTATION PP 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 08. SIGNAL PROCESSING LABORATORY TW 09. SIGNAL PROCESSING LABORATORY OR 10. HARDWARE LABORATORY TW 11. HARDWARE LABORATORY PR	100 40 100 40 100 40 100 40 100 40 50 20 50 20 25 10 50 20 25 10 50 20	56 P 62 P 40 P 50 P 46 P 20 P 26 P 14 P 20 P 16 P			
FIRST TERM TOTAL = 355/750. ORDN. 1 MARKS :					
T8054348 VAYKOLE TEJAS ANIL		KALPANA	, 71045656F ,	, , , , , , ,	PICT , T8054348
01. DATABASE MANAGEMENT SYSTEMS PP 02. DATA COMMUNICATION PP 03. MICROPROCESSORS & MICROCONTROLLERPP 04. DIGITAL SIGNAL PROCESSING PP 05. THEORY OF COMPUTATION PP 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 08. SIGNAL PROCESSING LABORATORY TW 09. SIGNAL PROCESSING LABORATORY OR 10. HARDWARE LABORATORY TW 11. HARDWARE LABORATORY PR	100 40 100 40 100 40 100 40 100 40 50 20 50 20 25 10 50 20 25 10 50 20	43 P 47 P 58 P 40 P 44 P 41 P 32 P 18 P 32 P 20 P 08 F			
FIRST TERM TOTAL = 383/750. ORDN. 1 MARKS :					
T8054349 VEER ANJALI SUNDARDAS		URMILA			
01. DATABASE MANAGEMENT SYSTEMS PP 02. DATA COMMUNICATION PP 03. MICROPROCESSORS & MICROCONTROLLERPP 04. DIGITAL SIGNAL PROCESSING PP 05. THEORY OF COMPUTATION PP 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 08. SIGNAL PROCESSING LABORATORY TW 09. SIGNAL PROCESSING LABORATORY OR 10. HARDWARE LABORATORY TW 11. HARDWARE LABORATORY PR FIRST TERM TOTAL = 469/750. ORDN. 1 MARKS:	100 40 100 40 100 40 100 40 100 40 50 20 50 20 25 10 50 20 25 10 50 20	67 P 54 P 64 P 52 P 51 P 36 P 39 P 22 P 27 P 22 P 35 P	, , , , , , , , , , , , , , , , , , , ,	, '	

UNIVERSITY OF PUNE ,T.E. (2008 PAT.) (COMPUTER) EXAMINATION NOV. 2011 DATE : 02 MAR. 2012 CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 49 (462) NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER T8054350 VEERKAR PARTHA PRABODH MEDHA , 71045657D , , PICT , T8054350 01. DATABASE MANAGEMENT SYSTEMS 02. DATA COMMUNICATION PP 100 40 44 P 40 55 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 04. DIGITAL SIGNAL PROCESSING PP 100 40 46 P 05. THEORY OF COMPUTATION 100 40 44 Р 06. RDBMS & VISUAL PROGRAMMING LAB. TW 20 41 50 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 43 08. SIGNAL PROCESSING LABORATORY TW 25 10 20 P 09. SIGNAL PROCESSING LABORATORY OR 50 20 20 P 10. HARDWARE LABORATORY TW 25 10 20 P 11. HARDWARE LABORATORY PR 50 20 33 P FIRST TERM TOTAL = 406/750. ORDN. 1 MARKS : PARVATI , 71045664G , , PICT , T8054351 T8054351 WAJE ROHIT GITARAM PP 100 40 01. DATABASE MANAGEMENT SYSTEMS 02. DATA COMMUNICATION PP 100 40 58 P 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 55 P 04. DIGITAL SIGNAL PROCESSING PP 100 40 56 P 05. THEORY OF COMPUTATION 100 40 53 PP 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 20 41 P 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 42 P 08. SIGNAL PROCESSING LABORATORY TW 10 22 09. SIGNAL PROCESSING LABORATORY 50 20 26 OR 10. HARDWARE LABORATORY TW 25 10 22 PR 50 20 36 P 11. HARDWARE LABORATORY FIRST TERM TOTAL = 463/750. ORDN. 1 MARKS : , 70925338K , T8054208 , PICT , T8054352 T8054352 ARASHDEEP SINGH HEIR RUPINDER PP 100 40 40 P C O1. DATABASE MANAGEMENT SYSTEMS 12. PRINCIPLES OF PROGRAMMING LANG. PP 100 02. DATA COMMUNICATION PP 100 40 51 P 13. COMPUTER NETWORKS PP 100 40 40 P C 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 40 P C 14. FINANCE & MANAGEMENT INFORMA.SYS.PP 100 40 AA F 04. DIGITAL SIGNAL PROCESSING PP 100 40 40 P
05. THEORY OF COMPUTATION PP 100 40 29 F 15. SYSTEMS PROGRAMMING & OPERA.SYS. PP 100 40 AA F 16. SOFTWARE ENGINEERING PP
17. SOFTWARE LABORATORY TW
18. SOFTWARE LABORATORY PR
19. COMPUTER NETWORK TW
20. COMPUTER NETWORK OR 100 40 14 F 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 20 20 PC 25 10 10 PC 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 20 P 50 20 08 F 08. SIGNAL PROCESSING LABORATORY TW
09. SIGNAL PROCESSING LABORATORY OR 25 10 10 P C 25 10 12 P C 50 20 22 P C 10. HARDWARE LABORATORY TW 25 10 10 P C 11. HARDWARE LABORATORY PR 50 20 26 P C GRAND TOTAL = 461/1500, RESULT: FAILS ORDN. 1 MARKS :

UNIVERSITY OF PUNE ,T.E. (2008 PAT.) (COMPUTER) EXAMINATION NOV. 2011 CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. DATE : 02 MAR. 2012 PAGE NO. 50 (463) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER T8054353 ARCHANA KUMAR SHYAMALA , 71072124C , T8054209 , PICT , T8054353 100 44 P C 12. PRINCIPLES OF PROGRAMMING LANG. PP 100 01. DATABASE MANAGEMENT SYSTEMS PP 57 P C 02. DATA COMMUNICATION 100 40 13. COMPUTER NETWORKS PP 100 40 71 P C 100 40 70 P C 03. MICROPROCESSORS & MICROCONTROLLERPP 66 P C 14. FINANCE & MANAGEMENT INFORMA.SYS.PP 100 40

15. SYSTEMS PROGRAMMING & OPERA.SYS. PP

16. SOFTWARE ENGINEERS

17. SOFTWARE LABORATORY
PR
TW

21. SOFTWARE DEVELOPMENT TOOLS LAB. TW

22. SEMINAR AND TECHNICAL COMMUNI. TW

19. COMPUTER NETWORK

20. COMPUTER NETWORK

100

100

25

50

25

50

TW

OR

60 P C

22 P C

34 P C

21 P C

52

20 36 P

50 20 43 P C

50 20 45 P.C

10

20

10

RESULT RESERVED FOR BKLG

RESULT RESERVED FOR BKLG

РС

100

100

50

50

25

50

25

TW

OR

TW

PR

40

40

20

20

43 P C

63 P C

47 P.C

36 P C

10 23 P C

20 20 P C

10 23 PC

50 20 35 P C

GRAND TOTAL = 974/1500, RESULT: FIRST CLASS ORDN. 1 MARKS :

04. DIGITAL SIGNAL PROCESSING PP

06. RDBMS & VISUAL PROGRAMMING LAB. TW

07. RDBMS & VISUAL PROGRAMMING LAB. PR

08. SIGNAL PROCESSING LABORATORY

09. SIGNAL PROCESSING LABORATORY

05. THEORY OF COMPUTATION

10. HARDWARE LABORATORY

11. HARDWARE LABORATORY

T8054354 ARUNDHATI NAVADA			REKHA	, 70925340M , T8054210 ,	PI	CT	, т	8054	354
01. DATABASE MANAGEMENT SYSTEMS PP	100	40	41 P C	12. PRINCIPLES OF PROGRAMMING LANG.	PP	100	40	52	РС
02. DATA COMMUNICATION PP	100	40	40 P C	13. COMPUTER NETWORKS	PP	100	40	57	РС
03. MICROPROCESSORS & MICROCONTROLLERPP	100	40	40 P C	14. FINANCE & MANAGEMENT INFORMA.SYS.	. PP	100	40	54	РС
04. DIGITAL SIGNAL PROCESSING PP	100	40	52 P	15. SYSTEMS PROGRAMMING & OPERA.SYS.	PP	100	40	47	РС
05. THEORY OF COMPUTATION PP	100	40	62 P C	16. SOFTWARE ENGINEERING	PP	100	40	40	РС
06. RDBMS & VISUAL PROGRAMMING LAB. TW	50	20	27 PC	17. SOFTWARE LABORATORY	TW	25	10	14	РС
07. RDBMS & VISUAL PROGRAMMING LAB. PR	50	20	30 P C	18. SOFTWARE LABORATORY	PR	50	20	35	РС
08. SIGNAL PROCESSING LABORATORY TW	25	10	11 PC	19. COMPUTER NETWORK	TW	25	10	14	РС
09. SIGNAL PROCESSING LABORATORY OR	50	20	28 P C	20. COMPUTER NETWORK	OR	50	20	26	Р
10. HARDWARE LABORATORY TW	25	10	11 PC	SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	28	РС
11. HARDWARE LABORATORY PR	50	20	34 P C	22. SEMINAR AND TECHNICAL COMMUNI.	TW	50	20	36	РС

GRAND TOTAL = 779/1500, RESULT: SECOND CLASS ORDN. 1 MARKS:

T8054355 AVLEEN UPPAL			INDERDEEP	, 70801350D , T8054213 , PICT	, т8054355
01. DATABASE MANAGEMENT SYSTEMS PP	100	40	46 P C	12. PRINCIPLES OF PROGRAMMING LANG. PP 100	40 AA F
O2. DATA COMMUNICATION PP	100	40	AA F	13. COMPUTER NETWORKS PP 100	40 52 PC
03. MICROPROCESSORS & MICROCONTROLLERPP	100	40	40 P C	14. FINANCE & MANAGEMENT INFORMA.SYS.PP 100	40 53 PC
04. DIGITAL SIGNAL PROCESSING PP	100	40	AA F	15. SYSTEMS PROGRAMMING & OPERA.SYS. PP 100	40 40 PC
05. THEORY OF COMPUTATION PP	100	40	40 P C	16. SOFTWARE ENGINEERING PP 100	40 AA F
06. RDBMS & VISUAL PROGRAMMING LAB. TW	50	20	22 P C	17. SOFTWARE LABORATORY TW 25	10 13 PC
07. RDBMS & VISUAL PROGRAMMING LAB. PR	50	20	AA F	18. SOFTWARE LABORATORY PR 50	20 aa f
08. SIGNAL PROCESSING LABORATORY TW	25	10	15 P C	19. COMPUTER NETWORK TW 25	10 12 PC
09. SIGNAL PROCESSING LABORATORY OR	50	20	27 P C	20. COMPUTER NETWORK OR 50	20 AA F
10. HARDWARE LABORATORY TW	25	10	16 P C	21. SOFTWARE DEVELOPMENT TOOLS LAB. TW 50	20 27 PC
11. HARDWARE LABORATORY PR	50	20	AA F	22. SEMINAR AND TECHNICAL COMMUNI. TW 50	20 38 P C

GRAND TOTAL = 441/1500, RESULT: FAILS ORDN. 1 MARKS :

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DATE: 02 MAR. 2012 CEN	ITRE : PUNE	INSTITUTE OF	(COMPUTER) EXAMINATION NOV. 2011 COMPUTER TECHNOLOGY, PUNE. PAGE NO. 51 (464)
NOTE: FIRST LINE : SEAT NO., NAME OF TH	IE CANDIDAT (. MARKS,	E, MOTHER, PE MIN. PASS MARK	RMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. S, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
T8054356 BAGADE PRASANNA MADHURAJE		SINDHU	, 70925350j , T8054214 , PICT , T8054356
O1. DATABASE MANAGEMENT SYSTEMS PP O2. DATA COMMUNICATION PP O3. MICROPROCESSORS & MICROCONTROLLERPP O4. DIGITAL SIGNAL PROCESSING PP O5. THEORY OF COMPUTATION PP O6. RDBMS & VISUAL PROGRAMMING LAB. TW O7. RDBMS & VISUAL PROGRAMMING LAB. PR O8. SIGNAL PROCESSING LABORATORY TW O9. SIGNAL PROCESSING LABORATORY OR 10. HARDWARE LABORATORY TW 11. HARDWARE LABORATORY PR	100 40 100 40 100 40 100 40 50 20 50 20 25 10 50 20 25 20	40 P C 48 P C 43 P C 40 P C 39 P C 19 P C 34 P C 18 P C	12. PRINCIPLES OF PROGRAMMING LANG. PP 100 40 43 P C 13. COMPUTER NETWORKS PP 100 40 41 P C 14. FINANCE & MANAGEMENT INFORMA.SYS.PP 100 40 49 P C 15. SYSTEMS PROGRAMMING & OPERA.SYS. PP 100 40 56 P C 16. SOFTWARE ENGINEERING PP 100 40 40 P C 17. SOFTWARE LABORATORY TW 25 10 19 P C 18. SOFTWARE LABORATORY PR 50 20 36 P 19. COMPUTER NETWORK TW 25 10 19 P C 20. COMPUTER NETWORK OR 50 20 30 P C 21. SOFTWARE DEVELOPMENT TOOLS LAB. TW 50 20 37 P C 22. SEMINAR AND TECHNICAL COMMUNI. TW 50 20 38 P C
GRAND TOTAL = 822+03/1500, RESULT: HIGHER ORDN. 1 MARKS :	SECOND CL.	ASS [0.2]	
		NALINI	, 70925364J , T8054219 , PICT , T8054357
01. DATABASE MANAGEMENT SYSTEMS PP 02. DATA COMMUNICATION PP 03. MICROPROCESSORS & MICROCONTROLLERPP 04. DIGITAL SIGNAL PROCESSING PP 05. THEORY OF COMPUTATION PP 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 08. SIGNAL PROCESSING LABORATORY TW 09. SIGNAL PROCESSING LABORATORY OR 10. HARDWARE LABORATORY TW 11. HARDWARE LABORATORY PR	100 40 100 40 100 40 100 40 50 20 50 20 25 10 50 20 25 10 50 20	54 P C 57 P C 58 P C 40 P C 42 P C 18 P C 20 P C 19 P C 38 P C	12. PRINCIPLES OF PROGRAMMING LANG. PP 100 40 44 P C 13. COMPUTER NETWORKS PP 100 40 45 P C 14. FINANCE & MANAGEMENT INFORMA.SYS.PP 100 40 61 P C 15. SYSTEMS PROGRAMMING & OPERA.SYS. PP 100 40 66 P C 16. SOFTWARE ENGINEERING PP 100 40 47 P C 17. SOFTWARE LABORATORY TW 25 10 19 P C 18. SOFTWARE LABORATORY PR 50 20 30 P C 19. COMPUTER NETWORK TW 25 10 21 P C 20. COMPUTER NETWORK OR 50 20 32 P 21. SOFTWARE DEVELOPMENT TOOLS LAB. TW 50 20 31 P C 22. SEMINAR AND TECHNICAL COMMUNI. TW 50 20 39 P C
GRAND TOTAL = 880/1500, RESULT: HIGHER SE ORDN. 1 MARKS :	COND CLASS		
T8054358 CHITALE MANDAR SUHAS		NEELA	, 70701524D , T8054223 , PICT , T8054358
01. DATABASE MANAGEMENT SYSTEMS PP 02. DATA COMMUNICATION PP 03. MICROPROCESSORS & MICROCONTROLLERPP 04. DIGITAL SIGNAL PROCESSING PP 05. THEORY OF COMPUTATION PP 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 08. SIGNAL PROCESSING LABORATORY TW 09. SIGNAL PROCESSING LABORATORY OR 10. HARDWARE LABORATORY TW 11. HARDWARE LABORATORY PR GRAND TOTAL = 786/1500, RESULT: SECOND CL ORDN. 1 MARKS:	50 20 25 10 50 20 25 10 50 20	41 P C 53 P C 40 P C 44 P C 42 P C 25 P 22 P C 37 P C 20 P C	12. PRINCIPLES OF PROGRAMMING LANG. PP 100 40 41 P C 13. COMPUTER NETWORKS PP 100 40 40 P C 14. FINANCE & MANAGEMENT INFORMA.SYS.PP 100 40 47 P C 15. SYSTEMS PROGRAMMING & OPERA.SYS. PP 100 40 51 P 16. SOFTWARE ENGINEERING PP 100 40 40 P C 17. SOFTWARE LABORATORY TW 25 10 20 P C 18. SOFTWARE LABORATORY PR 50 20 25 P 19. COMPUTER NETWORK TW 25 10 20 P C 20. COMPUTER NETWORK OR 50 20 35 P C 21. SOFTWARE DEVELOPMENT TOOLS LAB. TW 50 20 39 P C 22. SEMINAR AND TECHNICAL COMMUNI. TW 50 20 38 P C

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DATE: 02 MAR. 2012 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 52 (465) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER T8054359 DESHPANDE KAIVALYA ARUN SARITA , 70925397E , T8054227 , PICT , T8054359 01. DATABASE MANAGEMENT SYSTEMS PP 100 40 43 P C 12. PRINCIPLES OF PROGRAMMING LANG. PP 100 40 43 P C 02. DATA COMMUNICATION PP 100 40 55 P C 13. COMPUTER NETWORKS PP 100 40 65 P C 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 61 P C 14. FINANCE & MANAGEMENT INFORMA.SYS.PP 100 40 58 P C 04. DIGITAL SIGNAL PROCESSING PP 100 40 62 P C 15. SYSTEMS PROGRAMMING & OPERA.SYS. PP 100 40 69 P C 05. THEORY OF COMPUTATION PP 100 40 46 P C 16. SOFTWARE ENGINEERING PP 100 40 40 P C 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 20 34 P C 17. SOFTWARE LABORATORY TW 25 10 18 P C 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 30 P C 18. SOFTWARE LABORATORY PR 50 20 38 P C 08. SIGNAL PROCESSING LABORATORY TW 25 10 20 P C 19. COMPUTER NETWORK TW 25 10 18 P C 09. SIGNAL PROCESSING LABORATORY OR 50 20 30 P C 20. COMPUTER NETWORK OR 50 20 44 P C 10. HARDWARE LABORATORY TW 25 10 17 P C 21. SOFTWARE DEVELOPMENT TOOLS LAB. TW 50 20 30 P C 11. HARDWARE LABORATORY PR 50 20 33 P C 22. SEMINAR AND TECHNICAL COMMUNI. TW 50 20 33 P C GRAND TOTAL = 890+10/1500, RESULT: FIRST CLASS [0.2] ORDN. 1 MARKS: LATA , 70925401G , T8054229 , PICT , T8054360 T8054360 DHENDE ADITYA BARMA 01. DATABASE MANAGEMENT SYSTEMS PP 100 40 47 P C 12. PRINCIPLES OF PROGRAMMING LANG. PP 100 40 53 P C 02. DATA COMMUNICATION PP 100 40 50 P C 13. COMPUTER NETWORKS PP 100 40 66 P C

 02. DATA COMMUNICATION
 PP
 100
 40
 50
 P C
 13. COMPUTER NETWORKS
 PP
 100
 40
 66
 P C

 03. MICROPROCESSORS & MICROCONTROLLERPP
 100
 40
 63
 P C
 14. FINANCE & MANAGEMENT INFORMA.SYS.PP
 100
 40
 62
 P C

 04. DIGITAL SIGNAL PROCESSING
 PP
 100
 40
 45
 P C
 15. SYSTEMS PROGRAMMING & OPERA.SYS. PP
 100
 40
 48
 P C

 05. THEORY OF COMPUTATION
 PP
 100
 40
 46
 P C
 16. SOFTWARE ENGINEERING
 PP
 100
 40
 42
 P C

 06. RDBMS & VISUAL PROGRAMMING LAB. TW
 50
 20
 28
 P C
 17. SOFTWARE LABORATORY
 TW
 25
 10
 12
 P C

 07. RDBMS & VISUAL PROGRAMMING LAB. PR
 50
 20
 30
 P C
 18. SOFTWARE LABORATORY
 PR
 50
 20
 20
 P

 08. SIGNAL PROCESSING LABORATORY
 TW
 25
 10
 19
 P C
 19. COMPUTER NETWORK
 TW
 25
 10
 16
 P C

 09. SIGNAL PROCESSING LABORATORY
 TW GRAND TOTAL = 802/1500, RESULT: SECOND CLASS ORDN. 1 MARKS : , 70925410F , T8054232 , PICT , T8054361 T8054361 DONGRE PALLAVI TRYAMAKRAO KUSUM 01. DATABASE MANAGEMENT SYSTEMS PP 100 40 47 P C 02. DATA COMMUNICATION PP 100 40 52 P C 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 70 P C 12. PRINCIPLES OF PROGRAMMING LANG. PP 100 40 62 PC 13. COMPUTER NETWORKS PP 100 40 61 P C 14. FINANCE & MANAGEMENT INFORMA.SYS.PP 100 40 70 P C 04. DIGITAL SIGNAL PROCESSING PP 100 40 49 P C 15. SYSTEMS PROGRAMMING & OPERA.SYS. PP 100 40 P C 16. SOFTWARE ENGINEERING PP 100 40 47 P C
17. SOFTWARE LABORATORY TW 25 10 20 P C
18. SOFTWARE LABORATORY PR 50 20 25 P C
19. COMPUTER NETWORK TW 25 10 22 P C
20. COMPUTER NETWORK OR 50 20 37 P 05. THEORY OF COMPUTATION PP 100 40 51 P C 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 20 35 P C 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 34 P C 08. SIGNAL PROCESSING LABORATORY TW 25 10 19 P C 09. SIGNAL PROCESSING LABORATORY OR 50 20 27 P C 09. SIGNAL PROCESSING LABORATORY OR 50 20 27 P C 20. COMPUTER NETWORK OR 50 20 37 P 10. HARDWARE LABORATORY TW 25 10 18 P C 21. SOFTWARE DEVELOPMENT TOOLS LAB. TW 50 20 36 P C 11. HARDWARE LABORATORY PR 50 20 35 P C 22. SEMINAR AND TECHNICAL COMMUNI. TW 50 20 33 P C GRAND TOTAL = 890+10/1500, RESULT: FIRST CLASS [0.2] ORDN. 1 MARKS :

DATE : 02 MAR. 2012 CE	NTRE : PL	JŃE INŚTI	TUTE OF CON	1PUTEF	ER) EXAMINATION NOV. 2011 R TECHNOLOGY, PUNE.		53 (466)
NOTE: FIRST LINE : SEAT NO., NAME OF TO OTHER LINES: HEAD OF PASSING, MA	HE CANDID	DATE, MO	THER, PERMA	ANENT	REG. NO., PREVIOUS SEAT NO., CO	OLLEGE,	SEAT NO.
T8054362 GADIA VARUN RAJESH		SA	ADHANA		, 70925415G , т8054233 ,	PICT	, т8054362
01. DATABASE MANAGEMENT SYSTEMS 02. DATA COMMUNICATION 03. MICROPROCESSORS & MICROCONTROLLERPE 04. DIGITAL SIGNAL PROCESSING 05. THEORY OF COMPUTATION 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 08. SIGNAL PROCESSING LABORATORY 09. SIGNAL PROCESSING LABORATORY 10. HARDWARE LABORATORY 11. HARDWARE LABORATORY 12. GRAND TOTAL = 874/1500, RESULT: HIGHER SORDN. 1 MARKS:	100 100 100 100 50 50 25 50 25	40 44 40 52 40 51 40 62 20 28 20 30 10 15 20 35 10 11 20 34	PC	13. 14. 15. 16. 17. 18. 19. 20. 21.	PRINCIPLES OF PROGRAMMING LANG. COMPUTER NETWORKS FINANCE & MANAGEMENT INFORMA.SYS SYSTEMS PROGRAMMING & OPERA.SYS. SOFTWARE ENGINEERING SOFTWARE LABORATORY COMPUTER NETWORK COMPUTER NETWORK SOFTWARE DEVELOPMENT TOOLS LAB. SEMINAR AND TECHNICAL COMMUNI.	PP 100 PP 100 PP 100 PP 100 TW 25 PR 50 TW 25 OR 50	40 53 P C 40 72 P C 40 65 P C 40 43 P C 40 58 P C 10 17 P C 20 37 P 10 19 P C 20 37 P 20 28 P C 20 42 P C
oten I wante I							
T8054363 GAIKWAD SHIVRAJ VITTHALRAO		SI	INDHU		, 70925417С , т8054235 ,	PICT	, т8054363
01. DATABASE MANAGEMENT SYSTEMS 02. DATA COMMUNICATION 03. MICROPROCESSORS & MICROCONTROLLERPE 04. DIGITAL SIGNAL PROCESSING 05. THEORY OF COMPUTATION 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 08. SIGNAL PROCESSING LABORATORY 09. SIGNAL PROCESSING LABORATORY 10. HARDWARE LABORATORY 11. HARDWARE LABORATORY PR	100 100 100 100 50 50 25 50 25	40 47 40 45 40 54 40 46 20 20 20 21 10 16 20 40	P C P C P C P C P C	13. 14. 15. 16. 17. 18. 19. 20. 21.	PRINCIPLES OF PROGRAMMING LANG. COMPUTER NETWORKS FINANCE & MANAGEMENT INFORMA.SYS SYSTEMS PROGRAMMING & OPERA.SYS. SOFTWARE ENGINEERING SOFTWARE LABORATORY SOFTWARE LABORATORY COMPUTER NETWORK COMPUTER NETWORK SOFTWARE DEVELOPMENT TOOLS LAB. SEMINAR AND TECHNICAL COMMUNI.	PP 100 PP 100 PP 100 PP 100 TW 25 PR 50 TW 25 OR 50	40
GRAND TOTAL = 685/1500, RESULT: FAILS A. ORDN. 1 MARKS :	T.K.T.						
T8054364 GALANDE SAURABH SANJAY		 Sl			, 70925420C , T8054236 ,	PICT	
01. DATABASE MANAGEMENT SYSTEMS PP 02. DATA COMMUNICATION PP 03. MICROPROCESSORS & MICROCONTROLLERPP 04. DIGITAL SIGNAL PROCESSING PP 05. THEORY OF COMPUTATION PP 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 08. SIGNAL PROCESSING LABORATORY TW 09. SIGNAL PROCESSING LABORATORY OR 10. HARDWARE LABORATORY TW 11. HARDWARE LABORATORY PR GRAND TOTAL = 944/1500, RESULT: FIRST CLORDN. 1 MARKS:	100 100 100 100 50 50 25 50 25	40 48 40 74	P C P C P C P C P C	13. 14. 15. 16. 17. 18. 19. 20. 21.	PRINCIPLES OF PROGRAMMING LANG. COMPUTER NETWORKS FINANCE & MANAGEMENT INFORMA.SYS SYSTEMS PROGRAMMING & OPERA.SYS. SOFTWARE ENGINEERING SOFTWARE LABORATORY SOFTWARE LABORATORY COMPUTER NETWORK COMPUTER NETWORK SOFTWARE DEVELOPMENT TOOLS LAB. SEMINAR AND TECHNICAL COMMUNI.	PP 100 PP 100 .PP 100	40 58 P C 40 57 P C 40 57 P C 40 60 P C 40 40 P C 10 19 P C 20 35 P 10 20 P C 20 25 P C 20 37 P C 20 42 P C

NOTE: FIRST LINE : SEAT NO., NAME OF OTHER LINES: HEAD OF PASSING,	MAX.	MARKS	, MI	MO ⁻ N. P	THER, PE ASS MARE	ERMANENT (S, MARI	KS OBTAINED,	EVIOUS S P/F:PASS	/FAIL, C:	PREVI	OUS CAF	SEAT I	NO. VER	
T0054365														
T8054365 GHULE CHAITALI CHANDRASHE	KHAK			KA.	JESHREE		, 70925431	.J ,	18054243	, Р.	LCI	,	т8054	36
02. DATA COMMUNICATION 03. MICROPROCESSORS & MICROCONTROLLER 04. DIGITAL SIGNAL PROCESSING 05. THEORY OF COMPUTATION 06. RDBMS & VISUAL PROGRAMMING LAB. 07. RDBMS & VISUAL PROGRAMMING LAB. 08. SIGNAL PROCESSING LABORATORY 09. SIGNAL PROCESSING LABORATORY	PP PP PP TW PR TW OR TW	100 100 100 100 100 50 50 25 50 25	40 40 40 40 20 20 10 20 10 20	49 65 40 64 27 30 16 28 15	P C C C C C C C P C C C C P C C C P C C C P C C C P P C C C P P C P	13. 14. 15. 16. 17. 18. 19. 20. 21.	PRINCIPLES OF COMPUTER NETW FINANCE & MAN SYSTEMS PROGF SOFTWARE ENGISOFTWARE LABOR COMPUTER NETW COMPUTER NETW SOFTWARE DEVESEMINAR AND TOTAL PROCESSEMINAR PROCESSEMINAR AND TOTAL PROCESSEMINAR PROCESSEMIN	ORKS IAGEMENT AMMING & ENEERING PRATORY ORK ORK ORK ELOPMENT	INFORMA.SYS	PP (S.PP S.PP PP TW PR TW OR TW	100 100 100 100 25 50 25 50 50	40 40 40 40 10 20 10 20 20 20	59 56 60 65 40 18 30 16 38 33	P P P P P
AND TOTAL = 869/1500, RESULT: HIGHER DN. 1 MARKS :	R SECO	ND CL	ASS											
					 NCHAN		, 70925432		 т8054244		 TCT		 т8054	
	DD.	100	40			12								
02. DATA COMMUNICATION 03. MICROPROCESSORS & MICROCONTROLLER 04. DIGITAL SIGNAL PROCESSING 05. THEORY OF COMPUTATION 06. RDBMS & VISUAL PROGRAMMING LAB. 07. RDBMS & VISUAL PROGRAMMING LAB. 08. SIGNAL PROCESSING LABORATORY 09. SIGNAL PROCESSING LABORATORY 10. HARDWARE LABORATORY	PP PP PP PP TW	100 100 100 100 100 50 50 25 50 25	40 40 40 40 20 20 10 20 10 20	41 54 59 50 39 28 18 38 20	P C C C C C C C C C C C C C C C C C C C	13. 14. 15. 16. 17. 18. 19. 20.	PRINCIPLES OF COMPUTER NETW FINANCE & MAN SYSTEMS PROGESOFTWARE ENGISOFTWARE LABORD COMPUTER NETW COMPUTER NETW SOFTWARE DEVESEMINAR AND TOTAL NETW SEMINAR SEMINAR AND TOTAL NETW SEMINAR SEMINAR AND TOTAL NETW SEMINAR SEMINA	ORKS IAGEMENT LAMMING & ENEERING PRATORY ORATORY ORK ORK	INFORMA.SYS	PP (S.PP 5. PP PP TW PR TW OR	100 100 100 100 25 50 25 50 50	40 40 40 40 10 20 10 20 20 20	47 46 40 55 AA 18 28 13 36 34 36	P P P P P
AND TOTAL = 782/1500, RESULT: FAILS DN. 1 MARKS :	A.T.K	.т.												
				 KA	 LPANA		, 70925438	 F,	 т8054248	, P	 ICT		 т8054	
02. DATA COMMUNICATION 03. MICROPROCESSORS & MICROCONTROLLER 04. DIGITAL SIGNAL PROCESSING 05. THEORY OF COMPUTATION 06. RDBMS & VISUAL PROGRAMMING LAB. 07. RDBMS & VISUAL PROGRAMMING LAB. 08. SIGNAL PROCESSING LABORATORY 09. SIGNAL PROCESSING LABORATORY	PP RPP PP	100 100 100 100 100 50 50 25 50 25	40 40 40 40 20 20 10 20 10 20	40 40 44 56 30 28 15 21 16	P C C C C C C P C C C C P C C C P C C C P C C C P C C P C C P C C P P C C P P C C P P C P P C P	13. 14. 15. 16. 17. 18. 19. 20.	PRINCIPLES OF COMPUTER NETW FINANCE & MAN SYSTEMS PROGES OFTWARE ENGISOFTWARE LABORD COMPUTER NETW COMPUTER NETW SOFTWARE DEVESEMINAR AND TOTAL COMPUTER NETW SOFTWARE DEVESEMENT SOFTWARE DEVES SOFTWARE DEVESEMENT SOFTWARE DEVE	ORKS IAGEMENT LAMMING & ENEERING PRATORY ORATORY ORK ORK	INFORMA.SYS	PP (S.PP 5. PP PP TW PR TW OR	100 100 100 100 100 25 50 25 50 50	40 40 40 40 40 10 20 10 20 20 20	40 47 40 40 14 28 13 20\$	P P P P P P P

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DATE: 02 MAR. 2012 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 55 (468) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER T8054368 JAHAGIRDAR SOUMYA SUDHINDRA SUKANYA , 70701467M , T8054252 , PICT , T8054368 01. DATABASE MANAGEMENT SYSTEMS PP 100 40 46 P 12. PRINCIPLES OF PROGRAMMING LANG. PP 100 40 40 P C 02. DATA COMMUNICATION PP 100 40 42 P C 13. COMPUTER NETWORKS PP 100 40 58 P C 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 54 P C 14. FINANCE & MANAGEMENT INFORMA.SYS.PP 100 40 59 P C 04. DIGITAL SIGNAL PROCESSING PP 100 40 47 P C 15. SYSTEMS PROGRAMMING & OPERA.SYS. PP 100 40 43 P C 05. THEORY OF COMPUTATION PP 100 40 47 P C 16. SOFTWARE ENGINEERING PP 100 40 42 P C 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 20 35 P C 17. SOFTWARE LABORATORY TW 25 10 16 P C 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 25 P C 18. SOFTWARE LABORATORY PR 50 20 21 P 08. SIGNAL PROCESSING LABORATORY TW 25 10 15 P C 19. COMPUTER NETWORK TW 25 10 16 P C 09. SIGNAL PROCESSING LABORATORY OR 50 20 24 P C 20. COMPUTER NETWORK OR 50 20 28 P 10. HARDWARE LABORATORY PR 50 20 38 P C 22. SEMINAR AND TECHNICAL COMMUNI. TW 50 20 31 P C GRAND TOTAL = 765/1500, RESULT: SECOND CLASS ORDN. 1 MARKS: SULOCHANA , 70925450E , T8054254 , PICT , T8054369 T8054369 JAJU PRATIKSHA VIJAYKUMAR 01. DATABASE MANAGEMENT SYSTEMS PP 100 40 42 P C 12. PRINCIPLES OF PROGRAMMING LANG. PP 100 40 51 P C 02. DATA COMMUNICATION PP 100 40 40 P C 13. COMPUTER NETWORKS PP 100 40 57 P C

 02. DATA COMMUNICATION
 PP
 100
 40
 40
 P C
 13. COMPUTER NETWORKS
 PP
 100
 40
 57 P C

 03. MICROPROCESSORS & MICROCONTROLLERPP
 100
 40
 54 P C
 14. FINANCE & MANAGEMENT INFORMA.SYS.PP
 100
 40
 53 P C

 04. DIGITAL SIGNAL PROCESSING
 PP
 100
 40
 46 P C
 15. SYSTEMS PROGRAMMING & OPERA.SYS.PP
 100
 40
 55 P C

 05. THEORY OF COMPUTATION
 PP
 100
 40
 53 P C
 16. SOFTWARE ENGINEERING
 PP
 100
 40
 40 P C

 06. RDBMS & VISUAL PROGRAMMING LAB. TW
 50
 20
 32 P C
 17. SOFTWARE LABORATORY
 TW
 25 10
 20 P C

 07. RDBMS & VISUAL PROGRAMMING LAB. PR
 50
 20
 30 P C
 18. SOFTWARE LABORATORY
 PR
 50
 20
 36 P C

 08. SIGNAL PROCESSING LABORATORY
 TW
 25
 10
 16 P C
 19. COMPUTER NETWORK
 TW
 25
 10
 13 P C

 09. SIGNAL PROCESSING LABORATORY
 OR
 50
 20
 30 P C
 20. COMPUTER NETWORK
 OR
 50
 20
 35 P

 10. HARDWARE GRAND TOTAL = 815+10/1500, RESULT: HIGHER SECOND CLASS [0.2] ORDN. 1 MARKS : , 70925458L , T8054257 , PICT , T8054370 T8054370 KADAM SHRINIWAS MUKUNDRAO URMILA 01. DATABASE MANAGEMENT SYSTEMS PP 100 40 47 P C 02. DATA COMMUNICATION PP 100 40 40 P C 12. PRINCIPLES OF PROGRAMMING LANG. PP 100 40 51 P C 13. COMPUTER NETWORKS PP 100 40 49 P C 14. FINANCE & MANAGEMENT INFORMA.SYS.PP 100 40 48 P C 15. SYSTEMS PROGRAMMING & OPERA.SYS. PP 100 40 46 P C 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 43 P C 04. DIGITAL SIGNAL PROCESSING PP 100 40 40 P C 05. THEORY OF COMPUTATION PP 100 40 49 P C 16. SOFTWARE ENGINEERING PP 100 40 46 P C 16. SOFTWARE LABORATORY TW 25 10 12 P C 18. SOFTWARE LABORATORY PR 50 20 25 P C 19. COMPUTER NETWORK OR 50 20 32 P C 10. HARDWARE LABORATORY TW 25 10 12 P C 11. HARDWARE LABORATORY PR 50 20 23 P C 11. HARDWARE LABORATORY PR 50 20 23 P C 11. HARDWARE LABORATORY PR 50 20 23 P C 22. SEMINAR AND TECHNICAL COMMUNI. TW 50 20 25 P C GRAND TOTAL = 716/1500, RESULT: PASS CLASS ORDN. 1 MARKS :

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DATE: 02 MAR. 2012 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 56 (469) NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER T8054371 KATDARE SANCHIT VINAYAK ALKA , 70925470K , T8054263 , PICT , T8054371 01. DATABASE MANAGEMENT SYSTEMS PP 100 40 48 P C 12. PRINCIPLES OF PROGRAMMING LANG. PP 100 40 52 P C 02. DATA COMMUNICATION PP 100 40 40 P C 13. COMPUTER NETWORKS PP 100 40 58 P C 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 40 P C 14. FINANCE & MANAGEMENT INFORMA.SYS.PP 100 40 43 P C 04. DIGITAL SIGNAL PROCESSING PP 100 40 45 P C 15. SYSTEMS PROGRAMMING & OPERA.SYS. PP 100 40 45 P C 05. THEORY OF COMPUTATION PP 100 40 53 P C 16. SOFTWARE ENGINEERING PP 100 40 40 P C 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 20 36 P C 17. SOFTWARE LABORATORY TW 25 10 18 P C 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 40 P C 18. SOFTWARE LABORATORY PR 50 20 36 P C 08. SIGNAL PROCESSING LABORATORY TW 25 10 18 P C 19. COMPUTER NETWORK TW 25 10 16 P C 09. SIGNAL PROCESSING LABORATORY OR 50 20 23 P C 20. COMPUTER NETWORK OR 50 20 36 P C 10. HARDWARE LABORATORY TW 25 10 20 P C 21. SOFTWARE DEVELOPMENT TOOLS LAB. TW 50 20 36 P C 11. HARDWARE LABORATORY PR 50 20 36 P C 22. SEMINAR AND TECHNICAL COMMUNI. TW 50 20 36 P C GRAND TOTAL = 806/1500, RESULT: SECOND CLASS ORDN. 1 MARKS: SUREKHA , 70925471H , T8054264 , PICT , T8054372 T8054372 KAVITKE INDRAJEET ARVIND 01. DATABASE MANAGEMENT SYSTEMS PP 100 40 60 P C 12. PRINCIPLES OF PROGRAMMING LANG. PP 100 40 59 P C 02. DATA COMMUNICATION PP 100 40 47 P 13. COMPUTER NETWORKS PP 100 40 55 P C
 U2. DATA COMMUNICATION
 PP
 100
 40
 47 P
 13. COMPUTER NETWORKS
 PP
 100
 40
 55 P C

 03. MICROPROCESSORS & MICROCONTROLLERPP
 100
 40
 55 P C
 14. FINANCE & MANAGEMENT INFORMA.SYS.PP
 100
 40
 51 P C

 04. DIGITAL SIGNAL PROCESSING
 PP
 100
 40
 40 P C
 15. SYSTEMS PROGRAMMING & OPERA.SYS. PP
 100
 40
 52 P C

 05. THEORY OF COMPUTATION
 PP
 100
 40 P C
 16. SOFTWARE ENGINEERING
 PP
 100
 40
 40 P C

 06. RDBMS & VISUAL PROGRAMMING LAB. TW
 50
 20
 37 P C
 17. SOFTWARE LABORATORY
 TW
 25
 10
 19 P C

 07. RDBMS & VISUAL PROGRAMMING LAB. PR
 50
 20
 36 P C
 18. SOFTWARE LABORATORY
 PR
 50
 20
 30 P C

 08. SIGNAL PROCESSING LABORATORY
 TW
 25
 10
 18 P C
 19. COMPUTER NETWORK
 TW
 25
 10
 18 P C

 09. SIGNAL PROCESSING LABORATORY
 TW
 25
 10
 18 P C
 20. COMPUTER NETWORK
 GRAND TOTAL = 851/1500, RESULT: HIGHER SECOND CLASS ORDN. 1 MARKS : , 70925481E , T8054270 , PICT , T8054373 T8054373 KHUNE VIKRANT SUNIL CHARU 01. DATABASE MANAGEMENT SYSTEMS PP 100 40 52 P C 02. DATA COMMUNICATION PP 100 40 P C 12. PRINCIPLES OF PROGRAMMING LANG. PP 100 40 41 P C 13. COMPUTER NETWORKS PP 100 40 44 P C 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 47 P C 14. FINANCE & MANAGEMENT INFORMA.SYS.PP 100 40 52 P C 04. DIGITAL SIGNAL PROCESSING PP 100 40 40 P C 15. SYSTEMS PROGRAMMING & OPERA.SYS. PP 100 40 59 P C 16. SOFTWARE ENGINEERING PP 100 40 40 P C
17. SOFTWARE LABORATORY TW 25 10 17 P C
18. SOFTWARE LABORATORY PR 50 20 34 P C
19. COMPUTER NETWORK TW 25 10 15 P C
20. COMPUTER NETWORK OR 50 20 34 P PP 100 40 43 P C 05. THEORY OF COMPUTATION 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 20 24 P C 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 31 P C 08. SIGNAL PROCESSING LABORATORY TW
09. SIGNAL PROCESSING LABORATORY OR 25 10 12 P C 50 20 27 P C 20. COMPUTER NETWORK OR 50 20 34 P 25 10 12 P C 21. SOFTWARE DEVELOPMENT TOOLS LAB. TW 50 20 33 P C 50 20 22 P C 22. SEMINAR AND TECHNICAL COMMUNI. TW 50 20 25 P C 10. HARDWARE LABORATORY TW TW 25 10 12 P C

GRAND TOTAL = 744+06/1500, RESULT: SECOND CLASS [0.2] ORDN. 1 MARKS :

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DATE : 02 MAR. 2012 CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 57 (470)

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8054374 KSHIRSAGAR RUTUL ULHASRAO			SANGITA	, 70925489L , т8054276 ,	PI	CT	, 7	г8054	374
01. DATABASE MANAGEMENT SYSTEMS PP	100	40	53 P C	12. PRINCIPLES OF PROGRAMMING LANG.	PP	100	40	61	РС
02. DATA COMMUNICATION PP	100	40	60 P C	13. COMPUTER NETWORKS	PP	100	40	42	РС
03. MICROPROCESSORS & MICROCONTROLLERPP	100	40	63 P C	14. FINANCE & MANAGEMENT INFORMA.SYS	.PP	100	40	57	РС
04. DIGITAL SIGNAL PROCESSING PP	100	40	53 P C	15. SYSTEMS PROGRAMMING & OPERA.SYS.	PP	100	40	62	РС
O5. THEORY OF COMPUTATION PP	100	40	66 P C	16. SOFTWARE ENGINEERING	PP	100	40	51	РС
06. RDBMS & VISUAL PROGRAMMING LAB. TW	50	20	20 P C	17. SOFTWARE LABORATORY	TW	25	10	17	РС
07. RDBMS & VISUAL PROGRAMMING LAB. PR	50	20	20 P C	18. SOFTWARE LABORATORY	PR	50	20	42	РС
08. SIGNAL PROCESSING LABORATORY TW	25	10	10 P C	19. COMPUTER NETWORK	TW	25	10	12	РС
09. SIGNAL PROCESSING LABORATORY OR	50	20	25 P C	20. COMPUTER NETWORK	OR	50	20	33	Р
10. HARDWARE LABORATORY TW	25	10	10 P C	21. SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	27	РС
11. HARDWARE LABORATORY PR	50	20	34 P C	22. SEMINAR AND TECHNICAL COMMUNI.	TW	50	20	28	РС

GRAND TOTAL = 846/1500, RESULT: HIGHER SECOND CLASS ORDN. 1 MARKS:

T8054375 KUMAR HARISH BHIM				CH	ANDRA	AVATIDEVI	, 70925491в	, т8054277 ,	P:	ICT	, 7	г8054	375
01. DATABASE MANAGEMENT SYSTEMS	PP	100	40	45	РС	12.	PRINCIPLES OF PRO	OGRAMMING LANG.	PP	100	40	50	РС
02. DATA COMMUNICATION	PP	100	40	63	РС	13.	COMPUTER NETWORKS	5	PP	100	40	49	РС
03. MICROPROCESSORS & MICROCONTROLLE	RPP	100	40	53	РС	14.	FINANCE & MANAGE	MENT INFORMA.SYS	.PP	100	40	47	РС
04. DIGITAL SIGNAL PROCESSING	PP	100	40	55	РС	15.	SYSTEMS PROGRAMM	ING & OPERA.SYS.	PP	100	40	55	РС
05. THEORY OF COMPUTATION	PP	100	40	62	РС	16.	SOFTWARE ENGINEER	RING	PP	100	40	42	РС
06. RDBMS & VISUAL PROGRAMMING LAB.	TW	50	20	35	РС	17.	SOFTWARE LABORATO	DRY	TW	25	10	18	РС
07. RDBMS & VISUAL PROGRAMMING LAB.	PR	50	20	30	РС	18.	SOFTWARE LABORATO	DRY	PR	50	20	40	РС
08. SIGNAL PROCESSING LABORATORY	TW	25	10	18	РС	19.	COMPUTER NETWORK		TW	25	10	15	РС
09. SIGNAL PROCESSING LABORATORY	OR	50	20	34	РС	20.	COMPUTER NETWORK		OR	50	20	36	Р
10. HARDWARE LABORATORY	TW	25	10	18	РС	21.	SOFTWARE DEVELOP	MENT TOOLS LAB.	TW	50	20	36	РС
11. HARDWARE LABORATORY	PR	50	20	36	РС	22.	SEMINAR AND TECHI	NICAL COMMUNI.	TW	50	20	39	РС

GRAND TOTAL = 876/1500, RESULT: HIGHER SECOND CLASS ORDN. 1 MARKS:

T8054376 MAHAJAN VAIBHAV ANUP			ARTI	, 71072133В , т8054282 ,	PICT	, -	т8054	376
01. DATABASE MANAGEMENT SYSTEMS PP	100	40	40 P C	12. PRINCIPLES OF PROGRAMMING LANG. P	P 100	40	60	РС
02. DATA COMMUNICATION PP	100	40	48 P C	13. COMPUTER NETWORKS P	P 100	40	40	РС
03. MICROPROCESSORS & MICROCONTROLLERPP	100	40	51 PC	14. FINANCE & MANAGEMENT INFORMA.SYS.P	P 100	40	40	РС
04. DIGITAL SIGNAL PROCESSING PP	100	40	40 P C	15. SYSTEMS PROGRAMMING & OPERA.SYS. P	P 100	40	47	РС
05. THEORY OF COMPUTATION PP	100	40	47 P C	16. SOFTWARE ENGINEERING P	P 100	40	40	РС
06. RDBMS & VISUAL PROGRAMMING LAB. TW	50	20	36 P C	17. SOFTWARE LABORATORY T	W 25	10	18	РС
07. RDBMS & VISUAL PROGRAMMING LAB. PR	50	20	22 P	18. SOFTWARE LABORATORY P	r 50	20	26	Р
08. SIGNAL PROCESSING LABORATORY TW	25	10	14 P C	19. COMPUTER NETWORK T	W 25	10	21	РС
09. SIGNAL PROCESSING LABORATORY OR	50	20	26 P C	20. COMPUTER NETWORK O	R 50	20	30	РС
10. HARDWARE LABORATORY TW	25	10	15 P C	21. SOFTWARE DEVELOPMENT TOOLS LAB. T	w 50	20	38	РС
11. HARDWARE LABORATORY PR	50	20	32 P C	22. SEMINAR AND TECHNICAL COMMUNI. T	w 50	20	39	РС

GRAND TOTAL = 770/1500, RESULT: SECOND CLASS ORDN. 1 MARKS:

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UNIVERSITY DATE: 02 MAR. 2012 CEN	OF PUNE ITRE : PU	T.E.(20	008 PAT. ITUTE OF	.)(COMPUT F COMPUTE	ER) EXAMINATION NOV R TECHNOLOGY, PUNE.	. 2011	PAGE NO.	58 (471)
NOTE: FIRST LINE : SEAT NO., NAME OF THE OTHER LINES: HEAD OF PASSING, MAX	HE CANDID (. MARKS,	ATE, MO	OTHER, F PASS MAF	PERMANENT RKS, MAR	REG. NO., PREVIOU KS OBTAINED, P/F:P	S SEAT NO., CO PASS/FAIL, C:PI	OLLEGE, S REVIOUS CAF	SEAT NO. RRY OVER	
T8054377 MUNDHADA ABHISHEK HARISH								 , т80	
	100		EMLATA	12				•	
01. DATABASE MANAGEMENT SYSTEMS PP 02. DATA COMMUNICATION PP 03. MICROPROCESSORS & MICROCONTROLLERPP 04. DIGITAL SIGNAL PROCESSING PP 05. THEORY OF COMPUTATION PP 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 08. SIGNAL PROCESSING LABORATORY TW 09. SIGNAL PROCESSING LABORATORY OR 10. HARDWARE LABORATORY TW	100 100 100 100 50 50 25 50 25	40 67 40 64 40 40 40 63 20 24 20 31 10 13 20 23 10 14		13. 14. 15. 16. 17. 18. 19. 20.	FINANCE & MANAGEME SYSTEMS PROGRAMMIN SOFTWARE ENGINEERI SOFTWARE LABORATOR COMPUTER NETWORK COMPUTER NETWORK SOFTWARE DEVELOPME	NT INFORMA.SYS G & OPERA.SYS. NG Y Y	PP 100 PP 100 PP 100 PP 100 TW 25 PR 50 TW 25 OR 50 TW 50	40 4 40 4 40 6 40 4 10 1 20 2 10 1 20 3 20 2	1 P C C P C C C P C C P C C P C C P C C P C C P C C P C C P C C P C C P C C P C C P C C P C C P C P C C P C C P C C P C C P C C P C C P C C P C C P C C P C C P C C P C C P C P C C P
11. HARDWARE LABORATORY PR GRAND TOTAL = 819+06/1500, RESULT: HIGHER ORDN. 1 MARKS:		20 20 CLASS		22.	SEMINAR AND TECHNI	CAL COMMUNI.	TW 50	20 3	0 РС
T8054378 PATIL HARSHAL PRAVINSING		SA	ANDHYA		, 70925546C	, т8054299 ,	PICT	, т80	54378
01. DATABASE MANAGEMENT SYSTEMS PP 02. DATA COMMUNICATION PP 03. MICROPROCESSORS & MICROCONTROLLERPP 04. DIGITAL SIGNAL PROCESSING PP 05. THEORY OF COMPUTATION PP 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 08. SIGNAL PROCESSING LABORATORY TW 09. SIGNAL PROCESSING LABORATORY OR 10. HARDWARE LABORATORY TW 11. HARDWARE LABORATORY PR	100 100 100 100 50 50 25 50 25	40 43 40 43 40 45 20 21 20 20 10 13 20 26 10 13	P C P C P C P C P C P C P C P C	13. 14. 15. 16. 17. 18. 19. 20.	PRINCIPLES OF PROG COMPUTER NETWORKS FINANCE & MANAGEME SYSTEMS PROGRAMMIN SOFTWARE ENGINEERI SOFTWARE LABORATOR SOFTWARE LABORATOR COMPUTER NETWORK COMPUTER NETWORK SOFTWARE DEVELOPME SEMINAR AND TECHNI	NT INFORMA.SYS G & OPERA.SYS. NG Y Y	PP 100 PP 100 PP 100 PP 100 TW 25 PR 50 TW 25 OR 50	40 4 40 4 40 4 40 4 10 1 20 2 10 1 20 2 20 2	5 P 1 P 0 P 7 P C 0 P 0 P 0 P 0 P 0 P 2 P 5 P 7 P C
GRAND TOTAL = 675/1500, RESULT: PASS CLASORDN. 1 MARKS:	SS								
T8054379 PATIL PRIYANKA ARUN		Al	RUNA		, 70925550м	, т8054302 ,	PICT	, т80	54379
01. DATABASE MANAGEMENT SYSTEMS PP 02. DATA COMMUNICATION PP 03. MICROPROCESSORS & MICROCONTROLLERPP 04. DIGITAL SIGNAL PROCESSING PP 05. THEORY OF COMPUTATION PP 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 08. SIGNAL PROCESSING LABORATORY TW 09. SIGNAL PROCESSING LABORATORY OR 10. HARDWARE LABORATORY TW 11. HARDWARE LABORATORY PR GRAND TOTAL = 809/1500, RESULT: SECOND CL	100 100 100 100 50 50 25 50 25	40 45 40 58 40 40 40 53 20 36 20 20 10 22 20 20 10 19	P C P C P C	13. 14. 15. 16. 17. 18. 19. 20.	PRINCIPLES OF PROG COMPUTER NETWORKS FINANCE & MANAGEME SYSTEMS PROGRAMMIN SOFTWARE ENGINEERI SOFTWARE LABORATOR SOFTWARE LABORATOR COMPUTER NETWORK COMPUTER NETWORK SOFTWARE DEVELOPME SEMINAR AND TECHNI	NT INFORMA.SYS G & OPERA.SYS. NG Y Y	PP 100 .PP 100 PP 100	40 4 40 4 40 5 40 4 10 1 20 2 10 2 20 4 20 3	1 PC 9 PC 0 PC 8 PC 2 PC 2 PC 3 PC
ORDN. 1 MARKS :									

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NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MÍN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER T8054380 PATIL SWAPNIL PRABHAKAR URMILA , 70601435K , T8054303 , PICT , T8054380 PP 100 40 12. PRINCIPLES OF PROGRAMMING LANG. PP 100 40 40 P C 01. DATABASE MANAGEMENT SYSTEMS PP 100 40 40 P 13. COMPUTER NETWORKS PP 100 40 20 F 12. COMPUTER NETWORKS PP 100 40 40 46 P 15. SYSTEMS PROGRAMMING & OPERA.SYS.PP 100 40 42 P C 16. SOFTWARE ENGINEERING PP 100 40 40 40 P C 17. SOFTWARE LABORATORY TW 25 10 10 P C 18. PR 50 20 25 P C 18. SOFTWARE LABORATORY PR 50 20 25 P C 18. SOFTWARE LABORATORY PR 50 20 05 F 17. SOFTWARE LABORATORY PR 50 20 05 F 17. SOFTWARE LABORATORY PR 50 20 05 F 17. SOFTWARE LABORATORY PR 50 20 20 P C 18. SOFTWARE LABORATORY PR 50 20 20 22 P C 18. SOFTWARE LABORATORY PR 50 20 22 P C 18. SOFTWARE LABORATORY PR 50 20 22 P C 19. COMPUTER NETWORK PR 50 20 22 P C TW 25 10 14 P C 21. SOFTWARE DEVELOPMENT TOOLS LAB. TW 50 20 32 P C PR 50 20 24 P C 22. SEMINAR AND TECHNICAL COMMUNI. TW 50 20 37 P C PP 100 40 20 F 02. DATA COMMUNICATION 40 40 P 13. COMPUTER NETWORKS PP 100 03. MICROPROCESSORS & MICROCONTROLLERPP 04. DIGITAL SIGNAL PROCESSING PP 05. THEORY OF COMPUTATION 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 08. SIGNAL PROCESSING LABORATORY TW 09. SIGNAL PROCESSING LABORATORY 10. HARDWARE LABORATORY 11. HARDWARE LABORATORY GRAND TOTAL = 528/1500, RESULT: FAILS ORDN. 1 MARKS : , 70601438D , T8054304 , PICT , T8054381 T8054381 PATIL VINEET JAYANT PRAJAKTA PP 100 40 AA F 12. PRINCIPLES OF PROGRAMMING LANG. PP 100 01. DATABASE MANAGEMENT SYSTEMS 02. DATA COMMUNICATION PP 100 40 AA F 13. COMPUTER NETWORKS PP 100 40 AA F 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 AA F 14. FINANCE & MANAGEMENT INFORMA.SYS.PP 100 40 AA F 04. DIGITAL SIGNAL PROCESSING PP 100 40 27 F 15. SYSTEMS PROGRAMMING & OPERA.SYS. PP 100 40 AA F 16. SOFTWARE ENGINEERING PP
17. SOFTWARE LABORATORY TW
18. SOFTWARE LABORATORY PR
19. COMPUTER NETWORK TW
20. COMPUTER NETWORK OR 40 AA F 05. THEORY OF COMPUTATION 100 40 PP 100 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 20 20 P C 25 10 10 P C 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 AA F 50 20 AA F 19. COMPUTER NETWORK TW
20. COMPUTER NETWORK OR
21. SOFTWARE DEVELOPMENT TOOLS LAB. TW 08. SIGNAL PROCESSING LABORATORY TW 25 10 11 PC 25 10 12 PC 09. SIGNAL PROCESSING LABORATORY 50 20 22 P 20 AA F OR 50 10. HARDWARE LABORATORY 25 10 10 P C 50 20 21 P C TW PR 50 20 27 P.C 22. SEMINAR AND TECHNICAL COMMUNI. TW 50 20 21 P C 11. HARDWARE LABORATORY GRAND TOTAL = 266/1500, RESULT: FAILS ORDN. 1 MARKS :

T8054383 PRANAV	THAKUR				SU	NITA		, 708015	67M	, т8054309 ,	ΡI	CT.	, 7	8054	383
01. DATABASE MANAGE	MENT SYSTEMS	PP	100	40	45	РС	12.	PRINCIPLES	OF PROGRA	AMMING LANG.	PP	100	40	AA	F
02. DATA COMMUNICAT	ION	PP	100	40	AA	F	13.	COMPUTER NE	ETWORKS		PP	100	40	AA	F
03. MICROPROCESSORS	& MICROCONTROLLE	ERPP	100	40	AA	F	14.	FINANCE & M	MANAGEMEN [®]	T INFORMA.SYS	.PP	100	40	AA	F
04. DIGITAL SIGNAL	PROCESSING	PP	100	40	AA	F	15.	SYSTEMS PRO	OGRAMMING	& OPERA.SYS.	PP	100	40	41	РС
05. THEORY OF COMPU	TATION	PP	100	40	43	РС	16.	SOFTWARE EN	NGINEERING	G	PP	100	40	AA	F
06. RDBMS & VISUAL	PROGRAMMING LAB.	TW	50	20	20	РС	17.	SOFTWARE LA	ABORATORY		TW	25	10	13	РС
07. RDBMS & VISUAL	PROGRAMMING LAB.	PR	50	20	AA	F	18.	SOFTWARE LA	ABORATORY		PR	50	20	AA	F
08. SIGNAL PROCESSI	NG LABORATORY	TW	25	10	10	РС	19.	COMPUTER NE	TWORK		TW	25	10	10	РС
09. SIGNAL PROCESSI	NG LABORATORY	OR	50	20	29	РС	20.	COMPUTER NE	TWORK		OR	50	20	25	РС
10. HARDWARE LABOR	ATORY	TW	25	10	10	РС	21.	SOFTWARE DE	EVELOPMEN ³	T TOOLS LAB.	TW	50	20	21	РС
11. HARDWARE LABOR	ATORY	PR	50	20	26	РС	22.	SEMINAR AND) TECHNIC	AL COMMUNI.	TW	50	20	20	РС

RESULT RESERVED FOR BKLG

GRAND TOTAL = 313/1500, RESULT: FAILS

ORDN. 1 MARKS :

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DATE: 02 MAR. 2012 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 60 (473) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER T8054384 ROKADE VISHALSAGAR DEVIDAS VANDANA , 70701610L , T8054314 , PICT , T8054384 GRAND TOTAL = 684/1500, RESULT: FAILS A.T.K.T. [\$ 0.1] RESULT RESERVED FOR BKLG ORDN. 1 MARKS : (20)2. T8054385 SHANTANU SINGHAL SHIKHA , 70504046B , T8054320 , PICT , T8054385 01. DATABASE MANAGEMENT SYSTEMS PP 100 40 56 P C 12. PRINCIPLES OF PROGRAMMING LANG. PP 100 40 74 P C 02. DATA COMMUNICATION PP 100 40 45 P C 13. COMPUTER NETWORKS PP 100 40 45 P C 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 43 P 14. FINANCE & MANAGEMENT INFORMA.SYS.PP 100 40 46 P C

 03. MICROPROCESSORS & MICROCONTROLLERPP
 100
 40
 43
 P
 14. FINANCE & MANAGEMENT INFORMA.SYS.PP
 100
 40
 46
 P C

 04. DIGITAL SIGNAL PROCESSING
 PP
 100
 40
 47
 P C
 15. SYSTEMS PROGRAMMING & OPERA.SYS. PP
 100
 40
 53
 P C

 05. THEORY OF COMPUTATION
 PP
 100
 40
 40
 P C
 16. SOFTWARE ENGINEERING
 PP
 100
 40
 52
 P C

 06. RDBMS & VISUAL PROGRAMMING LAB. TW
 50
 20
 22
 P C
 17. SOFTWARE LABORATORY
 TW
 25
 10
 22
 P C

 07. RDBMS & VISUAL PROGRAMMING LAB. PR
 50
 20
 28
 P C
 18. SOFTWARE LABORATORY
 PR
 50
 20
 38
 P C

 08. SIGNAL PROCESSING LABORATORY
 TW
 25
 10
 11
 P C
 19. COMPUTER NETWORK
 TW
 25
 10
 12
 P C

 09. SIGNAL PROCESSING LABORATORY
 TW
 25
 10
 10
 P C
 20. COMPUTER NETWORK
 OR
 50
 20
 30
 P C

 10. HARDWARE LABORATORY
 PR
 GRAND TOTAL = 793/1500, RESULT: SECOND CLASS RESULT RESERVED FOR BKLG ORDN. 1 MARKS : , 70925597H , T8054321 , PICT , T8054386 T8054386 SHARMA AKANKSHA ANUPAM SHIKHA 01. DATABASE MANAGEMENT SYSTEMS PP 100 40 43 P C 02. DATA COMMUNICATION PP 100 40 60 P C 12. PRINCIPLES OF PROGRAMMING LANG. PP 100 40 54 P C 13. COMPUTER NETWORKS PP 100 40 57 P C 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 45 P C 14. FINANCE & MANAGEMENT INFORMA.SYS.PP 100 40 46 P C 04. DIGITAL SIGNAL PROCESSING PP 100 40 64 P C 15. SYSTEMS PROGRAMMING & OPERA.SYS. PP 100 40 55 P C 16. SOFTWARE ENGINEERING PP 100 40 49 P
17. SOFTWARE LABORATORY TW 25 10 16 P C
18. SOFTWARE LABORATORY PR 50 20 29 P C
19. COMPUTER NETWORK TW 25 10 17 P C
20. COMPUTER NETWORK OR 50 20 30 P C 05. THEORY OF COMPUTATION PP 100 40 40 P C 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 20 34 P C 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 40 P C 08. SIGNAL PROCESSING LABORATORY TW
09. SIGNAL PROCESSING LABORATORY OR 25 10 18 P C 50 20 30 P C 20. COMPUTER NETWORK OR 50 20 30 P C 25 10 17 P C 21. SOFTWARE DEVELOPMENT TOOLS LAB. TW 50 20 37 P C 50 20 32 P C 22. SEMINAR AND TECHNICAL COMMUNI. TW 50 20 24 P C 10. HARDWARE LABORATORY TW TW GRAND TOTAL = 837/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

UNIVERSITY OF PUNE ,T.E. (2008 PAT.) (COMPUTER) EXAMINATION NOV. 2011 DATE : 02 MAR. 2012 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 61 (474) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MÍN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER T8054387 SHUBHAM SINGH JAISHREE , 70601498H , T8054326 , PICT , T8054387 100 01. DATABASE MANAGEMENT SYSTEMS PP 02. DATA COMMUNICATION 100 40 AA F 40 19 F 03. MICROPROCESSORS & MICROCONTROLLERPP 100 04. DIGITAL SIGNAL PROCESSING PP 100 40 16 F AA F 05. THEORY OF COMPUTATION 100 40 06. RDBMS & VISUAL PROGRAMMING LAB. TW 20 20 P C 50 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 AA F 25 08. SIGNAL PROCESSING LABORATORY TW 10 11 PC 09. SIGNAL PROCESSING LABORATORY 50 20 20 P OR 10. HARDWARE LABORATORY 25 10 10 P C TW 11. HARDWARE LABORATORY PR 50 20 20 P FIRST TERM TOTAL = 116/750. ORDN. 1 MARKS: T8054388 SIDDHARTH JAIN RENU , 70925611G , T8054327 , PICT , т8054388 PP 100 40 43 P C 12. PRINCIPLES OF PROGRAMMING LANG. PP 100 40 45 P.C O1. DATABASE MANAGEMENT SYSTEMS 02. DATA COMMUNICATION PP 100 40 48 P.C 13. COMPUTER NETWORKS PP 100 40 40 PC 03. MICROPROCESSORS & MICROCONTROLLERPP 100 40 43 P C 14. FINANCE & MANAGEMENT INFORMA.SYS.PP 100 40 40 P C 04. DIGITAL SIGNAL PROCESSING PP 100 40 40 P C 15. SYSTEMS PROGRAMMING & OPERA.SYS. PP 100 40 46 P C 16. SOFTWARE ENGINEERING PP
17. SOFTWARE LABORATORY TW
18. SOFTWARE LABORATORY PR
19. COMPUTER NETWORK TW
20. COMPUTER NETWORK OR 05. THEORY OF COMPUTATION 100 40 20 F 40 40 P C PP 100 06. RDBMS & VISUAL PROGRAMMING LAB. TW 50 20 25 P C 25 10 14 P C 07. RDBMS & VISUAL PROGRAMMING LAB. PR 50 20 30 P C 50 20 35 P 08. SIGNAL PROCESSING LABORATORY TW 25 10 16 P C 25 10 15 P C 09. SIGNAL PROCESSING LABORATORY 50 20 36 P C 20. COMPUTER NETWORK 30 P C OR OR 50 10. HARDWARE LABORATORY 25 10 16 PC 50 20 29 P C TW 21. SOFTWARE DEVELOPMENT TOOLS LAB. TW 50 20 30 P C 50 20 27 P.C. 11. HARDWARE LABORATORY PR 22. SEMINAR AND TECHNICAL COMMUNI. TW GRAND TOTAL = 708/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS:

T8054389 TAYDE PRAFULLA ANILRAO			VANDANA	, 70925629к , т8054336 , ріст , т805438	39
01. DATABASE MANAGEMENT SYSTEMS PP	100	40	40 P C		P C
02. DATA COMMUNICATION PP	100	40	53 P C	13. COMPUTER NETWORKS PP 100 40 46 P	
03. MICROPROCESSORS & MICROCONTROLLERPP	100	40	52 P C	14. FINANCE & MANAGEMENT INFORMA.SYS.PP 100 40 51 P)
04. DIGITAL SIGNAL PROCESSING PP	100	40	41 P C	15. SYSTEMS PROGRAMMING & OPERA.SYS. PP 100 40 52 P	PC
05. THEORY OF COMPUTATION PP	100	40	40 P C	16. SOFTWARE ENGINEERING PP 100 40 40 P	P C
06. RDBMS & VISUAL PROGRAMMING LAB. TW	50	20	31 P C	17. SOFTWARE LABORATORY TW 25 10 19 P	P C
07. RDBMS & VISUAL PROGRAMMING LAB. PR	50	20	20 P C	18. SOFTWARE LABORATORY PR 50 20 37 P	P C
08. SIGNAL PROCESSING LABORATORY TW	25	10	17 P C	19. COMPUTER NETWORK TW 25 10 11 P	P C
09. SIGNAL PROCESSING LABORATORY OR	50	20	29 P C	20. COMPUTER NETWORK OR 50 20 28 P	P C
10. HARDWARE LABORATORY TW	25	10	19 P C	21. SOFTWARE DEVELOPMENT TOOLS LAB. TW 50 20 39 P	P C
11. HARDWARE LABORATORY PR	50	20	21 P C	22. SEMINAR AND TECHNICAL COMMUNI. TW 50 20 38 P	C

GRAND TOTAL = 780/1500, RESULT: SECOND CLASS ORDN. 1 MARKS :

DATE : 02 MAR. 2012 CEN	TRE : PUŃ	E INSTITUTE OF CO	COMPUTER) EXAMINATION NOV. 2011 OMPUTER TECHNOLOGY, PUNE.		62 (475)
NOTE: FIRST LINE : SEAT NO., NAME OF THOU OTHER LINES: HEAD OF PASSING. MAX	E CANDIDA	TE, MOTHER, PERM		COLLEGE,	SEAT NO.
T8054390 VAIDYA MANDAR RAGHUNATH		СННАҮА	, 71072141С , т8054342	, PICT	, т8054390
01. DATABASE MANAGEMENT SYSTEMS PP 02. DATA COMMUNICATION PP 03. MICROPROCESSORS & MICROCONTROLLERPP 04. DIGITAL SIGNAL PROCESSING PP 05. THEORY OF COMPUTATION PP 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 08. SIGNAL PROCESSING LABORATORY TW 09. SIGNAL PROCESSING LABORATORY OR 10. HARDWARE LABORATORY TW	100 4 100 4 100 4 100 4 100 4 50 2 50 2 25 1 50 2 25 1 50 2	0 45 P C 0 56 P C 0 50 P C 0 42 P C 0 45 P C 0 34 P C 0 21 P C 0 28 P C 0 21 P C 0 22 P C	12. PRINCIPLES OF PROGRAMMING LAN 13. COMPUTER NETWORKS 14. FINANCE & MANAGEMENT INFORMA. 15. SYSTEMS PROGRAMMING & OPERA.S 16. SOFTWARE ENGINEERING 17. SOFTWARE LABORATORY 18. SOFTWARE LABORATORY 19. COMPUTER NETWORK 20. COMPUTER NETWORK 21. SOFTWARE DEVELOPMENT TOOLS LA 22. SEMINAR AND TECHNICAL COMMUNI	PP 100 SYS.PP 100 YS.PP 100 PP 100 TW 25 PR 50 TW 25 OR 50 B. TW 50	40 67 P C 40 55 P C 40 61 P C 40 67 P C 40 46 P C 10 21 P C 20 26 P C 10 20 P C 20 32 P 20 42 P C 20 43 P C
GRAND TOTAL = 894+06/1500, RESULT: FIRST CLASS [0.2] ORDN. 1 MARKS :					
T8054391 WAGHMODE PRIYA SHIVAJI		MEERA	, 70925655) , т8054347	, PICT	, т8054391
01. DATABASE MANAGEMENT SYSTEMS PP 02. DATA COMMUNICATION PP 03. MICROPROCESSORS & MICROCONTROLLERPP 04. DIGITAL SIGNAL PROCESSING PP 05. THEORY OF COMPUTATION PP 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 08. SIGNAL PROCESSING LABORATORY TW 09. SIGNAL PROCESSING LABORATORY OR 10. HARDWARE LABORATORY TW 11. HARDWARE LABORATORY PR	100 4 100 4 100 4 100 4 100 4 50 2 50 2 50 2 50 2 25 1 50 2	0 43 P C 0 54 P C 0 40 P C 0 42 P C 0 31 P C 0 22 P C 0 18 P C 0 18 P C	12. PRINCIPLES OF PROGRAMMING LAN 13. COMPUTER NETWORKS 14. FINANCE & MANAGEMENT INFORMA. 15. SYSTEMS PROGRAMMING & OPERA.S 16. SOFTWARE ENGINEERING 17. SOFTWARE LABORATORY 18. SOFTWARE LABORATORY 19. COMPUTER NETWORK 20. COMPUTER NETWORK 21. SOFTWARE DEVELOPMENT TOOLS LA 22. SEMINAR AND TECHNICAL COMMUNI	PP 100 SYS.PP 100 YS.PP 100 PP 100 TW 25 PR 50 TW 25 OR 50 B. TW 50	40 52 P C 40 40 P 40 50 P C 40 59 P C 40 40 P C 10 14 P C 20 25 P C 10 14 P C 20 36 P C 20 32 P C 20 35 P C
GRAND TOTAL = 768/1500, RESULT: SECOND CLASS ORDN. 1 MARKS:					
T8054392 YUVRAJ BHARDWAJ		SANGEETA	, 70701703D , т8054348	, PICT	, т8054392
01. DATABASE MANAGEMENT SYSTEMS PP 02. DATA COMMUNICATION PP 03. MICROPROCESSORS & MICROCONTROLLERPP 04. DIGITAL SIGNAL PROCESSING PP 05. THEORY OF COMPUTATION PP 06. RDBMS & VISUAL PROGRAMMING LAB. TW 07. RDBMS & VISUAL PROGRAMMING LAB. PR 08. SIGNAL PROCESSING LABORATORY TW 09. SIGNAL PROCESSING LABORATORY OR 10. HARDWARE LABORATORY TW 11. HARDWARE LABORATORY PR	100 44 100 44 100 44 100 44 100 45 50 20 25 10 50 20 25 10 50 20	0 40 P C 0 21 F 0 26 F 0 AA F 0 20 P C 0 22 P C 0 10 P C 0 22 P C 0 10 P C	12. PRINCIPLES OF PROGRAMMING LAN 13. COMPUTER NETWORKS 14. FINANCE & MANAGEMENT INFORMA. 15. SYSTEMS PROGRAMMING & OPERA.S 16. SOFTWARE ENGINEERING 17. SOFTWARE LABORATORY 18. SOFTWARE LABORATORY 19. COMPUTER NETWORK 20. COMPUTER NETWORK 21. SOFTWARE DEVELOPMENT TOOLS LA 22. SEMINAR AND TECHNICAL COMMUNI	PP 100 SYS.PP 100 YS.PP 100 PP 100 TW 25 PR 50 TW 25 OR 50 B. TW 50	40 27 F 40 40 P 40 40 P C 40 46 P C 40 13 F 10 10 P C 20 05 F 10 10 P C 20 30 P C 20 21 P C 20 20 P C
CRAND TOTAL - 407/1500 DECILITY EATLS					

GRAND TOTAL = 497/1500, RESULT: FAILS ORDN. 1 MARKS :

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UNIVERSITY OF PUNE ,T.E. (2008 PAT) (INFORMATION TECHNOLOGY) EXAMINATION NOV. 2011
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                                                                                PAGE NO. 01 (476)
  NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
     OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
T8058501 AGHAV ISHWARI JAGANNATH
                                         VAISHALI , 71045363K , , PICT , T8058501
 01. OPERATING SYSTEM
02. THEORY OF COMPUTATION
03. COMPUTER NETWORK TECHNOLOGY
 01. OPERATING SYSTEM
                             PP 100
                                     40
                                         56 P
                                         80 P
                            PP
                                100
                                     40
 04. DATBASE MANAGEMENT SYSTEMS
                            PP
                                 100
                                     40
                                         65
                                            Р
 05. SOFTWARE ENGINEERING
                            PP
                                 100
                                     40
                                         66
                          TW
                                     10
                                         22
 06. OPERATING SYSTEM DESIGN LAB.
                                 25
 07. OPERATING SYSTEM DESIGN LAB. PR
                                 50
                                     20 38
                                 25
                                    10 19
 08. INFORMATION SYSTEMS DESIGN LAB. TW
 09. INFORMATION SYSTEMS DESIGN LAB. OR
                                 50 20 42
 10. NETWORK LABORATORY
                                 25 10 19 P
                   TW
 11. NETWORK LABORATORY
                           OR 50 20 41 P
 FIRST TERM TOTAL = 534/750.
ORDN. 1 MARKS :
T8058502 AGRAWAL NILIMA OMPRAKASH
                                         LATA
                                                   , 71134941L , , PICT , T8058502
                             PP 100 40
 01. OPERATING SYSTEM
 01. OPERATING SYSTEM PP 100 02. THEORY OF COMPUTATION PP 100
                                     40
                                        55 P
 03. COMPUTER NETWORK TECHNOLOGY PP 100
                                     40 63 P
 04. DATBASE MANAGEMENT SYSTEMS PP
                                100
                                     40
                                         60 P
 05. SOFTWARE ENGINEERING
                            PP
                                100
                                     40
                                         53
 07. OPERATING SYSTEM DESIGN LAB.
08. INFORMATION SYSTEM
                                 25
                                     10
                                         23
                            TW
                                 50
                                     20 35
                            PR
 08. INFORMATION SYSTEMS DESIGN LAB. TW
                                     10
 09. INFORMATION SYSTEMS DESIGN LAB. OR
                                 50
                                     20
                                         37
                     TW
                                 25
                                     10 21
 10. NETWORK LABORATORY
                                 50 20 36 P
 11. NETWORK LABORATORY
 FIRST TERM TOTAL = 481/750.
ORDN. 1 MARKS :
, 71134942J , , PICT , T8058503
 T8058503 AHER SHUBHAM SURESH
                                         LATA
                            PP 100 40
 01. OPERATING SYSTEM PP 100 40 54 P 02. THEORY OF COMPUTATION PP 100 40 55 P 03. COMPUTER NETWORK TECHNOLOGY PP 100 40 85 P
 01. OPERATING SYSTEM
 04. DATBASE MANAGEMENT SYSTEMS PP 100
                                     40 65 P
                            PP
                                100
                                     40 70
 05. SOFTWARE ENGINEERING
 06. OPERATING SYSTEM DESIGN LAB.
                                 25
                                     10 23
                           TW
 07. OPERATING SYSTEM DESIGN LAB.
                            PR
                                 50
 08. INFORMATION SYSTEMS DESIGN LAB. TW
                                     10
 09. INFORMATION SYSTEMS DESIGN LAB. OR
                                 50
                                     20
                                         37
                                 25
                                         22
 10. NETWORK LABORATORY
                            TW
                                     10
                                            Р
                                 50 20
                                         39
 11. NETWORK LABORATORY
                         OR
 FIRST TERM TOTAL = 534/750.
ORDN. 1 MARKS:
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UNIVERSITY OF PUNE ,T.E. (2008 PAT) (INFORMATION TECHNOLOGY) EXAMINATION NOV. 2011
   DATE : 02 MAR. 2012 CENTRÉ : PUNE INSTITUTE OF COMPUTER TECHNOLÓGY, PUNE.
                                                                                PAGE NO. 02 ( 477)
  NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
     OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
T8058504 ALWAL ANNAPURNA RAMESH
                                         RENUKA , 71134943G , , PICT , T8058504
 01. OPERATING SYSTEM
02. THEORY OF COMPUTATION
03. COMPUTED NETWORK TECHNOLOGY
                            PP 100
                                     40
                                        45 P
                                        80 P
                                100
                                     40
                            PP
 04. DATBASE MANAGEMENT SYSTEMS
                            PP
                                100
                                     40
                                        67
                                           Р
 05. SOFTWARE ENGINEERING
                            PP
                                100
                                     40
                                        66
                          TW
                                     10 19
 06. OPERATING SYSTEM DESIGN LAB.
                                 25
 07. OPERATING SYSTEM DESIGN LAB. PR
                                 50
                                     20 22
                                 25
                                    10 19
 08. INFORMATION SYSTEMS DESIGN LAB. TW
 09. INFORMATION SYSTEMS DESIGN LAB. OR
                                 50 20 38 P
 10. NETWORK LABORATORY
                                 25 10 19 P
                   TW
 11. NETWORK LABORATORY
                           OR
                                 50 20 35 P
 FIRST TERM TOTAL = 486/750.
ORDN. 1 MARKS :
T8058505 ANKIT SHARMA
                                                        , 71045372J , , PICT , T8058505
                                         ANITA
                               100
                                    40
 01. OPERATING SYSTEM PP 02. THEORY OF COMPUTATION PP
                                     40 40 P
                               100
 03. COMPUTER NETWORK TECHNOLOGY PP
                               100
                                     40 43 P
 04. DATBASE MANAGEMENT SYSTEMS
                          PP
                                100
                                     40
                                        50 P
                            PP
 05. SOFTWARE ENGINEERING
                                100
                                     40
                                        51
 07. OPERATING SYSTEM DESIGN LAB.
08. INFORMATION SYSTEM
                                 25
                                     10
                                        16
                            TW
                                 50
                                     20
                                        30
                            PR
 08. INFORMATION SYSTEMS DESIGN LAB. TW
                                 25
                                     10
                                        20
 09. INFORMATION SYSTEMS DESIGN LAB. OR
                                 50
                                     20
                                        38
                     TW
                                 25
                                    10 17
 10. NETWORK LABORATORY
                                           Р
                                 50 20 36
 11. NETWORK LABORATORY
 FIRST TERM TOTAL = 398/750.
ORDN. 1 MARKS:
, 71045373G , , PICT , T8058506
 T8058506 ANUJA WANGNOO
                                         AMBICA
 01. OPERATING SYSTEM PP 100
02. THEORY OF COMPUTATION PP 100
03. COMPUTER NETWORK TECHNOLOGY PP 100
                                   40
                                   40 44 P
                                    40 41 P
                            PP
                                100
                                    40 45
 04. DATBASE MANAGEMENT SYSTEMS
                            PP
                                100
                                     40 40
 05. SOFTWARE ENGINEERING
 06. OPERATING SYSTEM DESIGN LAB.
                                 25
                                     10 15 P
                           TW
 07. OPERATING SYSTEM DESIGN LAB.
                            PR
                                 50
                                     20 22
 08. INFORMATION SYSTEMS DESIGN LAB. TW
                                     10 15
                                        21
 09. INFORMATION SYSTEMS DESIGN LAB. OR
                                 50
                                     20
                                 25
 10. NETWORK LABORATORY
                            TW
                                     10
                                        16
                                           Р
                                 50 20
                                        25
 11. NETWORK LABORATORY
                         OR
 FIRST TERM TOTAL = 341/750.
ORDN. 1 MARKS:
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UNIVERSITY OF PUNE ,T.E. (2008 PAT) (INFORMATION TECHNOLOGY) EXAMINATION NOV. 2011
   DATE : 02 MAR. 2012 CENTRÉ : PÛNE INSTITÛTE OF COMPUTER TECHNOLÔGY, PUNE.
                                                                                PAGE NO. 03 (478)
NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
     OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
T8058507 ASHISH SANADHYA
                                         MADHU
                                                         , 71045374E , , PICT , T8058507
 01. OPERATING SYSTEM
02. THEORY OF COMPUTATION
03. COMPUTER NETWORK TECHNOLOGY
 01. OPERATING SYSTEM
                            PP 100
                                         49 P
                                     40
                            PP
                                100
                                     40
                                        71 P
 04. DATBASE MANAGEMENT SYSTEMS
                          PP
                                100
                                     40
                                         62
                                            Р
 05. SOFTWARE ENGINEERING
                            PP
                                100
                                     40
                                         60
                                            Р
                          TW
                                     10
                                         20
 06. OPERATING SYSTEM DESIGN LAB.
                                 25
 07. OPERATING SYSTEM DESIGN LAB. PR
                                 50
                                     20
                                         39
                                 25
                                    10 19
 08. INFORMATION SYSTEMS DESIGN LAB. TW
 09. INFORMATION SYSTEMS DESIGN LAB. OR
                                 50 20 30 P
 10. NETWORK LABORATORY
                                 25 10 21 P
                   TW
 11. NETWORK LABORATORY
                           OR
 FIRST TERM TOTAL = 489/750.
ORDN. 1 MARKS :
T8058508 ASHTEKAR KRUTTIKA CHANDRAKANT
                                       SUREKHA
                                                   , 71045375C , , PICT , T8058508
 01. OPERATING SYSTEM PP 100
02. THEORY OF COMPUTATION PP 100
03. COMPUTER NETWORK TECHNOLOGY PP 100
                            PP 100 40 40 P
                                    40
                                       45 P
                                     40 52 P
 04. DATBASE MANAGEMENT SYSTEMS PP 100
                                     40 57
 05. SOFTWARE ENGINEERING
                            PP
                                100
                                     40
 07. OPERATING SYSTEM DESIGN LAB.
08. INFORMATION SYSTEM
                                 25
                                     10
                                        21
                            TW
                                 50
                                     20 32
                                           Р
                            PR
 08. INFORMATION SYSTEMS DESIGN LAB. TW
                                 25
                                     10
                                         21
 09. INFORMATION SYSTEMS DESIGN LAB. OR
                                 50
                                     20
                    TW
                                 25
                                    10 20 P
 10. NETWORK LABORATORY
                                50 20 37
 11. NETWORK LABORATORY
 FIRST TERM TOTAL = 420/750.
ORDN. 1 MARKS :
, 71045380K , , PICT , T8058509
 T8058509 BADGUJAR PRATIKSHA BHASKAR
                                         ANITA
                            PP 100 40
 01. OPERATING SYSTEM
 01. OPERATING SYSTEM
02. THEORY OF COMPUTATION
03. COMPUTER NETWORK TECHNOLOGY
                            PP 100 40 44 P
                            PP 100
                                    40 65 P
 04. DATBASE MANAGEMENT SYSTEMS PP
                                100
                                     40 55
                                            Р
                            PP
                                100
                                     40 55
 05. SOFTWARE ENGINEERING
 06. OPERATING SYSTEM DESIGN LAB.
                                 25
                                     10
                                       20
                          TW
 07. OPERATING SYSTEM DESIGN LAB. PR
                                 50
                                     20 21
 08. INFORMATION SYSTEMS DESIGN LAB. TW
                                        27
 09. INFORMATION SYSTEMS DESIGN LAB. OR
                                 50
                                     20
                   TW
OR
                                 25
 10. NETWORK LABORATORY
                                     10
                                        21
                                            Р
                                 50 20
 11. NETWORK LABORATORY
                                         32
 FIRST TERM TOTAL = 432/750.
ORDN. 1 MARKS:
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UNIVERSITY OF PUNE ,T.E. (2008 PAT) (INFORMATION TECHNOLOGY) EXAMINATION NOV. 2011
   DATE : 02 MAR. 2012 CENTRÉ : PUNE INSTITUTE OF COMPUTER TECHNOLÓGY, PUNE.
                                                                                PAGE NO. 04 (479)
NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
     OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
T8058510 BHALERAO MANGESH GANESH
                                         NANDA
                                                         , 70925361D , , PICT , T8058510
 01. OPERATING SYSTEM
02. THEORY OF COMPUTATION
03. COMPUTER NETWORK TECHNOLOGY
 01. OPERATING SYSTEM
                            PP 100
                                        40 P
                                     40
                            PP
                                100
                                     40 40 P
 04. DATBASE MANAGEMENT SYSTEMS
                            PP
                                100
                                     40
                                       40 P
 05. SOFTWARE ENGINEERING
                            PP
                                100
                                     40 40
                          TW
                                     10 15
 06. OPERATING SYSTEM DESIGN LAB.
                                 25
 07. OPERATING SYSTEM DESIGN LAB. PR
                                 50
                                     20 08 F
                                 25
                                    10 15
 08. INFORMATION SYSTEMS DESIGN LAB. TW
 09. INFORMATION SYSTEMS DESIGN LAB. OR
                                 50 20 41 P
 10. NETWORK LABORATORY
                                 25 10 16 P
                   TW
 11. NETWORK LABORATORY
                          OR 50 20 32 P
 FIRST TERM TOTAL = 348/750.
ORDN. 1 MARKS :
T8058511 BHARTENDU BHARTI
                                         MANISHA DEVI , 71045387G , , PICT , T8058511
 01. OPERATING SYSTEM PP 100 02. THEORY OF COMPUTATION PP 100
                                   40 41 P
                                    40
                                       45 P
 03. COMPUTER NETWORK TECHNOLOGY PP 100
                                     40 65 P
 04. DATBASE MANAGEMENT SYSTEMS
                          PP
                                100
                                     40 63 P
                            PP
 05. SOFTWARE ENGINEERING
                                100
                                     40
                                        40 P
 07. OPERATING SYSTEM DESIGN LAB.
08. INFORMATION SYSTEM
                                 25
                                     10 18
                            TW
                                 50
                                     20 40 P
                            PR
 08. INFORMATION SYSTEMS DESIGN LAB. TW
                                 25
                                     10
                                        18
 09. INFORMATION SYSTEMS DESIGN LAB. OR
                                 50
                                     20 33
                    TW
                                 25
                                    10 17
 10. NETWORK LABORATORY
                                           Р
                                 50 20 10 F
 11. NETWORK LABORATORY
 FIRST TERM TOTAL = 411/750.
ORDN. 1 MARKS :
SANGEETA , 71134944E , , , PICT , T8058512
 T8058512 BHINTADE VIVEK ASHOK
 01. OPERATING SYSTEM PP 100
02. THEORY OF COMPUTATION PP 100
03. COMPUTER NETWORK TECHNOLOGY PP 100
                            PP 100 40
                                    40 56 P
                                    40 84 P
 04. DATBASE MANAGEMENT SYSTEMS PP 100
                                    40 62
                                           Р
                            PP
                                100
                                     40 64
 05. SOFTWARE ENGINEERING
 06. OPERATING SYSTEM DESIGN LAB.
                                 25
                                     10 18
                          TW
 07. OPERATING SYSTEM DESIGN LAB. PR
                                 50
 08. INFORMATION SYSTEMS DESIGN LAB. TW
                                     10 19
 09. INFORMATION SYSTEMS DESIGN LAB. OR
                                 50
                                     20
                                        10
                                 25
 10. NETWORK LABORATORY
                            TW
                                     10
                                        16
                                           Р
                        OR
                                 50 20
                                        34
 11. NETWORK LABORATORY
 FIRST TERM TOTAL = 477/750.
ORDN. 1 MARKS:
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UNIVERSITY OF PUNE ,T.E. (2008 PAT) (INFORMATION TECHNOLOGY) EXAMINATION NOV. 2011
   DATE : 02 MAR. 2012 CENTRÉ : PUNE INSTITUTE OF COMPUTER TECHNOLÓGY, PUNE.
                                                                               PAGE NO. 05 (480)
  NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
     OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
T8058513 BHOSALE SHREEYA DEEPAKRAO
                                        JYOTSNA , 71045394K , , PICT , T8058513
 01. OPERATING SYSTEM
02. THEORY OF COMPUTATION
WETWORK TECHNOLOGY
                            PP 100
                                        53 P
                                    40
                            PP
                                        78
                                100
                                    40
                                           Ρ
 04. DATBASE MANAGEMENT SYSTEMS
                            PP
                                100
                                    40
                                        59
                                           Р
 05. SOFTWARE ENGINEERING
                            PP
                                100
                                    40
                                        60
                          TW
                                    10 23
 06. OPERATING SYSTEM DESIGN LAB.
                                25
 07. OPERATING SYSTEM DESIGN LAB. PR
                                 50
                                    20 40
                                25
                                    10 23
 08. INFORMATION SYSTEMS DESIGN LAB. TW
 09. INFORMATION SYSTEMS DESIGN LAB. OR
                                 50 20 32
 10. NETWORK LABORATORY
                                25 10 23 P
                   TW
 11. NETWORK LABORATORY
                          OR 50 20 34 P
 FIRST TERM TOTAL = 504/750.
ORDN. 1 MARKS :
UJWALA , 71045398B , , PICT , T8058514
 T8058514 BORDE ROHAN SATISHRAO
                            PP 100 40 51 P
 01. OPERATING SYSTEM
 01. OPERATING SYSTEM PP 100 02. THEORY OF COMPUTATION PP 100
                                    40
                                       61 P
 03. COMPUTER NETWORK TECHNOLOGY PP 100
                                    40 68 P
 04. DATBASE MANAGEMENT SYSTEMS PP
                                100
                                    40 65 P
                            PP
 05. SOFTWARE ENGINEERING
                                100
                                    40
                                        59
 07. OPERATING SYSTEM DESIGN LAB. TW
08. INFORMATION SYSTEM
                                25
                                    10
                                        19
                                 50
                                    20
                                        30 P
 08. INFORMATION SYSTEMS DESIGN LAB. TW
                                 25
                                    10 19
 09. INFORMATION SYSTEMS DESIGN LAB. OR
                                 50
                                    20 10 F
                   TW
                                25
                                    10 19 P
 10. NETWORK LABORATORY
                               50 20 40
 11. NETWORK LABORATORY
 FIRST TERM TOTAL = 461/750.
ORDN. 1 MARKS :
VIDULA , 71134945C , , PICT , T8058515
 T8058515 BRAHME NACHIKET SHRIKANT
 01. OPERATING SYSTEM
                            PP 100 40
 01. OPERATING SYSTEM PP 100
02. THEORY OF COMPUTATION PP 100
03. COMPUTER NETWORK TECHNOLOGY PP 100
                                   40 46 P
                                    40 72
                                           Р
 04. DATBASE MANAGEMENT SYSTEMS PP
                                100
                                    40
                                        56
                                           Р
                            PP
                                100
                                    40 55
                                           Р
 05. SOFTWARE ENGINEERING
 06. OPERATING SYSTEM DESIGN LAB.
                                25
                                    10 21 P
                          TW
 07. OPERATING SYSTEM DESIGN LAB. PR
                                 50
 08. INFORMATION SYSTEMS DESIGN LAB. TW
                                    10
                                       19
 09. INFORMATION SYSTEMS DESIGN LAB. OR
                                 50
                                    20
                                        36
                                25
                                        22
 10. NETWORK LABORATORY
                   TW
OR
                                    10
                                           Р
                                50 20
                                        35
 11. NETWORK LABORATORY
 FIRST TERM TOTAL = 471/750.
ORDN. 1 MARKS:
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UNIVERSITY OF PUNE ,T.E. (2008 PAT) (INFORMATION TECHNOLOGY) EXAMINATION NOV. 2011
   DATE : 02 MAR. 2012 CENTRÉ : PUNE INSTITUTE OF COMPUTER TECHNOLÓGY, PUNE.
                                                                              PAGE NO. 06 (481)
NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
     OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
T8058516 CHAKRAVARTHY ROHAN KUMAR
                                        MONICA
                                                       , 71045400H , , PICT , T8058516
 01. OPERATING SYSTEM
02. THEORY OF COMPUTATION

NETWORK TECHNOLOGY
                            PP 100
                                       55 P
                                    40
                                       63 P
                            PP
                               100
                                    40
 04. DATBASE MANAGEMENT SYSTEMS
                         PP
                               100
                                    40
                                       64
                                          Р
 05. SOFTWARE ENGINEERING
                            PP
                               100
                                    40
                                       51
                          TW
                                    10 15
 06. OPERATING SYSTEM DESIGN LAB.
                                25
 07. OPERATING SYSTEM DESIGN LAB. PR
                                50
                                    20 34
                                25
                                   10 15
 08. INFORMATION SYSTEMS DESIGN LAB. TW
 09. INFORMATION SYSTEMS DESIGN LAB. OR
                                50 20 21 P
 10. NETWORK LABORATORY
                                25 10 12 P
                   TW
 11. NETWORK LABORATORY
                          OR 50 20 40 P
 FIRST TERM TOTAL = 431/750.
ORDN. 1 MARKS :
, 71134946M , , PICT , T8058517
 T8058517 CHANDAK RASHMI VIJAYKUMARJI
                                        SHYAMA
                            PP 100 40 45 P
 01. OPERATING SYSTEM
 02. THEORY OF COMPUTATION PP 100
                                    40
                                      58 P
 03. COMPUTER NETWORK TECHNOLOGY PP 100
                                    40 68 P
 04. DATBASE MANAGEMENT SYSTEMS PP
                               100
                                    40 53 P
                           PP
 05. SOFTWARE ENGINEERING
                               100
                                    40
                                       57
 07. OPERATING SYSTEM DESIGN LAB.
08. INFORMATION SYSTEM
                                25
                                    10
                                       23
                           TW
                                50
                                    20
                                       25
                           PR
 08. INFORMATION SYSTEMS DESIGN LAB. TW
                                    10
                                       23
 09. INFORMATION SYSTEMS DESIGN LAB. OR
                                50
                                    20
                                       36
                   TW
                                25
                                    10 22
 10. NETWORK LABORATORY
                                          Р
                               50 20 34
 11. NETWORK LABORATORY
 FIRST TERM TOTAL = 466/750.
ORDN. 1 MARKS :
, 71058517к , , , , , рІСТ , Т8058518
 T8058518 CHANGEDIYA SUNNY SANJAY
                                        ASHA
 01. OPERATING SYSTEM
                            PP 100 40
 01. OPERATING SYSTEM PP 100
02. THEORY OF COMPUTATION PP 100
03. COMPUTER NETWORK TECHNOLOGY PP 100
                                   40 57
                                   40 69
 04. DATBASE MANAGEMENT SYSTEMS PP
                               100
                                    40 69
                           PP
                               100
                                    40 58
 05. SOFTWARE ENGINEERING
 06. OPERATING SYSTEM DESIGN LAB.
                                25
                                    10 20
                          TW
 07. OPERATING SYSTEM DESIGN LAB.
                           PR
                                50
 08. INFORMATION SYSTEMS DESIGN LAB. TW
                                    10
 09. INFORMATION SYSTEMS DESIGN LAB. OR
                                50
                                    20
                                       43
                                25
                                       22
 10. NETWORK LABORATORY
                            TW
                                    10
                                          Р
                        OR
                                50 20
                                       39
 11. NETWORK LABORATORY
 FIRST TERM TOTAL = 472/750.
ORDN. 1 MARKS:
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UNIVERSITY OF PUNE ,T.E. (2008 PAT) (INFORMATION TECHNOLOGY) EXAMINATION NOV. 2011
   DATE : 02 MAR. 2012 CENTRÉ : PUNE INSTITUTE OF COMPUTER TECHNOLÓGY, PUNE.
                                                                                  PAGE NO. 07 ( 482)
  NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
     OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
T8058519 CHAUDHAR SATISH RAOSAHEB
                                          SANGITA , 71045402D , , PICT , T8058519
 01. OPERATING SYSTEM
02. THEORY OF COMPUTATION

THEORY OF COMPUTATION

THEORY OF COMPUTATION

THEORY OF COMPUTATION
                             PP 100
                                         49 P
                                     40
                                         77
                             PP
                                 100
                                      40
                                            Ρ
 04. DATBASE MANAGEMENT SYSTEMS
                          PP
                                 100
                                      40
                                         69
                                            Р
 05. SOFTWARE ENGINEERING
                             PP
                                 100
                                      40
                                         55
                           TW
                                      10 21
 06. OPERATING SYSTEM DESIGN LAB.
                                  25
 07. OPERATING SYSTEM DESIGN LAB. PR
                                  50
                                     20 27
                                  25
                                     10 21
 08. INFORMATION SYSTEMS DESIGN LAB. TW
 09. INFORMATION SYSTEMS DESIGN LAB. OR
                                  50 20 10 F
 10. NETWORK LABORATORY TW
                                  25 10 20 P
 11. NETWORK LABORATORY
                           OR 50 20 40 P
 FIRST TERM TOTAL = 461/750.
ORDN. 1 MARKS :
SHUBHANGI , 71045403B , , PICT , T8058520
 T8058520 CHAUDHARI AMAR SHRIKANT
 01. OPERATING SYSTEM PP 100
02. THEORY OF COMPUTATION PP 100
03. COMPUTER NETWORK TECHNOLOGY PP 100
                             PP 100 40 58 P
                                     40
                                        46 P
                                     40 81 P
 04. DATBASE MANAGEMENT SYSTEMS PP
                                 100
                                      40 55 P
 05. SOFTWARE ENGINEERING
                             PP
                                 100
                                      40
                                         55 P
 06. OPERATING SYSTEM DESIGN LAB. TW 07. OPERATING SYSTEM DESIGN LAB. PR
                                  25
                                      10 16
                                  50
                                      20 44 P
 08. INFORMATION SYSTEMS DESIGN LAB. TW
                                     10 18
 09. INFORMATION SYSTEMS DESIGN LAB. OR
                                  50
                                      20 37
                    TW
                                  25
                                     10 17
 10. NETWORK LABORATORY
                                50 20 40
 11. NETWORK LABORATORY
 FIRST TERM TOTAL = 484/750.
ORDN. 1 MARKS :
, 70925378J , , PICT , T8058521
 T8058521 CHAUDHARI MAHESH PRABHAKAR
                                          MANGALA
                             PP 100 40
 01. OPERATING SYSTEM
                                         46 P
 01. OPERATING SYSTEM PP 100
02. THEORY OF COMPUTATION PP 100
03. COMPUTER NETWORK TECHNOLOGY PP 100
                             PP 100 40 44 P
                                     40 68 P
 04. DATBASE MANAGEMENT SYSTEMS PP 100
                                     40 55
                                            Р
                           PP
                                 100
                                     40 48
 05. SOFTWARE ENGINEERING
 06. OPERATING SYSTEM DESIGN LAB.
                                  25
                                     10 19
                           TW
 07. OPERATING SYSTEM DESIGN LAB. PR
                                  50
 08. INFORMATION SYSTEMS DESIGN LAB. TW
                                      10 17
 09. INFORMATION SYSTEMS DESIGN LAB. OR
                                  50
                                      20
                                         37
                   TW OR
                                  25
 10. NETWORK LABORATORY
                                     10
                                         21
                                            Р
                                  50 20
 11. NETWORK LABORATORY
                                         37
 FIRST TERM TOTAL = 455/750.
ORDN. 1 MARKS:
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UNIVERSITY OF PUNE ,T.E. (2008 PAT) (INFORMATION TECHNOLOGY) EXAMINATION NOV. 2011
   DATE : 02 MAR. 2012 CENTRÉ : PUNE INSTITUTE OF COMPUTER TECHNOLÓGY, PUNE.
                                                                               PAGE NO. 08 (483)
  NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
     OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
T8058522 CHAVAN PUSHPAK VILAS
                                         SUREKHA , 71134947K , , PICT , T8058522
 01. OPERATING SYSTEM
 02. THEORY OF COMPUTATION PP
03. COMPUTER NETWORK TECHNOLOGY PP
                            PP 100
                                    40
                                        48 P
                                100
                                    40
                                        64 P
 04. DATBASE MANAGEMENT SYSTEMS
                            PP
                                100
                                    40
                                        57
                                           Р
 05. SOFTWARE ENGINEERING
                            PP
                                100
                                    40
                                        69
                          TW
                                    10 19
 06. OPERATING SYSTEM DESIGN LAB.
                                 25
 07. OPERATING SYSTEM DESIGN LAB. PR
                                 50
                                    20 34
                                 25
                                    10 17
 08. INFORMATION SYSTEMS DESIGN LAB. TW
 09. INFORMATION SYSTEMS DESIGN LAB. OR
                                 50 20 10 F
 10. NETWORK LABORATORY
                                 25 10 17 P
                   TW
 11. NETWORK LABORATORY
                          OR 50 20 35 P
 FIRST TERM TOTAL = 448/750.
ORDN. 1 MARKS :
T8058523 DARP YOGESH RAMCHANDRA
                                                  , 71134948H , , PICT , T8058523
                                         SANDHYA
                            PP 100 40
                                        50 P
 01. OPERATING SYSTEM PP 100 02. THEORY OF COMPUTATION PP 100
                                    40
                                        40 P
 03. COMPUTER NETWORK TECHNOLOGY PP 100
                                    40 56 P
 04. DATBASE MANAGEMENT SYSTEMS PP
                                100
                                    40
                                        60 P
 05. SOFTWARE ENGINEERING
                            PP
                                100
                                    40
                                        58
 07. OPERATING SYSTEM DESIGN LAB.
08. INFORMATION SYSTEM
                                25
                                    10
                                        18
                            TW
                                 50
                                    20 30 P
                            PR
 08. INFORMATION SYSTEMS DESIGN LAB. TW
                                 25
                                    10 18
 09. INFORMATION SYSTEMS DESIGN LAB. OR
                                 50
                                    20 40
                    TW
                                 25
                                    10 19 P
 10. NETWORK LABORATORY
                               50 20 37
 11. NETWORK LABORATORY
 FIRST TERM TOTAL = 445/750.
ORDN. 1 MARKS :
, 71134949F , , PICT , T8058524
 T8058524 DAWANGE VISHAL SANJAY
                                         KALPANA
 01. OPERATING SYSTEM PP 100
02. THEORY OF COMPUTATION PP 100
03. COMPUTER NETWORK TECHNOLOGY PP 100
 01. OPERATING SYSTEM
                            PP 100 40
                                    40
                                        66 P
                                    40 69
 04. DATBASE MANAGEMENT SYSTEMS PP 100
                                    40 67
                                           Р
                            PP
                                100
                                    40
                                       63
 05. SOFTWARE ENGINEERING
 06. OPERATING SYSTEM DESIGN LAB.
                                25
                                    10 22
                          TW
 07. OPERATING SYSTEM DESIGN LAB. PR
                                 50
 08. INFORMATION SYSTEMS DESIGN LAB. TW
                                    10
 09. INFORMATION SYSTEMS DESIGN LAB. OR
                                 50
                                    20
                                        38
                                 25
                                        21
 10. NETWORK LABORATORY
                            TW
                                    10
                                           Р
                                 50 20
 11. NETWORK LABORATORY
                        OR
                                        37
 FIRST TERM TOTAL = 521/750.
ORDN. 1 MARKS:
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UNIVERSITY OF PUNE ,T.E. (2008 PAT) (INFORMATION TECHNOLOGY) EXAMINATION NOV. 2011
   DATE : 02 MAR. 2012 CENTRÉ : PUNE INSTITUTE OF COMPUTER TECHNOLÓGY, PUNE.
                                                                                PAGE NO. 09 (484)
  NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
     OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
T8058525 DEORE NIKHIL BAPUSAHEB
                                         MANDAKINI , 70801397L , , PICT , T8058525
 01. OPERATING SYSTEM
02. THEORY OF COMPUTATION
03. COMPUTER NETWORK TECHNOLOGY
                            PP 100
                                        40 P
                                     40
                                        50 P
                            PP
                                100
                                     40
 04. DATBASE MANAGEMENT SYSTEMS
                            PP
                                100
                                     40
                                        41
                                           Р
 05. SOFTWARE ENGINEERING
                            PP
                                100
                                     40 43
                          TW
                                     10 14 P
 06. OPERATING SYSTEM DESIGN LAB.
                                 25
 07. OPERATING SYSTEM DESIGN LAB. PR
                                 50
                                     20 06 F
                                 25
                                    10 13
 08. INFORMATION SYSTEMS DESIGN LAB. TW
 09. INFORMATION SYSTEMS DESIGN LAB. OR
                                 50 20 35 P
 10. NETWORK LABORATORY
                                 25 10 12 P
                   TW
 11. NETWORK LABORATORY
                           OR
                                 50 20 11 F
 FIRST TERM TOTAL = 327/750.
ORDN. 1 MARKS :
T8058526 DESHPANDE KIRTI VINOD
                                         SHALINI , 71134950K , , PICT , T8058526
                            PP 100
                                   40
 01. OPERATING SYSTEM PP 100 02. THEORY OF COMPUTATION PP 100
                                    40
                                       55 P
 03. COMPUTER NETWORK TECHNOLOGY PP 100
                                     40 68 P
 04. DATBASE MANAGEMENT SYSTEMS PP
                                100
                                     40 63 P
 05. SOFTWARE ENGINEERING
                            PP
                                100
                                     40
                                        67
 07. OPERATING SYSTEM DESIGN LAB.
08. INFORMATION SYSTEM
                                 25
                                     10
                                        18
                            TW
                                 50
                                     20
                                        38
                            PR
 08. INFORMATION SYSTEMS DESIGN LAB. TW
                                 25
                                     10
                                        18
 09. INFORMATION SYSTEMS DESIGN LAB. OR
                                 50
                                    20
                                        34
                    TW
                                 25
                                    10 19 P
 10. NETWORK LABORATORY
                                 50 20 40
 11. NETWORK LABORATORY
 FIRST TERM TOTAL = 492/750.
ORDN. 1 MARKS :
KISHORI , 71134951H , , PICT , T8058527
 T8058527 DESHPANDE NACHIKET CHANDRAKANT
 01. OPERATING SYSTEM
                            PP 100
                                   40
 01. OPERATING SYSTEM PP 100
02. THEORY OF COMPUTATION PP 100
03. COMPUTER NETWORK TECHNOLOGY PP 100
                                    40 46 P
                                    40 62
                                           Р
                            PP
                                100
                                    40 61
                                           Р
 04. DATBASE MANAGEMENT SYSTEMS
                            PP
                                100
                                    40 57
 05. SOFTWARE ENGINEERING
 06. OPERATING SYSTEM DESIGN LAB.
                                 25
                                    10 22
                          TW
 07. OPERATING SYSTEM DESIGN LAB.
                            PR
                                 50
 08. INFORMATION SYSTEMS DESIGN LAB. TW
                                        21
 09. INFORMATION SYSTEMS DESIGN LAB. OR
                                 50
                                     20
                                        33
                                 25
                                        20
 10. NETWORK LABORATORY
                            TW
                                    10
                                           Р
                        OR
                                 50 20
                                        40
 11. NETWORK LABORATORY
 FIRST TERM TOTAL = 470/750.
ORDN. 1 MARKS:
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NOTE: FIRST LINE : SEAT NO., NAME (OTHER LINES: HEAD OF PASSING,	OF THE MAX.	CANDI MARKS	DATE,	, MO ⁻ IN. P	THEI ASS		NO.,	PREVIOUS	SEAT NO.,	COL		SEAT	
T8058528 DHADIWAL KOMAL AJIT				ASI			711349	52F	,	,	PICT	,	т8058528
01. OPERATING SYSTEM 02. THEORY OF COMPUTATION 03. COMPUTER NETWORK TECHNOLOGY 04. DATBASE MANAGEMENT SYSTEMS 05. SOFTWARE ENGINEERING 06. OPERATING SYSTEM DESIGN LAB. 07. OPERATING SYSTEM DESIGN LAB. 08. INFORMATION SYSTEMS DESIGN LAB. 09. INFORMATION SYSTEMS DESIGN LAB. 10. NETWORK LABORATORY 11. NETWORK LABORATORY	PP PP PP	100 100 100 100 100 25 50 25 50 25 50	40 40 40 40 10 20 10 20 10 20	61 58 77 64 74 21 35 20 25 22 44	P P P P P P P P								
FIRST TERM TOTAL = 524/750. ORDN. 1 MARKS :													
T8058529 DHULE AKSHAYKUMAR MAHAD	 EVAPPA			 SH:	 IVAI		 710454	 16D	 ,		PICT		т8058529
01. OPERATING SYSTEM 02. THEORY OF COMPUTATION 03. COMPUTER NETWORK TECHNOLOGY 04. DATBASE MANAGEMENT SYSTEMS 05. SOFTWARE ENGINEERING 06. OPERATING SYSTEM DESIGN LAB. 07. OPERATING SYSTEM DESIGN LAB. 08. INFORMATION SYSTEMS DESIGN LAB. 09. INFORMATION SYSTEMS DESIGN LAB. 10. NETWORK LABORATORY	PP PP PP	100 100 100 100 100 25 50 25 50 25 50	40 40 40 40 10 20 10 20 10 20	45 56 74 59 68 20 00 21 33 18 34	P P P P P P P P P								
FIRST TERM TOTAL = 449/750. ORDN. 1 MARKS :													
T8058530 DIKE ASHISH VIJAY				RA:	JAN:	ı,	710454	18L	,	,	PICT	,	т8058530
01. OPERATING SYSTEM 02. THEORY OF COMPUTATION 03. COMPUTER NETWORK TECHNOLOGY 04. DATBASE MANAGEMENT SYSTEMS 05. SOFTWARE ENGINEERING 06. OPERATING SYSTEM DESIGN LAB. 07. OPERATING SYSTEM DESIGN LAB. 08. INFORMATION SYSTEMS DESIGN LAB. 09. INFORMATION SYSTEMS DESIGN LAB. 10. NETWORK LABORATORY 11. NETWORK LABORATORY	PP PP PP	100 100 100 100 100 25 50 25 50 25 50	40 40 40 40 40 10 20 10 20	19 09 45 40 46 12 AA 10 07 10 AA	F F P P F P F P F								
FIRST TERM TOTAL = 213/750. ORDN. 1 MARKS :													

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  NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
     OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
T8058531 DIVEKAR SANDIP LAXMAN
                                         SHOBHA
                                                , 71134953D , , PICT , T8058531
 01. OPERATING SYSTEM
02. THEORY OF COMPUTATION
03. COMPUTER NETWORK TECHNOLOGY
                            PP 100
                                     40
                                         44 P
                                         68 P
                            PP
                                100
                                     40
 04. DATBASE MANAGEMENT SYSTEMS
                            PP
                                100
                                     40
                                         69
                                            Р
 05. SOFTWARE ENGINEERING
                            PP
                                100
                                     40
                                         53
                          TW
                                     10
                                         21
 06. OPERATING SYSTEM DESIGN LAB.
                                 25
 07. OPERATING SYSTEM DESIGN LAB. PR
                                 50
                                     20
                                 25
                                     10 20
 08. INFORMATION SYSTEMS DESIGN LAB. TW
 09. INFORMATION SYSTEMS DESIGN LAB. OR
                                 50 20 39 P
 10. NETWORK LABORATORY
                                 25 10 22 P
                   TW
 11. NETWORK LABORATORY
                           OR 50 20 36 P
 FIRST TERM TOTAL = 486/750.
ORDN. 1 MARKS :
T8058532 DUMAVAT KUNAL BHAVARLAL
                                         VIDYA
                                               , 71134954B , , PICT , T8058532
 01. OPERATING SYSTEM PP 100
02. THEORY OF COMPUTATION PP 100
03. COMPUTER NETWORK TECHNOLOGY PP 100
                            PP 100 40 52 P
                                     40 40 P
                                     40 66 P
 04. DATBASE MANAGEMENT SYSTEMS PP 100
                                     40 66 P
                            PP
 05. SOFTWARE ENGINEERING
                                100
                                     40
                                         59
 07. OPERATING SYSTEM DESIGN LAB. TW
08. INFORMATION SYSTEM
                                 25
                                     10
                                         20
                                 50
                                     20 32
 08. INFORMATION SYSTEMS DESIGN LAB. TW
                                     10 19
 09. INFORMATION SYSTEMS DESIGN LAB. OR
                                 50
                                     20 28
                                     10 18 P
                    TW
                                 25
 10. NETWORK LABORATORY
                                50 20 35
 11. NETWORK LABORATORY
 FIRST TERM TOTAL = 456/750.
ORDN. 1 MARKS :
VITHABAI , 71134955L , , PICT , T8058533
 T8058533 EKHANDE DNYANESHWAR KACHARU
 01. OPERATING SYSTEM
                             PP 100 40
 01. OPERATING SYSTEM PP 100
02. THEORY OF COMPUTATION PP 100
03. COMPUTER NETWORK TECHNOLOGY PP 100
                                    40
                                         52 P
                                     40 79
 04. DATBASE MANAGEMENT SYSTEMS PP 100
                                     40
                                         59
                                            Р
                            PP
                                100
                                     40
                                         69
 05. SOFTWARE ENGINEERING
 06. OPERATING SYSTEM DESIGN LAB.
                                 25
                                     10 23 P
                           TW
 07. OPERATING SYSTEM DESIGN LAB. PR
                                 50
 08. INFORMATION SYSTEMS DESIGN LAB. TW
                                     10
 09. INFORMATION SYSTEMS DESIGN LAB. OR
                                 50
                                     20
                                         38
                   TW
OR
                                 25
                                         22
 10. NETWORK LABORATORY
                                     10
                                            Р
                                 50 20
                                         40
 11. NETWORK LABORATORY
 FIRST TERM TOTAL = 522/750.
ORDN. 1 MARKS:
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     OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
T8058534 GADKARI ASHISH VASANTRAO
                                        SANGITA , 71134956J , , PICT , T8058534
 01. OPERATING SYSTEM
02. THEORY OF COMPUTATION
WETWORK TECHNOLOGY
                            PP 100
                                        60 P
                                    40
                            PP
                                100
                                    40
                                        78
                                           Ρ
 04. DATBASE MANAGEMENT SYSTEMS
                            PP
                                100
                                    40
                                        63
                                           Р
 05. SOFTWARE ENGINEERING
                            PP
                                100
                                    40
                                        64
                                           Р
                          TW
                                    10
                                        24
 06. OPERATING SYSTEM DESIGN LAB.
                                 25
 07. OPERATING SYSTEM DESIGN LAB. PR
                                 50
                                    20 36
                                 25
                                    10 22
 08. INFORMATION SYSTEMS DESIGN LAB. TW
 09. INFORMATION SYSTEMS DESIGN LAB. OR
                                 50 20 43
 10. NETWORK LABORATORY
                                 25 10 24 P
                   TW
 11. NETWORK LABORATORY
                          OR 50 20 37 P
 FIRST TERM TOTAL = 526/750.
ORDN. 1 MARKS :
T8058535 GAIKWAD AKSHAY SHAHAJI
                                         MAHANANDA , 71045430K , , PICT , T8058535
                            PP 100 40 45 P
 01. OPERATING SYSTEM
 01. OPERATING SYSTEM PP 100 02. THEORY OF COMPUTATION PP 100
                                    40 48 P
 03. COMPUTER NETWORK TECHNOLOGY PP 100
                                    40 AA F
 04. DATBASE MANAGEMENT SYSTEMS PP
                                100
                                    40 52 P
                            PP
 05. SOFTWARE ENGINEERING
                                100
                                    40 44 P
 07. OPERATING SYSTEM DESIGN LAB.
08. INFORMATION SYSTEM
                                25
                                    10 10 P
                            TW
                                 50
                                    20 AA F
                            PR
 08. INFORMATION SYSTEMS DESIGN LAB. TW
                                 25
                                    10 10
 09. INFORMATION SYSTEMS DESIGN LAB. OR
                                 50
                                    20 06 F
                   TW
                                 25
                                    10 10 P
 10. NETWORK LABORATORY
                               50 20 AA F
 11. NETWORK LABORATORY
 FIRST TERM TOTAL = 240/750.
ORDN. 1 MARKS :
, 71045773B , , PICT , T8058536
 T8058536 GAIKWAD SAGAR ANAND
                                         SUREKHA
                            PP 100 40
 01. OPERATING SYSTEM PP 100
02. THEORY OF COMPUTATION PP 100
03. COMPUTER NETWORK TECHNOLOGY PP 100
 01. OPERATING SYSTEM
                                   40 63 P
                                    40 68 P
 04. DATBASE MANAGEMENT SYSTEMS PP 100
                                    40 50
                                           Р
                            PP
                                100
                                    40 43
 05. SOFTWARE ENGINEERING
 06. OPERATING SYSTEM DESIGN LAB.
                                25
                                    10 21 P
                          TW
 07. OPERATING SYSTEM DESIGN LAB. PR
                                 50
 08. INFORMATION SYSTEMS DESIGN LAB. TW
                                    10
 09. INFORMATION SYSTEMS DESIGN LAB. OR
                                 50
                                    20
                                        37
                                 25
                                        22
 10. NETWORK LABORATORY
                            TW
                                    10
                                           Р
                        OR
                                 50 20
                                        40
 11. NETWORK LABORATORY
 FIRST TERM TOTAL = 482/750.
ORDN. 1 MARKS:
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     OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
T8058537 GEHANI ASHISH MOHAN
                                        BHARTI , 71134957G , , PICT , T8058537
 01. OPERATING SYSTEM
02. THEORY OF COMPUTATION PP
NETWORK TECHNOLOGY PP
                            PP 100
                                    40
                                        81 P
                                100
                                    40
                                        90 P
 04. DATBASE MANAGEMENT SYSTEMS
                           PP
                                100
                                    40
                                        69
 05. SOFTWARE ENGINEERING
                            PP
                                100
                                    40
                                        69
                          TW
                                    10 24
 06. OPERATING SYSTEM DESIGN LAB.
                                25
 07. OPERATING SYSTEM DESIGN LAB. PR
                                 50
                                    20 48
                                25
                                    10 24
 08. INFORMATION SYSTEMS DESIGN LAB. TW
 09. INFORMATION SYSTEMS DESIGN LAB. OR
                                 50 20 37
 10. NETWORK LABORATORY
                                25 10 23 P
                   TW
 11. NETWORK LABORATORY
                          OR 50 20 45 P
 FIRST TERM TOTAL = 597/750.
ORDN. 1 MARKS :
T8058538 GHADGE TEJAL RAJENDRA
                                        ROHINI , 71134958E , , PICT , T8058538
 01. OPERATING SYSTEM PP 100 40 02. THEORY OF COMPUTATION PP 100 40
                                       59 P
 03. COMPUTER NETWORK TECHNOLOGY PP 100
                                    40 81 P
 04. DATBASE MANAGEMENT SYSTEMS PP 100
                                    40 65 P
                           PP
 05. SOFTWARE ENGINEERING
                                100
                                    40
                                        61
 07. OPERATING SYSTEM DESIGN LAB.
08. INFORMATION SYSTEM
                                25
                                    10
                                       24
                           TW
                                 50
                                    20 45 P
                           PR
 08. INFORMATION SYSTEMS DESIGN LAB. TW
                                 25
                                    10 24
 09. INFORMATION SYSTEMS DESIGN LAB. OR
                                 50
                                    20 42
                   TW
                                25
                                    10 24 P
 10. NETWORK LABORATORY
                               50 20 46 P
 11. NETWORK LABORATORY
 FIRST TERM TOTAL = 559/750.
ORDN. 1 MARKS:
UJWALA , 71134959C , , PICT , T8058539
 T8058539 GUNDECHA ANIKET PRAKASH
 01. OPERATING SYSTEM
                            PP 100 40
 01. OPERATING SYSTEM PP 100
02. THEORY OF COMPUTATION PP 100
03. COMPUTER NETWORK TECHNOLOGY PP 100
                                   40 40 P
                                    40 69 P
 04. DATBASE MANAGEMENT SYSTEMS PP 100
                                    40 61
                                           Р
                            PP
                                100
                                    40 48
 05. SOFTWARE ENGINEERING
 06. OPERATING SYSTEM DESIGN LAB.
                                25
                                    10 18
                          TW
 07. OPERATING SYSTEM DESIGN LAB. PR
                                 50
 08. INFORMATION SYSTEMS DESIGN LAB. TW
                                    10 15
 09. INFORMATION SYSTEMS DESIGN LAB. OR
                                 50
                                    20
                                        28
                                25
 10. NETWORK LABORATORY
                            TW
                                    10
                                        16
                                           Р
                        OR
                                50 20
                                        39
 11. NETWORK LABORATORY
 FIRST TERM TOTAL = 439/750.
ORDN. 1 MARKS:
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  NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
     OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
T8058540 GUPTA VANI JITENDRA
                                         RITU
                                                         , 71045443M , , , PICT , T8058540
 01. OPERATING SYSTEM
 02. THEORY OF COMPUTATION PP
03. COMPUTER NETWORK TECHNOLOGY PP
                            PP 100
                                         73 P
                                     40
                                         77
                                100
                                     40
                                            Ρ
                           PP
 04. DATBASE MANAGEMENT SYSTEMS
                                 100
                                     40 72
                                            Р
 05. SOFTWARE ENGINEERING
                             PP
                                 100
                                     40 43
                                            Р
                          TW
                                     10 21
 06. OPERATING SYSTEM DESIGN LAB.
                                 25
 07. OPERATING SYSTEM DESIGN LAB. PR
                                 50
                                     20 42
                                 25
                                     10 20
 08. INFORMATION SYSTEMS DESIGN LAB. TW
 09. INFORMATION SYSTEMS DESIGN LAB. OR
                                 50 20 41 P
 10. NETWORK LABORATORY
                                 25 10 21 P
                   TW
 11. NETWORK LABORATORY
                           OR 50 20 44 P
 FIRST TERM TOTAL = 531/750.
ORDN. 1 MARKS :
T8058541 HIRAN PRANAV PRAMODKUMAR
                                         REKHA
                                                         , 71045447D , , PICT , T8058541
 01. OPERATING SYSTEM PP 100
02. THEORY OF COMPUTATION PP 100
03. COMPUTER NETWORK TECHNOLOGY PP 100
                             PP 100 40 40 P
                                     40 55 P
                                     40 50 P
 04. DATBASE MANAGEMENT SYSTEMS PP
                                100
                                     40 61 P
 05. SOFTWARE ENGINEERING
                            PP
                                100
                                     40 42
 07. OPERATING SYSTEM DESIGN LAB.
08. INFORMATION SYSTEM
                                 25
                                     10
                                         20
                            TW
                                 50
                                     20
                                         30 P
                            PR
 08. INFORMATION SYSTEMS DESIGN LAB. TW
                                 25
                                     10 20
 09. INFORMATION SYSTEMS DESIGN LAB. OR
                                 50
                                     20 41
                    TW
                                 25
                                     10 20 P
 10. NETWORK LABORATORY
                                50 20 36 P
 11. NETWORK LABORATORY
 FIRST TERM TOTAL = 436/750.
ORDN. 1 MARKS:
, 71045448B , , PICT , T8058542
 T8058542 HIRE PRATIK PARASHURAM
                                          MANGALA
 01. OPERATING SYSTEM PP 100
02. THEORY OF COMPUTATION PP 100
03. COMPUTER NETWORK TECHNOLOGY PP 100
 01. OPERATING SYSTEM
                             PP 100 40
                                    40
                                         56 P
                                     40 57
                                            Р
                             PP
                                100
                                     40
                                         52
                                            Р
 04. DATBASE MANAGEMENT SYSTEMS
                             PP
                                100
                                     40 40
                                            Р
 05. SOFTWARE ENGINEERING
 06. OPERATING SYSTEM DESIGN LAB.
                                 25
                                     10 20
                           TW
 07. OPERATING SYSTEM DESIGN LAB.
                            PR
                                 50
 08. INFORMATION SYSTEMS DESIGN LAB. TW
                                     10
 09. INFORMATION SYSTEMS DESIGN LAB. OR
                                 50
                                     20
                                         10
                                 25
 10. NETWORK LABORATORY
                             TW
                                     10
                                         18
                                            Р
                                 50 20
                                         36
 11. NETWORK LABORATORY
                         OR
 FIRST TERM TOTAL = 415/750.
ORDN. 1 MARKS:
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  NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
     OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
T8058543 INAMDAR MOHSIN IMTIYAZ
                                         SHAHENAZ , 71045449L , , PICT , T8058543
 01. OPERATING SYSTEM
 01. OPERATING SYSIEM
02. THEORY OF COMPUTATION
03. COMPUTER NETWORK TECHNOLOGY
                            PP 100
                                     40
                                         44 P
                                        40 P
                            PP
                                100
                                     40
 04. DATBASE MANAGEMENT SYSTEMS
                            PP
                                 100
                                     40
                                        45
                                            Р
 05. SOFTWARE ENGINEERING
                            PP
                                 100
                                     40
                                         21
                          TW
                                     10 19
 06. OPERATING SYSTEM DESIGN LAB.
                                 25
 07. OPERATING SYSTEM DESIGN LAB. PR
                                 50
                                     20 42
                                 25
                                     10 20
 08. INFORMATION SYSTEMS DESIGN LAB. TW
 09. INFORMATION SYSTEMS DESIGN LAB. OR
                                 50 20 37
 10. NETWORK LABORATORY
                                 25 10 22 P
                   TW
 11. NETWORK LABORATORY
                           OR
 FIRST TERM TOTAL = 386/750.
ORDN. 1 MARKS :
T8058544 INGALE KOMAL RAJENDRA
                                         USHA
                                                         , 71045450D , , PICT , T8058544
                            PP 100 40
 01. OPERATING SYSTEM PP 100 02. THEORY OF COMPUTATION PP 100
                                    40 73 P
 03. COMPUTER NETWORK TECHNOLOGY PP 100
                                     40 76 P
 04. DATBASE MANAGEMENT SYSTEMS PP
                                100
                                     40 62 P
                            PP
 05. SOFTWARE ENGINEERING
                                100
                                     40 46
 07. OPERATING SYSTEM DESIGN LAB.
08. INFORMATION SYSTEM
                                 25
                                     10
                                         23
                            TW
                                 50
                                     20
                                         39
                            PR
 08. INFORMATION SYSTEMS DESIGN LAB. TW
                                 25
                                     10
                                         23
 09. INFORMATION SYSTEMS DESIGN LAB. OR
                                 50
                                     20
                    TW
                                 25
                                     10 22
 10. NETWORK LABORATORY
                                            Р
                                 50 20 32
 11. NETWORK LABORATORY
 FIRST TERM TOTAL = 519/750.
ORDN. 1 MARKS :
, 71045451в    ,        ,          ,           , т8058545
 T8058545 INGOLE PRACHI VASANT
                                         NILIMA
 01. OPERATING SYSTEM PP 100
02. THEORY OF COMPUTATION PP 100
03. COMPUTER NETWORK TECHNOLOGY PP 100
                            PP 100 40
                                    40
                                         60 P
                                    40 66 P
 04. DATBASE MANAGEMENT SYSTEMS PP
                                100
                                     40 64
                                            Р
                            PP
                                100
                                     40 49
 05. SOFTWARE ENGINEERING
 06. OPERATING SYSTEM DESIGN LAB.
                                 25
                                     10 21 P
                           TW
 07. OPERATING SYSTEM DESIGN LAB.
                            PR
                                 50
                                     20 25
 08. INFORMATION SYSTEMS DESIGN LAB. TW
 09. INFORMATION SYSTEMS DESIGN LAB. OR
                                 50
                                     20
                                         35
                                 25
                                         22
 10. NETWORK LABORATORY
                            TW
                                     10
                                            Р
                                 50 20
                                         33
 11. NETWORK LABORATORY
                         OR
 FIRST TERM TOTAL = 474/750.
ORDN. 1 MARKS:
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     OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
T8058546 ISHAN AGARWAL
                                         MAMTA
                                                         , 71045452L , , , PICT , T8058546
 01. OPERATING SYSTEM
 01. OPERATING SYSTEM
02. THEORY OF COMPUTATION
03. COMPUTER NETWORK TECHNOLOGY
                            PP 100
                                     40 47 P
                                     40
                                         67
                            PP
                                100
 04. DATBASE MANAGEMENT SYSTEMS
                            PP
                                 100
                                     40 47
 05. SOFTWARE ENGINEERING
                            PP
                                 100
                                     40
                                         31
                          TW
                                     10 19
 06. OPERATING SYSTEM DESIGN LAB.
                                 25
 07. OPERATING SYSTEM DESIGN LAB. PR
                                 50
                                     20 41
                                 25
                                     10 19
 08. INFORMATION SYSTEMS DESIGN LAB. TW
 09. INFORMATION SYSTEMS DESIGN LAB. OR
                                 50 20 28 P
 10. NETWORK LABORATORY
                                 25 10 19 P
                   TW
 11. NETWORK LABORATORY
                           OR 50 20 37 P
 FIRST TERM TOTAL = 420/750.
ORDN. 1 MARKS :
T8058547 JADHAV DHANRAJ KHANDERAO
                                               , 71134960G , , PICT , T8058547
                                         CHHAYA
 01. OPERATING SYSTEM PP 100
02. THEORY OF COMPUTATION PP 100
03. COMPUTER NETWORK TECHNOLOGY PP 100
                            PP 100 40
                                         59 P
                                     40
                                        72 P
                                     40 58 P
 04. DATBASE MANAGEMENT SYSTEMS PP
                                100
                                     40 57 P
 05. SOFTWARE ENGINEERING
                            PP
                                100
                                     40
                                         60
 07. OPERATING SYSTEM DESIGN LAB.
08. INFORMATION SYSTEM
                                 25
                                     10
                                        22
                            TW
                                 50
                                     20 41 P
                            PR
 08. INFORMATION SYSTEMS DESIGN LAB. TW
                                     10 22
 09. INFORMATION SYSTEMS DESIGN LAB. OR
                                 50
                                     20 43
                    TW
                                 25
                                     10 22
 10. NETWORK LABORATORY
                                50 20 37
 11. NETWORK LABORATORY
 FIRST TERM TOTAL = 516/750.
ORDN. 1 MARKS:
JANABAI , 71134961E , , , PICT , T8058548
 T8058548 JADHAV MAYUR JAGANNATH
                             PP 100 40
 01. OPERATING SYSTEM
 01. OPERATING SYSTEM PP 100
02. THEORY OF COMPUTATION PP 100
03. COMPUTER NETWORK TECHNOLOGY PP 100
                                    40 72 P
                                     40 73 P
 04. DATBASE MANAGEMENT SYSTEMS PP 100
                                     40
                                         51 P
                            PP
                                100
                                     40 50
 05. SOFTWARE ENGINEERING
 06. OPERATING SYSTEM DESIGN LAB.
                                 25
                                     10 22 P
                           TW
 07. OPERATING SYSTEM DESIGN LAB. PR
                                 50
 08. INFORMATION SYSTEMS DESIGN LAB. TW
                                     10 22
 09. INFORMATION SYSTEMS DESIGN LAB. OR
                                 50
                                     20
                                         40
                                 25
                                         20
 10. NETWORK LABORATORY
                            TW
                                     10
                                            Р
                         OR
                                 50 20
                                         38
 11. NETWORK LABORATORY
 FIRST TERM TOTAL = 504/750.
ORDN. 1 MARKS:
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     OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
T8058549 JODHWANI SAHIL PRADEEP
                                         BABITA
                                                         , 71045462H , , PICT , T8058549
 01. OPERATING SYSTEM
02. THEORY OF COMPUTATION
03. COMPUTER NETWORK TECHNOLOGY
                             PP 100
                                         59 P
                                     40
                                         67
                            PP
                                100
                                     40
                                            Р
 04. DATBASE MANAGEMENT SYSTEMS
                            PP
                                 100
                                     40
                                         55
                                            Р
 05. SOFTWARE ENGINEERING
                            PP
                                 100
                                     40 40
                          TW
                                     10
                                        19
 06. OPERATING SYSTEM DESIGN LAB.
                                 25
 07. OPERATING SYSTEM DESIGN LAB. PR
                                 50
                                     20 37
                                 25
                                     10 19
 08. INFORMATION SYSTEMS DESIGN LAB. TW
 09. INFORMATION SYSTEMS DESIGN LAB. OR
                                 50 20 41 P
 10. NETWORK LABORATORY
                                 25 10 22 P
                   TW
 11. NETWORK LABORATORY
                           OR
                                 50 20 25 P
 FIRST TERM TOTAL = 464/750.
ORDN. 1 MARKS :
T8058550 JUVVADI MANOBHIRAM
                                         J VIJAYASHRI , 71057070J , , PICT , T8058550
                            PP 100
                                    40 49 P
 01. OPERATING SYSTEM PP 100 02. THEORY OF COMPUTATION PP 100
                                        43 P
                                     40
                          PP 100
 03. COMPUTER NETWORK TECHNOLOGY
                                     40 62 P
 04. DATBASE MANAGEMENT SYSTEMS
                           PP
                                100
                                     40 54 P
                            PP
 05. SOFTWARE ENGINEERING
                                100
                                     40
 07. OPERATING SYSTEM DESIGN LAB.
08. INFORMATION SYSTEM DESIGN LAB.
                                 25
                                     10
                                        18
                            TW
                                 50
                                     20 40 P
                            PR
 08. INFORMATION SYSTEMS DESIGN LAB. TW
                                 25
                                     10
                                        17
 09. INFORMATION SYSTEMS DESIGN LAB. OR
                                 50
                                     20 35
                     TW
                                 25
                                     10 18 P
 10. NETWORK LABORATORY
                                 50 20 39
 11. NETWORK LABORATORY
 FIRST TERM TOTAL = 431/750.
ORDN. 1 MARKS:
MANDAKINI , 71134962C , , PICT , T8058551
 T8058551 KADAM AMIT ASHOKRAO
 01. OPERATING SYSTEM PP 100
02. THEORY OF COMPUTATION PP 100
03. COMPUTER NETWORK TECHNOLOGY PP 100
 01. OPERATING SYSTEM
                            PP 100 40
                                    40 53 P
                                    40 54 P
                            PP
                                100
                                     40 49
                                            Р
 04. DATBASE MANAGEMENT SYSTEMS
                            PP
                                100
                                     40 45 P
 05. SOFTWARE ENGINEERING
 06. OPERATING SYSTEM DESIGN LAB.
                                 25
                                     10 21 P
                           TW
 07. OPERATING SYSTEM DESIGN LAB.
                            PR
                                 50
 08. INFORMATION SYSTEMS DESIGN LAB. TW
                                     10
                                         21
 09. INFORMATION SYSTEMS DESIGN LAB. OR
                                 50
                                     20
                                         40
                                 25
                                         22
 10. NETWORK LABORATORY
                            TW
                                     10
                                            Р
                                 50 20
                                         25
 11. NETWORK LABORATORY
                         OR
 FIRST TERM TOTAL = 403/750.
ORDN. 1 MARKS:
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     OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
T8058552 KADAM PRADNYA PRADEEP
                                         PRANITA , 71134963M , , PICT , T8058552
 01. OPERATING SYSTEM
02. THEORY OF COMPUTATION
03. COMPUTER NETWORK TECHNOLOGY
 01. OPERATING SYSTEM
                             PP 100
                                     40
                                         45 P
                                     40
                                         62 P
                            PP
                                100
 04. DATBASE MANAGEMENT SYSTEMS
                            PP
                                 100
                                     40
                                         62
                                            Р
 05. SOFTWARE ENGINEERING
                             PP
                                 100
                                     40
                                         58
                          TW
                                     10
                                         23
 06. OPERATING SYSTEM DESIGN LAB.
                                 25
 07. OPERATING SYSTEM DESIGN LAB. PR
                                 50
                                     20 35
                                 25
                                     10 23
 08. INFORMATION SYSTEMS DESIGN LAB. TW
 09. INFORMATION SYSTEMS DESIGN LAB. OR
                                 50 20 32
 10. NETWORK LABORATORY
                                 25 10 21 P
                   TW
 11. NETWORK LABORATORY
                           OR 50 20 45 P
 FIRST TERM TOTAL = 476/750.
ORDN. 1 MARKS :
T8058553 KADAM RAHUL DNYANESHWAR
                                         INDUBAI , 71134964K , , PICT , T8058553
 01. OPERATING SYSTEM PP 100
02. THEORY OF COMPUTATION PP 100
03. COMPUTER NETWORK TECHNOLOGY PP 100
                             PP 100 40 61 P
                                     40
                                         69 P
                                     40 78 P
 04. DATBASE MANAGEMENT SYSTEMS PP 100
                                     40
                                         67 P
                            PP
 05. SOFTWARE ENGINEERING
                                100
                                     40
                                         53
 07. OPERATING SYSTEM DESIGN LAB.
08. INFORMATION SYSTEM
                                 25
                                     10
                                         21
                            TW
                                 50
                                     20 41 P
                            PR
 08. INFORMATION SYSTEMS DESIGN LAB. TW
                                 25
                                     10
                                         20
 09. INFORMATION SYSTEMS DESIGN LAB. OR
                                 50
                                     20
                                         37
                    TW
                                 25
                                     10 22
 10. NETWORK LABORATORY
                                            Р
                                50 20 43 P
 11. NETWORK LABORATORY
 FIRST TERM TOTAL = 534/750.
ORDN. 1 MARKS:
SUNITA , 71134965H , PICT , T8058554
 T8058554 KAKADE POOJA DILIPRAO
                             PP 100 40
 01. OPERATING SYSTEM PP 100
02. THEORY OF COMPUTATION PP 100
03. COMPUTER NETWORK TECHNOLOGY PP 100
 01. OPERATING SYSTEM
                                    40
                                         64 P
                                     40 74
                                            Р
                             PP 100
                                     40
                                         58
                                            Р
 04. DATBASE MANAGEMENT SYSTEMS
                             PP
                                100
                                     40 55
 05. SOFTWARE ENGINEERING
 06. OPERATING SYSTEM DESIGN LAB.
                                 25
                                     10
                                        23 P
                           TW
 07. OPERATING SYSTEM DESIGN LAB. PR
                                 50
                                     20 37
 08. INFORMATION SYSTEMS DESIGN LAB. TW
                                     10
 09. INFORMATION SYSTEMS DESIGN LAB. OR
                                 50
                                     20
                                         32
                                 25
                                         22
 10. NETWORK LABORATORY
                             TW
                                     10
                                            Р
                                 50 20
 11. NETWORK LABORATORY
                         OR
                                         36
 FIRST TERM TOTAL = 507/750.
ORDN. 1 MARKS:
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     OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
T8058555 KALBANDE GAURI RAJENDRA
                                         PRABHA
                                               , 71134966F , , PICT , T8058555
 01. OPERATING SYSTEM
 01. OPERALING SYSTEM
02. THEORY OF COMPUTATION
03. COMPUTER NETWORK TECHNOLOGY
                            PP 100
                                    40
                                        60 P
                                        73
                                100
                                    40
                            PP
                                           Ρ
 04. DATBASE MANAGEMENT SYSTEMS
                            PP
                                100
                                    40
                                        60
                                           Р
 05. SOFTWARE ENGINEERING
                            PP
                                100
                                    40
                                        45
                          TW
                                    10 20
 06. OPERATING SYSTEM DESIGN LAB.
                                 25
 07. OPERATING SYSTEM DESIGN LAB. PR
                                 50
                                    20 42
                                 25
                                    10 22
 08. INFORMATION SYSTEMS DESIGN LAB. TW
 09. INFORMATION SYSTEMS DESIGN LAB. OR
                                 50 20 38
 10. NETWORK LABORATORY
                                 25 10 21 P
                   TW
 11. NETWORK LABORATORY
                          OR
                                 50 20 42 P
 FIRST TERM TOTAL = 506/750.
ORDN. 1 MARKS :
T8058556 KALRA JASBIRKAUR AMARJIT SINGH
                                        MEENA
                                                        , 71134967D , , PICT , T8058556
                            PP 100 40
 01. OPERATING SYSTEM
 02. THEORY OF COMPUTATION PP 100
                                    40
                                       51 P
 03. COMPUTER NETWORK TECHNOLOGY PP 100
                                    40
                                        68 P
 04. DATBASE MANAGEMENT SYSTEMS PP
                                100
                                    40
                                        50 P
                            PP
 05. SOFTWARE ENGINEERING
                                100
                                    40
                                        48
 07. OPERATING SYSTEM DESIGN LAB.
08. INFORMATION SYSTEM
                                 25
                                    10
                                        23
                            TW
                                 50
                                    20 44 P
                            PR
 08. INFORMATION SYSTEMS DESIGN LAB. TW
                                 25
                                    10
 09. INFORMATION SYSTEMS DESIGN LAB. OR
                                 50
                                    20
                                        33
                     TW
                                 25
                                    10 21 P
 10. NETWORK LABORATORY
                                 50 20 40
 11. NETWORK LABORATORY
 FIRST TERM TOTAL = 484/750.
ORDN. 1 MARKS:
, 71045466L , , PICT , T8058557
 T8058557 KAMBLE JITESH SHAMRAO
                                         PUSHPA
 01. OPERATING SYSTEM PP 100
02. THEORY OF COMPUTATION PP 100
03. COMPUTER NETWORK TECHNOLOGY PP 100
 01. OPERATING SYSTEM
                            PP 100 40
                                    40
                                        68 P
                                    40 78 P
                            PP
                                100
                                    40 51
                                           Р
 04. DATBASE MANAGEMENT SYSTEMS
                            PP
                                100
                                    40 45
                                           Р
 05. SOFTWARE ENGINEERING
 06. OPERATING SYSTEM DESIGN LAB.
                                 25
                                    10 21 P
                          TW
 07. OPERATING SYSTEM DESIGN LAB.
                            PR
                                 50
 08. INFORMATION SYSTEMS DESIGN LAB. TW
                                    10 20
 09. INFORMATION SYSTEMS DESIGN LAB. OR
                                 50
                                    20
                                        41
                                 25
                                        20
 10. NETWORK LABORATORY
                            TW
                                    10
                                           Р
                                 50 20
                                        45
 11. NETWORK LABORATORY
                         OR
 FIRST TERM TOTAL = 505/750.
ORDN. 1 MARKS:
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     OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
T8058558 KARACHIWALA HAMZA SHABBIR
                                        DURRIYA , 71045470J , , PICT , T8058558
 01. OPERATING SYSTEM
 02. THEORY OF COMPUTATION
                            PP 100
                                        74 P
                                    40
                                        71
                            PP
                               100
                                    40
 03. COMPUTER NETWORK TECHNOLOGY
                                          Ρ
 04. DATBASE MANAGEMENT SYSTEMS
                           PP
                                100
                                    40
                                       58
                                          Р
 05. SOFTWARE ENGINEERING
                            PP
                                100
                                    40 44
                                           Р
                          TW
                                    10 18
 06. OPERATING SYSTEM DESIGN LAB.
                                25
 07. OPERATING SYSTEM DESIGN LAB. PR
                                50
                                    20 42
                                25
                                   10 19
 08. INFORMATION SYSTEMS DESIGN LAB. TW
 09. INFORMATION SYSTEMS DESIGN LAB. OR
                                50 20 41 P
 10. NETWORK LABORATORY
                                25 10 19 P
                   TW
 11. NETWORK LABORATORY
                          OR
 FIRST TERM TOTAL = 495/750.
ORDN. 1 MARKS :
KAVITA , 71045474M , , PICT , T8058559
 T8058559 KARISHMA GARG
                           PP 100
 01. OPERATING SYSTEM
 01. OPERATING SYSTEM PP 100 02. THEORY OF COMPUTATION PP 100
                                    40
                                        59 P
 03. COMPUTER NETWORK TECHNOLOGY PP 100
                                    40
                                        56 P
 04. DATBASE MANAGEMENT SYSTEMS
                          PP
                               100
                                    40 58 P
 05. SOFTWARE ENGINEERING
                           PP
                               100
                                    40
 07. OPERATING SYSTEM DESIGN LAB.
08. INFORMATION SYSTEM
                                25
                                    10
                                       23
                           TW
                                50
                                    20 42
                           PR
 08. INFORMATION SYSTEMS DESIGN LAB. TW
                                25
                                    10
                                        23
 09. INFORMATION SYSTEMS DESIGN LAB. OR
                                50
                                    20
                    TW
                                25
                                    10 22
 10. NETWORK LABORATORY
                                50 20 40
 11. NETWORK LABORATORY
 FIRST TERM TOTAL = 467/750.
ORDN. 1 MARKS:
, 71045478D , , PICT , T8058560
 T8058560 KAZI SANA NAFIS
                                        FARJANA
 01. OPERATING SYSTEM PP 100 40
02. THEORY OF COMPUTATION PP 100 40
03. COMPUTER NETWORK TECHNOLOGY PP 100 40
                                   40 73 P
                                   40 72
                                           Р
                            PP
                               100
                                    40 59
                                           Р
 04. DATBASE MANAGEMENT SYSTEMS
                            PP
                               100
                                    40 45 P
 05. SOFTWARE ENGINEERING
 06. OPERATING SYSTEM DESIGN LAB.
                                25
                                    10 23
                          TW
 07. OPERATING SYSTEM DESIGN LAB. PR
                                50
 08. INFORMATION SYSTEMS DESIGN LAB. TW
                                    10
 09. INFORMATION SYSTEMS DESIGN LAB. OR
                                50
                                    20
                                        31
                                25
                                       22
 10. NETWORK LABORATORY
                            TW
                                    10
                                           Р
                                50 20
                                       23
 11. NETWORK LABORATORY
                        OR
 FIRST TERM TOTAL = 479/750.
ORDN. 1 MARKS:
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     OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
T8058561 KELKAR ANUJA MILIND
                                        SUPRIYA , 71045480F , , PICT , T8058561
 01. OPERATING SYSTEM
02. THEORY OF COMPUTATION PP
03. COMPUTER NETWORK TECHNOLOGY PP
 O1. OPERATING SYSTEM
                            PP 100
                                        58 P
                                    40
                                        67
                                100
                                    40
                                           Р
 04. DATBASE MANAGEMENT SYSTEMS
                           PP
                                100
                                    40
                                        60
                                           Р
 05. SOFTWARE ENGINEERING
                            PP
                                100
                                    40
                                        56
                          TW
                                    10 23
 06. OPERATING SYSTEM DESIGN LAB.
                                25
 07. OPERATING SYSTEM DESIGN LAB. PR
                                 50
                                    20 40
                                25
                                    10 22
 08. INFORMATION SYSTEMS DESIGN LAB. TW
 09. INFORMATION SYSTEMS DESIGN LAB. OR
                                 50 20 42
 10. NETWORK LABORATORY
                                25 10 23 P
                   TW
 11. NETWORK LABORATORY
                          OR
 FIRST TERM TOTAL = 514/750.
ORDN. 1 MARKS :
SUNITA , 70925476J , , PICT , T8058562
 T8058562 KHANDAGALE SURAJ PANDURANG
                            PP 100 40 45 P
 01. OPERATING SYSTEM
 02. THEORY OF COMPUTATION PP 100
                                    40
                                       57 P
 03. COMPUTER NETWORK TECHNOLOGY
                           PP 100
                                    40 62 P
 04. DATBASE MANAGEMENT SYSTEMS PP
                                100
                                    40 51 P
 05. SOFTWARE ENGINEERING
                            PP
                                100
                                    40
 07. OPERATING SYSTEM DESIGN LAB.
08. INFORMATION SYSTEM
                                25
                                    10
                                        19
                           TW
                                 50
                                    20
                                        28
                           PR
 08. INFORMATION SYSTEMS DESIGN LAB. TW
                                 25
                                    10 19
 09. INFORMATION SYSTEMS DESIGN LAB. OR
                                 50
                                    20 40
                   TW
                                25
                                    10 18 P
 10. NETWORK LABORATORY
                                50 20 37
 11. NETWORK LABORATORY
 FIRST TERM TOTAL = 433/750.
ORDN. 1 MARKS:
, 71045486E , , PICT , T8058563
 T8058563 KOTHAWALE AKASH SANJAY
                                         ANITA
                            PP 100 40
 01. OPERATING SYSTEM
 01. OPERATING SYSTEM PP 100
02. THEORY OF COMPUTATION PP 100
03. COMPUTER NETWORK TECHNOLOGY PP 100
                                   40 61 P
                                    40 69
 04. DATBASE MANAGEMENT SYSTEMS PP
                                100
                                    40 42
                                           Р
                            PP
                                100
                                    40 51
                                           Р
 05. SOFTWARE ENGINEERING
 06. OPERATING SYSTEM DESIGN LAB.
                                25
                                    10
                                       23 P
                          TW
 07. OPERATING SYSTEM DESIGN LAB.
                           PR
                                 50
                                    20 26
 08. INFORMATION SYSTEMS DESIGN LAB. TW
                                    10
                                       22
 09. INFORMATION SYSTEMS DESIGN LAB. OR
                                 50
                                    20
                                        41
                                25
                                        22
 10. NETWORK LABORATORY
                            TW
                                    10
                                           Р
                                50 20
                                        37
 11. NETWORK LABORATORY
                        OR
 FIRST TERM TOTAL = 468/750.
ORDN. 1 MARKS:
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  NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
     OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
T8058564 KULKARNI AMITA DILIP
                                        MEGHA
                                                        , 71045492K , , , PICT , T8058564
 01. OPERATING SYSTEM
02. THEORY OF COMPUTATION
03. COMPUTER NETWORK TECHNOLOGY
 01. OPERATING SYSTEM
                            PP 100
                                        69 P
                                    40
                                100
                                    40
                                        64 P
                            PP
 04. DATBASE MANAGEMENT SYSTEMS
                            PP
                                100
                                    40
                                        57
                                           Р
 05. SOFTWARE ENGINEERING
                            PP
                                100
                                    40
                                        40
                          TW
                                    10
                                        23
 06. OPERATING SYSTEM DESIGN LAB.
                                25
 07. OPERATING SYSTEM DESIGN LAB. PR
                                 50
                                    20
                                25
                                    10 23
 08. INFORMATION SYSTEMS DESIGN LAB. TW
 09. INFORMATION SYSTEMS DESIGN LAB. OR
                                 50 20 31 P
 10. NETWORK LABORATORY
                                25 10 21 P
                   TW
 11. NETWORK LABORATORY
                          OR
                                50 20 33 P
 FIRST TERM TOTAL = 470/750.
ORDN. 1 MARKS :
T8058565 KULKARNI PRAMOD SHIVAJIRAO
                                        SUMATI , 71045672H , , PICT , T8058565
                            PP 100 40 40 P
 01. OPERATING SYSTEM
 02. THEORY OF COMPUTATION PP 100
                                    40
                                       40 P
 03. COMPUTER NETWORK TECHNOLOGY
                           PP 100
                                    40 61 P
 04. DATBASE MANAGEMENT SYSTEMS PP
                                100
                                    40 49 P
                                        40
 05. SOFTWARE ENGINEERING
                            PP
                                100
                                    40
 07. OPERATING SYSTEM DESIGN LAB.
08. INFORMATION SYSTEM
                                25
                                    10
                                        12
                            TW
                                 50
                                    20 06 F
                           PR
 08. INFORMATION SYSTEMS DESIGN LAB. TW
                                 25
                                    10
                                        10
 09. INFORMATION SYSTEMS DESIGN LAB. OR
                                 50
                                    20 25
                   TW
                                25
                                    10 10 P
 10. NETWORK LABORATORY
                                50 20 12 F
 11. NETWORK LABORATORY
 FIRST TERM TOTAL = 317/750.
ORDN. 1 MARKS:
, 71045502L , , PICT , T8058566
 T8058566 LAKHEPATIL AJINKYA RAMRAJE
                                        HEMLATA
 01. OPERATING SYSTEM
                            PP 100 40
 01. OPERATING SYSTEM
02. THEORY OF COMPUTATION
03. COMPUTER NETWORK TECHNOLOGY
                            PP 100
                                   40 65 P
                            PP 100
                                    40 67
                                           Р
                            PP
                                100
                                    40 63
                                           Р
 04. DATBASE MANAGEMENT SYSTEMS
                            PP
                                100
                                    40 44
 05. SOFTWARE ENGINEERING
 06. OPERATING SYSTEM DESIGN LAB.
                                25
                                    10 24
                          TW
 07. OPERATING SYSTEM DESIGN LAB.
                           PR
                                 50
 08. INFORMATION SYSTEMS DESIGN LAB. TW
                                    10
 09. INFORMATION SYSTEMS DESIGN LAB. OR
                                 50
                                    20
                                        35
                                25
                                        22
 10. NETWORK LABORATORY
                            TW
                                    10
                                           Р
                                50 20
 11. NETWORK LABORATORY
                        OR
                                        36
 FIRST TERM TOTAL = 490/750.
ORDN. 1 MARKS:
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     OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
T8058567 LONKAR GORAKH SUBHASH
                                          SHOBHA
                                                , 71134968B , , PICT , T8058567
 01. OPERATING SYSTEM
02. THEORY OF COMPUTATION
03. COMPUTER NETWORK TECHNOLOGY
 01. OPERATING SYSTEM
                             PP 100
                                     40
                                         66 P
                            PP
                                 100
                                     40
                                         78
                                            Ρ
 04. DATBASE MANAGEMENT SYSTEMS
                            PP
                                 100
                                     40
                                         58
                                            Р
 05. SOFTWARE ENGINEERING
                             PP
                                 100
                                     40
                                         56
                                            Р
                           TW
                                     10 24
 06. OPERATING SYSTEM DESIGN LAB.
                                 25
 07. OPERATING SYSTEM DESIGN LAB. PR
                                 50
                                     20 42
                                 25
                                     10 24
 08. INFORMATION SYSTEMS DESIGN LAB. TW
 09. INFORMATION SYSTEMS DESIGN LAB. OR
                                 50 20 39
 10. NETWORK LABORATORY
                                 25 10 24 P
                   TW
 11. NETWORK LABORATORY
                           OR
 FIRST TERM TOTAL = 536/750.
ORDN. 1 MARKS :
T8058568 MAHAJAN AKSHATA DEVENDRA
                                         JAISHREE , 71045507M , , PICT , T8058568
 01. OPERATING SYSTEM PP 100
02. THEORY OF COMPUTATION PP 100
03. COMPUTER NETWORK TECHNOLOGY PP 100
                             PP 100 40
                                        51 P
                                     40
                                        75 P
                                     40 66 P
 04. DATBASE MANAGEMENT SYSTEMS PP
                                100
                                     40
                                         62 P
                            PP
 05. SOFTWARE ENGINEERING
                                 100
                                     40
                                         53
 07. OPERATING SYSTEM DESIGN LAB.
08. INFORMATION SYSTEM
                                 25
                                     10
                                         21
                            TW
                                 50
                                     20 41 P
                            PR
 08. INFORMATION SYSTEMS DESIGN LAB. TW
                                 25
                                     10 20
 09. INFORMATION SYSTEMS DESIGN LAB. OR
                                 50
                                     20 40 P
                    TW
                                     10 22 P
                                 25
 10. NETWORK LABORATORY
                                50 20 44
 11. NETWORK LABORATORY
 FIRST TERM TOTAL = 516/750.
ORDN. 1 MARKS:
, 71045509н , , , ріст , т8058570
 T8058570 MAHALPURE SHRUTI SATISH
                                          SUSHMA
                             PP 100 40
 01. OPERATING SYSTEM
 01. OPERATING SYSTEM PP 100
02. THEORY OF COMPUTATION PP 100
03. COMPUTER NETWORK TECHNOLOGY PP 100
                                    40 53 P
                                     40 65 P
 04. DATBASE MANAGEMENT SYSTEMS PP
                                100
                                     40 56
                                            Р
                             PP
                                 100
                                     40 45
                                            Р
 05. SOFTWARE ENGINEERING
 06. OPERATING SYSTEM DESIGN LAB.
                                 25
                                     10 24 P
                           TW
 07. OPERATING SYSTEM DESIGN LAB.
                            PR
                                 50
                                     20
 08. INFORMATION SYSTEMS DESIGN LAB. TW
                                     10
 09. INFORMATION SYSTEMS DESIGN LAB. OR
                                 50
                                     20
                                         31
                                 25
                                         22
 10. NETWORK LABORATORY
                             TW
                                     10
                                            Р
                                 50 20
                                         40
 11. NETWORK LABORATORY
                         OR
 FIRST TERM TOTAL = 463/750.
ORDN. 1 MARKS:
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NOTE: FIRST LINE : SEAT NO., NAME OF OTHER LINES: HEAD OF PASSING,	F THE	CAND	IDATE	, MO	THER, PERMAN	NENT REG	. NO.,	PREVIOU	S SEAT N	o., co	LLEGE,	SEAT NO.
T8058571 MARDA SAKSHI BRIJMOHAN				JY	ті		, 710455	21G	,	,	PICT	, т805857
01. OPERATING SYSTEM 02. THEORY OF COMPUTATION 03. COMPUTER NETWORK TECHNOLOGY 04. DATBASE MANAGEMENT SYSTEMS 05. SOFTWARE ENGINEERING 06. OPERATING SYSTEM DESIGN LAB. 07. OPERATING SYSTEM DESIGN LAB. 08. INFORMATION SYSTEMS DESIGN LAB. 09. INFORMATION SYSTEMS DESIGN LAB. 10. NETWORK LABORATORY 11. NETWORK LABORATORY	PP PP PP PP TW PR TW OR TW OR	100 100 100 100 100 25 50 25 50 25 50	40 40 40 40 10 20 10 20 10 20	40 57 61 55 41 22 38 22 39 21 32	P P P P P P P P P P P P P P P							
FIRST TERM TOTAL = 450/750. RDN. 1 MARKS :												
T8058572 MARWA MAYUR RAJESH				 AS	 HA			 22 F			 PICT	
01. OPERATING SYSTEM 02. THEORY OF COMPUTATION 03. COMPUTER NETWORK TECHNOLOGY 04. DATBASE MANAGEMENT SYSTEMS 05. SOFTWARE ENGINEERING 06. OPERATING SYSTEM DESIGN LAB. 07. OPERATING SYSTEM DESIGN LAB. 08. INFORMATION SYSTEMS DESIGN LAB. 09. INFORMATION SYSTEMS DESIGN LAB. 10. NETWORK LABORATORY	PP PP PP PP TW PR TW OR TW OR	100 100 100 100 100 25 50 25 50 25	40 40 40 40 10 20 10 20 10 20	40 29 58 45 40 11 28 12 AA 10	P F P P P P F F							
FIRST TERM TOTAL = 295/750. RDN. 1 MARKS :												
T8058573 MAYANK SINGH				 AR			 , 710455	 24м			 PICT	
01. OPERATING SYSTEM 02. THEORY OF COMPUTATION 03. COMPUTER NETWORK TECHNOLOGY 04. DATBASE MANAGEMENT SYSTEMS 05. SOFTWARE ENGINEERING 06. OPERATING SYSTEM DESIGN LAB. 07. OPERATING SYSTEM DESIGN LAB. 08. INFORMATION SYSTEMS DESIGN LAB. 09. INFORMATION SYSTEMS DESIGN LAB. 10. NETWORK LABORATORY	PP PP PP PP TW PR TW OR TW OR	100 100 100 100 100 25 50 25 50 25	40 40 40 40 10 20 10 20 10 20	40 55 45 61 47 17 37 17 24 16 39	P P P P P P P P P P P P P P P P P P P							
FIRST TERM TOTAL = 408/750. RDN. 1 MARKS :												

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                                                                              PAGE NO. 25 ( 500)
  NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
     OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
T8058574 MOHAMMED AAQUIB ANSARI MOHAMMED YUSUF SUFIYA , 71045526H , , PICT , T8058574
 01. OPERATING SYSTEM
 02. THEORY OF COMPUTATION
                               100
                                    40
                                        40 P
                                100
                                    40
                                       45 P
 03. COMPUTER NETWORK TECHNOLOGY
                            PP
 04. DATBASE MANAGEMENT SYSTEMS
                           PP
                                100
                                    40
                                       40
                                          Р
 05. SOFTWARE ENGINEERING
                            PP
                                100
                                    40
                                       40
                          TW
                                    10 10 P
 06. OPERATING SYSTEM DESIGN LAB.
                                25
 07. OPERATING SYSTEM DESIGN LAB. PR
                                50
                                    20 AA F
                                25
                                   10 10 P
 08. INFORMATION SYSTEMS DESIGN LAB. TW
 09. INFORMATION SYSTEMS DESIGN LAB. OR
                                50 20 AA F
 10. NETWORK LABORATORY
                                25 10 10 P
                   TW
 11. NETWORK LABORATORY
                          OR
                                50 20 11 F
 FIRST TERM TOTAL = 246/750.
ORDN. 1 MARKS :
T8058575 MULAY AMIT YESHWANT
                                        SNEHAL
                                              , 71045530F , , PICT , T8058575
                            PP 100 40
 01. OPERATING SYSTEM PP 100 02. THEORY OF COMPUTATION PP 100
                                    40
                                        52 P
 03. COMPUTER NETWORK TECHNOLOGY
                         PP 100
                                    40 58 P
 04. DATBASE MANAGEMENT SYSTEMS
                          PP
                               100
                                    40 48 P
                           PP
 05. SOFTWARE ENGINEERING
                               100
                                    40
                                        46
 07. OPERATING SYSTEM DESIGN LAB.
08. INFORMATION SYSTEM
                                25
                                    10
                                       15
                           TW
                                50
                                    20 05 F
                           PR
 08. INFORMATION SYSTEMS DESIGN LAB. TW
                                25
                                    10
                                       15
 09. INFORMATION SYSTEMS DESIGN LAB. OR
                                50
                                    20
                                      10 F
                    TW
                                25
                                    10 14 P
 10. NETWORK LABORATORY
                                50 20 12
 11. NETWORK LABORATORY
 FIRST TERM TOTAL = 325/750.
ORDN. 1 MARKS:
, 71134969L , , PICT , T8058576
 T8058576 MULEY PRASAD MUKUNDRAO
                                        SUSHMA
 01. OPERATING SYSTEM
02. THEORY OF COMPUTATION
03. COMPUTER NETWORK TECHNOLOGY
 01. OPERATING SYSTEM
                            PP 100 40
                            PP 100
                                   40
                                        58 P
                            PP 100
                                   40 70 P
                            PP
                               100
                                    40 55
                                          Р
 04. DATBASE MANAGEMENT SYSTEMS
                            PP
                               100
                                    40 46
 05. SOFTWARE ENGINEERING
 06. OPERATING SYSTEM DESIGN LAB.
                                25
                                    10 23
                          TW
 07. OPERATING SYSTEM DESIGN LAB.
                           PR
                                50
 08. INFORMATION SYSTEMS DESIGN LAB. TW
                                    10 23
 09. INFORMATION SYSTEMS DESIGN LAB. OR
                                50
                                    20
                                        41
                                25
                                       22
 10. NETWORK LABORATORY
                            TW
                                    10
                                           Р
                                50 20
                                        40
 11. NETWORK LABORATORY
                         OR
 FIRST TERM TOTAL = 473/750.
ORDN. 1 MARKS:
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UNIVERSITY OF PUNE ,T.E. (2008 PAT) (INFORMATION TECHNOLOGY) EXAMINATION NOV. 2011
   DATE : 02 MAR. 2012 CENTRÉ : PUNE INSTITUTE OF COMPUTER TECHNOLÓGY, PUNE.
                                                                               PAGE NO. 26 ( 501)
NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
     OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
T8058578 MURKUTE DEVYANI BALASAHEB
                                        KALPANA , 71134971B , , PICT , T8058578
 01. OPERATING SYSTEM
02. THEORY OF COMPUTATION
WETWORK TECHNOLOGY
                            PP 100
                                        50 P
                                    40
                            PP
                                        65 P
                                100
                                    40
 04. DATBASE MANAGEMENT SYSTEMS
                           PP
                                100
                                    40 48
                                           Р
 05. SOFTWARE ENGINEERING
                            PP
                                100
                                    40 47
                          TW
                                    10
 06. OPERATING SYSTEM DESIGN LAB.
                                25
 07. OPERATING SYSTEM DESIGN LAB. PR
                                 50
                                    20 38
                                25
                                    10 21
 08. INFORMATION SYSTEMS DESIGN LAB. TW
 09. INFORMATION SYSTEMS DESIGN LAB. OR
                                 50 20 20 P
 10. NETWORK LABORATORY TW
                                25 10 21 P
 11. NETWORK LABORATORY
                          OR 50 20 38 P
 FIRST TERM TOTAL = 439/750.
ORDN. 1 MARKS :
T8058579 NAIKNAWARE UTKARSH MARUTI
                                        MAHANANDA , 71045538M , , PICT , T8058579
                            PP 100 40 40 P
 01. OPERATING SYSTEM
 01. OPERATING SYSTEM PP 100 02. THEORY OF COMPUTATION PP 100
                                    40
                                       48 P
 03. COMPUTER NETWORK TECHNOLOGY PP 100
                                    40 52 P
 04. DATBASE MANAGEMENT SYSTEMS PP
                                100
                                    40 50 P
                           PP
 05. SOFTWARE ENGINEERING
                                100
                                    40
 07. OPERATING SYSTEM DESIGN LAB.
08. INFORMATION SYSTEM
                                25
                                    10
                                        23
                           TW
                                 50
                                    20 32
                           PR
 08. INFORMATION SYSTEMS DESIGN LAB. TW
                                 25
                                    10
                                        20
 09. INFORMATION SYSTEMS DESIGN LAB. OR
                                 50
                                    20
                                        30
                   TW
                                25
                                    10 22
 10. NETWORK LABORATORY
                                           Р
                               50 20 35 P
 11. NETWORK LABORATORY
 FIRST TERM TOTAL = 413/750.
ORDN. 1 MARKS:
, 71134972L , , PICT , T8058580
 T8058580 NIKAM SONALI JIVAN
                                         ARUNA
 01. OPERATING SYSTEM PP 100 40 02. THEORY OF COMPUTATION PP 100 40 03. COMPUTER NETWORK TECHNOLOGY PP 100 40
 01. OPERATING SYSTEM
                            PP 100 40
                                    40 74 P
                            PP 100
                                    40 68 P
 04. DATBASE MANAGEMENT SYSTEMS
                            PP
                                100
                                    40 60
 05. SOFTWARE ENGINEERING
 06. OPERATING SYSTEM DESIGN LAB.
                                25
                                    10 24
                          TW
 07. OPERATING SYSTEM DESIGN LAB. PR
                                 50
 08. INFORMATION SYSTEMS DESIGN LAB. TW
                                       21
                                        27
 09. INFORMATION SYSTEMS DESIGN LAB. OR
                                 50
                                    20
                   TW
OR
                                25
                                        23
 10. NETWORK LABORATORY
                                    10
                                           Р
                                50 20
                                        25
 11. NETWORK LABORATORY
 FIRST TERM TOTAL = 483/750.
ORDN. 1 MARKS:
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   DATE : 02 MAR. 2012 CENTRÉ : PÛNE INSTITÛTE OF COMPUTER TECHNOLÔGY, PUNE.
                                                                                PAGE NO. 27 ( 502)
NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
     OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
T8058581 NILESH D PHADTARE
                                         KUNDA
                                                         , 70925526J , , PICT , T8058581
 01. OPERATING SYSTEM
 01. OPERATING SYSTEM
02. THEORY OF COMPUTATION
03. COMPUTER NETWORK TECHNOLOGY
                            PP 100
                                     40
                                        45 P
                            PP
                                100
                                     40 45 P
 04. DATBASE MANAGEMENT SYSTEMS
                            PP
                                100
                                     40
                                        40 P
 05. SOFTWARE ENGINEERING
                            PP
                                100
                                     40
                                        31
                          TW
                                     10
 06. OPERATING SYSTEM DESIGN LAB.
                                 25
 07. OPERATING SYSTEM DESIGN LAB. PR
                                 50
                                     20 28
                                 25
                                    10 21
 08. INFORMATION SYSTEMS DESIGN LAB. TW
 09. INFORMATION SYSTEMS DESIGN LAB. OR
                                 50 20 31 P
 10. NETWORK LABORATORY
                                 25 10 20 P
                   TW
 11. NETWORK LABORATORY
                           OR
                                 50 20 32 P
 FIRST TERM TOTAL = 376/750.
ORDN. 1 MARKS :
T8058582 NIMBALKAR ABHIJIT SHIRISH
                                         SUREKHA
                                                , 71057179」 , , PICT , T8058582
                            PP 100 40 53 P
 01. OPERATING SYSTEM
 01. OPERATING SYSTEM PP 100 02. THEORY OF COMPUTATION PP 100
                                     40
                                        51 P
 03. COMPUTER NETWORK TECHNOLOGY
                            PP 100
                                     40
                                        68 P
 04. DATBASE MANAGEMENT SYSTEMS PP
                                100
                                     40 57
 05. SOFTWARE ENGINEERING
                            PP
                                100
                                     40
                                        58
 07. OPERATING SYSTEM DESIGN LAB.
08. INFORMATION SYSTEM
                                 25
                                     10
                                        22
                            TW
                                 50
                                     20 38
                            PR
 08. INFORMATION SYSTEMS DESIGN LAB. TW
                                 25
                                     10 22
 09. INFORMATION SYSTEMS DESIGN LAB. OR
                                 50
                                     20 40
                    TW
                                 25
                                    10 21 P
 10. NETWORK LABORATORY
                                 50 20 40 P
 11. NETWORK LABORATORY
 FIRST TERM TOTAL = 492/750.
ORDN. 1 MARKS:
YI MALIVATTEY , 70925519F , , PICT , T8058583
 T8058583 NY GARIN
 01. OPERATING SYSTEM PP 100
02. THEORY OF COMPUTATION PP 100
03. COMPUTER NETWORK TECHNOLOGY PP 100
                                    40
                                        23 F
                                    40 AA F
                                    40 46 P
                            PP 100
                                     40 40
                                           Р
 04. DATBASE MANAGEMENT SYSTEMS
                            PP
                                100
                                     40 30 F
 05. SOFTWARE ENGINEERING
 06. OPERATING SYSTEM DESIGN LAB.
                                 25
                                     10 19
                           TW
 07. OPERATING SYSTEM DESIGN LAB.
                            PR
                                 50
                                     20 27
 08. INFORMATION SYSTEMS DESIGN LAB. TW
                                     10
 09. INFORMATION SYSTEMS DESIGN LAB. OR
                                 50
                                     20
                                        31
                                 25
 10. NETWORK LABORATORY
                            TW
                                     10
                                        18
                                           Р
                                 50 20
                                        08
 11. NETWORK LABORATORY
                         OR
 FIRST TERM TOTAL = 281/750.
ORDN. 1 MARKS:
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02. THEORY OF COMPUTATION
NETWORK TECHNOLOGY PP 100 40 P 40 PP 60 P 100 40 04. DATBASE MANAGEMENT SYSTEMS PP 100 40 50 Р 05. SOFTWARE ENGINEERING PP 100 40 48 TW 10 19 06. OPERATING SYSTEM DESIGN LAB. 25 07. OPERATING SYSTEM DESIGN LAB. PR 50 20 38 25 10 18 08. INFORMATION SYSTEMS DESIGN LAB. TW 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 15 F 10. NETWORK LABORATORY 25 10 19 P TW 11. NETWORK LABORATORY OR FIRST TERM TOTAL = 418/750. ORDN. 1 MARKS : T8058585 PANSARE ROHAN SAMPAT MANGAL PP 100 40 50 P 01. OPERATING SYSTEM 01. OPERATING SYSTEM PP 100 02. THEORY OF COMPUTATION PP 100 40 49 P 03. COMPUTER NETWORK TECHNOLOGY PP 100 40 69 P 04. DATBASE MANAGEMENT SYSTEMS PP 100 40 60 P PP 05. SOFTWARE ENGINEERING 100 40 07. OPERATING SYSTEM DESIGN LAB.
08. INFORMATION SYSTEM 25 10 22 TW 50 20 39 PR 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 20 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 28 TW 25 10 20 P 10. NETWORK LABORATORY 50 20 10 F 11. NETWORK LABORATORY FIRST TERM TOTAL = 437/750. ORDN. 1 MARKS: , 71045555M , , PICT , T8058586 T8058586 PARADKAR SUPRIYA SUNIL CHITRA PP 100 40 01. OPERATING SYSTEM 01. OPERATING SYSTEM PP 100
02. THEORY OF COMPUTATION PP 100
03. COMPUTER NETWORK TECHNOLOGY PP 100 40 65 P 40 61 P 04. DATBASE MANAGEMENT SYSTEMS PP 100 40 51 Р PP 100 40 45 Р 05. SOFTWARE ENGINEERING 06. OPERATING SYSTEM DESIGN LAB. 25 10 21 P TW 07. OPERATING SYSTEM DESIGN LAB. PR 50 20 38 08. INFORMATION SYSTEMS DESIGN LAB. TW 27 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 25 10. NETWORK LABORATORY TW 10 21 Р 50 20 30 11. NETWORK LABORATORY OR FIRST TERM TOTAL = 449/750. ORDN. 1 MARKS:

UNIVERSITY OF PUNE ,T.E. (2008 PAT) (INFORMATION TECHNOLOGY) EXAMINATION NOV. 2011 DATE : 02 MAR. 2012 CENTRÉ : PUNE INSTITUTE OF COMPUTER TECHNOLÓGY, PUNE. PAGE NO. 29 (504) NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER T8058587 PARAKH KUNAL KAILAS MEENA , 71045556K , , PICT , T8058587 O1. OPERATING SYSTEM 02. THEORY OF COMPUTATION PP 100 56 P 40 79 P 100 40 03. COMPUTER NETWORK TECHNOLOGY PP 04. DATBASE MANAGEMENT SYSTEMS PP 100 40 56 Р 05. SOFTWARE ENGINEERING PP 100 40 53 Р TW 10 21 06. OPERATING SYSTEM DESIGN LAB. 25 07. OPERATING SYSTEM DESIGN LAB. PR 50 20 42 25 10 20 08. INFORMATION SYSTEMS DESIGN LAB. TW 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 32 10. NETWORK LABORATORY 25 10 21 P TW 11. NETWORK LABORATORY OR 50 20 38 P FIRST TERM TOTAL = 494/750. ORDN. 1 MARKS : T8058588 PAREKH SONALI SUNIL REKHA , 71045558F , , , PICT , T8058588 PP 100 40 48 P 01. OPERATING SYSTEM 01. OPERATING SYSTEM PP 100 02. THEORY OF COMPUTATION PP 100 40 47 P PP 100 03. COMPUTER NETWORK TECHNOLOGY 40 67 P 04. DATBASE MANAGEMENT SYSTEMS PP 100 40 54 P 05. SOFTWARE ENGINEERING PP 100 40 07. OPERATING SYSTEM DESIGN LAB.
08. INFORMATION SYSTEM 25 10 18 TW 50 20 43 PR 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 19 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 26 TW 25 10 18 10. NETWORK LABORATORY Р 50 20 35 11. NETWORK LABORATORY FIRST TERM TOTAL = 436/750. ORDN. 1 MARKS: , 71045560н , , , РІСТ , Т8058589 T8058589 PARTH SARTHI PIPLANI SUREKHA PP 100 40 01. OPERATING SYSTEM PP 100
02. THEORY OF COMPUTATION PP 100
03. COMPUTER NETWORK TECHNOLOGY PP 100 01. OPERATING SYSTEM 40 40 P 40 40 P PP 100 40 40 Р 04. DATBASE MANAGEMENT SYSTEMS PP 100 40 41 05. SOFTWARE ENGINEERING 06. OPERATING SYSTEM DESIGN LAB. 25 10 16 P TW 07. OPERATING SYSTEM DESIGN LAB. PR 50 08. INFORMATION SYSTEMS DESIGN LAB. TW 10 10 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 AA 25 12 10. NETWORK LABORATORY TW 10 Р 50 20 07 11. NETWORK LABORATORY OR FIRST TERM TOTAL = 260/750. ORDN. 1 MARKS:

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UNIVERSITY OF PUNE ,T.E. (2008 PAT) (INFORMATION TECHNOLOGY) EXAMINATION NOV. 2011
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                                                                                PAGE NO. 30 (505)
  NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
     OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
T8058590 PATEL NAZNIN JAVED
                                         MAHERUNISSA , 71134973J , , PICT , T8058590
 01. OPERATING SYSTEM PP
02. THEORY OF COMPUTATION PP
03. COMPUTER NETWORK TECHNOLOGY PP
 O1. OPERATING SYSTEM
                            PP 100
                                         30 F
                                     40
                                         61 P
                                100
                                     40
 04. DATBASE MANAGEMENT SYSTEMS
                            PP
                                100
                                     40
                                        48
                                           Р
 05. SOFTWARE ENGINEERING
                            PP
                                100
                                     40
                                        40
                                           Ρ
                          TW
                                     10 21
 06. OPERATING SYSTEM DESIGN LAB.
                                 25
 07. OPERATING SYSTEM DESIGN LAB. PR
                                 50
                                     20 15
                                 25
                                     10 19
 08. INFORMATION SYSTEMS DESIGN LAB. TW
 09. INFORMATION SYSTEMS DESIGN LAB. OR
                                 50 20 20 P
 10. NETWORK LABORATORY
                                 25 10 21 P
                   TW
 11. NETWORK LABORATORY
                           OR
                                 50 20 28 P
 FIRST TERM TOTAL = 362/750.
ORDN. 1 MARKS :
, 71045563B , , PICT , T8058591
 T8058591 PATIL DNYANESHWARI PRAKASH
                                         SANGITA
 01. OPERATING SYSTEM PP 100
02. THEORY OF COMPUTATION PP 100
03. COMPUTER NETWORK TECHNOLOGY PP 100
                            PP 100 40 45 P
                                     40
                                        70 P
                                     40 69 P
 04. DATBASE MANAGEMENT SYSTEMS PP
                                100
                                     40 46 P
 05. SOFTWARE ENGINEERING
                            PP
                                100
                                     40 46
 07. OPERATING SYSTEM DESIGN LAB.
08. INFORMATION SYSTEM
                                 25
                                     10
                                        21
                            TW
                                 50
                                     20 35 P
                            PR
 08. INFORMATION SYSTEMS DESIGN LAB. TW
                                 25
                                     10
                                        19
 09. INFORMATION SYSTEMS DESIGN LAB. OR
                                 50
                                     20
                                         33
                     TW
                                 25
                                     10 20 P
 10. NETWORK LABORATORY
                                 50 20 25
 11. NETWORK LABORATORY
 FIRST TERM TOTAL = 451/750.
ORDN. 1 MARKS:
T8058592 PATIL KHUSHBU PRAKASH
                                         CHHAYA
                            PP 100 40
 01. OPERATING SYSTEM PP 100
02. THEORY OF COMPUTATION PP 100
03. COMPUTER NETWORK TECHNOLOGY PP 100
 01. OPERATING SYSTEM
                                    40 76 P
                                    40 62
                                           Р
                            PP 100
                                     40 63
                                            Р
 04. DATBASE MANAGEMENT SYSTEMS
                            PP
                                100
                                     40 40
 05. SOFTWARE ENGINEERING
 06. OPERATING SYSTEM DESIGN LAB.
                                 25
                                     10
                                        19
                           TW
 07. OPERATING SYSTEM DESIGN LAB.
                            PR
                                 50
                                     20 31
 08. INFORMATION SYSTEMS DESIGN LAB. TW
                                     10
                                       18
                                         27
 09. INFORMATION SYSTEMS DESIGN LAB. OR
                                 50
                                     20
                                 25
                                        19
 10. NETWORK LABORATORY
                            TW
                                     10
                                            Р
                                        28
                                 50 20
 11. NETWORK LABORATORY
                         OR
 FIRST TERM TOTAL = 448/750.
ORDN. 1 MARKS:
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UNIVERSITY OF PUNE ,T.E. (2008 PAT) (INFORMATION TECHNOLOGY) EXAMINATION NOV. 2011
   DATE : 02 MAR. 2012 CENTRÉ : PUNE INSTITUTE OF COMPUTER TECHNOLÓGY, PUNE.
                                                                               PAGE NO. 31 (506)
  NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
     OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
T8058593 PAWAR BHANUPRIYA VITTHAL
                                         SHAKUNTALA , 71045571C , , PICT , T8058593
 01. OPERATING SYSTEM
02. THEORY OF COMPUTATION
WETWORK TECHNOLOGY
                            PP 100
                                        55 P
                                    40
                            PP
                                100
                                    40
                                        44 P
 04. DATBASE MANAGEMENT SYSTEMS
                            PP
                                100
                                    40
                                       42
                                           Р
 05. SOFTWARE ENGINEERING
                            PP
                                100
                                    40 41
                                           Р
                          TW
                                    10
                                        20
 06. OPERATING SYSTEM DESIGN LAB.
                                 25
 07. OPERATING SYSTEM DESIGN LAB. PR
                                 50
                                    20 32
                                 25
                                    10 18
 08. INFORMATION SYSTEMS DESIGN LAB. TW
 09. INFORMATION SYSTEMS DESIGN LAB. OR
                                 50 20 28
 10. NETWORK LABORATORY
                                 25 10 19 P
                   TW
 11. NETWORK LABORATORY
                          OR
                                 50 20 25 P
 FIRST TERM TOTAL = 383/750.
ORDN. 1 MARKS :
T8058594 PHUTANE AMEY UDAY
                                         SARITA , 71054577M , , PICT , T8058594
                            PP 100
                                   40
 01. OPERATING SYSTEM
 01. OPERATING SYSTEM PP 100 02. THEORY OF COMPUTATION PP 100
                                    40
                                        61 P
                          PP 100
 03. COMPUTER NETWORK TECHNOLOGY
                                    40 74 P
 04. DATBASE MANAGEMENT SYSTEMS
                          PP
                                100
                                    40 55 P
 05. SOFTWARE ENGINEERING
                            PP
                                100
                                    40
                                        52
 07. OPERATING SYSTEM DESIGN LAB.
08. INFORMATION SYSTEM
                                 25
                                    10
                                        18
                            TW
                                 50
                                    20 41 P
                            PR
 08. INFORMATION SYSTEMS DESIGN LAB. TW
                                 25
                                    10
                                        19
 09. INFORMATION SYSTEMS DESIGN LAB. OR
                                 50
                                    20 38
                    TW
                                 25
                                    10 17
 10. NETWORK LABORATORY
                                           Р
                                 50 20 34
 11. NETWORK LABORATORY
 FIRST TERM TOTAL = 478/750.
ORDN. 1 MARKS:
, 71134974G , , PICT , T8058595
 T8058595 PINGLE ADITYA ULHAS
                                         ARCHANA
                            PP 100 40
 01. OPERATING SYSTEM PP 100
02. THEORY OF COMPUTATION PP 100
03. COMPUTER NETWORK TECHNOLOGY PP 100
 01. OPERATING SYSTEM
                                   40 55 P
                                    40 74 P
                            PP 100
                                    40 55
                                           Р
 04. DATBASE MANAGEMENT SYSTEMS
                            PP
                                100
                                    40 50
 05. SOFTWARE ENGINEERING
 06. OPERATING SYSTEM DESIGN LAB.
                                 25
                                    10 23 P
                          TW
 07. OPERATING SYSTEM DESIGN LAB.
                            PR
                                 50
 08. INFORMATION SYSTEMS DESIGN LAB. TW
                                    10
                                       21
 09. INFORMATION SYSTEMS DESIGN LAB. OR
                                 50
                                    20
                                        40
                                 25
                                        22
 10. NETWORK LABORATORY
                            TW
                                    10
                                           Р
                                 50 20
 11. NETWORK LABORATORY
                         OR
 FIRST TERM TOTAL = 495/750.
ORDN. 1 MARKS:
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UNIVERSITY OF PUNE ,T.E. (2008 PAT) (INFORMATION TECHNOLOGY) EXAMINATION NOV. 2011
   DATE : 02 MAR. 2012 CENTRÉ : PUNE INSTITUTE OF COMPUTER TECHNOLÓGY, PUNE.
                                                                               PAGE NO. 32 ( 507)
  NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
     OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
T8058596 POLE AKSHAYKUMAR RAHUL
                                         SHEELA , 71045576D , , PICT , T8058596
 01. OPERATING SYSTEM
02. THEORY OF COMPUTATION
03. COMPUTER NETWORK TECHNOLOGY
                            PP 100
                                        20 F
                                    40
                                        55 P
                            PP
                                100
                                     40
 04. DATBASE MANAGEMENT SYSTEMS
                            PP
                                100
                                     40 42
                                           Р
 05. SOFTWARE ENGINEERING
                            PP
                                100
                                     40 40
                                           Р
                          TW
                                     10 12
 06. OPERATING SYSTEM DESIGN LAB.
                                 25
 07. OPERATING SYSTEM DESIGN LAB. PR
                                 50
                                     20 20
                                 25
                                    10 10
 08. INFORMATION SYSTEMS DESIGN LAB. TW
 09. INFORMATION SYSTEMS DESIGN LAB. OR
                                 50 20 10 F
 10. NETWORK LABORATORY
                                 25 10 19 P
                   TW
 11. NETWORK LABORATORY
                           OR
                                 50 20 08 F
 FIRST TERM TOTAL = 296/750.
ORDN. 1 MARKS :
T8058597 POTE SMITA RAGHUNATH
                                               , 71045577B , , PICT , T8058597
                                         VIJAYA
                            PP 100 40 48 P
 01. OPERATING SYSTEM
 01. OPERATING SYSTEM PP 100 02. THEORY OF COMPUTATION PP 100
                                    40 46 P
 03. COMPUTER NETWORK TECHNOLOGY PP 100
                                    40 49 P
 04. DATBASE MANAGEMENT SYSTEMS
                          PP
                                100
                                     40 40 P
 05. SOFTWARE ENGINEERING
                            PP
                                100
                                     40
                                        41 P
 07. OPERATING SYSTEM DESIGN LAB.
08. INFORMATION SYSTEM
                                 25
                                    10
                                        20
                            TW
                                 50
                                     20
                                        32
                            PR
 08. INFORMATION SYSTEMS DESIGN LAB. TW
                                 25
                                    10
                                        20
 09. INFORMATION SYSTEMS DESIGN LAB. OR
                                 50
                                     20
                                        38
                     TW
                                 25
                                    10 21
 10. NETWORK LABORATORY
                                           Р
                                 50 20 30
 11. NETWORK LABORATORY
 FIRST TERM TOTAL = 404/750.
ORDN. 1 MARKS:
, 71045578L , , , PICT , T8058598
 T8058598 POTEY AKSHAY MADHUKAR
                                         NILIMA
                            PP 100 40
 01. OPERATING SYSTEM PP 100
02. THEORY OF COMPUTATION PP 100
03. COMPUTER NETWORK TECHNOLOGY PP 100
 01. OPERATING SYSTEM
                                    40 42 P
                                    40 66 P
                            PP 100
                                    40 47
                                           Р
 04. DATBASE MANAGEMENT SYSTEMS
                            PP
                                100
                                    40 40
 05. SOFTWARE ENGINEERING
 06. OPERATING SYSTEM DESIGN LAB.
                                 25
                                    10 18
                          TW
 07. OPERATING SYSTEM DESIGN LAB.
                            PR
                                 50
 08. INFORMATION SYSTEMS DESIGN LAB. TW
                                    10 15
 09. INFORMATION SYSTEMS DESIGN LAB. OR
                                 50
                                     20
                                       AA
                                 25
 10. NETWORK LABORATORY
                            TW
                                    10
                                        11
                                           Р
                                 50 20 AA F
 11. NETWORK LABORATORY
                         OR
 FIRST TERM TOTAL = 311/750.
ORDN. 1 MARKS:
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UNIVERSITY OF PUNE ,T.E. (2008 PAT) (INFORMATION TECHNOLOGY) EXAMINATION NOV. 2011
   DATE : 02 MAR. 2012 CENTRÉ : PÛNE INSTITÛTE OF COMPUTER TECHNOLÔGY, PUNE.
                                                                               PAGE NO. 33 ( 508)
NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
     OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
T8058599 PRIYA KASHYAP
                                         REETA
                                                        , 71045582J , , PICT , T8058599
 01. OPERATING SYSTEM
02. THEORY OF COMPUTATION
03. COMPUTER NETWORK TECHNOLOGY
 01. OPERATING SYSTEM
                            PP 100
                                    40
                                        64 P
                                        51
                            PP
                                100
                                    40
 04. DATBASE MANAGEMENT SYSTEMS
                            PP
                                100
                                     40
                                           Р
 05. SOFTWARE ENGINEERING
                            PP
                                100
                                     40
                                        54
                                           Р
                          TW
                                     10
                                        20
 06. OPERATING SYSTEM DESIGN LAB.
                                 25
 07. OPERATING SYSTEM DESIGN LAB. PR
                                 50
                                     20 20
                                 25
                                    10 20
 08. INFORMATION SYSTEMS DESIGN LAB. TW
 09. INFORMATION SYSTEMS DESIGN LAB. OR
                                 50 20 20 P
 10. NETWORK LABORATORY
                                 25 10 20 P
                   TW
 11. NETWORK LABORATORY
                          OR 50 20 30 P
 FIRST TERM TOTAL = 404/750.
ORDN. 1 MARKS :
T8058600 PUNTAMBEKAR SHREYA SHAILESH
                               SEEMA
                                                   , 71045584E , , PICT , T8058600
                            PP 100 40 31 F
 01. OPERATING SYSTEM
 02. THEORY OF COMPUTATION PP 100
03. COMPUTER NETWORK TECHNOLOGY PP 100
                                    40 19 F
                                    40 46 P
 04. DATBASE MANAGEMENT SYSTEMS PP 100
                                     40 40 P
                                        25 F
 05. SOFTWARE ENGINEERING
                            PP
                                100
                                     40
 07. OPERATING SYSTEM DESIGN LAB. TW
08. INFORMATION SYSTEM
                                 25
                                    10 18
                                 50
                                     20 15 F
 08. INFORMATION SYSTEMS DESIGN LAB. TW
                                 25
                                    10 18
 09. INFORMATION SYSTEMS DESIGN LAB. OR
                                 50
                                     20 38
                   TW
                                 25
                                    10 19 P
 10. NETWORK LABORATORY
                               50 20 28
 11. NETWORK LABORATORY
 FIRST TERM TOTAL = 314/750.
ORDN. 1 MARKS :
RATNAPRABHA , 71134975E , , PICT , T8058601
 T8058601 RAIJADE DHANVANTARI BALKRISHANA
 01. OPERATING SYSTEM
                                    40 46
 01. OPERATING SYSTEM PP 100
02. THEORY OF COMPUTATION PP 100
03. COMPUTER NETWORK TECHNOLOGY PP 100
                                    40 47
                                           Р
                                    40 55 P
 04. DATBASE MANAGEMENT SYSTEMS PP 100
                                    40 47
                                           Р
                            PP
                                100
                                    40 48
 05. SOFTWARE ENGINEERING
 06. OPERATING SYSTEM DESIGN LAB.
                                 25
                                    10 23
                          TW
 07. OPERATING SYSTEM DESIGN LAB. PR
                                 50
 08. INFORMATION SYSTEMS DESIGN LAB. TW
                                        25
 09. INFORMATION SYSTEMS DESIGN LAB. OR
                                 50
                                     20
                   TW OR
                                 25
                                        22
 10. NETWORK LABORATORY
                                    10
                                           Р
                                 50 20
                                        28
 11. NETWORK LABORATORY
 FIRST TERM TOTAL = 416/750.
ORDN. 1 MARKS:
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UNIVERSITY OF PUNE ,T.E. (2008 PAT) (INFORMATION TECHNOLOGY) EXAMINATION NOV. 2011
   DATE : 02 MAR. 2012 CENTRÉ : PUNE INSTITUTE OF COMPUTER TECHNOLÓGY, PUNE.
                                                                                PAGE NO. 34 (509)
  NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
     OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
T8058602 RAJOLE VISHAL VILASRAO
                                         SUNITA , 71134976C , , PICT , T8058602
 01. OPERATING SYSTEM
02. THEORY OF COMPUTATION
03. COMPUTED NETWORK TECHNOLOGY
                            PP
                                100
                                         72 P
                                     40
                                100
                                     40
                                         71
                            PP
                                           Ρ
 04. DATBASE MANAGEMENT SYSTEMS
                            PP
                                100
                                     40
                                         60
                                           Р
 05. SOFTWARE ENGINEERING
                            PP
                                100
                                     40
                                         60
                                           Ρ
                          TW
                                     10 22
 06. OPERATING SYSTEM DESIGN LAB.
                                 25
 07. OPERATING SYSTEM DESIGN LAB. PR
                                 50
                                     20 41
                                 25
                                     10 21
 08. INFORMATION SYSTEMS DESIGN LAB. TW
 09. INFORMATION SYSTEMS DESIGN LAB. OR
                                 50 20 40 P
 10. NETWORK LABORATORY
                                 25 10 22 P
                   TW
 11. NETWORK LABORATORY
                           OR
                                 50 20 35 P
 FIRST TERM TOTAL = 517/750.
ORDN. 1 MARKS :
T8058603 RISHABH PANDITA
                                         USHA
                                                         , 71045589F , , PICT , T8058603
                            PP 100
                                    40
 01. OPERATING SYSTEM
 01. OPERATING SYSTEM PP 100 02. THEORY OF COMPUTATION PP 100
                                     40
                                       59 P
 03. COMPUTER NETWORK TECHNOLOGY PP 100
                                     40 67 P
 04. DATBASE MANAGEMENT SYSTEMS
                           PP
                                100
                                     40 60 P
                            PP
 05. SOFTWARE ENGINEERING
                                100
                                     40
                                         46
 07. OPERATING SYSTEM DESIGN LAB.
08. INFORMATION SYSTEM
                                 25
                                     10
                                        21
                            TW
                                 50
                                     20 41 P
                            PR
 08. INFORMATION SYSTEMS DESIGN LAB. TW
                                 25
                                     10
                                         21
 09. INFORMATION SYSTEMS DESIGN LAB. OR
                                 50
                                     20
                                         30 P
                     TW
                                 25
                                     10 20 P
 10. NETWORK LABORATORY
                                 50 20 44
 11. NETWORK LABORATORY
 FIRST TERM TOTAL = 482/750.
ORDN. 1 MARKS:
MEENAKSHI , 71045590K , , PICT , T8058604
 T8058604 ROHAN MARWADI
 01. OPERATING SYSTEM PP 100
02. THEORY OF COMPUTATION PP 100
03. COMPUTER NETWORK TECHNOLOGY PP 100
                                    40
                                    40 27 F
                                     40 31 F
                            PP
                                100
                                     40 28
 04. DATBASE MANAGEMENT SYSTEMS
                            PP
                                100
                                     40 45
                                           Р
 05. SOFTWARE ENGINEERING
 06. OPERATING SYSTEM DESIGN LAB.
                                 25
                                     10 17 P
                           TW
 07. OPERATING SYSTEM DESIGN LAB.
                            PR
                                 50
 08. INFORMATION SYSTEMS DESIGN LAB. TW
                                     10 19
 09. INFORMATION SYSTEMS DESIGN LAB. OR
                                 50
                                     20
                                         40
                                 25
                                        19
 10. NETWORK LABORATORY
                            TW
                                     10
                                            Р
                                 50 20
                                        22
 11. NETWORK LABORATORY
                         OR
 FIRST TERM TOTAL = 309/750.
ORDN. 1 MARKS:
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UNIVERSITY OF PUNE ,T.E. (2008 PAT) (INFORMATION TECHNOLOGY) EXAMINATION NOV. 2011
   DATE : 02 MAR. 2012 CENTRÉ : PÛNE INSTITÛTE OF COMPUTER TECHNOLÔGY, PUNE.
                                                                                 PAGE NO. 35 (510)
  NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
     OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
T8058605 SACHIN SUKHLECHA
                                          SANGEETA , 71045592F , , PICT , T8058605
 01. OPERATING SYSTEM PP
02. THEORY OF COMPUTATION PP
03. COMPUTER NETWORK TECHNOLOGY PP
 O1. OPERATING SYSTEM
                            PP 100
                                     40
                                         65 P
                                 100
                                     40 44 P
 04. DATBASE MANAGEMENT SYSTEMS
                           PP
                                 100
                                     40
                                        41
                                            Р
 05. SOFTWARE ENGINEERING
                             PP
                                 100
                                     40 45
                           TW
                                     10
                                         20
 06. OPERATING SYSTEM DESIGN LAB.
                                 25
 07. OPERATING SYSTEM DESIGN LAB. PR
                                 50
                                     20 37
                                 25
                                     10 16
 08. INFORMATION SYSTEMS DESIGN LAB. TW
 09. INFORMATION SYSTEMS DESIGN LAB. OR
                                 50 20 33 P
 10. NETWORK LABORATORY
                                 25 10 20 P
                   TW
 11. NETWORK LABORATORY
                           OR
                                 50 20 32 P
 FIRST TERM TOTAL = 414/750.
ORDN. 1 MARKS :
KAVITA , 71045594B , , PICT , T8058606
 T8058606 SALVE AJINKYA PRABHAKAR
 01. OPERATING SYSTEM PP 100
02. THEORY OF COMPUTATION PP 100
03. COMPUTER NETWORK TECHNOLOGY PP 100
                             PP 100 40 51 P
                                     40
                                         61 P
                                     40 72 P
 04. DATBASE MANAGEMENT SYSTEMS PP
                                100
                                     40
                                         51 P
 05. SOFTWARE ENGINEERING
                            PP
                                 100
                                     40
                                         55 P
 07. OPERATING SYSTEM DESIGN LAB.
08. INFORMATION SYSTEM
                                 25
                                     10 16
                            TW
                                 50
                                     20 44 P
                            PR
 08. INFORMATION SYSTEMS DESIGN LAB. TW
                                 25
                                     10 17
 09. INFORMATION SYSTEMS DESIGN LAB. OR
                                 50
                                     20 41
                    TW
                                 25
                                     10 15 P
 10. NETWORK LABORATORY
                                 50 20 30
 11. NETWORK LABORATORY
 FIRST TERM TOTAL = 467/750.
ORDN. 1 MARKS:
, 71053988G , , PICT , T8058607
 T8058607 SANJAY SINGH
                                          PREMA
 01. OPERATING SYSTEM PP 100 40 02. THEORY OF COMPUTATION PP 100 40 03. COMPUTER NETWORK TECHNOLOGY PP 100 40
                                         56 P
                                     40 77
                                            Р
                             PP
                                100
                                     40
                                         52
                                            Р
 04. DATBASE MANAGEMENT SYSTEMS
                             PP
                                 100
                                     40
                                         50
 05. SOFTWARE ENGINEERING
 06. OPERATING SYSTEM DESIGN LAB.
                                 25
                                     10 21 P
                           TW
 07. OPERATING SYSTEM DESIGN LAB. PR
                                 50
 08. INFORMATION SYSTEMS DESIGN LAB. TW
                                     10 19
 09. INFORMATION SYSTEMS DESIGN LAB. OR
                                 50
                                     20
                                         41
                                 25
                                         20
 10. NETWORK LABORATORY
                             TW
                                     10
                                            Р
                                 50 20
                                         32
 11. NETWORK LABORATORY
                         OR
 FIRST TERM TOTAL = 495/750.
ORDN. 1 MARKS:
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UNIVERSITY OF PUNE ,T.E. (2008 PAT) (INFORMATION TECHNOLOGY) EXAMINATION NOV. 2011
   DATE : 02 MAR. 2012 CENTRÉ : PÛNE INSTITÛTE OF COMPUTER TECHNOLÔGY, PUNE.
                                                                                 PAGE NO. 36 (511)
NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
     OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
T8058608 SARTHAK MAJITHIA
                                          POOJA
                                                         , 71045598E , , PICT , T8058608
 01. OPERATING SYSTEM PP
02. THEORY OF COMPUTATION PP
03. COMPUTER NETWORK TECHNOLOGY PP
 01. OPERATING SYSTEM
                            PP 100
                                         09 F
                                     40
                                 100
                                     40 AA F
 04. DATBASE MANAGEMENT SYSTEMS
                           PP
                                 100
                                     40
                                         AA F
 05. SOFTWARE ENGINEERING
                             PP
                                 100
                                     40 24
                           TW
                                     10 11
 06. OPERATING SYSTEM DESIGN LAB.
                                 25
 07. OPERATING SYSTEM DESIGN LAB. PR
                                 50
                                     20 22
                                 25
                                     10 12
 08. INFORMATION SYSTEMS DESIGN LAB. TW
 09. INFORMATION SYSTEMS DESIGN LAB. OR
                                 50 20 AA F
 10. NETWORK LABORATORY
                                 25 10 10 P
                   TW
 11. NETWORK LABORATORY
                           OR
                                 50 20 AA F
 FIRST TERM TOTAL = 146/750.
ORDN. 1 MARKS :
T8058610 SATPUTE ABHINANDAN ASHOK
                                         SAVITA , 71134977M , , PICT , T8058610
                             PP 100 40 52 P
 01. OPERATING SYSTEM PP 100
02. THEORY OF COMPUTATION PP 100
03. COMPUTER NETWORK TECHNOLOGY PP 100
                                     40
                                         62 P
                                     40 80 P
 04. DATBASE MANAGEMENT SYSTEMS PP
                                100
                                     40 65 P
 05. SOFTWARE ENGINEERING
                            PP
                                 100
                                     40
                                         62
 07. OPERATING SYSTEM DESIGN LAB.
08. INFORMATION SYSTEM
                                 25
                                     10
                                         23
                            TW
                                 50
                                     20 36 P
                            PR
 08. INFORMATION SYSTEMS DESIGN LAB. TW
                                     10
                                         21
 09. INFORMATION SYSTEMS DESIGN LAB. OR
                                 50
                                     20
                                         37
                    TW
                                 25
                                     10 23 P
 10. NETWORK LABORATORY
                                50 20 40
 11. NETWORK LABORATORY
 FIRST TERM TOTAL = 520/750.
ORDN. 1 MARKS:
, 71045600L , , PICT , T8058611
 T8058611 SATPUTE SWAPNIL BHASKAR
                                          SUREKHA
 01. OPERATING SYSTEM
                             PP 100 40
 01. OPERATING SYSTEM PP 100
02. THEORY OF COMPUTATION PP 100
03. COMPUTER NETWORK TECHNOLOGY PP 100
                                         46
                                    40 42 P
                                     40 50 P
 04. DATBASE MANAGEMENT SYSTEMS PP 100
                                     40 44
                                            Р
                            PP
                                 100
                                     40 40
 05. SOFTWARE ENGINEERING
 06. OPERATING SYSTEM DESIGN LAB.
                                 25
                                     10 15
                           TW
 07. OPERATING SYSTEM DESIGN LAB. PR
                                 50
 08. INFORMATION SYSTEMS DESIGN LAB. TW
                                     10
                                        15
 09. INFORMATION SYSTEMS DESIGN LAB. OR
                                 50
                                     20
                                         30
                   TW
OR
                                 25
                                         15
 10. NETWORK LABORATORY
                                     10
                                            Р
                                 50 20
                                         35
 11. NETWORK LABORATORY
 FIRST TERM TOTAL = 389/750.
ORDN. 1 MARKS:
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UNIVERSITY OF PUNE ,T.E. (2008 PAT) (INFORMATION TECHNOLOGY) EXAMINATION NOV. 2011
   DATE : 02 MAR. 2012 CENTRÉ : PÛNE INSTITÛTE OF COMPUTER TECHNOLÔGY, PUNE.
                                                                              PAGE NO. 37 (512)
  NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
     OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
T8058612 SETHI SUPREET SATISHKUMAR
                                        PREETI
                                                       , 71045605M , , PICT , T8058612
 01. OPERATING SYSTEM
 02. THEORY OF COMPUTATION
                            PP 100
                                    40
                                        41 P
                                        29 F
                            PP
                                100
                                    40
 03. COMPUTER NETWORK TECHNOLOGY
 04. DATBASE MANAGEMENT SYSTEMS
                           PP
                                100
                                    40
                                        30 F
 05. SOFTWARE ENGINEERING
                            PP
                                100
                                    40
                                       AA F
                          TW
                                    10 14
 06. OPERATING SYSTEM DESIGN LAB.
                                25
 07. OPERATING SYSTEM DESIGN LAB. PR
                                50
                                    20 39
                                25
                                    10 18
 08. INFORMATION SYSTEMS DESIGN LAB. TW
 09. INFORMATION SYSTEMS DESIGN LAB. OR
                                50 20 39 P
 10. NETWORK LABORATORY
                                25 10 11 P
                   TW
 11. NETWORK LABORATORY
                          OR
                                50 20 06 F
 FIRST TERM TOTAL = 282/750.
ORDN. 1 MARKS :
T8058613 SHAH KUNAL ARVIND
                                        PRAMILA
                                                  , 71045607H , , PICT , T8058613
                            PP 100
                                   40
                                       28 F
 01. OPERATING SYSTEM PP 100 02. THEORY OF COMPUTATION PP 100
                                    40
                                        49 P
 03. COMPUTER NETWORK TECHNOLOGY
                         PP 100
                                    40 59 P
 04. DATBASE MANAGEMENT SYSTEMS
                          PP
                                100
                                    40 45 P
                            PP
                                        40
 05. SOFTWARE ENGINEERING
                                100
                                    40
 07. OPERATING SYSTEM DESIGN LAB.
08. INFORMATION SYSTEM
                                25
                                    10
                                        21
                            TW
                                50
                                    20
                                        38
                           PR
 08. INFORMATION SYSTEMS DESIGN LAB. TW
                                25
                                    10
                                        20
 09. INFORMATION SYSTEMS DESIGN LAB. OR
                                50
                                    20
                    TW
                                25
                                    10 21
 10. NETWORK LABORATORY
                                           Р
                                50 20 30
 11. NETWORK LABORATORY
 FIRST TERM TOTAL = 403/750.
ORDN. 1 MARKS:
, 71134978к , , , рІСТ , Т8058614
 T8058614 SHAIKH SHEEBANRAZA ZAHEER
                                        NASEEM
                            PP 100 40
 01. OPERATING SYSTEM
 01. OPERATING SYSTEM
02. THEORY OF COMPUTATION
03. COMPUTER NETWORK TECHNOLOGY
                            PP 100
                                   40
                                        56 P
                            PP 100
                                    40
                                      73
                                           Р
                            PP
                               100
                                    40
                                        55
                                           Р
 04. DATBASE MANAGEMENT SYSTEMS
                            PP
                                100
                                    40
                                        56
                                           Р
 05. SOFTWARE ENGINEERING
 06. OPERATING SYSTEM DESIGN LAB.
                                25
                                    10
                                       21
                          TW
 07. OPERATING SYSTEM DESIGN LAB.
                           PR
                                50
 08. INFORMATION SYSTEMS DESIGN LAB. TW
                                    10
                                        22
 09. INFORMATION SYSTEMS DESIGN LAB. OR
                                50
                                    20
                                        32
                                25
                                        19
 10. NETWORK LABORATORY
                            TW
                                    10
                                           Р
                                50 20
                                        35
 11. NETWORK LABORATORY
                        OR
 FIRST TERM TOTAL = 480/750.
ORDN. 1 MARKS:
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UNIVERSITY OF PUNE ,T.E. (2008 PAT) (INFORMATION TECHNOLOGY) EXAMINATION NOV. 2011 DATE : 02 MAR. 2012 CENTRÉ : PUNE INSTITUTE OF COMPUTER TECHNOLÓGY, PUNE. PAGE NO. 38 (513) NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER T8058615 SHEIKH LINA JAMIL AHMED NISHAT , 71134979H , , PICT , T8058615 01. OPERATING SYSTEM
02. THEORY OF COMPUTATION
03. COMPUTER NETWORK TECHNOLOGY PP 100 53 P 40 78 P PP 100 40 04. DATBASE MANAGEMENT SYSTEMS PP 100 40 72 Р 05. SOFTWARE ENGINEERING PP 100 40 61 Р TW 10 22 06. OPERATING SYSTEM DESIGN LAB. 25 07. OPERATING SYSTEM DESIGN LAB. PR 50 20 41 25 10 20 P 08. INFORMATION SYSTEMS DESIGN LAB. TW 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 10 F 10. NETWORK LABORATORY 25 10 21 P TW 11. NETWORK LABORATORY OR FIRST TERM TOTAL = 485/750. ORDN. 1 MARKS : T8058616 SHETTY SHEFALI RAVINDRA , 71045612D , , PICT , T8058616 VANDANA PP 100 40 50 P 01. OPERATING SYSTEM 01. OPERATING SYSTEM PP 100 02. THEORY OF COMPUTATION PP 100 40 51 P 03. COMPUTER NETWORK TECHNOLOGY PP 100 40 62 P 04. DATBASE MANAGEMENT SYSTEMS PP 100 40 53 P 05. SOFTWARE ENGINEERING PP 100 40 42 07. OPERATING SYSTEM DESIGN LAB.
08. INFORMATION SYSTEM 25 10 22 TW 50 20 42 PR 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 23 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 28 TW 25 10 21 10. NETWORK LABORATORY Р 50 20 30 11. NETWORK LABORATORY FIRST TERM TOTAL = 445/750. ORDN. 1 MARKS : , 71045613B , , PICT , T8058617 T8058617 SHEWALE ANULA TUSHAR NEETA PP 100 40 01. OPERATING SYSTEM PP 100
02. THEORY OF COMPUTATION PP 100
03. COMPUTER NETWORK TECHNOLOGY PP 100 01. OPERATING SYSTEM 40 40 P 40 49 04. DATBASE MANAGEMENT SYSTEMS PP 100 40 40 PP 100 40 29 05. SOFTWARE ENGINEERING 06. OPERATING SYSTEM DESIGN LAB. 25 10 TW 07. OPERATING SYSTEM DESIGN LAB. PR 50 20 25 08. INFORMATION SYSTEMS DESIGN LAB. TW 30 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 25 10. NETWORK LABORATORY TW 10 16 Р 50 20 28 11. NETWORK LABORATORY OR FIRST TERM TOTAL = 345/750. ORDN. 1 MARKS:

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UNIVERSITY OF PUNE ,T.E. (2008 PAT) (INFORMATION TECHNOLOGY) EXAMINATION NOV. 2011
   DATE : 02 MAR. 2012 CENTRÉ : PÛNE INSTITÛTE OF COMPUTER TECHNOLÔGY, PUNE.
                                                                                PAGE NO. 39 (514)
  NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
     OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
T8058618 SHINDE AKASH ARUN
                                         SHOBHA
                                                , 71134980M , , PICT , T8058618
 01. OPERATING SYSTEM
 01. OPERATING SYSTEM
02. THEORY OF COMPUTATION
03. COMPUTER NETWORK TECHNOLOGY
                            PP 100
                                         52 P
                                     40
                                         72
                            PP
                                100
                                     40
                                            Ρ
 04. DATBASE MANAGEMENT SYSTEMS
                            PP
                                100
                                     40
                                         50
                                         55
 05. SOFTWARE ENGINEERING
                            PP
                                100
                                     40
                          TW
                                     10
                                         21
 06. OPERATING SYSTEM DESIGN LAB.
                                 25
 07. OPERATING SYSTEM DESIGN LAB. PR
                                 50
                                     20
                                 25
                                    10 20
 08. INFORMATION SYSTEMS DESIGN LAB. TW
 09. INFORMATION SYSTEMS DESIGN LAB. OR
                                 50 20 32
 10. NETWORK LABORATORY
                                 25 10 18 P
                   TW
 11. NETWORK LABORATORY
                           OR
 FIRST TERM TOTAL = 458/750.
ORDN. 1 MARKS :
T8058619 SHINGANE ANKUSH DINANATH
                                         VEENA
                                                   , 71134981K , , PICT , T8058619
                            PP 100 40
 01. OPERATING SYSTEM
 01. OPERATING SYSTEM PP 100 02. THEORY OF COMPUTATION PP 100
                                     40
                                        57 P
 03. COMPUTER NETWORK TECHNOLOGY PP 100
                                     40 66 P
 04. DATBASE MANAGEMENT SYSTEMS PP
                                100
                                     40 62
 05. SOFTWARE ENGINEERING
                            PP
                                100
                                     40
                                         56
 07. OPERATING SYSTEM DESIGN LAB.
08. INFORMATION SYSTEM
                                 25
                                     10
                                        17
                            TW
                                 50
                                     20
                                         39
                            PR
 08. INFORMATION SYSTEMS DESIGN LAB. TW
                                 25
                                     10 17
 09. INFORMATION SYSTEMS DESIGN LAB. OR
                                 50
                                     20 40
                    TW
                                 25
                                     10 17
 10. NETWORK LABORATORY
                                            Р
                                 50 20 22
 11. NETWORK LABORATORY
 FIRST TERM TOTAL = 477/750.
ORDN. 1 MARKS :
, 71059492F , , PICT , T8058620
 T8058620 SHIRUDE NEHA RAJIV
                                         SMITA
 01. OPERATING SYSTEM PP 100 40 47 02. THEORY OF COMPUTATION PP 100 40 42 03. COMPUTER NETWORK TECHNOLOGY PP 100 40 65
 01. OPERATING SYSTEM
                            PP 100 40
                                    40 65 P
                            PP 100
                                     40 53
                                            Р
 04. DATBASE MANAGEMENT SYSTEMS
                            PP
                                100
                                     40 46
 05. SOFTWARE ENGINEERING
 06. OPERATING SYSTEM DESIGN LAB.
                                 25
                                     10 22
                           TW
 07. OPERATING SYSTEM DESIGN LAB.
                            PR
                                 50
 08. INFORMATION SYSTEMS DESIGN LAB. TW
                                     10
 09. INFORMATION SYSTEMS DESIGN LAB. OR
                                 50
                                     20
                                         32
                                 25
 10. NETWORK LABORATORY
                            TW
                                     10
                                         21
                                            Р
                                 50 20
                                         30
 11. NETWORK LABORATORY
                         OR
 FIRST TERM TOTAL = 438/750.
ORDN. 1 MARKS :
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   DATE : 02 MAR. 2012 CENTRÉ : PUNE INSTITUTE OF COMPUTER TECHNOLÓGY, PUNE.
                                                                               PAGE NO. 40 (515)
  NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
     OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
T8058621 SHIVADE PRATHAMESH SHRIKANT
                                        NILIMA , 70925608G , , PICT , T8058621
 01. OPERATING SYSTEM
02. THEORY OF COMPUTATION
NETWORK TECHNOLOGY
                            PP 100
                                        27 F
                                    40
                                        50 P
                            PP
                                100
                                    40
 04. DATBASE MANAGEMENT SYSTEMS
                            PP
                                100
                                    40
                                        40
                                           Р
 05. SOFTWARE ENGINEERING
                            PP
                                100
                                    40 40
                          TW
                                    10 14
 06. OPERATING SYSTEM DESIGN LAB.
                                 25
 07. OPERATING SYSTEM DESIGN LAB. PR
                                 50
                                    20 40
                                 25
                                    10 15
 08. INFORMATION SYSTEMS DESIGN LAB. TW
 09. INFORMATION SYSTEMS DESIGN LAB. OR
                                 50 20 41 P
 10. NETWORK LABORATORY
                                 25 10 14 P
                   TW
 11. NETWORK LABORATORY
                          OR
 FIRST TERM TOTAL = 352/750.
ORDN. 1 MARKS :
T8058622 SHRIKANTH JAIKUMAR
                                              , 70801622H , , PICT , T8058622
                                         BHUVNA
                            PP 100
                                   40
 01. OPERATING SYSTEM
 01. OPERATING SYSTEM PP 100 02. THEORY OF COMPUTATION PP 100
                                    40
                                       40 P
 03. COMPUTER NETWORK TECHNOLOGY PP 100
                                    40 62 P
 04. DATBASE MANAGEMENT SYSTEMS
                          PP
                                100
                                    40 47 P
                            PP
 05. SOFTWARE ENGINEERING
                                100
                                    40
 07. OPERATING SYSTEM DESIGN LAB.
08. INFORMATION SYSTEM
                                 25
                                    10
                                        22
                            TW
                                 50
                                    20 45 P
                            PR
 08. INFORMATION SYSTEMS DESIGN LAB. TW
                                 25
                                    10
                                        20
 09. INFORMATION SYSTEMS DESIGN LAB. OR
                                 50
                                    20
                                        38
                   TW
                                 25
                                    10 22
 10. NETWORK LABORATORY
                                           Р
                                 50 20 30
 11. NETWORK LABORATORY
 FIRST TERM TOTAL = 427/750.
ORDN. 1 MARKS:
, 71045620E , , PICT , T8058623
 T8058623 SHUBHI YEDE
                                         ANJU
 01. OPERATING SYSTEM PP 100
02. THEORY OF COMPUTATION PP 100
03. COMPUTER NETWORK TECHNOLOGY PP 100
                                   40
                                        50 P
                                    40 61 P
                            PP
                                100
                                    40 45
 04. DATBASE MANAGEMENT SYSTEMS
                            PP
                                100
                                    40 52
 05. SOFTWARE ENGINEERING
                                    10 18
 06. OPERATING SYSTEM DESIGN LAB.
                                25
                          TW
 07. OPERATING SYSTEM DESIGN LAB. PR
                                 50
 08. INFORMATION SYSTEMS DESIGN LAB. TW
                                    10
                                       19
 09. INFORMATION SYSTEMS DESIGN LAB. OR
                                 50
                                    20
                                        31
                                 25
 10. NETWORK LABORATORY
                            TW
                                    10
                                        18
                                           Р
                                 50 20
                                        30
 11. NETWORK LABORATORY
                         OR
 FIRST TERM TOTAL = 436/750.
ORDN. 1 MARKS :
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                                                                               PAGE NO. 41 (516)
  NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
     OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
T8058624 SINGH PRABHAKAR KRISHNAKUMAR
                                        GAYTRIDEVI , 71134982H , , PICT , T8058624
 01. OPERATING SYSTEM
02. THEORY OF COMPUTATION

NETWORK TECHNOLOGY
                            PP 100
                                        66 P
                                    40
                                        73
                            PP
                                100
                                    40
                                           Ρ
 04. DATBASE MANAGEMENT SYSTEMS
                            PP
                                100
                                    40
                                        60
                                           Р
 05. SOFTWARE ENGINEERING
                            PP
                                100
                                    40
                                        63
                          TW
                                    10 23
 06. OPERATING SYSTEM DESIGN LAB.
                                 25
 07. OPERATING SYSTEM DESIGN LAB. PR
                                 50
                                    20 43
                                 25
                                    10 23
 08. INFORMATION SYSTEMS DESIGN LAB. TW
 09. INFORMATION SYSTEMS DESIGN LAB. OR
                                 50 20 32
 10. NETWORK LABORATORY
                                 25 10 23 P
                   TW
 11. NETWORK LABORATORY
                          OR
 FIRST TERM TOTAL = 510/750.
ORDN. 1 MARKS :
T8058625 SOMANI NEHA NANDKISHOR
                                              , 71045628L , , PICT , T8058625
                                         ARATI
                            PP 100 40 52 P
 01. OPERATING SYSTEM PP 100 02. THEORY OF COMPUTATION PP 100
                                    40
                                        48 P
 03. COMPUTER NETWORK TECHNOLOGY PP 100
                                    40 57 P
 04. DATBASE MANAGEMENT SYSTEMS PP
                                100
                                    40 49 P
 05. SOFTWARE ENGINEERING
                            PP
                                100
                                    40
 07. OPERATING SYSTEM DESIGN LAB.
08. INFORMATION SYSTEM
                                 25
                                    10 19
                            TW
                                 50
                                    20 41 P
                            PR
 08. INFORMATION SYSTEMS DESIGN LAB. TW
                                 25
                                    10
                                        18
 09. INFORMATION SYSTEMS DESIGN LAB. OR
                                 50
                                    20 35
                    TW
                                 25
                                    10 19 P
 10. NETWORK LABORATORY
                                 50 20 32
 11. NETWORK LABORATORY
 FIRST TERM TOTAL = 431/750.
ORDN. 1 MARKS :
, 71045629J , , PICT , T8058626
 T8058626 SOMWANSHI HARSHAL SURESH
                                         NUTAN
 01. OPERATING SYSTEM
                            PP 100 40
 01. OPERATING SYSTEM PP 100
02. THEORY OF COMPUTATION PP 100
03. COMPUTER NETWORK TECHNOLOGY PP 100
                                    40
                                        50 P
                                    40 56
 04. DATBASE MANAGEMENT SYSTEMS PP
                                100
                                    40 46
                            PP
                                100
                                    40 40
 05. SOFTWARE ENGINEERING
 06. OPERATING SYSTEM DESIGN LAB.
                                 25
                                    10 19
                          TW
 07. OPERATING SYSTEM DESIGN LAB. PR
                                 50
 08. INFORMATION SYSTEMS DESIGN LAB. TW
                                    10
                                        20
 09. INFORMATION SYSTEMS DESIGN LAB. OR
                                 50
                                    20
                                 25
                                        19
 10. NETWORK LABORATORY
                            TW
                                    10
                                           Р
                                 50 20
                                        30
 11. NETWORK LABORATORY
                        OR
 FIRST TERM TOTAL = 406/750.
ORDN. 1 MARKS :
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UNIVERSITY OF PUNE ,T.E. (2008 PAT) (INFORMATION TECHNOLOGY) EXAMINATION NOV. 2011
   DATE : 02 MAR. 2012 CENTRÉ : PUNE INSTITUTE OF COMPUTER TECHNOLÓGY, PUNE.
                                                                               PAGE NO. 42 ( 517)
NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
     OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
T8058627 SONAWANE JAYANT SHAM
                                         RAJESHRI , 71045630B , , PICT , T8058627
 01. OPERATING SYSTEM
 02. THEORY OF COMPUTATION PP
03. COMPUTER NETWORK TECHNOLOGY PP
                            PP 100
                                    40
                                        56 P
                                        64 P
                                100
                                    40
 04. DATBASE MANAGEMENT SYSTEMS
                            PP
                                100
                                    40
                                        60
                                           Р
 05. SOFTWARE ENGINEERING
                            PP
                                100
                                    40 47
                          TW
                                    10 18
 06. OPERATING SYSTEM DESIGN LAB.
                                 25
 07. OPERATING SYSTEM DESIGN LAB. PR
                                 50
                                    20 37
                                 25
                                    10 17
 08. INFORMATION SYSTEMS DESIGN LAB. TW
 09. INFORMATION SYSTEMS DESIGN LAB. OR
                                 50 20 35 P
 10. NETWORK LABORATORY
                                 25 10 18 P
                   TW
 11. NETWORK LABORATORY
                          OR
                                 50 20 25 P
 FIRST TERM TOTAL = 424/750.
ORDN. 1 MARKS :
T8058628 SONY PRERNA ASHOK
                                         BHAGWATI , 71134983F , , PICT , T8058628
                            PP 100
                                   40
                                        50 P
 01. OPERATING SYSTEM
 01. OPERATING SYSTEM PP 100 02. THEORY OF COMPUTATION PP 100
                                    40
                                        41 P
 03. COMPUTER NETWORK TECHNOLOGY PP 100
                                    40 57 P
 04. DATBASE MANAGEMENT SYSTEMS
                          PP
                                100
                                    40 53 P
                            PP
 05. SOFTWARE ENGINEERING
                                100
                                    40
 07. OPERATING SYSTEM DESIGN LAB.
08. INFORMATION SYSTEM
                                 25
                                    10
                                        22
                            TW
                                 50
                                    20 35 P
                            PR
 08. INFORMATION SYSTEMS DESIGN LAB. TW
                                 25
                                    10
                                        20
 09. INFORMATION SYSTEMS DESIGN LAB. OR
                                 50
                                    20
                                        38
                    TW
                                 25
                                    10 20 P
 10. NETWORK LABORATORY
                                 50 20 24
 11. NETWORK LABORATORY
 FIRST TERM TOTAL = 420/750.
ORDN. 1 MARKS :
, 71134984D , , PICT , T8058629
 T8058629 SURADKAR SWAPNIL RAJENDRA
                                         SANGITA
                            PP 100 40
 01. OPERATING SYSTEM
 01. OPERATING SYSTEM PP 100
02. THEORY OF COMPUTATION PP 100
03. COMPUTER NETWORK TECHNOLOGY PP 100
                                    40
                                        30 F
                                    40 63 P
 04. DATBASE MANAGEMENT SYSTEMS PP
                                100
                                    40 58
                                           Р
                            PP
                                100
                                    40 46
 05. SOFTWARE ENGINEERING
 06. OPERATING SYSTEM DESIGN LAB.
                                 25
                                    10 20
                          TW
 07. OPERATING SYSTEM DESIGN LAB. PR
                                 50
 08. INFORMATION SYSTEMS DESIGN LAB. TW
                                    10
                                        21
 09. INFORMATION SYSTEMS DESIGN LAB. OR
                                 50
                                    20
                                        31
                   TW
OR
                                 25
 10. NETWORK LABORATORY
                                    10
                                        18
                                           Р
                                 50 20
                                        24
 11. NETWORK LABORATORY
 FIRST TERM TOTAL = 413/750.
ORDN. 1 MARKS :
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UNIVERSITY OF PUNE ,T.E. (2008 PAT) (INFORMATION TECHNOLOGY) EXAMINATION NOV. 2011
   DATE : 02 MAR. 2012 CENTRÉ : PUNE INSTITUTE OF COMPUTER TECHNOLÓGY, PUNE.
                                                                               PAGE NO. 43 (518)
  NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
     OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
T8058630 SURYAWANSHI NIRAJ NITIN
                                        NEHA
                                                        , 70701667D , , PICT , T8058630
 01. OPERATING SYSTEM
 01. OPERATING SYSIEM
02. THEORY OF COMPUTATION
03. COMPUTER NETWORK TECHNOLOGY
                            PP 100
                                    40
                                        22 F
                                    40 40 P
                            PP
                                100
 04. DATBASE MANAGEMENT SYSTEMS
                            PP
                                100
                                    40
                                       44
                                           Р
                                    40 25
 05. SOFTWARE ENGINEERING
                            PP
                                100
                          TW
                                    10 12
 06. OPERATING SYSTEM DESIGN LAB.
                                 25
 07. OPERATING SYSTEM DESIGN LAB. PR
                                 50
                                    20 AA F
                                 25
                                    10 14
 08. INFORMATION SYSTEMS DESIGN LAB. TW
 09. INFORMATION SYSTEMS DESIGN LAB. OR
                                 50 20 20 P
 10. NETWORK LABORATORY
                                 25 10 11 P
                   TW
 11. NETWORK LABORATORY
                          OR
                                 50 20 28 P
 FIRST TERM TOTAL = 262/750.
ORDN. 1 MARKS :
SHAILA , 70925625G , , PICT , T8058631
 T8058631 SUYASH PANDEY
                            PP 100
                                    40
                                       31 F
 01. OPERATING SYSTEM
 01. OPERATING SYSTEM PP 100 02. THEORY OF COMPUTATION PP 100
                                    40
                                       30 F
 03. COMPUTER NETWORK TECHNOLOGY PP 100
                                    40 29 F
 04. DATBASE MANAGEMENT SYSTEMS
                          PP
                                100
                                    40 AA F
                            PP
 05. SOFTWARE ENGINEERING
                                100
                                    40 40 P
 07. OPERATING SYSTEM DESIGN LAB.
08. INFORMATION SYSTEM
                                 25
                                    10 11 P
                            TW
                                 50
                                    20 AA F
                            PR
 08. INFORMATION SYSTEMS DESIGN LAB. TW
                                 25
                                    10 14
 09. INFORMATION SYSTEMS DESIGN LAB. OR
                                 50
                                    20 28 P
                    TW
                                 25
                                    10 10 P
 10. NETWORK LABORATORY
                                 50 20 AA F
 11. NETWORK LABORATORY
 FIRST TERM TOTAL = 208/750.
ORDN. 1 MARKS :
, 71045640к , , , ріст , т8058632
 T8058632 TAORI SANIKA SANJAY
                                         RUPALI
                            PP 100 40
 01. OPERATING SYSTEM
 01. OPERATING SYSTEM PP 100 02. THEORY OF COMPUTATION PP 100
                                   40 48 P
 03. COMPUTER NETWORK TECHNOLOGY
                          PP 100
                                    40 59
                            PP
                                100
                                    40 65
                                           Р
 04. DATBASE MANAGEMENT SYSTEMS
                            PP
                                100
                                    40 47
 05. SOFTWARE ENGINEERING
 06. OPERATING SYSTEM DESIGN LAB.
                                25
                                    10
                          TW
 07. OPERATING SYSTEM DESIGN LAB.
                           PR
                                 50
 08. INFORMATION SYSTEMS DESIGN LAB. TW
                                    10
                                        21
 09. INFORMATION SYSTEMS DESIGN LAB. OR
                                 50
                                    20
                                        32
                                 25
                                        23
 10. NETWORK LABORATORY
                            TW
                                    10
                                           Р
                                 50 20
 11. NETWORK LABORATORY
                         OR
 FIRST TERM TOTAL = 465/750.
ORDN. 1 MARKS :
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	CENT	ΓRÉ :	PUNE :	INSTI	TUTE OF	COMPUTE	R TECH	NOLOGY,	PUNE					 4 (519)
NOTE: FIRST LINE : SEAT NO., NAME O OTHER LINES: HEAD OF PASSING,	MAX	. MARK	S, M	ÍN. P	ASS MAF		RKS OBT	AINÉD,	P/F:I	PASS/F	۹IL, ٔ	C:PRE		T NO. OVER
T8058633 TATHE VISHAL BHAGWAT					PANA			 7113498		,		,	PICT	 , т8058633
01. OPERATING SYSTEM 02. THEORY OF COMPUTATION 03. COMPUTER NETWORK TECHNOLOGY 04. DATBASE MANAGEMENT SYSTEMS 05. SOFTWARE ENGINEERING 06. OPERATING SYSTEM DESIGN LAB. 07. OPERATING SYSTEM DESIGN LAB. 08. INFORMATION SYSTEMS DESIGN LAB. 09. INFORMATION SYSTEMS DESIGN LAB. 10. NETWORK LABORATORY 11. NETWORK LABORATORY	PP PP PP PP TW PR TW OR TW OR	100 100 100 100 100 25 50 25 50 25 50	40 40 40 40 10 20 10 20 10 20	52 55 71 61 60 23 41 23 31 22 30	P P P P P P P									
FIRST TERM TOTAL = 490/750. ORDN. 1 MARKS :														
T8058634 THAKUR BHAVANA PRAKASH				AR	CHANA		,	7092563	3н	,		,	PICT	, т8058634
01. OPERATING SYSTEM 02. THEORY OF COMPUTATION 03. COMPUTER NETWORK TECHNOLOGY 04. DATBASE MANAGEMENT SYSTEMS 05. SOFTWARE ENGINEERING 06. OPERATING SYSTEM DESIGN LAB. 07. OPERATING SYSTEM DESIGN LAB. 08. INFORMATION SYSTEMS DESIGN LAB. 09. INFORMATION SYSTEMS DESIGN LAB. 10. NETWORK LABORATORY 11. NETWORK LABORATORY	PP PP PP PP TW PR TW OR TW OR	100 100 100 100 25 50 25 50 25	40 40 40 40 10 20 10 20 10 20	43 52 46 41 41 17 35 17 12 18 24	P P P P P P F P									
FIRST TERM TOTAL = 367/750. ORDN. 1 MARKS :														
T8058635 THOMBRE PRIYANKA MILIND				JA	YMALA		,	7104564	9C	,		,	PICT	, т8058635
01. OPERATING SYSTEM 02. THEORY OF COMPUTATION 03. COMPUTER NETWORK TECHNOLOGY 04. DATBASE MANAGEMENT SYSTEMS 05. SOFTWARE ENGINEERING 06. OPERATING SYSTEM DESIGN LAB. 07. OPERATING SYSTEM DESIGN LAB. 08. INFORMATION SYSTEMS DESIGN LAB. 09. INFORMATION SYSTEMS DESIGN LAB. 10. NETWORK LABORATORY 11. NETWORK LABORATORY	PP PP PP PP TW PR TW OR TW	100 100 100 100 25 50 25 50 25 50	40 40 40 40 10 20 10 20 10 20	16 56 27 44 42 17 35 15 38 18 08	F P F P P P F									
FIRST TERM TOTAL = 336/750. ORDN. 1 MARKS :														

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                                                                                PAGE NO. 45 ( 520)
  NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
     OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
T8058636 TIWARI ROHIT VINOD
                                         SHAKUNTALA , 71045652C , , PICT , T8058636
 01. OPERATING SYSTEM PP
02. THEORY OF COMPUTATION PP
03. COMPUTER NETWORK TECHNOLOGY PP
 01. OPERATING SYSTEM
                            PP 100
                                        20 F
                                     40
                                100
                                     40
                                        43 P
 04. DATBASE MANAGEMENT SYSTEMS
                            PP
                                100
                                     40
                                        30 F
 05. SOFTWARE ENGINEERING
                            PP
                                100
                                     40
                                        29
                          TW
                                     10 15 P
 06. OPERATING SYSTEM DESIGN LAB.
                                 25
 07. OPERATING SYSTEM DESIGN LAB. PR
                                 50
                                     20 AA F
                                 25
                                    10 17
 08. INFORMATION SYSTEMS DESIGN LAB. TW
 09. INFORMATION SYSTEMS DESIGN LAB. OR
                                 50 20 28 P
 10. NETWORK LABORATORY
                                 25 10 14 P
                   TW
 11. NETWORK LABORATORY
                           OR 50 20 10 F
 FIRST TERM TOTAL = 262/750.
ORDN. 1 MARKS :
JAYASHRI , 71134986L , , PICT , T8058637
 T8058637 UKARANDE SNEHAL ANIL
 01. OPERATING SYSTEM PP 100 40 48 P 02. THEORY OF COMPUTATION PP 100 40 43 P
 03. COMPUTER NETWORK TECHNOLOGY PP 100
                                     40 58 P
 04. DATBASE MANAGEMENT SYSTEMS PP 100
                                     40
                                        61 P
                            PP
 05. SOFTWARE ENGINEERING
                                100
                                     40
                                        42
 07. OPERATING SYSTEM DESIGN LAB.
08. INFORMATION SYSTEM
                                 25
                                     10
                                        22
                            TW
                                 50
                                     20
                                        30
                            PR
 08. INFORMATION SYSTEMS DESIGN LAB. TW
                                 25
                                     10
                                        23
 09. INFORMATION SYSTEMS DESIGN LAB. OR
                                 50
                                     20
                                        31
                    TW
                                 25
                                    10 22
 10. NETWORK LABORATORY
                                50 20 31 P
 11. NETWORK LABORATORY
 FIRST TERM TOTAL = 432/750.
ORDN. 1 MARKS :
, 71045653M , , PICT , T8058638
 T8058638 UPPOD BALAJI SAMBHAJI
                                         SHOBHA
                            PP 100 40
 01. OPERATING SYSTEM PP 100
02. THEORY OF COMPUTATION PP 100
03. COMPUTER NETWORK TECHNOLOGY PP 100
 01. OPERATING SYSTEM
                                    40
                                        40 P
                                    40 54 P
 04. DATBASE MANAGEMENT SYSTEMS PP 100
                                    40 41
                                           Р
                            PP
                                100
                                     40 27
 05. SOFTWARE ENGINEERING
 06. OPERATING SYSTEM DESIGN LAB.
                                 25
                                     10 15 P
                          TW
 07. OPERATING SYSTEM DESIGN LAB. PR
                                 50
                                     20 28
 08. INFORMATION SYSTEMS DESIGN LAB. TW
                                     10 17
 09. INFORMATION SYSTEMS DESIGN LAB. OR
                                 50
                                     20
                                        41
                   TW
OR
                                 25
                                        15
 10. NETWORK LABORATORY
                                     10
                                           Р
                                 50 20
                                        08
 11. NETWORK LABORATORY
 FIRST TERM TOTAL = 346/750.
ORDN. 1 MARKS :
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UNIVERSITY OF PUNE ,T.E. (2008 PAT) (INFORMATION TECHNOLOGY) EXAMINATION NOV. 2011 DATE : 02 MAR. 2012 CENTRÉ : PUNE INSTITUTE OF COMPUTER TECHNOLÓGY, PUNE. PAGE NO. 46 (521) NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER T8058639 VADASADAWALA SHABNAM WAHID BHAI FARIDA , 71045655H , , PICT , T8058639 01. OPERATING SYSTEM
02. THEORY OF COMPUTATION
WETWORK TECHNOLOGY 100 58 P 40 69 P 100 40 PP PP 04. DATBASE MANAGEMENT SYSTEMS 100 40 51 Р 05. SOFTWARE ENGINEERING PP 100 40 43 TW 10 21 06. OPERATING SYSTEM DESIGN LAB. 25 07. OPERATING SYSTEM DESIGN LAB. PR 50 20 38 25 10 21 08. INFORMATION SYSTEMS DESIGN LAB. TW 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 28 P 10. NETWORK LABORATORY 25 10 19 P TW 11. NETWORK LABORATORY OR 50 20 33 P FIRST TERM TOTAL = 454/750. ORDN. 1 MARKS : T8058640 VARSHA SINHA SEEMA SINHA , 71058776H , , PICT , T8058640 PP 100 40 58 P 01. OPERATING SYSTEM PP 100 02. THEORY OF COMPUTATION PP 100 40 68 P 03. COMPUTER NETWORK TECHNOLOGY PP 100 40 69 P 04. DATBASE MANAGEMENT SYSTEMS PP 100 40 67 P PP 05. SOFTWARE ENGINEERING 100 40 54 07. OPERATING SYSTEM DESIGN LAB.
08. INFORMATION SYSTEM 25 10 24 TW 20 40 P 50 PR 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 22 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 10 TW 25 10 23 P 10. NETWORK LABORATORY 50 20 24 11. NETWORK LABORATORY FIRST TERM TOTAL = 482/750. ORDN. 1 MARKS : , 71045658B , , PICT , T8058641 T8058641 VISHWAS JAIN GEETA 01. OPERATING SYSTEM PP 100 02. THEORY OF COMPUTATION PP 100 03. COMPUTER NETWORK TECHNOLOGY PP 100 PP 100 40 45 P 40 57 PP 100 40 57 Р 04. DATBASE MANAGEMENT SYSTEMS PP 100 40 50 05. SOFTWARE ENGINEERING 06. OPERATING SYSTEM DESIGN LAB. 25 10 23 TW 07. OPERATING SYSTEM DESIGN LAB. PR 50 08. INFORMATION SYSTEMS DESIGN LAB. TW 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 37 25 22 10. NETWORK LABORATORY TW 10 Р 50 20 28 11. NETWORK LABORATORY OR FIRST TERM TOTAL = 438/750. ORDN. 1 MARKS :

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UNIVERSITY OF PUNE ,T.E. (2008 PAT) (INFORMATION TECHNOLOGY) EXAMINATION NOV. 2011
   DATE : 02 MAR. 2012 CENTRÉ : PUNE INSTITUTE OF COMPUTER TECHNOLÓGY, PUNE.
                                                                              PAGE NO. 47 ( 522)
  NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
     OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
T8058642 VIVEK GIRIDHAR KANNAKE
                                        PUSHPA
                                                       , 71045659L , , PICT , T8058642
 01. OPERATING SYSTEM
02. THEORY OF COMPUTATION

NETWORK TECHNOLOGY
                            PP
                               100
                                    40
                                       41 P
                               100
                                    40
                                       49 P
                            PP
 04. DATBASE MANAGEMENT SYSTEMS
                           PP
                               100
                                    40
                                       55
                                          Р
 05. SOFTWARE ENGINEERING
                            PP
                               100
                                    40
                                       40
                          TW
                                    10
                                       20
 06. OPERATING SYSTEM DESIGN LAB.
                                25
 07. OPERATING SYSTEM DESIGN LAB. PR
                                50
                                    20 27
                                25
                                    10 21
 08. INFORMATION SYSTEMS DESIGN LAB. TW
 09. INFORMATION SYSTEMS DESIGN LAB. OR
                                50 20 31 P
 10. NETWORK LABORATORY
                                25 10 20 P
                   TW
 11. NETWORK LABORATORY
                          OR
                                50 20 24 P
 FIRST TERM TOTAL = 370/750.
ORDN. 1 MARKS :
T8058643 VIVEK KUMAR
                                                       , 70925651F , , PICT , T8058643
                                        SUMAN
                              100
 01. OPERATING SYSTEM
 02. THEORY OF COMPUTATION PP
                                    40
                              100
                                       53 P
 03. COMPUTER NETWORK TECHNOLOGY PP
                              100
                                    40
                                       59 P
 04. DATBASE MANAGEMENT SYSTEMS
                          PP
                               100
                                    40
                                       52 P
                           PP
 05. SOFTWARE ENGINEERING
                               100
                                    40
                                       40
 07. OPERATING SYSTEM DESIGN LAB.
08. INFORMATION SYSTEM
                                25
                                    10
                                       21
                           TW
                                50
                                    20
                                       39 P
                           PR
 08. INFORMATION SYSTEMS DESIGN LAB. TW
                                25
                                    10
                                       18
 09. INFORMATION SYSTEMS DESIGN LAB. OR
                                50
                                    20
                                       28
                    TW
                                25
                                    10 20 P
 10. NETWORK LABORATORY
                                50 20 30
 11. NETWORK LABORATORY
 FIRST TERM TOTAL = 426/750.
ORDN. 1 MARKS:
, 71045660D , , PICT , T8058644
 T8058644 VIVERT JAIN
                                        NIDHI
 01. OPERATING SYSTEM
02. THEORY OF COMPUTATION
03. COMPUTER NETWORK TECHNOLOGY
                           PP 100
                           PP 100
                                   40 45 P
                            PP 100
                                   40 62
                            PP
                               100
                                    40
                                       52
                                          Р
 04. DATBASE MANAGEMENT SYSTEMS
                                       57
                            PP
                               100
                                    40
                                          Р
 05. SOFTWARE ENGINEERING
 06. OPERATING SYSTEM DESIGN LAB.
                                25
                                    10
                          TW
 07. OPERATING SYSTEM DESIGN LAB.
                           PR
                                50
 08. INFORMATION SYSTEMS DESIGN LAB. TW
 09. INFORMATION SYSTEMS DESIGN LAB. OR
                                50
                                    20
                                       28
                                25
 10. NETWORK LABORATORY
                            TW
                                    10
                                       21
                                          Р
                                50 20
 11. NETWORK LABORATORY
                         OR
                                       32
 FIRST TERM TOTAL = 371/750.
ORDN. 1 MARKS:
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UNIVERSITY OF PUNE ,T.E. (2008 PAT) (INFORMATION TECHNOLOGY) EXAMINATION NOV. 2011 DATE : 02 MAR. 2012 CENTRÉ : PUNE INSTITUTE OF COMPUTER TECHNOLÓGY, PUNE. PAGE NO. 48 (523) NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER T8058646 WAGHMODE SACHIN DADASO SHOBHA , 71045663J , , PICT , T8058646 01. OPERATING SYSTEM
02. THEORY OF COMPUTATION
03. COMPUTER NETWORK TECHNOLOGY PP 100 60 P 40 58 PP 100 40 Ρ 04. DATBASE MANAGEMENT SYSTEMS PP 100 40 65 Р 05. SOFTWARE ENGINEERING PP 100 40 TW 10 19 06. OPERATING SYSTEM DESIGN LAB. 25 07. OPERATING SYSTEM DESIGN LAB. PR 50 20 28 25 10 21 08. INFORMATION SYSTEMS DESIGN LAB. TW 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 28 P 10. NETWORK LABORATORY 25 10 20 P TW 11. NETWORK LABORATORY OR 50 20 10 F FIRST TERM TOTAL = 431/750. ORDN. 1 MARKS : T8058647 WAYKOLE VRUSHALI PRAVIN ARUNA , 71045666C , , PICT , T8058647 01. OPERATING SYSTEM PP 100 02. THEORY OF COMPUTATION PP 100 03. COMPUTER NETWORK TECHNOLOGY PP 100 PP 100 40 42 P 40 50 P 40 47 P 04. DATBASE MANAGEMENT SYSTEMS PP 100 40 50 P 05. SOFTWARE ENGINEERING PP 100 40 43 07. OPERATING SYSTEM DESIGN LAB.
08. INFORMATION SYSTEM 25 10 23 TW 50 20 39 PR 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 12 TW 25 10 22 10. NETWORK LABORATORY Р 50 20 24 11. NETWORK LABORATORY FIRST TERM TOTAL = 393/750. ORDN. 1 MARKS : , 71134987J , , PICT , T8058648 T8058648 YADAV SNEHA HANAMANT NANDA PP 100 40 01. OPERATING SYSTEM PP 100
02. THEORY OF COMPUTATION PP 100
03. COMPUTER NETWORK TECHNOLOGY PP 100 01. OPERATING SYSTEM 40 68 P 40 70 P PP 100 40 68 Р 04. DATBASE MANAGEMENT SYSTEMS PP 100 40 65 05. SOFTWARE ENGINEERING 06. OPERATING SYSTEM DESIGN LAB. 25 10 TW 07. OPERATING SYSTEM DESIGN LAB. PR 50 08. INFORMATION SYSTEMS DESIGN LAB. TW 10 23 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 31 25 22 10. NETWORK LABORATORY TW 10 Р 50 20 30 11. NETWORK LABORATORY OR FIRST TERM TOTAL = 511/750. ORDN. 1 MARKS:

UNIVERSITY OF PUNE ,T.E. (2008 PAT) (INFORMATION TECHNOLOGY) EXAMINATION NOV. 2011 DATE : 02 MAR. 2012 CENTRÉ : PUNE INSTITUTE OF COMPUTER TECHNOLÓGY, PUNE. PAGE NO. 49 (524) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER T8058649 ZAJRIYA AASHAY MAHESH CHHAYA , 71045670M , , PICT , T8058649 01. OPERATING SYSTEM
02. THEORY OF COMPUTATION PP
DEPTH OF THE WORK TECHNOLOGY PP PP 100 40 PP 100 50 P 40 63 P 100 40 04. DATBASE MANAGEMENT SYSTEMS PP 100 40 58 Р 05. SOFTWARE ENGINEERING PP 100 40 53 Р TW 10 23 06. OPERATING SYSTEM DESIGN LAB. 25 07. OPERATING SYSTEM DESIGN LAB. PR 50 20 41 25 10 24 08. INFORMATION SYSTEMS DESIGN LAB. TW 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 20 P 10. NETWORK LABORATORY TW 25 10 23 P 11. NETWORK LABORATORY OR 50 20 32 P FIRST TERM TOTAL = 454/750. ORDN. 1 MARKS : T8058650 ABHINAV CHATURVEDI , 70801315F , T8058501 , PICT , T8058650 ABHA 01. OPERATING SYSTEM PP 100 40 40 P 13. SYSTEM SOFTWARE PROGRAMMING 02. THEORY OF COMPUTATION PP 100 40 41 P 14. MANAGEMENT INFORMATION SYSTEMS 03. COMPUTER NETWORK TECHNOLOGY PP 100 40 41 P 15. PROGRAMMING PARADICMS 40 AA F 14. MANAGEMENT INFORMATION SYSTEMS PP 100 40 48 P.C 15. PROGRAMMING PARADIGMS PP 100 40 41 P.C 04. DATBASE MANAGEMENT SYSTEMS PP 100 05. SOFTWARE ENGINEERING PP 100 40 27 F 16. DESIGN & ANALYSIS OF ALGORITHMS PP 100 40 45 P 40 40 P C 17. HUMAN COMPU.INTERACTION & USABI. PP 40 47 P C 100 06. OPERATING SYSTEM DESIGN LAB. TW
07. OPERATING SYSTEM DESIGN LAB. PR 18. SOFTWARE DESIGN LABORATORY TW
19. SOFTWARE DESIGN LABORATORY PR 25 10 10 P C 50 20 20 P C 19. SOFTWARE DESIGN LABORATION.
20. SOFTWARE DEVELOPMENT TOOLS LAB. TW 50 20 32 P 21. SOFTWARE DEVELOPMENT TOOLS LAB. OR 50 20 32 P 22. SEMINAR AND TECHNICAL COMMUN. TW 50 20 20 P C 50 20 AA F 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 10 P C 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 27 P C 25 10 10 P C TW 10. NETWORK LABORATORY OR 50 20 26 P.C. 11. NETWORK LABORATORY GRAND TOTAL = 579/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS : , 70925316J , T8058502 , PICT , T8058651 T8058651 ABNAVE NRUPEN PRAKASH HEMLATA 01. OPERATING SYSTEM PP 100 40 40 P C 02. THEORY OF COMPUTATION PP 100 40 40 P C 03. COMPUTER NETWORK TECHNOLOGY PP 100 40 40 P C PP 100 40 40 P C 13. SYSTEM SOFTWARE PROGRAMMING PP 100 40 43 P.C. 14. MANAGEMENT INFORMATION SYSTEMS PP 100 40 41 P C 15. PROGRAMMING PARADIGMS PP 100 40 47 P 04. DATBASE MANAGEMENT SYSTEMS PP 100 40 40 P C 05. SOFTWARE ENGINEERING PP 100 40 40 P C 16. DESIGN & ANALYSIS OF ALGORITHMS PP 100 40 40 P 17. HUMAN COMPU.INTERACTION & USABI. PP 100 40 50 P C 06. OPERATING SYSTEM DESIGN LAB. TW 25 10 10 P C 18. SOFTWARE DESIGN LABORATORY TW 50 20 20 P C 07. OPERATING SYSTEM DESIGN LAB. PR 50 20 28 P C 19. SOFTWARE DESIGN LABORATORY PR 50 20 32 P C 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 10 P C 20. SOFTWARE DEVELOPMENT TOOLS LAB. TW 50 20 20 P C 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 32 P C 21. SOFTWARE DEVELOPMENT TOOLS LAB. OR 50 20 P TW OR 25 10 10 P C 22. SEMINAR AND TECHNICAL COMMUN. TW 50 20 27 P C 10. NETWORK LABORATORY TW 50 20 21 P 11. NETWORK LABORATORY

GRAND TOTAL = 661/1500, RESULT: PASS CLASS ORDN. 1 MARKS:

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DATE: 02 MAR. 2012 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 50 (525)

NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8058652 AGARWAL AYUSH ATUL NISHI , 70503791G , T8058504 , PICT , T8058652

01. OPERATING SYSTEM 100 44 P C PP 58 P C 13. SYSTEM SOFTWARE PROGRAMMING 02. THEORY OF COMPUTATION 45 P C PP 100 40 16 F 14. MANAGEMENT INFORMATION SYSTEMS PP 100 40 40 43 P C 03. COMPUTER NETWORK TECHNOLOGY PP 100 15. PROGRAMMING PARADIGMS PP 100 40 AA F 04. DATBASE MANAGEMENT SYSTEMS PP 100 40 44 P C 16. DESIGN & ANALYSIS OF ALGORITHMS PP 100 40 40 P C 43 P C 05. SOFTWARE ENGINEERING PP 100 40 40 P C 17. HUMAN COMPU.INTERACTION & USABI. PP 100 40 10 19 P C 20 06. OPERATING SYSTEM DESIGN LAB. TW 25 18. SOFTWARE DESIGN LABORATORY TW 50 34 P C 07. OPERATING SYSTEM DESIGN LAB. 50 20 42 P C 19. SOFTWARE DESIGN LABORATORY PR 50 20 40 P C PR 25 50 20 33 P C 08. INFORMATION SYSTEMS DESIGN LAB. TW 10 18 P C 20. SOFTWARE DEVELOPMENT TOOLS LAB. TW 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 38 P C 21. SOFTWARE DEVELOPMENT TOOLS LAB. OR 50 20 30 P C 10. NETWORK LABORATORY 25 10 19 P C 22. SEMINAR AND TECHNICAL COMMUN. TW 50 20 27 P C TW 11. NETWORK LABORATORY 50 20 38 P C OR

GRAND TOTAL = 731/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS :

T8058653 AKSHAT GOEL				TO	OLIKA	, 70925323M , T8058507 , PICT , T805869	53
01. OPERATING SYSTEM	PP	100	40	55	РС	13. SYSTEM SOFTWARE PROGRAMMING PP 100 40 75 F	РС
02. THEORY OF COMPUTATION	PP	100	40	60	РС	14. MANAGEMENT INFORMATION SYSTEMS PP 100 40 62 F	РС
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	58	РС	15. PROGRAMMING PARADIGMS PP 100 40 53 F	РС
04. DATBASE MANAGEMENT SYSTEMS	PP	100	40	41	РС	16. DESIGN & ANALYSIS OF ALGORITHMS PP 100 40 50 F	РС
05. SOFTWARE ENGINEERING	PP	100	40	54	РС	17. HUMAN COMPU.INTERACTION & USABI. PP 100 40 69 F	РС
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	10	РС	18. SOFTWARE DESIGN LABORATORY TW 50 20 1	РС
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	32	РС	19. SOFTWARE DESIGN LABORATORY PR 50 20 21 F	Р
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	10	РС	20. SOFTWARE DEVELOPMENT TOOLS LAB. TW 50 20 25 F	РС
O9. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	38	РС	21. SOFTWARE DEVELOPMENT TOOLS LAB. OR 50 20 34 F	Р
10. NETWORK LABORATORY	TW	25	10	10	РС	22. SEMINAR AND TECHNICAL COMMUN. TW 50 20 34 F	РС
11. NETWORK LABORATORY	OR	50	20	35	PС		

GRAND TOTAL = 856/1500, RESULT: HIGHER SECOND CLASS ORDN. 1 MARKS:

T8058654 AKSHAY MADHOGARIA				ANITA	, 70925325н , т8058509 , рі	СТ	, т8058654	
01. OPERATING SYSTEM 02. THEORY OF COMPUTATION 03. COMPUTER NETWORK TECHNOLOGY 04. DATBASE MANAGEMENT SYSTEMS 05. SOFTWARE ENGINEERING	PP PP PP PP	100 100 100 100 100	40 40 40 40 40	40 P C 55 P C 55 P C 40 P C	13. SYSTEM SOFTWARE PROGRAMMING PP 14. MANAGEMENT INFORMATION SYSTEMS PP 15. PROGRAMMING PARADIGMS PP 16. DESIGN & ANALYSIS OF ALGORITHMS PP 17. HUMAN COMPULINTERACTION & USABI. PP	100 4 100 4 100 4 100 4 100 4	0 50 0 52 0 48	P C P C P C P C
06. OPERATING SYSTEM DESIGN LAB. 07. OPERATING SYSTEM DESIGN LAB. 08. INFORMATION SYSTEMS DESIGN LAB.	TW PR TW	25 50 25	10 20 10	10 P C 35 P C 10 P C	18. SOFTWARE DESIGN LABORATORY 19. SOFTWARE DESIGN LABORATORY 20. SOFTWARE DEVELOPMENT TOOLS LAB. TW	50 2 50 2	0 20	P C P P C
09. INFORMATION SYSTEMS DESIGN LAB. 10. NETWORK LABORATORY 11. NETWORK LABORATORY	OR TW OR	50 25 50	20 10 20	32 P C 10 P C 22 P C	21. SOFTWARE DEVELOPMENT TOOLS LAB. OR 22. SEMINAR AND TECHNICAL COMMUN. TW		0 22 0 25	P P C

GRAND TOTAL = 737/1500, RESULT: PASS CLASS ORDN. 1 MARKS:

UNIVERSITY OF PUNE ,T.E.(2008 PAT)(INFORMATION TECHNOLOGY) EXAMINATION NOV. 2011

DATE : 02 MAR. 2012 CENTRE : PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 51 (526)

NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8058655 ANIKET PALLEWAD				NEELAWATI	, 70925330D , T8058511 , PICT , T8058	655
01. OPERATING SYSTEM 02. THEORY OF COMPUTATION 03. COMPUTER NETWORK TECHNOLOGY	PP PP PP	100 100 100	40 40 40	50 P C 44 P C 45 P C		P C P C P C
04. DATBASE MANAGEMENT SYSTEMS 05. SOFTWARE ENGINEERING	PP PP	100 100	40 40	41 P C 44 P C	16. DESIGN & ANALYSIS OF ALGORITHMS PP 100 40 40 17. HUMAN COMPULINTERACTION & USABIL PP 100 40 58	P C P C
06. OPERATING SYSTEM DESIGN LAB. 07. OPERATING SYSTEM DESIGN LAB.	TW PR	25 50	10 20	17 P C 25 P C	18. SOFTWARE DESIGN LABORATORY TW 50 20 25 19. SOFTWARE DESIGN LABORATORY PR 50 20 38	PC
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	17 P C	20. SOFTWARE DEVELOPMENT TOOLS LAB. TW 50 20 22	РС
09. INFORMATION SYSTEMS DESIGN LAB. 10. NETWORK LABORATORY	OR TW	50 25	20 10	25 P C 15 P C	21. SOFTWARE DEVELOPMENT TOOLS LAB. OR 50 20 08 22. SEMINAR AND TECHNICAL COMMUN. TW 50 20 20	F P C
11. NETWORK LABORATORY	OR	50	20	23 P C		

GRAND TOTAL = 737/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS:

T8058656 ANIL KUMAR				NA	NINA DEVI		, 70925331в , т8058512 ,	, P	ICT	,	T8058	656
01. OPERATING SYSTEM	PP	100	40	40	РС	13.	SYSTEM SOFTWARE PROGRAMMING	PP	100	40	64	РС
02. THEORY OF COMPUTATION	PP	100	40	43	РC	14.	MANAGEMENT INFORMATION SYSTEMS	PP	100	40	51	РС
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	55	РC	15.	PROGRAMMING PARADIGMS	PP	100	40	40	РС
04. DATBASE MANAGEMENT SYSTEMS	PP	100	40	40	PС	16.	DESIGN & ANALYSIS OF ALGORITHMS	PP	100	40	55	РС
05. SOFTWARE ENGINEERING	PP	100	40	46	PC	17.	HUMAN COMPU.INTERACTION & USABI.	. PP	100	40	61	РС
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	14	PC	18.	SOFTWARE DESIGN LABORATORY	TW	50	20	20	РС
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	36	PC	19.	SOFTWARE DESIGN LABORATORY	PR	50	20	20	Р
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	14	РC	20.	SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	24	РС
O9. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	30	PС	21.	SOFTWARE DEVELOPMENT TOOLS LAB.	OR	50	20	28	Ρ
10. NETWORK LABORATORY	TW	25	10	15	PC	22.	SEMINAR AND TECHNICAL COMMUN.	TW	50	20	30	РС
11. NETWORK LABORATORY	OR	50	20	28	РC							

GRAND TOTAL = 772/1500, RESULT: SECOND CLASS ORDN. 1 MARKS :

T8058657 ARJAN BAJAJ				BANDANA	, 70701364L , T8058516 , PICT	, т8058657
01. OPERATING SYSTEM 02. THEORY OF COMPUTATION 03. COMPUTER NETWORK TECHNOLOGY 04. DATBASE MANAGEMENT SYSTEMS 05. SOFTWARE ENGINEERING 06. OPERATING SYSTEM DESIGN LAB. 07. OPERATING SYSTEM DESIGN LAB. 08. INFORMATION SYSTEMS DESIGN LAB. 09. INFORMATION SYSTEMS DESIGN LAB. 10. NETWORK LABORATORY	PP PP PP PP TW PR TW OR TW	100 100 100 100 100 25 50 25 50	40 40 40 40 40 10 20 10 20	40 P C 46 P C 55 P C 40 P C 10 P C 28 P C 10 P C 30 P C	13. SYSTEM SOFTWARE PROGRAMMING PP 100 14. MANAGEMENT INFORMATION SYSTEMS PP 100 15. PROGRAMMING PARADIGMS PP 100 16. DESIGN & ANALYSIS OF ALGORITHMS PP 100 17. HUMAN COMPU.INTERACTION & USABI. PP 100 18. SOFTWARE DESIGN LABORATORY TW 50 19. SOFTWARE DESIGN LABORATORY PR 50 20. SOFTWARE DEVELOPMENT TOOLS LAB. TW 50 21. SOFTWARE DEVELOPMENT TOOLS LAB. OR 50 22. SEMINAR AND TECHNICAL COMMUN. TW 50	40 66 P C 40 44 P C 40 44 P C 40 44 P C 40 56 P C 20 34 P C 20 21 P C 20 26 P 20 24 P C
11. NETWORK LABORATORY	OR	50	20	21 P		

GRAND TOTAL = 730/1500, RESULT: PASS CLASS ORDN. 1 MARKS:

RESULT RESERVED FOR BKLG

UNIVERSITY OF PUNE ,T.E.(2008 PAT)(INFORMATION TECHNOLOGY) EXAMINATION NOV. 2011

DATE: 02 MAR. 2012 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 52 (527)

NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8058658 AVASARE ARUN RAKHAMAJI PADMINI , 70925345B , T8058517 , PICT , T8058658

T8058658 AVASARE ARUN RAKHAMAJI				PA	DMINI	, 70925345B , T8058517 , PICT , T8058658
01. OPERATING SYSTEM	PP	100	40	40	РС	13. SYSTEM SOFTWARE PROGRAMMING PP 100 40 48 P C
02. THEORY OF COMPUTATION	PP	100	40	40	PС	14. MANAGEMENT INFORMATION SYSTEMS PP 100 40 AA F
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	42	РС	15. PROGRAMMING PARADIGMS PP 100 40 40 P
04. DATBASE MANAGEMENT SYSTEMS	PP	100	40	40	РС	16. DESIGN & ANALYSIS OF ALGORITHMS PP 100 40 47 P
05. SOFTWARE ENGINEERING	PP	100	40	40	РС	17. HUMAN COMPU.INTERACTION & USABI. PP 100 40 47 P C
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	14	РС	18. SOFTWARE DESIGN LABORATORY TW 50 20 P C
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	37	РС	19. SOFTWARE DESIGN LABORATORY PR 50 20 34 P C
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	12	РС	20. SOFTWARE DEVELOPMENT TOOLS LAB. TW 50 20 23 P C
O9. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	40	РС	21. SOFTWARE DEVELOPMENT TOOLS LAB. OR 50 20 25 P
10. NETWORK LABORATORY	TW	25	10	16	РС	22. SEMINAR AND TECHNICAL COMMUN. TW 50 20 34 P C
11. NETWORK LABORATORY	OR	50	20	35	РС	

GRAND TOTAL = 692/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS :

T8058659 BERDE SIDDESH VISHWANATH	ł		VAISHALI			, 71073822G , T8058523 , PICT , T805	, т8058659		
01. OPERATING SYSTEM	PP	100	40	45	РС	13. SYSTEM SOFTWARE PROGRAMMING PP 100 40 55	5 P C		
02. THEORY OF COMPUTATION	PP	100	40	40	РС	14. MANAGEMENT INFORMATION SYSTEMS PP 100 40 51	LPC		
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	56	РС	15. PROGRAMMING PARADIGMS PP 100 40 44	1 P C		
04. DATBASE MANAGEMENT SYSTEMS	PP	100	40	40	РС	16. DESIGN & ANALYSIS OF ALGORITHMS PP 100 40 AA	A F		
05. SOFTWARE ENGINEERING	PP	100	40	61	РС	17. HUMAN COMPU.INTERACTION & USABI. PP 100 40 68	3 PC		
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	12	РС	18. SOFTWARE DESIGN LABORATORY TW 50 20 20) P C		
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	37	РС	19. SOFTWARE DESIGN LABORATORY PR 50 20 37	7 P C		
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	11	РС	20. SOFTWARE DEVELOPMENT TOOLS LAB. TW 50 20 30) P C		
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	32	РС	21. SOFTWARE DEVELOPMENT TOOLS LAB. OR 50 20 30) P C		
10. NETWORK LABORATORY	TW	25	10	12	РС	22. SEMINAR AND TECHNICAL COMMUN. TW 50 20 3!	5 P C		
11. NETWORK LABORATORY	OR	50	20	33	РС				

GRAND TOTAL = 770/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS:

RESULT RESERVED FOR BKLG

T8058660 BHANGALE DEEPAK SATISH				SEEMA	, 70503833F , T8058525 , PICT , T8058660	0
01. OPERATING SYSTEM	PP	100	40	46 P C	13. SYSTEM SOFTWARE PROGRAMMING PP 100 40 58 P	С
02. THEORY OF COMPUTATION	PP	100	40	42 P C	14. MANAGEMENT INFORMATION SYSTEMS PP 100 40 49 P	C
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	40 P	15. PROGRAMMING PARADIGMS PP 100 40 40 P	C
04. DATBASE MANAGEMENT SYSTEMS	PP	100	40	43 P	16. DESIGN & ANALYSIS OF ALGORITHMS PP 100 40 46 P	
05. SOFTWARE ENGINEERING	PP	100	40	40 P C	17. HUMAN COMPU.INTERACTION & USABI. PP 100 40 49 P	
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	20 P C	18. SOFTWARE DESIGN LABORATORY TW 50 20 41 P	C
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	32 P C	19. SOFTWARE DESIGN LABORATORY PR 50 20 43 P	C
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	18 P C	20. SOFTWARE DEVELOPMENT TOOLS LAB. TW 50 20 39 P	C
09. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	40 P C	21. SOFTWARE DEVELOPMENT TOOLS LAB. OR 50 20 30 P	C
10. NETWORK LABORATORY	TW	25	10	18 P C	22. SEMINAR AND TECHNICAL COMMUN. TW 50 20 30 P	C
11. NETWORK LABORATORY	OR	50	20	28 P C		

GRAND TOTAL = 812/1500, RESULT: SECOND CLASS ORDN. 1 MARKS:

UNIVERSITY OF PUNE ,T.E. (2008 PAT) (INFORMATION TECHNOLOGY) EXAMINATION NOV. 2011 DATE : 02 MAR. 2012 CENTRÉ : PUNE INSTITUTE OF COMPUTER TECHNOLÓGY, PUNE. PAGE NO. 53 (528) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER T8058661 GHODE PRAVIN EKNATH YAMUNABAI , 70801429B , T8058553 , PICT , T8058661 PP 100 40 47 P C PP 100 40 58 P C 01. OPERATING SYSTEM 13. SYSTEM SOFTWARE PROGRAMMING 01. OPERATING SYSTEM PP 100 40
02. THEORY OF COMPUTATION PP 100 40
03. COMPUTER NETWORK TECHNOLOGY PP 100 40
04. DATBASE MANAGEMENT SYSTEMS PP 100 40 14. MANAGEMENT INFORMATION SYSTEMS PP 100 51 PC 40 48 P.C. 02. IMEDIT OF COMPUTATION PP 100 40 51 P C 14. MANAGEMENT INFORMATION SYSTEMS PP 100 40 48 P C 03. COMPUTER NETWORK TECHNOLOGY PP 100 40 58 P C 15. PROGRAMMING PARADIGMS PP 100 40 40 P C 05. SOFTWARE ENGINEERING PP 100 40 45 P C 16. DESIGN & ANALYSIS OF ALGORITHMS PP 100 40 42 P C 16. DESIGN & ANALYSIS OF ALGORITHMS PP 100 40 59 P C 17. HUMAN COMPU.INTERACTION & USABI. PP 100 40 59 P C 18. SOFTWARE DESIGN LABORATORY TW 50 20 20 P C 18. SOFTWARE DESIGN LABORATORY PR 50 20 20 P C 19. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 12 P C 20. SOFTWARE DESIGN LABORATORY PR 50 20 20 P C 10. NETWORK LABORATORY TW 25 10 10 P C 21. SOFTWARE DEVELOPMENT TOOLS LAB. TW 50 20 20 P C 11. NETWORK LABORATORY OR 50 20 35 P C TW 25 10 10 P C OR 50 20 35 P C 11. NETWORK LABORATORY GRAND TOTAL = 731/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS: ARUNA , 70925445J , T8058557 , PICT , T8058662 T8058662 JADHAV DEEPANJAN MADHAV U1. OPERATING SYSTEM PP 100 40 45 P C 02. THEORY OF COMPUTATION PP 100 40 52 P C 03. COMPUTER NETWORK TECHNOLOGY PP 100 40 72 P C 04. DATRASE MANAGEMENT SYSTEM PP 100 40 59 P.C 13. SYSTEM SOFTWARE PROGRAMMING 14. MANAGEMENT INFORMATION SYSTEMS PP 100 40 47 P.C 15. PROGRAMMING PARADIGMS PP 100 40 40 P C 03. COMPUTER NETWORK TECHNOLOGY PP 100 40 72 P C 15. PROGRAMMING PARADIGMS PP 100 40 40 P C 04. DATBASE MANAGEMENT SYSTEMS PP 100 40 53 P 16. DESIGN & ANALYSIS OF ALGORITHMS PP 100 40 43 P C 17. HUMAN COMPULINTERACTION & USABI. PP 100 40 55 P C 18. SOFTWARE DESIGN LABORATORY TW 50 20 24 P C 19. SOFTWARE DESIGN LABORATORY PR 50 20 AA F 19. SOFTWARE DESIGN LABORATORY PR 50 20 AA F 19. SOFTWARE DESIGN LABORATORY PR 50 20 AA F 19. SOFTWARE DESIGN LABORATORY PR 50 20 AA F 19. SOFTWARE DESIGN LABORATORY PR 50 20 AA F 19. SOFTWARE DESIGN LABORATORY PR 50 20 AA F 19. SOFTWARE DESIGN LABORATORY PR 50 20 AA F 19. SOFTWARE DEVELOPMENT TOOLS LAB. TW 50 20 20 P C 10. NETWORK LABORATORY TW 25 10 16 P C 21. SOFTWARE DEVELOPMENT TOOLS LAB. OR 50 20 34 P C 11. NETWORK LABORATORY OR 50 20 42 P C GRAND TOTAL = 712/1500, RESULT: FAILS A.T.K.T. RESULT RESERVED FOR BKLG ORDN. 1 MARKS : , 70925460B , T8058565 , PICT , T8058663 T8058663 KAKADE SHASHIKANT AVINASH NANDA

 01. OPERATING SYSTEM
 PP
 100
 40
 48
 P C

 02. THEORY OF COMPUTATION
 PP
 100
 40
 51
 P C

 03. COMPUTER NETWORK TECHNOLOGY
 PP
 100
 40
 47
 P C

 04. DATBASE MANAGEMENT SYSTEMS
 PP
 100
 40
 40
 P C

 05. SOFTWARE ENGINEERING
 PP
 100
 40
 40
 P C

 13. SYSTEM SOFTWARE PROGRAMMING PP 100 40 50 P.C. 14. MANAGEMENT INFORMATION SYSTEMS PP 100 40 45 P C 15. PROGRAMMING PARADIGMS PP 100 40 45 P 16. DESIGN & ANALYSIS OF ALGORITHMS PP 100 40 41 P 17. HUMAN COMPU.INTERACTION & USABI. PP 100 40 49 P C 06. OPERATING SYSTEM DESIGN LAB. TW 25 10 14 P C 18. SOFTWARE DESIGN LABORATORY TW 50 20 22 P C 07. OPERATING SYSTEM DESIGN LAB. PR 50 20 35 P C 19. SOFTWARE DESIGN LABORATORY PR 50 20 38 P.C. 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 12 P C 20. SOFTWARE DEVELOPMENT TOOLS LAB. TW 50 20 24 P C 21. SOFTWARE DEVELOPMENT TOOLS LAB. OR 50 20 35 P C 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 34 P C TW OR 25 10 12 P C 22. SEMINAR AND TECHNICAL COMMUN. TW 50 20 26 P C 10. NETWORK LABORATORY TW 50 20 20 P 11. NETWORK LABORATORY

GRAND TOTAL = 740+10/1500, RESULT: SECOND CLASS [0.2] ORDN. 1 MARKS:

UNIVERSITY OF PUNE ,T.E. (2008 PAT) (INFORMATION TECHNOLOGY) EXAMINATION NOV. 2011 DATE : 02 MAR. 2012 CENTRÉ : PÛNE INSTITÛTE OF COMPUTER TECHNOLÔGY, PUNE. PAGE NO. 54 (529) NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MÍN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER T8058664 MADGULWAR PRASHANT ASHOKRAO VIJAYALAKSHMI , 70503949J , T8058576 , PICT , т8058664 100 43 P C 01. OPERATING SYSTEM 40 P C 13. SYSTEM SOFTWARE PROGRAMMING 02. THEORY OF COMPUTATION PP 100 40 40 P C 14. MANAGEMENT INFORMATION SYSTEMS PP 100 40 40 P C 40 03. COMPUTER NETWORK TECHNOLOGY PP 100 62 P C 15. PROGRAMMING PARADIGMS PP 100 40 40 P C 04. DATBASE MANAGEMENT SYSTEMS PP 100 40 40 P C 16. DESIGN & ANALYSIS OF ALGORITHMS PP 100 40 40 P C

17. HUMAN COMPU.INTERACTION & USABI. PP

20. SOFTWARE DEVELOPMENT TOOLS LAB. TW

21. SOFTWARE DEVELOPMENT TOOLS LAB. OR

22. SEMINAR AND TECHNICAL COMMUN. TW

18. SOFTWARE DESIGN LABORATORY

19. SOFTWARE DESIGN LABORATORY

100

50

50

50

TW

PR

40

20

20

20

50 20 22 P

50 20 20 P C

61 P C

25 P C

20 P C

15* P

GRAND TOTAL = 666/1500, RESULT: PASS CLASS * [0.4] ORDN. 1 MARKS:

PP

TW

PR

TW

OR

100

25

50

25

50

25

40

10

20

10

20

10

50 20 22 P

41 P C

12 P C

22 P C

12 P C

29 P C

10 P C

05. SOFTWARE ENGINEERING

10. NETWORK LABORATORY

11. NETWORK LABORATORY

06. OPERATING SYSTEM DESIGN LAB.

07. OPERATING SYSTEM DESIGN LAB.

08. INFORMATION SYSTEMS DESIGN LAB. TW

09. INFORMATION SYSTEMS DESIGN LAB. OR

T8058665 MAGAR ASHWINI DEELIP				SH	ALINI	, 71073835J , T8058577 , PICT , T8058665
01. OPERATING SYSTEM	PP	100	40	45	РС	13. SYSTEM SOFTWARE PROGRAMMING PP 100 40 62 PC
02. THEORY OF COMPUTATION	PP	100	40	48	РС	14. MANAGEMENT INFORMATION SYSTEMS PP 100 40 59 P (
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	68	РС	15. PROGRAMMING PARADIGMS PP 100 40 61 P (
04. DATBASE MANAGEMENT SYSTEMS	PP	100	40	40	РС	16. DESIGN & ANALYSIS OF ALGORITHMS PP 100 40 44 P (
05. SOFTWARE ENGINEERING	PP	100	40	42	PС	17. HUMAN COMPU.INTERACTION & USABI. PP 100 40 61 P (
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	19	РС	18. SOFTWARE DESIGN LABORATORY TW 50 20 38 P (
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	AA	F	19. SOFTWARE DESIGN LABORATORY PR 50 20 38 P (
O8. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	18	РС	20. SOFTWARE DEVELOPMENT TOOLS LAB. TW 50 20 43 P (
O9. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	32	РС	21. SOFTWARE DEVELOPMENT TOOLS LAB. OR 50 20 35 P (
10. NETWORK LABORATORY	TW	25	10	20	РС	22. SEMINAR AND TECHNICAL COMMUN. TW 50 20 38 P (
11. NETWORK LABORATORY	OR	50	20	40	РС	

GRAND TOTAL = 871/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS :

T8058666 MANDORA BHAGYASHREE SATI	ESH			SA	APANA	, 70925498K , T8058578 , PICT	, т	80586	66
01. OPERATING SYSTEM 02. THEORY OF COMPUTATION 03. COMPUTER NETWORK TECHNOLOGY 04. DATBASE MANAGEMENT SYSTEMS	PP PP PP	100 100 100 100	40 40 40 40	47 58 57 51	P C P C P C	13. SYSTEM SOFTWARE PROGRAMMING PP 100 14. MANAGEMENT INFORMATION SYSTEMS PP 100 15. PROGRAMMING PARADIGMS PP 100 16. DESIGN & ANALYSIS OF ALGORITHMS PP 100	40 40 40 40	40 44	P C P C P C
05. SOFTWARE ENGINEERING	PP	100	40	56		17. HUMAN COMPU.INTERACTION & USABI. PP 100	40		PC
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	16	РС	18. SOFTWARE DESIGN LABORATORY TW 50	20	26	РС
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	32	РС	19. SOFTWARE DESIGN LABORATORY PR 50	20		РС
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	14	РС	20. SOFTWARE DEVELOPMENT TOOLS LAB. TW 50	20		РС
O9. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	36	РС	21. SOFTWARE DEVELOPMENT TOOLS LAB. OR 50	20	35	Р
10. NETWORK LABORATORY	TW	25	10	16		22. SEMINAR AND TECHNICAL COMMUN. TW 50	20	32	РС
11. NETWORK LABORATORY	OR	50	20	36	РС				

GRAND TOTAL = 820+05/1500, RESULT: HIGHER SECOND CLASS [0.2] ORDN. 1 MARKS :

UNIVERSITY OF PUNE ,T.E. (2008 PAT) (INFORMATION TECHNOLOGY) EXAMINATION NOV. 2011 DATE : 02 MAR. 2012 CENTRÉ : PÛNE INSTITÛTE OF COMPUTER TECHNOLÓGY, PUNE. PAGE NO. 55 (530) NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8058667 MAYANK BHATNAGAR				SH	IEETAL	, 70925502м , т8058580 , ріст , т8058667	
01. OPERATING SYSTEM	PP	100	40	1.5	РС	13. SYSTEM SOFTWARE PROGRAMMING PP 100 40 53 P	_
02. THEORY OF COMPUTATION	PP	100	40	45	РС	14. MANAGEMENT INFORMATION SYSTEMS PP 100 40 41 P	C
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	62	РС	15. PROGRAMMING PARADIGMS PP 100 40 50 P	C
04. DATBASE MANAGEMENT SYSTEMS	PP	100	40	43	РС	16. DESIGN & ANALYSIS OF ALGORITHMS PP 100 40 54 P	
05. SOFTWARE ENGINEERING	PP	100	40	43	РС	17. HUMAN COMPU.INTERACTION & USABI. PP 100 40 56 P	C
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	18	РС	18. SOFTWARE DESIGN LABORATORY TW 50 20 35 P	C
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	38	РС	19. SOFTWARE DESIGN LABORATORY PR 50 20 36 P	C
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	17	РС	20. SOFTWARE DEVELOPMENT TOOLS LAB. TW 50 20 39 P	C
O9. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	42	РС	21. SOFTWARE DEVELOPMENT TOOLS LAB. OR 50 20 32 P	
10. NETWORK LABORATORY	TW	25	10	19	РС	22. SEMINAR AND TECHNICAL COMMUN. TW 50 20 37 P	C
11. NETWORK LABORATORY	OR	50	20	42	PС		

GRAND TOTAL = 867/1500, RESULT: HIGHER SECOND CLASS ORDN. 1 MARKS :

T8058668 MOHD ZEESHAN MOHD ROSHAN	SHA:	IKH		SH	AMIM P	ARVIN	, 70925506D , Т8058581	, P	ICT	,	т8058	668
01. OPERATING SYSTEM	PP	100	40	48	РС	13.	SYSTEM SOFTWARE PROGRAMMING	PP	100	40	65	РС
02. THEORY OF COMPUTATION	PP	100	40	44	PС	14.	MANAGEMENT INFORMATION SYSTEMS	PP	100	40	48	РС
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	61	PС	15.	PROGRAMMING PARADIGMS	PP	100	40	59	РС
04. DATBASE MANAGEMENT SYSTEMS	PP	100	40	40	PС	16.	DESIGN & ANALYSIS OF ALGORITHMS	PP	100	40	49	РС
05. SOFTWARE ENGINEERING	PP	100	40	40	PС	17.	HUMAN COMPU.INTERACTION & USABI	. PP	100	40	63	РС
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	17	PС	18.	SOFTWARE DESIGN LABORATORY	TW	50	20	38	РС
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	32	PС	19.	SOFTWARE DESIGN LABORATORY	PR	50	20	40	РС
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	15	РС	20.	SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	39	РС
O9. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	29	РС	21.	SOFTWARE DEVELOPMENT TOOLS LAB.	OR	50	20	28	Р
10. NETWORK LABORATORY	TW	25	10	16	PС	22.	SEMINAR AND TECHNICAL COMMUN.	TW	50	20	37	РС
11. NETWORK LABORATORY	OR	50	20	32	РС							

GRAND TOTAL = 859/1500, RESULT: HIGHER SECOND CLASS ORDN. 1 MARKS:

T8058669 MONISH PATEL				NE	ETA	,	70925508L	, т8058582 ,	, P	ICT	, -	г8058	669
01. OPERATING SYSTEM 02. THEORY OF COMPUTATION 03. COMPUTER NETWORK TECHNOLOGY 04. DATBASE MANAGEMENT SYSTEMS 05. SOFTWARE ENGINEERING 06. OPERATING SYSTEM DESIGN LAB. 07. OPERATING SYSTEM DESIGN LAB. 08. INFORMATION SYSTEMS DESIGN LAB. 09. INFORMATION SYSTEMS DESIGN LAB.	PP PP PP PP TW PR TW OR	100 100 100 100 100 25 50 25 50	40 40 40 40 40 10 20 10 20	40 40 42 42 48 10 20 10 21	P C P C P C P C P C	13. SYSTE 14. MANAC 15. PROGE 16. DESIG 17. HUMAN 18. SOFTN 19. SOFTN 20. SOFTN	EM SOFTWARE GEMENT INFO RAMMING PAR GN & ANALYS N COMPU.INT WARE DESIGN WARE DESIGN WARE DEVELO	PROGRAMMING RMATION SYSTEMS	PP PP PP PP PP TW PR TW	100 100 100 100 100 50 50 50	40 40 40 40 40 20 20 20 20	45 50 AA AA 53 20 25 20	P C P C P C F
10. NETWORK LABORATORY 11. NETWORK LABORATORY	TW OR	25 50	10 20	10 24	P C P C	22. SEMIN	NAR AND TEC	HNICAL COMMUN.	TW	50	20	20	PC

GRAND TOTAL = 557/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS :

RESULT RESERVED FOR BKLG

UNIVERSITY OF PUNE ,T.E. (2008 PAT) (INFORMATION TECHNOLOGY) EXAMINATION NOV. 2011 DATE : 02 MAR. 2012 CENTRÉ : PUNE INSTITUTE OF COMPUTER TECHNOLÓGY, PUNE. PAGE NO. 56 (531) NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO. OTHER LINES: HEAD OF PASSING, MAX. MARKS, MÍN. PASS MARKS, MARKS OBTAINÉD, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER T8058670 PALAK AGRAWAL PRITI , 70925531E , T8058591 , PICT , т8058670 PP 100 PP 100 40 55 P C 01. OPERATING SYSTEM 47 P.C 13. SYSTEM SOFTWARE PROGRAMMING 02. THEORY OF COMPUTATION 100 PP 40 40 P C 14. MANAGEMENT INFORMATION SYSTEMS PP 100 40 53 P.C. 100 40 51 P C 03. COMPUTER NETWORK TECHNOLOGY PP 66 P C 15. PROGRAMMING PARADIGMS PP 100 40 04. DATBASE MANAGEMENT SYSTEMS PP 100 40 40 P C 16. DESIGN & ANALYSIS OF ALGORITHMS PP 100 40 40 P C 05. SOFTWARE ENGINEERING PP 100 40 59 P.C 17. HUMAN COMPU.INTERACTION & USABI. PP 100 72 P.C TW 10 10 PC 20 20 P C 06. OPERATING SYSTEM DESIGN LAB. 25 18. SOFTWARE DESIGN LABORATORY TW 50 07. OPERATING SYSTEM DESIGN LAB. PR 50 20 28 P C 19. SOFTWARE DESIGN LABORATORY PR 50 20 15* P 25 10 10 P C 50 20 30 P C 08. INFORMATION SYSTEMS DESIGN LAB. TW 20. SOFTWARE DEVELOPMENT TOOLS LAB. TW 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 20 P C 21. SOFTWARE DEVELOPMENT TOOLS LAB. OR 50 20 20 P 10. NETWORK LABORATORY 25 10 10 P C 22. SEMINAR AND TECHNICAL COMMUN. TW 50 20 30 P C TW 11. NETWORK LABORATORY 50 20 23 P C OR GRAND TOTAL = 754/1500, RESULT: SECOND CLASS * [0.4] ORDN. 1 MARKS : , 70925542L , T8058599 , PICT , T8058672 T8058672 PATHAK ANIKET SANJAY ASHA PP 100 40 40 PC PP 100 40 56 P.C 01. OPERATING SYSTEM 13. SYSTEM SOFTWARE PROGRAMMING PP 100 PP 100 02. THEORY OF COMPUTATION 40 AA F 14. MANAGEMENT INFORMATION SYSTEMS PP 100 40 49 P.C 03. COMPUTER NETWORK TECHNOLOGY PP 100 40 57 P C 15. PROGRAMMING PARADIGMS PP 100 40 40 P C 04. DATBASE MANAGEMENT SYSTEMS PP 100 40 40 P C 16. DESIGN & ANALYSIS OF ALGORITHMS PP 100 40 42 P.C 05. SOFTWARE ENGINEERING 100 40 47 P C 17. HUMAN COMPU.INTERACTION & USABI. PP 40 51 P C PP 100 Ub. UPERATING SYSTEM DESIGN LAB.
07. OPERATING SYSTEM DESIGN LAB. 25 10 12 P C 18. SOFTWARE DESIGN LABORATORY TW 50 20 26 P C TW 50 20 20 P C 19. SOFTWARE DESIGN LABORATORY 50 20 PR PR AA F 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 12 P C 20. SOFTWARE DEVELOPMENT TOOLS LAB. TW 50 20 24 P C 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 33 P C 21. SOFTWARE DEVELOPMENT TOOLS LAB. OR 50 20 36 P C 25 10 12 PC 22. SEMINAR AND TECHNICAL COMMUN. TW 50 20 10. NETWORK LABORATORY TW 32 P.C 50 20 30 P C 11. NETWORK LABORATORY GRAND TOTAL = 677/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS :

02. THEORY OF COMPUTATION PP 100 40 59 P C 14. MANAGEMENT INFORMATION SYSTEMS PP 100 40 50 P 03. COMPUTER NETWORK TECHNOLOGY PP 100 40 66 P C 15. PROGRAMMING PARADIGMS PP 100 40 45 P 04. DATBASE MANAGEMENT SYSTEMS PP 100 40 44 P C 16. DESIGN & ANALYSIS OF ALGORITHMS PP 100 40 43 P 05. SOFTWARE ENGINEERING PP 100 40 57 P C 17. HUMAN COMPULINTERACTION & USABIL PP 100 40 70 P 06. OPERATING SYSTEM DESIGN LAB. TW 25 10 14 P C 18. SOFTWARE DESIGN LABORATORY TW 50 20 33 P 07. OPERATING SYSTEM DESIGN LAB. PR 50 20 33 P 08. INFORMATION SYSTEMS DESIGN LAB. TW 25 10 15 P C 20. SOFTWARE DESIGN LABORATORY PR 50 20 36 P 09. INFORMATION SYSTEMS DESIGN LAB. OR 50 20 44 P C 21. SOFTWARE DEVELOPMENT TOOLS LAB. OR 50 20 37 P	Т8	058674 PRIYANKA PANDE				RAS	IKA	,	70925570F	, т8058607 ,	P:	ICT	, 7	г8058	674
10. NETWORK LABORATORY TW 25 10 16 P C 22. SEMINAR AND TECHNICAL COMMUN. TW 50 20 37 P	02 03 04 05 06 07	. OPERATING SYSTEM . THEORY OF COMPUTATION . COMPUTER NETWORK TECHNOLOGY . DATBASE MANAGEMENT SYSTEMS . SOFTWARE ENGINEERING . OPERATING SYSTEM DESIGN LAB OPERATING SYSTEM DESIGN LAB INFORMATION SYSTEMS DESIGN LAB INFORMATION SYSTEMS DESIGN LAB.	PP PP PP PP TW PR TW	100 100 100 100 25 50 25	40 40 40 40 10 20 10	50 59 66 44 57 14 33 15	P C P C P C P C P C P C	14. MANA 15. PROG 16. DESI 17. HUMA 18. SOFT 19. SOFT 20. SOFT 21. SOFT	EM SOFTWARE GEMENT INFOI RAMMING PARA GN & ANALYS: N COMPU.INTI WARE DESIGN WARE DESIGN WARE DEVELOI WARE DEVELOI WARE DEVELOI	PROGRAMMING RMATION SYSTEMS ADIGMS IS OF ALGORITHMS ERACTION & USABI. LABORATORY LABORATORY PMENT TOOLS LAB. PMENT TOOLS LAB.	PP PP PP PP TW PR TW	100 100 100 100 100 50 50	40 40 40 40 40 20 20 20	69 50 45 43 70 33 42 36	P C P C P C P C

GRAND TOTAL = 901/1500, RESULT: FIRST CLASS ORDN. 1 MARKS:

UNIVERSITY OF PUNE ,T.E.(2008 PAT)(INFORMATION TECHNOLOGY) EXAMINATION NOV. 2011
DATE: 02 MAR. 2012 CENTRE: PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE. PAGE NO. 57 (532)

NOTE: FIRST LINE: SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

T8058675 RAHUL RANJAN				KU	IMKUM	, 70701594E , T8058610 , PICT , T8058	675
01. OPERATING SYSTEM	PP	100	40	28	F	13. SYSTEM SOFTWARE PROGRAMMING PP 100 40 40	РС
02. THEORY OF COMPUTATION	PP	100	40	40	РС	14. MANAGEMENT INFORMATION SYSTEMS PP 100 40 AA	F
03. COMPUTER NETWORK TECHNOLOGY	PP	100	40	42	РС	15. PROGRAMMING PARADIGMS PP 100 40 40	РС
04. DATBASE MANAGEMENT SYSTEMS	PP	100	40	41	РС	16. DESIGN & ANALYSIS OF ALGORITHMS PP 100 40 AA	F
05. SOFTWARE ENGINEERING	PP	100	40	31	F	17. HUMAN COMPU.INTERACTION & USABI. PP 100 40 44	РС
06. OPERATING SYSTEM DESIGN LAB.	TW	25	10	10	РС	18. SOFTWARE DESIGN LABORATORY TW 50 20 20	РС
07. OPERATING SYSTEM DESIGN LAB.	PR	50	20	AA	F	19. SOFTWARE DESIGN LABORATORY PR 50 20 22	РС
08. INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	10	РС	20. SOFTWARE DEVELOPMENT TOOLS LAB. TW 50 20 20	РС
O9. INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	23	РС	21. SOFTWARE DEVELOPMENT TOOLS LAB. OR 50 20 AA	F
10. NETWORK LABORATORY	TW	25	10	10	РС	22. SEMINAR AND TECHNICAL COMMUN. TW 50 20 20	РС
11. NETWORK LABORATORY	OR	50	20	20	Р		

GRAND TOTAL = 471/1500, RESULT: FAILS ORDN. 1 MARKS:

Т80	58676 SHIRA ANJALI BHAGWANSING	i			MA	LTABAI		, 70801618к , т8058620 ,	, P.	ICT	,	T8058	676
01.	OPERATING SYSTEM	PP	100	40	42	РС	13.	SYSTEM SOFTWARE PROGRAMMING	PP	100	40	45	РС
02.	THEORY OF COMPUTATION	PP	100	40	41	РС	14.	MANAGEMENT INFORMATION SYSTEMS	PP	100	40	AA	F
03.	COMPUTER NETWORK TECHNOLOGY	PP	100	40	51	РС	15.	PROGRAMMING PARADIGMS	PP	100	40	41	РС
04.	DATBASE MANAGEMENT SYSTEMS	PP	100	40	40	РС	16.	DESIGN & ANALYSIS OF ALGORITHMS	PP	100	40	40	РС
05.	SOFTWARE ENGINEERING	PP	100	40	40	РС	17.	HUMAN COMPU.INTERACTION & USABI.	. PP	100	40	47	РС
06.	OPERATING SYSTEM DESIGN LAB.	TW	25	10	18	РС	18.	SOFTWARE DESIGN LABORATORY	TW	50	20	31	РС
07.	OPERATING SYSTEM DESIGN LAB.	PR	50	20	15	F	19.	SOFTWARE DESIGN LABORATORY	PR	50	20	07	F
08.	INFORMATION SYSTEMS DESIGN LAB.	TW	25	10	15	РС	20.	SOFTWARE DEVELOPMENT TOOLS LAB.	TW	50	20	33	РС
09.	INFORMATION SYSTEMS DESIGN LAB.	OR	50	20	25	РС	21.	SOFTWARE DEVELOPMENT TOOLS LAB.	OR	50	20	08	F
10.	NETWORK LABORATORY	TW	25	10	17	РС	22.	SEMINAR AND TECHNICAL COMMUN.	TW	50	20	38	РС
11.	NETWORK LABORATORY	OR	50	20	34	РС							

GRAND TOTAL = 649/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS :

T8058677 TANAWADE AMOL SHAMRAO				MA	NDA		, 70801648M	, т8058629 ,	P.	ICT	, -	т8058	677
01. OPERATING SYSTEM 02. THEORY OF COMPUTATION 03. COMPUTER NETWORK TECHNOLOGY 04. DATBASE MANAGEMENT SYSTEMS 05. SOFTWARE ENGINEERING	PP PP PP PP	100 100 100 100 100	40 40 40 40	40 40 43 40 40	P C P C P C P C	13. 14. 15. 16.		MATION SYSTEMS ADIGMS S OF ALGORITHMS ERACTION & USABI.		100 100 100 100 100	40 40 40 40 40	59 45 41 40 57	P C P C P C P C
06. OPERATING SYSTEM DESIGN LAB. 07. OPERATING SYSTEM DESIGN LAB.	TW PR	25 50	10 20	12 05	P C F	18. 19.	SOFTWARE DESIGN SOFTWARE DESIGN		TW PR	50 50	20 20	39 30	P C P
08. INFORMATION SYSTEMS DESIGN LAB. 09. INFORMATION SYSTEMS DESIGN LAB.	TW OR	25 50	10 20	12 27	P C P C	20. 21.	SOFTWARE DEVELOR SOFTWARE DEVELOR		TW OR	50 50	20 20	36 36	P C P C
10. NETWORK LABORATORY 11. NETWORK LABORATORY	TW OR	25 50	10 20	10 33	P C P C	22.	SEMINAR AND TECH	INICAL COMMUN.	TW	50	20	41	РС

GRAND TOTAL = 740/1500, RESULT: FAILS A.T.K.T. ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME O OTHER LINES: HEAD OF PASSING,	OF THE MAX.	CAND: MARKS	IDATE 5, M	, MO IN. P	THER, PE	RMANENT S, MAR	REG. NO., KS OBTAINED,	PREVIOUS P/F:PA	SEAT NO., C	OLLEC PREVIO	GE, S DUS CAI	SEAT (NO. /ER
T8058678 TATHE SANDEEP HARIDAS					KHA		, 707016		, т8058630 ,				 г8058
O1. OPERATING SYSTEM O2. THEORY OF COMPUTATION O3. COMPUTER NETWORK TECHNOLOGY O4. DATBASE MANAGEMENT SYSTEMS O5. SOFTWARE ENGINEERING O6. OPERATING SYSTEM DESIGN LAB. O7. OPERATING SYSTEM DESIGN LAB. O8. INFORMATION SYSTEMS DESIGN LAB. O9. INFORMATION SYSTEMS DESIGN LAB. 10. NETWORK LABORATORY 11. NETWORK LABORATORY	PP PP PP PP TW PR TW	100 100 100 100 25 50 25 50 25 50	40 40 10 20 10	40 44 40 57 10 AA 10 28 10	P C P C P C F P C	14. 15. 16. 17. 18. 19. 20.	PROGRAMMING DESIGN & AN HUMAN COMPU SOFTWARE DE SOFTWARE DE SOFTWARE DE	INFORMAT PARADIG ALYSIS O INTERAC SIGN LAB SIGN LAB VELOPMEN VELOPMEN	TION SYSTEMS MS F ALGORITHMS TION & USABI. ORATORY ORATORY IT TOOLS LAB. IT TOOLS LAB.	PP PP PP TW PR TW	100 100 100 100 100 50 50 50 50	40 40 40 40 20 20 20 20 20	40 57 50 40 40 20 33 20 25 22
GRAND TOTAL = 674/1500, RESULT: FAILS ORDN. 1 MARKS :	6 A.T.	K.T.								RESU	JLT RE	SERVEI) FOF
T8058679 VED PRAKASH SINGH					 J LAXMI		708016						 г8058
	PP	100	40	AA		12	SYSTEM SOFT				100	, 40	40
01. OPERATING SYSTEM 02. THEORY OF COMPUTATION 03. COMPUTER NETWORK TECHNOLOGY 04. DATBASE MANAGEMENT SYSTEMS 05. SOFTWARE ENGINEERING 06. OPERATING SYSTEM DESIGN LAB. 07. OPERATING SYSTEM DESIGN LAB. 08. INFORMATION SYSTEMS DESIGN LAB. 09. INFORMATION SYSTEMS DESIGN LAB. 10. NETWORK LABORATORY 11. NETWORK LABORATORY	PP PP PP TW PR TW	100 100 100 100 25 50 25 50 25 50	40 40	52 46 AA 40 10 20 10 21 10		14. 15. 16. 17. 18. 19. 20.	MANAGEMENT PROGRAMMING DESIGN & AN HUMAN COMPU SOFTWARE DE SOFTWARE DE SOFTWARE DE SOFTWARE DE	INFORMAT PARADIG ALYSIS O INTERAC SIGN LAB SIGN LAB VELOPMEN VELOPMEN	TION SYSTEMS MS DF ALGORITHMS TION & USABI. ORATORY	PP PP PP TW PR TW	100 100 100 100 50 50 50 50	40 40 40 40 20 20 20 20 20	46 AA AA 44 21 AA 20 AA 22
GRAND TOTAL = 438/1500, RESULT: FAILS ORDN. 1 MARKS :	5												
T8058680 WAKHARE PRACHI PRAKASH				 NT	 RMALA		, 709256	 580	, т8058644 ,		 ICT		 г8058
	DD	100	40		P C	12			GRAMMING		100		75
05. SOFTWARE ENGINEERING 06. OPERATING SYSTEM DESIGN LAB. 07. OPERATING SYSTEM DESIGN LAB.	PP PP TW PR TW	100 100 100 100 25 50 25 50 25 50	40 40 40 40 10 20 10 20 10 20	51 78 41 71 21 35 19 39 22	P C P C P C	14. 15. 16. 17. 18. 19. 20. 21.	MANAGEMENT PROGRAMMING DESIGN & AN HUMAN COMPU SOFTWARE DE SOFTWARE DE SOFTWARE DE	INFORMAT PARADIG ALYSIS O INTERAC SIGN LAB SIGN LAB VELOPMEN VELOPMEN	TION SYSTEMS MS F ALGORITHMS TION & USABI. ORATORY ORATORY IT TOOLS LAB. IT TOOLS LAB.	PP PP PP TW PR TW	100 100	40 40	58 72