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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S80053001   ABHIMANYU SINGH                 NIRMAL KUMARI      , 71100712J   , S80053001 ,  PICT    , S80053001
01. SIGNAL AND SYSTEMS            PP   100 40   40 P C   11. ENGINEERING MATHEMATICS III    PP   100 40   40 P
02. SIGNAL AND SYSTEMS            OR    50 20   23 P     12. ENGINEERING MATHEMATICS III    TW    25 10   16 P
03. SOLID STATES DEVICES AND CIRCUITSPP  100 40   AA F  13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40  AA F
04. SOLID STATES DEVICES AND CIRCUITSPR   50 20  27 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR  50 20  27 P
05. NETWORK ANALYSIS             PP   100 40   AA F     15. ELECTROMAGNETIC                PP   100 40   AA F
06. DIGITAL LOGIC DESIGN         PP   100 40   AA F     16. ELECTROMAGNETIC                TW    25 10   19 P
07. DIGITAL LOGIC DESIGN         PR    50 20   35 P C   17. DATA STRUCTURES               PP   100 40   AA F
08. POWER DEVICES AND MACHINES   PP   100 40   AA F     18. DATA STRUCTURES               PR    50 20   AA F
09. NETWORK AND POWER LAB.       TW    50 20   33 P C   19. COMMUNICATION THEORY          PP   100 40   AA F
10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20  29 P C   20. COMMUNICATION THEORY          OR    50 20   36 P
                                                        21. CIRCUIT SIMULATION AND TOOLS  TW    50 20   34 P
GRAND TOTAL =  359/1500, RESULT: FAILS                            RESULT RESERVED FOR BKLG
ORDN. 1 MARKS :
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S80053002   ABHISHEK ANAND                  SINDHU             , 71200748C   , S80053002 ,  PICT    , S80053002
01. SIGNAL AND SYSTEMS            PP   100 40   57 P C   11. ENGINEERING MATHEMATICS III    PP   100 40   51 P
02. SIGNAL AND SYSTEMS            OR    50 20   31 P C   12. ENGINEERING MATHEMATICS III    TW    25 10   13 P
03. SOLID STATES DEVICES AND CIRCUITSPP  100 40   45 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40  52 P
04. SOLID STATES DEVICES AND CIRCUITSPR   50 20  38 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR  50 20  35 P
05. NETWORK ANALYSIS             PP   100 40   40 P C   15. ELECTROMAGNETIC                PP   100 40   63 P
06. DIGITAL LOGIC DESIGN         PP   100 40   65 P     16. ELECTROMAGNETIC                TW    25 10   15 P
07. DIGITAL LOGIC DESIGN         PR    50 20   34 P C   17. DATA STRUCTURES               PP   100 40   63 P
08. POWER DEVICES AND MACHINES   PP   100 40   43 P C   18. DATA STRUCTURES               PR    50 20   25 P
09. NETWORK AND POWER LAB.       TW    50 20   31 P C   19. COMMUNICATION THEORY          PP   100 40   62 P
10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20  34 P C   20. COMMUNICATION THEORY          OR    50 20   35 P
                                                        21. CIRCUIT SIMULATION AND TOOLS  TW    50 20   20 P
GRAND TOTAL =  852/1500, RESULT: HIGHER SECOND CLASS
ORDN. 1 MARKS :
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S80053003   AGRAWAL MUKUL RAVINDRA          ASHA               , 71200751C   , S80053003 ,  PICT    , S80053003
01. SIGNAL AND SYSTEMS            PP   100 40   70 P C   11. ENGINEERING MATHEMATICS III    PP   100 40   80 P
02. SIGNAL AND SYSTEMS            OR    50 20   37 P C   12. ENGINEERING MATHEMATICS III    TW    25 10   20 P
03. SOLID STATES DEVICES AND CIRCUITSPP  100 40   65 P C 13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40  64 P
04. SOLID STATES DEVICES AND CIRCUITSPR   50 20  35 P C 14. INTEGRATED CIRCUITS APPLICATIONS PR  50 20  34 P
05. NETWORK ANALYSIS             PP   100 40   52 P C   15. ELECTROMAGNETIC                PP   100 40   68 P
06. DIGITAL LOGIC DESIGN         PP   100 40   62 P C   16. ELECTROMAGNETIC                TW    25 10   21 P
07. DIGITAL LOGIC DESIGN         PR    50 20   36 P C   17. DATA STRUCTURES               PP   100 40   79 P
08. POWER DEVICES AND MACHINES   PP   100 40   62 P C   18. DATA STRUCTURES               PR    50 20   40 P
09. NETWORK AND POWER LAB.       TW    50 20   41 P C   19. COMMUNICATION THEORY          PP   100 40   81 P
10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20  38 P C   20. COMMUNICATION THEORY          OR    50 20   39 P
                                                        21. CIRCUIT SIMULATION AND TOOLS  TW    50 20   40 P
GRAND TOTAL = 1064/1500, RESULT: FIRST CLASS WITH DISTINCTION
ORDN. 1 MARKS :
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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.
      OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S80053004   AHIRE BHUSHAN SURESH              LAXMIBAI           , 71344345G    , S80053004 ,   PICT     , S80053004
01. SIGNAL AND SYSTEMS            PP   100  40   44  P C    11. ENGINEERING MATHEMATICS III     PP   100  40   42  P
02. SIGNAL AND SYSTEMS            OR    50  20   25  P C    12. ENGINEERING MATHEMATICS III     TW    25  10   21  P
03. SOLID STATES DEVICES AND CIRCUITSPP  100  40   40  P C    13. INTEGRATED CIRCUITS APPLICATIONS PP  100  40   58  P
04. SOLID STATES DEVICES AND CIRCUITSPR   50  20   27  P C    14. INTEGRATED CIRCUITS APPLICATIONS PR   50  20   25  P
05. NETWORK ANALYSIS             PP   100  40   42  P      15. ELECTROMAGNETIC                 PP   100  40   49  P
06. DIGITAL LOGIC DESIGN         PP   100  40   61  P      16. ELECTROMAGNETIC                 TW    25  10   20  P
07. DIGITAL LOGIC DESIGN         PR    50  20   25  P      17. DATA STRUCTURES                 PP   100  40   51  P
08. POWER DEVICES AND MACHINES   PP   100  40   49  P C    18. DATA STRUCTURES                 PR    50  20   36  P
09. NETWORK AND POWER LAB.       TW    50  20   36  P C    19. COMMUNICATION THEORY            PP   100  40   63  P
10. ELECTRONIC INSTRUMENTS AND TOOLS TW   50  20   35  P C    20. COMMUNICATION THEORY            OR    50  20   32  P
                                                           21. CIRCUIT SIMULATION AND TOOLS   TW    50  20   37  P
GRAND TOTAL =  818+07/1500, RESULT: HIGHER SECOND CLASS  [O.2]
ORDN. 1 MARKS :
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S80053005   ALGUR GAURAV ISHWAR              VASANTI            , 71200757B    , S80053005 ,   PICT     , S80053005
01. SIGNAL AND SYSTEMS            PP   100  40   43  P C    11. ENGINEERING MATHEMATICS III     PP   100  40   54  P
02. SIGNAL AND SYSTEMS            OR    50  20   23  P C    12. ENGINEERING MATHEMATICS III     TW    25  10   15  P
03. SOLID STATES DEVICES AND CIRCUITSPP  100  40   AA  F     13. INTEGRATED CIRCUITS APPLICATIONS PP  100  40   18  F
04. SOLID STATES DEVICES AND CIRCUITSPR   50  20   22  P C    14. INTEGRATED CIRCUITS APPLICATIONS PR   50  20   20  P
05. NETWORK ANALYSIS             PP   100  40   40  P      15. ELECTROMAGNETIC                 PP   100  40   40  P
06. DIGITAL LOGIC DESIGN         PP   100  40   40  P C    16. ELECTROMAGNETIC                 TW    25  10   14  P
07. DIGITAL LOGIC DESIGN         PR    50  20   20  P C    17. DATA STRUCTURES                 PP   100  40   40  P
08. POWER DEVICES AND MACHINES   PP   100  40   27  F      18. DATA STRUCTURES                 PR    50  20   22  P
09. NETWORK AND POWER LAB.       TW    50  20   25  P C    19. COMMUNICATION THEORY            PP   100  40   51  P
10. ELECTRONIC INSTRUMENTS AND TOOLS TW   50  20   29  P C    20. COMMUNICATION THEORY            OR    50  20   34  P
                                                           21. CIRCUIT SIMULATION AND TOOLS   TW    50  20   25  P
GRAND TOTAL =  602/1500, RESULT: FAILS A.T.K.T.
ORDN. 1 MARKS :
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S80053006   ANIRUDDHA SINGH CHOUHAN          RENUKA             , 71200761L    , S80053006 ,   PICT     , S80053006
01. SIGNAL AND SYSTEMS            PP   100  40   40  P C    11. ENGINEERING MATHEMATICS III     PP   100  40   64  P
02. SIGNAL AND SYSTEMS            OR    50  20   30  P C    12. ENGINEERING MATHEMATICS III     TW    25  10   16  P
03. SOLID STATES DEVICES AND CIRCUITSPP  100  40   40  P C    13. INTEGRATED CIRCUITS APPLICATIONS PP  100  40   40  P
04. SOLID STATES DEVICES AND CIRCUITSPR   50  20   32  P C    14. INTEGRATED CIRCUITS APPLICATIONS PR   50  20   20  P
05. NETWORK ANALYSIS             PP   100  40   40  P C    15. ELECTROMAGNETIC                 PP   100  40   51  P
06. DIGITAL LOGIC DESIGN         PP   100  40   49  P C    16. ELECTROMAGNETIC                 TW    25  10   17  P
07. DIGITAL LOGIC DESIGN         PR    50  20   34  P C    17. DATA STRUCTURES                 PP   100  40   64  P
08. POWER DEVICES AND MACHINES   PP   100  40   40  P      18. DATA STRUCTURES                 PR    50  20   30  P
09. NETWORK AND POWER LAB.       TW    50  20   35  P C    19. COMMUNICATION THEORY            PP   100  40   44  P
10. ELECTRONIC INSTRUMENTS AND TOOLS TW   50  20   34  P C    20. COMMUNICATION THEORY            OR    50  20   33  P
                                                           21. CIRCUIT SIMULATION AND TOOLS   TW    50  20   31  P
GRAND TOTAL =  784/1500, RESULT: SECOND CLASS
ORDN. 1 MARKS :
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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S80053007   ASHISH VILAS PATIL   SHAILA PATIL   , 71344346E   , S80053007 ,   PICT   , S80053007
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01. SIGNAL AND SYSTEMS              PP   100  40   72  P C      11. ENGINEERING MATHEMATICS III       PP   100  40   86  P
02. SIGNAL AND SYSTEMS              OR    50  20   30  P C      12. ENGINEERING MATHEMATICS III       TW    25  10   21  P
03. SOLID STATES DEVICES AND CIRCUITSPP   100  40   59  P C      13. INTEGRATED CIRCUITS APPLICATIONS PP   100  40   72  P
04. SOLID STATES DEVICES AND CIRCUITSPR    50  20   40  P C      14. INTEGRATED CIRCUITS APPLICATIONS PR    50  20   37  P
05. NETWORK ANALYSIS               PP   100  40   63  P C      15. ELECTROMAGNETIC                  PP   100  40   63  P
06. DIGITAL LOGIC DESIGN           PP   100  40   57  P C      16. ELECTROMAGNETIC                  TW    25  10   21  P
07. DIGITAL LOGIC DESIGN           PR    50  20   41  P C      17. DATA STRUCTURES                 PP   100  40   71  P
08. POWER DEVICES AND MACHINES      PP   100  40   63  P C      18. DATA STRUCTURES                 PR    50  20   41  P
09. NETWORK AND POWER LAB.          TW    50  20   43  P C      19. COMMUNICATION THEORY            PP   100  40   77  P
10. ELECTRONIC INSTRUMENTS AND TOOLS TW    50  20   41  P C      20. COMMUNICATION THEORY            OR    50  20   43  P
                                                              21. CIRCUIT SIMULATION AND TOOLS    TW    50  20   42  P
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GRAND TOTAL = 1083/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

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S80053008   ATHAWALE SHRADDHA MAHADEV   MAYA   , 71212091C   , S80053008 ,   PICT   , S80053008
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01. SIGNAL AND SYSTEMS              PP   100  40   46  P C      11. ENGINEERING MATHEMATICS III       PP   100  40   64  P
02. SIGNAL AND SYSTEMS              OR    50  20   32  P C      12. ENGINEERING MATHEMATICS III       TW    25  10   19  P
03. SOLID STATES DEVICES AND CIRCUITSPP   100  40   40  P C      13. INTEGRATED CIRCUITS APPLICATIONS PP   100  40   46  P
04. SOLID STATES DEVICES AND CIRCUITSPR    50  20   36  P C      14. INTEGRATED CIRCUITS APPLICATIONS PR    50  20   28  P
05. NETWORK ANALYSIS               PP   100  40   43  P C      15. ELECTROMAGNETIC                  PP   100  40   61  P
06. DIGITAL LOGIC DESIGN           PP   100  40   40  P C      16. ELECTROMAGNETIC                  TW    25  10   20  P
07. DIGITAL LOGIC DESIGN           PR    50  20   22  P C      17. DATA STRUCTURES                 PP   100  40   55  P
08. POWER DEVICES AND MACHINES      PP   100  40   47  P        18. DATA STRUCTURES                 PR    50  20   43  P
09. NETWORK AND POWER LAB.          TW    50  20   39  P C      19. COMMUNICATION THEORY            PP   100  40   45  P
10. ELECTRONIC INSTRUMENTS AND TOOLS TW    50  20   39  P C      20. COMMUNICATION THEORY            OR    50  20   29  P
                                                              21. CIRCUIT SIMULATION AND TOOLS    TW    50  20   39  P
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GRAND TOTAL = 833/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

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S80053010   AWACHAR MOHAN UDDHAO   KAVERI   , 71200767K   , S80053010 ,   PICT   , S80053010
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01. SIGNAL AND SYSTEMS              PP   100  40   71  P C      11. ENGINEERING MATHEMATICS III       PP   100  40   65  P
02. SIGNAL AND SYSTEMS              OR    50  20   35  P C      12. ENGINEERING MATHEMATICS III       TW    25  10   19  P
03. SOLID STATES DEVICES AND CIRCUITSPP   100  40   40  P C      13. INTEGRATED CIRCUITS APPLICATIONS PP   100  40   47  P
04. SOLID STATES DEVICES AND CIRCUITSPR    50  20   39  P C      14. INTEGRATED CIRCUITS APPLICATIONS PR    50  20   36  P
05. NETWORK ANALYSIS               PP   100  40   55  P C      15. ELECTROMAGNETIC                  PP   100  40   73  P
06. DIGITAL LOGIC DESIGN           PP   100  40   49  P C      16. ELECTROMAGNETIC                  TW    25  10   19  P
07. DIGITAL LOGIC DESIGN           PR    50  20   20  P C      17. DATA STRUCTURES                 PP   100  40   62  P
08. POWER DEVICES AND MACHINES      PP   100  40   47  P C      18. DATA STRUCTURES                 PR    50  20   38  P
09. NETWORK AND POWER LAB.          TW    50  20   43  P C      19. COMMUNICATION THEORY            PP   100  40   61  P
10. ELECTRONIC INSTRUMENTS AND TOOLS TW    50  20   39  P C      20. COMMUNICATION THEORY            OR    50  20   31  P
                                                              21. CIRCUIT SIMULATION AND TOOLS    TW    50  20   36  P
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GRAND TOTAL = 925/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S80053011   B KUMAR ANUPAM   LAKSHMI PRIYA   , 71200768H   , S80053011 ,   PICT   , S80053011

01. SIGNAL AND SYSTEMS	PP	100	40	40	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	22	F	
02. SIGNAL AND SYSTEMS	OR	50	20	25	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	14	P	
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	48	P	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	28	F
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	39	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	25	P
05. NETWORK ANALYSIS	PP	100	40	40	P C	15. ELECTROMAGNETIC	PP	100	40	43	P
06. DIGITAL LOGIC DESIGN	PP	100	40	40	P	16. ELECTROMAGNETIC	TW	25	10	16	P
07. DIGITAL LOGIC DESIGN	PR	50	20	27	P C	17. DATA STRUCTURES	PP	100	40	40	P
08. POWER DEVICES AND MACHINES	PP	100	40	40	P C	18. DATA STRUCTURES	PR	50	20	36	P
09. NETWORK AND POWER LAB.	TW	50	20	35	P C	19. COMMUNICATION THEORY	PP	100	40	46	P
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	38	P C	20. COMMUNICATION THEORY	OR	50	20	32	P
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	32	P

GRAND TOTAL = 706/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

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S80053012   BADE ABHIJIT ARJUN   ASHA   , 71200769F   , S80053012 ,   PICT   , S80053012

01. SIGNAL AND SYSTEMS	PP	100	40	69	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	57	P	
02. SIGNAL AND SYSTEMS	OR	50	20	31	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	21	P	
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	40	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	40	P
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	38	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	33	P
05. NETWORK ANALYSIS	PP	100	40	40	P C	15. ELECTROMAGNETIC	PP	100	40	52	P
06. DIGITAL LOGIC DESIGN	PP	100	40	60	P	16. ELECTROMAGNETIC	TW	25	10	20	P
07. DIGITAL LOGIC DESIGN	PR	50	20	28	P C	17. DATA STRUCTURES	PP	100	40	70	P
08. POWER DEVICES AND MACHINES	PP	100	40	44	P C	18. DATA STRUCTURES	PR	50	20	35	P
09. NETWORK AND POWER LAB.	TW	50	20	41	P C	19. COMMUNICATION THEORY	PP	100	40	47	P
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	38	P C	20. COMMUNICATION THEORY	OR	50	20	31	P
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	39	P

GRAND TOTAL = 874/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

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S80053013   BAGDE PRAJAKTA ASHOK   MANISHA   , 71200770K   , S80053013 ,   PICT   , S80053013

01. SIGNAL AND SYSTEMS	PP	100	40	75	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	92	P	
02. SIGNAL AND SYSTEMS	OR	50	20	34	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	20	P	
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	40	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	45	P
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	37	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	33	P
05. NETWORK ANALYSIS	PP	100	40	62	P	15. ELECTROMAGNETIC	PP	100	40	63	P
06. DIGITAL LOGIC DESIGN	PP	100	40	41	P C	16. ELECTROMAGNETIC	TW	25	10	20	P
07. DIGITAL LOGIC DESIGN	PR	50	20	32	P C	17. DATA STRUCTURES	PP	100	40	66	P
08. POWER DEVICES AND MACHINES	PP	100	40	51	P C	18. DATA STRUCTURES	PR	50	20	33	P
09. NETWORK AND POWER LAB.	TW	50	20	41	P C	19. COMMUNICATION THEORY	PP	100	40	68	P
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	42	P C	20. COMMUNICATION THEORY	OR	50	20	30	P
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	36	P

GRAND TOTAL = 961/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.

          OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S80053014   BAIRAGI SURAJ KACHARDAS   SULOCHANA   , 71200774B   , S80053014 ,   PICT   , S80053014

01. SIGNAL AND SYSTEMS	PP	100	40	82	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	88	P	
02. SIGNAL AND SYSTEMS	OR	50	20	38	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	23	P	
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	55	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	55	P
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	43	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	44	P
05. NETWORK ANALYSIS	PP	100	40	59	P C	15. ELECTROMAGNETIC	PP	100	40	82	P
06. DIGITAL LOGIC DESIGN	PP	100	40	48	P C	16. ELECTROMAGNETIC	TW	25	10	22	P
07. DIGITAL LOGIC DESIGN	PR	50	20	39	P C	17. DATA STRUCTURES	PP	100	40	70	P
08. POWER DEVICES AND MACHINES	PP	100	40	63	P C	18. DATA STRUCTURES	PR	50	20	27	P
09. NETWORK AND POWER LAB.	TW	50	20	43	P C	19. COMMUNICATION THEORY	PP	100	40	78	P
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	42	P C	20. COMMUNICATION THEORY	OR	50	20	40	P
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	43	P

GRAND TOTAL = 1084/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

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S80053015   BAJAJ POOJA BALAPRASAD   SAVITA   , 71200776J   , S80053015 ,   PICT   , S80053015

01. SIGNAL AND SYSTEMS	PP	100	40	58	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	67	P	
02. SIGNAL AND SYSTEMS	OR	50	20	34	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	17	P	
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	55	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	29	F
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	35	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	30	P
05. NETWORK ANALYSIS	PP	100	40	41	P C	15. ELECTROMAGNETIC	PP	100	40	40	P
06. DIGITAL LOGIC DESIGN	PP	100	40	57	P	16. ELECTROMAGNETIC	TW	25	10	18	P
07. DIGITAL LOGIC DESIGN	PR	50	20	30	P C	17. DATA STRUCTURES	PP	100	40	53	P
08. POWER DEVICES AND MACHINES	PP	100	40	41	P C	18. DATA STRUCTURES	PR	50	20	43	P
09. NETWORK AND POWER LAB.	TW	50	20	36	P C	19. COMMUNICATION THEORY	PP	100	40	56	P
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	39	P C	20. COMMUNICATION THEORY	OR	50	20	30	P
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	36	P

GRAND TOTAL = 845/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

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S80053016   BAKRE ANUJ RAMESH   JAYASHREE   , 71200777G   , S80053016 ,   PICT   , S80053016

01. SIGNAL AND SYSTEMS	PP	100	40	71	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	94	P	
02. SIGNAL AND SYSTEMS	OR	50	20	38	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	23	P	
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	53	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	72	P
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	41	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	45	P
05. NETWORK ANALYSIS	PP	100	40	67	P C	15. ELECTROMAGNETIC	PP	100	40	82	P
06. DIGITAL LOGIC DESIGN	PP	100	40	61	P C	16. ELECTROMAGNETIC	TW	25	10	21	P
07. DIGITAL LOGIC DESIGN	PR	50	20	44	P C	17. DATA STRUCTURES	PP	100	40	76	P
08. POWER DEVICES AND MACHINES	PP	100	40	55	P C	18. DATA STRUCTURES	PR	50	20	45	P
09. NETWORK AND POWER LAB.	TW	50	20	44	P C	19. COMMUNICATION THEORY	PP	100	40	79	P
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	43	P C	20. COMMUNICATION THEORY	OR	50	20	35	P
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	44	P

GRAND TOTAL = 1133/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
         OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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S80053017   BARKADE VIJAYA VASANT   JAYASHRI   , 71344347C   , S80053017 ,   PICT   , S80053017
01. SIGNAL AND SYSTEMS   PP   100 40   64 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   77 P
02. SIGNAL AND SYSTEMS   OR   50 20   25 P C   12. ENGINEERING MATHEMATICS III   TW   25 10   22 P
03. SOLID STATES DEVICES AND CIRCUITSPP   100 40   54 P C   13. INTEGRATED CIRCUITS APPLICATIONS PP   100 40   58 P
04. SOLID STATES DEVICES AND CIRCUITSPR   50 20   30 P C   14. INTEGRATED CIRCUITS APPLICATIONS PR   50 20   32 P
05. NETWORK ANALYSIS   PP   100 40   49 P C   15. ELECTROMAGNETIC   PP   100 40   62 P
06. DIGITAL LOGIC DESIGN   PP   100 40   40 P C   16. ELECTROMAGNETIC   TW   25 10   21 P
07. DIGITAL LOGIC DESIGN   PR   50 20   26 P C   17. DATA STRUCTURES   PP   100 40   63 P
08. POWER DEVICES AND MACHINES   PP   100 40   61 P C   18. DATA STRUCTURES   PR   50 20   38 P
09. NETWORK AND POWER LAB.   TW   50 20   40 P C   19. COMMUNICATION THEORY   PP   100 40   73 P
10. ELECTRONIC INSTRUMENTS AND TOOLS TW   50 20   37 P C   20. COMMUNICATION THEORY   OR   50 20   34 P
                                                             21. CIRCUIT SIMULATION AND TOOLS   TW   50 20   42 P
GRAND TOTAL = 948/1500, RESULT: FIRST CLASS
ORDN. 1 MARKS :

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S80053018   BASHIRABADKAR SAURABH MOHANKUMAR   ANUPAMA   , 71200785H   , S80053018 ,   PICT   , S80053018
01. SIGNAL AND SYSTEMS   PP   100 40   58 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   67 P
02. SIGNAL AND SYSTEMS   OR   50 20   30 P C   12. ENGINEERING MATHEMATICS III   TW   25 10   18 P
03. SOLID STATES DEVICES AND CIRCUITSPP   100 40   40 P C   13. INTEGRATED CIRCUITS APPLICATIONS PP   100 40   49 P
04. SOLID STATES DEVICES AND CIRCUITSPR   50 20   28 P C   14. INTEGRATED CIRCUITS APPLICATIONS PR   50 20   36 P
05. NETWORK ANALYSIS   PP   100 40   43 P C   15. ELECTROMAGNETIC   PP   100 40   49 P
06. DIGITAL LOGIC DESIGN   PP   100 40   45 P C   16. ELECTROMAGNETIC   TW   25 10   18 P
07. DIGITAL LOGIC DESIGN   PR   50 20   32 P C   17. DATA STRUCTURES   PP   100 40   69 P
08. POWER DEVICES AND MACHINES   PP   100 40   43 P C   18. DATA STRUCTURES   PR   50 20   33 P
09. NETWORK AND POWER LAB.   TW   50 20   41 P C   19. COMMUNICATION THEORY   PP   100 40   66 P
10. ELECTRONIC INSTRUMENTS AND TOOLS TW   50 20   39 P C   20. COMMUNICATION THEORY   OR   50 20   34 P
                                                             21. CIRCUIT SIMULATION AND TOOLS   TW   50 20   33 P
GRAND TOTAL = 871/1500, RESULT: HIGHER SECOND CLASS
ORDN. 1 MARKS :

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S80053019   BAVKAR INDRANEEL NANDKUMAR   RADHIKA   , 71200787D   , S80053019 ,   PICT   , S80053019
01. SIGNAL AND SYSTEMS   PP   100 40   66 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   92 P
02. SIGNAL AND SYSTEMS   OR   50 20   32 P C   12. ENGINEERING MATHEMATICS III   TW   25 10   21 P
03. SOLID STATES DEVICES AND CIRCUITSPP   100 40   47 P C   13. INTEGRATED CIRCUITS APPLICATIONS PP   100 40   40 P
04. SOLID STATES DEVICES AND CIRCUITSPR   50 20   36 P C   14. INTEGRATED CIRCUITS APPLICATIONS PR   50 20   30 P
05. NETWORK ANALYSIS   PP   100 40   49 P C   15. ELECTROMAGNETIC   PP   100 40   62 P
06. DIGITAL LOGIC DESIGN   PP   100 40   47 P C   16. ELECTROMAGNETIC   TW   25 10   21 P
07. DIGITAL LOGIC DESIGN   PR   50 20   30 P C   17. DATA STRUCTURES   PP   100 40   65 P
08. POWER DEVICES AND MACHINES   PP   100 40   43 P C   18. DATA STRUCTURES   PR   50 20   37 P
09. NETWORK AND POWER LAB.   TW   50 20   42 P C   19. COMMUNICATION THEORY   PP   100 40   50 P
10. ELECTRONIC INSTRUMENTS AND TOOLS TW   50 20   43 P C   20. COMMUNICATION THEORY   OR   50 20   32 P
                                                             21. CIRCUIT SIMULATION AND TOOLS   TW   50 20   42 P
GRAND TOTAL = 927/1500, RESULT: FIRST CLASS
ORDN. 1 MARKS :

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.
       OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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S80053020   BAYAS DIPALI SUNILSING   URMILA   , 71344348M   , S80053020 ,   PICT   , S80053020

01. SIGNAL AND SYSTEMS	PP	100	40	57	P	C	11. ENGINEERING MATHEMATICS III	PP	100	40	72	P	
02. SIGNAL AND SYSTEMS	OR	50	20	28	P	C	12. ENGINEERING MATHEMATICS III	TW	25	10	19	P	
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	50	P	C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	67	P
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	30	P	C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	33	P
05. NETWORK ANALYSIS	PP	100	40	53	P	C	15. ELECTROMAGNETIC	PP	100	40	46	P	
06. DIGITAL LOGIC DESIGN	PP	100	40	43	P	C	16. ELECTROMAGNETIC	TW	25	10	20	P	
07. DIGITAL LOGIC DESIGN	PR	50	20	35	P	C	17. DATA STRUCTURES	PP	100	40	65	P	
08. POWER DEVICES AND MACHINES	PP	100	40	46	P	C	18. DATA STRUCTURES	PR	50	20	25	P	
09. NETWORK AND POWER LAB.	TW	50	20	40	P	C	19. COMMUNICATION THEORY	PP	100	40	79	P	
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	39	P	C	20. COMMUNICATION THEORY	OR	50	20	40	P
							21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	39	P	

GRAND TOTAL = 926/1500, RESULT: FIRST CLASS
ORDN. 1 MARKS :
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S80053021   BERAD SUPRIYA LAXMIKANT   SUNANDA   , 71200788B   , S80053021 ,   PICT   , S80053021

01. SIGNAL AND SYSTEMS	PP	100	40	75	P	C	11. ENGINEERING MATHEMATICS III	PP	100	40	93	P	
02. SIGNAL AND SYSTEMS	OR	50	20	40	P	C	12. ENGINEERING MATHEMATICS III	TW	25	10	23	P	
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	53	P	C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	49	P
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	40	P	C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	37	P
05. NETWORK ANALYSIS	PP	100	40	50	P	C	15. ELECTROMAGNETIC	PP	100	40	76	P	
06. DIGITAL LOGIC DESIGN	PP	100	40	55	P	C	16. ELECTROMAGNETIC	TW	25	10	22	P	
07. DIGITAL LOGIC DESIGN	PR	50	20	43	P	C	17. DATA STRUCTURES	PP	100	40	86	P	
08. POWER DEVICES AND MACHINES	PP	100	40	65	P	C	18. DATA STRUCTURES	PR	50	20	39	P	
09. NETWORK AND POWER LAB.	TW	50	20	43	P	C	19. COMMUNICATION THEORY	PP	100	40	72	P	
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	40	P	C	20. COMMUNICATION THEORY	OR	50	20	35	P
							21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	41	P	

GRAND TOTAL = 1077/1500, RESULT: FIRST CLASS WITH DISTINCTION
ORDN. 1 MARKS :
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S80053022   BHALERAO MAHESH GOPAL   SANGITA   , 71344349K   , S80053022 ,   PICT   , S80053022

01. SIGNAL AND SYSTEMS	PP	100	40	52	P	C	11. ENGINEERING MATHEMATICS III	PP	100	40	44	P	
02. SIGNAL AND SYSTEMS	OR	50	20	23	P	C	12. ENGINEERING MATHEMATICS III	TW	25	10	18	P	
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	48	P	C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	60	P
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	32	P	C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	38	P
05. NETWORK ANALYSIS	PP	100	40	40	P	C	15. ELECTROMAGNETIC	PP	100	40	54	P	
06. DIGITAL LOGIC DESIGN	PP	100	40	53	P		16. ELECTROMAGNETIC	TW	25	10	20	P	
07. DIGITAL LOGIC DESIGN	PR	50	20	22	P	C	17. DATA STRUCTURES	PP	100	40	70	P	
08. POWER DEVICES AND MACHINES	PP	100	40	54	P	C	18. DATA STRUCTURES	PR	50	20	25	P	
09. NETWORK AND POWER LAB.	TW	50	20	39	P	C	19. COMMUNICATION THEORY	PP	100	40	68	P	
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	36	P	C	20. COMMUNICATION THEORY	OR	50	20	30	P
							21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	38	P	

GRAND TOTAL = 864/1500, RESULT: HIGHER SECOND CLASS
ORDN. 1 MARKS :
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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
        OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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S80053023   BHANDARI AISHWARYA ABHAY   SANGITA   , 71200791B   , S80053023 ,   PICT   , S80053023
01. SIGNAL AND SYSTEMS   PP   100 40 80 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   84 P
02. SIGNAL AND SYSTEMS   OR   50 20 43 P C   12. ENGINEERING MATHEMATICS III   TW   25 10   22 P
03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 50 P C   13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40   59 P
04. SOLID STATES DEVICES AND CIRCUITSPR   50 20 44 P C   14. INTEGRATED CIRCUITS APPLICATIONS PR   50 20   39 P
05. NETWORK ANALYSIS   PP   100 40 57 P C   15. ELECTROMAGNETIC   PP   100 40   70 P
06. DIGITAL LOGIC DESIGN   PP   100 40 48 P C   16. ELECTROMAGNETIC   TW   25 10   22 P
07. DIGITAL LOGIC DESIGN   PR   50 20 45 P C   17. DATA STRUCTURES   PP   100 40   63 P
08. POWER DEVICES AND MACHINES   PP   100 40 53 P C   18. DATA STRUCTURES   PR   50 20   46 P
09. NETWORK AND POWER LAB.   TW   50 20 44 P C   19. COMMUNICATION THEORY   PP   100 40   63 P
10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 43 P C   20. COMMUNICATION THEORY   OR   50 20   42 P
                                                         21. CIRCUIT SIMULATION AND TOOLS TW   50 20   45 P
GRAND TOTAL = 1062/1500, RESULT: FIRST CLASS WITH DISTINCTION
ORDN. 1 MARKS :

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S80053024   BHARGANDE SAURABH VIJAYKUMAR   SUSHMA   , 71200794G   , S80053024 ,   PICT   , S80053024
01. SIGNAL AND SYSTEMS   PP   100 40 40 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   40 P
02. SIGNAL AND SYSTEMS   OR   50 20 35 P C   12. ENGINEERING MATHEMATICS III   TW   25 10   20 P
03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 49 P   13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40   44 P
04. SOLID STATES DEVICES AND CIRCUITSPR   50 20 30 P C   14. INTEGRATED CIRCUITS APPLICATIONS PR   50 20   30 P
05. NETWORK ANALYSIS   PP   100 40 40 P   15. ELECTROMAGNETIC   PP   100 40   42 P
06. DIGITAL LOGIC DESIGN   PP   100 40 46 P   16. ELECTROMAGNETIC   TW   25 10   19 P
07. DIGITAL LOGIC DESIGN   PR   50 20 36 P C   17. DATA STRUCTURES   PP   100 40   48 P
08. POWER DEVICES AND MACHINES   PP   100 40 42 P   18. DATA STRUCTURES   PR   50 20   40 P
09. NETWORK AND POWER LAB.   TW   50 20 37 P C   19. COMMUNICATION THEORY   PP   100 40   43 P
10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 36 P C   20. COMMUNICATION THEORY   OR   50 20   24 P
                                                         21. CIRCUIT SIMULATION AND TOOLS TW   50 20   36 P
GRAND TOTAL = 777/1500, RESULT: SECOND CLASS
ORDN. 1 MARKS :

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S80053025   BHAVSAR RUCHIKA KISHOR   SAVITA   , 71344350C   , S80053025 ,   PICT   , S80053025
01. SIGNAL AND SYSTEMS   PP   100 40 51 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   71 P
02. SIGNAL AND SYSTEMS   OR   50 20 29 P C   12. ENGINEERING MATHEMATICS III   TW   25 10   24 P
03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 41 P C   13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40   67 P
04. SOLID STATES DEVICES AND CIRCUITSPR   50 20 38 P C   14. INTEGRATED CIRCUITS APPLICATIONS PR   50 20   34 P
05. NETWORK ANALYSIS   PP   100 40 50 P C   15. ELECTROMAGNETIC   PP   100 40   75 P
06. DIGITAL LOGIC DESIGN   PP   100 40 53 P C   16. ELECTROMAGNETIC   TW   25 10   23 P
07. DIGITAL LOGIC DESIGN   PR   50 20 38 P C   17. DATA STRUCTURES   PP   100 40   58 P
08. POWER DEVICES AND MACHINES   PP   100 40 48 P C   18. DATA STRUCTURES   PR   50 20   40 P
09. NETWORK AND POWER LAB.   TW   50 20 41 P C   19. COMMUNICATION THEORY   PP   100 40   76 P
10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 42 P C   20. COMMUNICATION THEORY   OR   50 20   39 P
                                                         21. CIRCUIT SIMULATION AND TOOLS TW   50 20   44 P
GRAND TOTAL = 982/1500, RESULT: FIRST CLASS
ORDN. 1 MARKS :

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.
       OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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  S80053026   BHINGE ABHIJEET SOMESHWAR   RUPALI   , 71200795E   , S80053026 ,   PICT   , S80053026
  01. SIGNAL AND SYSTEMS   PP   100 40   82 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   87 P
  02. SIGNAL AND SYSTEMS   OR   50 20   43 P C   12. ENGINEERING MATHEMATICS III   TW   25 10   23 P
  03. SOLID STATES DEVICES AND CIRCUITSPP   100 40   60 P C   13. INTEGRATED CIRCUITS APPLICATIONS PP   100 40   64 P
  04. SOLID STATES DEVICES AND CIRCUITSPR   50 20   44 P C   14. INTEGRATED CIRCUITS APPLICATIONS PR   50 20   45 P
  05. NETWORK ANALYSIS   PP   100 40   45 P C   15. ELECTROMAGNETIC   PP   100 40   83 P
  06. DIGITAL LOGIC DESIGN   PP   100 40   42 P C   16. ELECTROMAGNETIC   TW   25 10   23 P
  07. DIGITAL LOGIC DESIGN   PR   50 20   48 P C   17. DATA STRUCTURES   PP   100 40   64 P
  08. POWER DEVICES AND MACHINES   PP   100 40   65 P C   18. DATA STRUCTURES   PR   50 20   44 P
  09. NETWORK AND POWER LAB.   TW   50 20   45 P C   19. COMMUNICATION THEORY   PP   100 40   81 P
  10. ELECTRONIC INSTRUMENTS AND TOOLS TW   50 20   45 P C   20. COMMUNICATION THEORY   OR   50 20   49 P
                                                               21. CIRCUIT SIMULATION AND TOOLS   TW   50 20   44 P
GRAND TOTAL = 1126/1500, RESULT: FIRST CLASS WITH DISTINCTION
ORDN. 1 MARKS :

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  S80053027   BHORDE MONALI MARUTI   SUNANDA   , 71344351M   , S80053027 ,   PICT   , S80053027
  01. SIGNAL AND SYSTEMS   PP   100 40   65 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   80 P
  02. SIGNAL AND SYSTEMS   OR   50 20   30 P C   12. ENGINEERING MATHEMATICS III   TW   25 10   22 P
  03. SOLID STATES DEVICES AND CIRCUITSPP   100 40   40 P C   13. INTEGRATED CIRCUITS APPLICATIONS PP   100 40   59 P
  04. SOLID STATES DEVICES AND CIRCUITSPR   50 20   36 P C   14. INTEGRATED CIRCUITS APPLICATIONS PR   50 20   36 P
  05. NETWORK ANALYSIS   PP   100 40   48 P C   15. ELECTROMAGNETIC   PP   100 40   59 P
  06. DIGITAL LOGIC DESIGN   PP   100 40   40 P C   16. ELECTROMAGNETIC   TW   25 10   22 P
  07. DIGITAL LOGIC DESIGN   PR   50 20   32 P C   17. DATA STRUCTURES   PP   100 40   75 P
  08. POWER DEVICES AND MACHINES   PP   100 40   54 P C   18. DATA STRUCTURES   PR   50 20   44 P
  09. NETWORK AND POWER LAB.   TW   50 20   38 P C   19. COMMUNICATION THEORY   PP   100 40   71 P
  10. ELECTRONIC INSTRUMENTS AND TOOLS TW   50 20   36 P C   20. COMMUNICATION THEORY   OR   50 20   30 P
                                                               21. CIRCUIT SIMULATION AND TOOLS   TW   50 20   42 P
GRAND TOTAL = 959/1500, RESULT: FIRST CLASS
ORDN. 1 MARKS :

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  S80053028   BHOSALE JUILEE UMESH   ANITA   , 71200797M   , S80053028 ,   PICT   , S80053028
  01. SIGNAL AND SYSTEMS   PP   100 40   76 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   93 P
  02. SIGNAL AND SYSTEMS   OR   50 20   42 P C   12. ENGINEERING MATHEMATICS III   TW   25 10   24 P
  03. SOLID STATES DEVICES AND CIRCUITSPP   100 40   53 P C   13. INTEGRATED CIRCUITS APPLICATIONS PP   100 40   50 P
  04. SOLID STATES DEVICES AND CIRCUITSPR   50 20   36 P C   14. INTEGRATED CIRCUITS APPLICATIONS PR   50 20   40 P
  05. NETWORK ANALYSIS   PP   100 40   68 P C   15. ELECTROMAGNETIC   PP   100 40   73 P
  06. DIGITAL LOGIC DESIGN   PP   100 40   50 P C   16. ELECTROMAGNETIC   TW   25 10   23 P
  07. DIGITAL LOGIC DESIGN   PR   50 20   32 P C   17. DATA STRUCTURES   PP   100 40   82 P
  08. POWER DEVICES AND MACHINES   PP   100 40   58 P C   18. DATA STRUCTURES   PR   50 20   45 P
  09. NETWORK AND POWER LAB.   TW   50 20   43 P C   19. COMMUNICATION THEORY   PP   100 40   80 P
  10. ELECTRONIC INSTRUMENTS AND TOOLS TW   50 20   40 P C   20. COMMUNICATION THEORY   OR   50 20   41 P
                                                               21. CIRCUIT SIMULATION AND TOOLS   TW   50 20   45 P
GRAND TOTAL = 1094/1500, RESULT: FIRST CLASS WITH DISTINCTION
ORDN. 1 MARKS :

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S80053029   BIDE DADA VITHOBA   JIJABAI   , 71200799H   , S80053029 , PICT   , S80053029

01. SIGNAL AND SYSTEMS	PP	100	40	58	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	50	P
02. SIGNAL AND SYSTEMS	OR	50	20	20	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	18	P
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	55	P	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	51	P
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	32	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	20	P
05. NETWORK ANALYSIS	PP	100	40	40	P	15. ELECTROMAGNETIC	PP	100	40	61	P
06. DIGITAL LOGIC DESIGN	PP	100	40	59	P	16. ELECTROMAGNETIC	TW	25	10	15	P
07. DIGITAL LOGIC DESIGN	PR	50	20	28	P	17. DATA STRUCTURES	PP	100	40	42	P
08. POWER DEVICES AND MACHINES	PP	100	40	51	P	18. DATA STRUCTURES	PR	50	20	32	P
09. NETWORK AND POWER LAB.	TW	50	20	32	P C	19. COMMUNICATION THEORY	PP	100	40	52	P
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	36	P C	20. COMMUNICATION THEORY	OR	50	20	22	P
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	33	P

GRAND TOTAL = 807/1500, RESULT: SECOND CLASS

ORDN. 1 MARKS :

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S80053030   BOBADE AJINKYA CHANDRAKANT   CHHAYA   , 71200802M   , S80053030 , PICT   , S80053030

01. SIGNAL AND SYSTEMS	PP	100	40	44	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	55	P
02. SIGNAL AND SYSTEMS	OR	50	20	25	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	19	P
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	40	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	29	F
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	35	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	29	P
05. NETWORK ANALYSIS	PP	100	40	40	P	15. ELECTROMAGNETIC	PP	100	40	46	P
06. DIGITAL LOGIC DESIGN	PP	100	40	40	P	16. ELECTROMAGNETIC	TW	25	10	20	P
07. DIGITAL LOGIC DESIGN	PR	50	20	27	P C	17. DATA STRUCTURES	PP	100	40	50	P
08. POWER DEVICES AND MACHINES	PP	100	40	40	P C	18. DATA STRUCTURES	PR	50	20	34	P
09. NETWORK AND POWER LAB.	TW	50	20	38	P C	19. COMMUNICATION THEORY	PP	100	40	45	P
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	39	P C	20. COMMUNICATION THEORY	OR	50	20	31	P
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	37	P

GRAND TOTAL = 763/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

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S80053031   BONDE KAJAL SURESH   KALPANA   , 71344352K   , S80053031 , PICT   , S80053031

01. SIGNAL AND SYSTEMS	PP	100	40	48	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	40	P
02. SIGNAL AND SYSTEMS	OR	50	20	25	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	20	P
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	42	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	58	P
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	33	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	32	P
05. NETWORK ANALYSIS	PP	100	40	40	P C	15. ELECTROMAGNETIC	PP	100	40	53	P
06. DIGITAL LOGIC DESIGN	PP	100	40	62	P	16. ELECTROMAGNETIC	TW	25	10	20	P
07. DIGITAL LOGIC DESIGN	PR	50	20	35	P C	17. DATA STRUCTURES	PP	100	40	49	P
08. POWER DEVICES AND MACHINES	PP	100	40	45	P C	18. DATA STRUCTURES	PR	50	20	26	P
09. NETWORK AND POWER LAB.	TW	50	20	38	P C	19. COMMUNICATION THEORY	PP	100	40	60	P
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	36	P C	20. COMMUNICATION THEORY	OR	50	20	38	P
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	39	P

GRAND TOTAL = 839/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.
        OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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S80053032   BOOB SURABHI RAMDHAN   SANGEETA   , 71200803K   , S80053032 ,   PICT   , S80053032

01. SIGNAL AND SYSTEMS	PP	100	40	73	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	67	P	
02. SIGNAL AND SYSTEMS	OR	50	20	41	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	19	P	
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	63	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	62	P
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	41	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	36	P
05. NETWORK ANALYSIS	PP	100	40	63	P C	15. ELECTROMAGNETIC	PP	100	40	73	P
06. DIGITAL LOGIC DESIGN	PP	100	40	53	P C	16. ELECTROMAGNETIC	TW	25	10	19	P
07. DIGITAL LOGIC DESIGN	PR	50	20	43	P C	17. DATA STRUCTURES	PP	100	40	83	P
08. POWER DEVICES AND MACHINES	PP	100	40	53	P C	18. DATA STRUCTURES	PR	50	20	41	P
09. NETWORK AND POWER LAB.	TW	50	20	42	P C	19. COMMUNICATION THEORY	PP	100	40	67	P
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	42	P C	20. COMMUNICATION THEORY	OR	50	20	40	P
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	35	P

GRAND TOTAL = 1056/1500, RESULT: FIRST CLASS WITH DISTINCTION
ORDN. 1 MARKS :

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S80053033   CHASKAR DIPALI SHRIDHAR   ALKA   , 71200810B   , S80053033 ,   PICT   , S80053033

01. SIGNAL AND SYSTEMS	PP	100	40	66	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	68	P	
02. SIGNAL AND SYSTEMS	OR	50	20	25	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	21	P	
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	64	P	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	40	P
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	27	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	20	P
05. NETWORK ANALYSIS	PP	100	40	43	P C	15. ELECTROMAGNETIC	PP	100	40	57	P
06. DIGITAL LOGIC DESIGN	PP	100	40	54	P	16. ELECTROMAGNETIC	TW	25	10	22	P
07. DIGITAL LOGIC DESIGN	PR	50	20	30	P C	17. DATA STRUCTURES	PP	100	40	53	P
08. POWER DEVICES AND MACHINES	PP	100	40	56	P C	18. DATA STRUCTURES	PR	50	20	25	P
09. NETWORK AND POWER LAB.	TW	50	20	39	P C	19. COMMUNICATION THEORY	PP	100	40	54	P
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	38	P C	20. COMMUNICATION THEORY	OR	50	20	28	P
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	40	P

GRAND TOTAL = 870/1500, RESULT: HIGHER SECOND CLASS
ORDN. 1 MARKS :

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S80053034   CHAVAN ROHIT UTTAM   MEENA   , 71200816M   , S80053034 ,   PICT   , S80053034

01. SIGNAL AND SYSTEMS	PP	100	40	75	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	78	P	
02. SIGNAL AND SYSTEMS	OR	50	20	41	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	23	P	
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	48	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	63	P
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	43	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	36	P
05. NETWORK ANALYSIS	PP	100	40	73	P C	15. ELECTROMAGNETIC	PP	100	40	84	P
06. DIGITAL LOGIC DESIGN	PP	100	40	48	P C	16. ELECTROMAGNETIC	TW	25	10	22	P
07. DIGITAL LOGIC DESIGN	PR	50	20	45	P C	17. DATA STRUCTURES	PP	100	40	68	P
08. POWER DEVICES AND MACHINES	PP	100	40	66	P C	18. DATA STRUCTURES	PR	50	20	45	P
09. NETWORK AND POWER LAB.	TW	50	20	44	P C	19. COMMUNICATION THEORY	PP	100	40	74	P
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	45	P C	20. COMMUNICATION THEORY	OR	50	20	41	P
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	45	P

GRAND TOTAL = 1107/1500, RESULT: FIRST CLASS WITH DISTINCTION
ORDN. 1 MARKS :

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.
      OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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S80053035   CHIRANIYA ANKITA GOPAL                RAKHI              , 71200821H    , S80053035 ,  PICT     , S80053035
01. SIGNAL AND SYSTEMS           PP   100 40   51 P C    11. ENGINEERING MATHEMATICS III     PP   100 40   54 P
02. SIGNAL AND SYSTEMS           OR    50 20   20 P      12. ENGINEERING MATHEMATICS III     TW    25 10   16 P
03. SOLID STATES DEVICES AND CIRCUITSPP   100 40   63 P      13. INTEGRATED CIRCUITS APPLICATIONS PP   100 40   42 P
04. SOLID STATES DEVICES AND CIRCUITSPR    50 20   27 P C    14. INTEGRATED CIRCUITS APPLICATIONS PR    50 20   15# P
05. NETWORK ANALYSIS             PP   100 40   43 P      15. ELECTROMAGNETIC                 PP   100 40   43 P
06. DIGITAL LOGIC DESIGN         PP   100 40   49 P      16. ELECTROMAGNETIC                 TW    25 10   17 P
07. DIGITAL LOGIC DESIGN         PR    50 20   31 P C    17. DATA STRUCTURES                 PP   100 40   62 P
08. POWER DEVICES AND MACHINES   PP   100 40   40 P C    18. DATA STRUCTURES                 PR    50 20   27 P
09. NETWORK AND POWER LAB.       TW    50 20   34 P C    19. COMMUNICATION THEORY            PP   100 40   48 P
10. ELECTRONIC INSTRUMENTS AND TOOLS TW    50 20   32 P C    20. COMMUNICATION THEORY            OR    50 20   30 P
                                                        21. CIRCUIT SIMULATION AND TOOLS   TW    50 20   30 P
GRAND TOTAL =  774/1500, RESULT: SECOND CLASS   # [O.4]
ORDN. 1 MARKS :
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S80053036   CHOWDHARY NOOPUR YUVRAJKUMAR          HARSHA             , 71200823D    , S80053036 ,  PICT     , S80053036
01. SIGNAL AND SYSTEMS           PP   100 40   40 P C    11. ENGINEERING MATHEMATICS III     PP   100 40   43 P
02. SIGNAL AND SYSTEMS           OR    50 20   33 P C    12. ENGINEERING MATHEMATICS III     TW    25 10   18 P
03. SOLID STATES DEVICES AND CIRCUITSPP   100 40   65 P      13. INTEGRATED CIRCUITS APPLICATIONS PP   100 40   55 P
04. SOLID STATES DEVICES AND CIRCUITSPR    50 20   35 P C    14. INTEGRATED CIRCUITS APPLICATIONS PR    50 20   25 P
05. NETWORK ANALYSIS             PP   100 40   48 P      15. ELECTROMAGNETIC                 PP   100 40   53 P
06. DIGITAL LOGIC DESIGN         PP   100 40   51 P      16. ELECTROMAGNETIC                 TW    25 10   18 P
07. DIGITAL LOGIC DESIGN         PR    50 20   32 P C    17. DATA STRUCTURES                 PP   100 40   49 P
08. POWER DEVICES AND MACHINES   PP   100 40   51 P C    18. DATA STRUCTURES                 PR    50 20   30 P
09. NETWORK AND POWER LAB.       TW    50 20   31 P C    19. COMMUNICATION THEORY            PP   100 40   51 P
10. ELECTRONIC INSTRUMENTS AND TOOLS TW    50 20   33 P C    20. COMMUNICATION THEORY            OR    50 20   33 P
                                                        21. CIRCUIT SIMULATION AND TOOLS   TW    50 20   33 P
GRAND TOTAL =  827/1500, RESULT: HIGHER SECOND CLASS
ORDN. 1 MARKS :
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S80053037   DAKE ABOLI BALASAHEB                  ARUNA              , 71200826J    , S80053037 ,  PICT     , S80053037
01. SIGNAL AND SYSTEMS           PP   100 40   53 P C    11. ENGINEERING MATHEMATICS III     PP   100 40   40 P
02. SIGNAL AND SYSTEMS           OR    50 20   30 P C    12. ENGINEERING MATHEMATICS III     TW    25 10   17 P
03. SOLID STATES DEVICES AND CIRCUITSPP   100 40   40 P C    13. INTEGRATED CIRCUITS APPLICATIONS PP   100 40   40 P
04. SOLID STATES DEVICES AND CIRCUITSPR    50 20   36 P C    14. INTEGRATED CIRCUITS APPLICATIONS PR    50 20   35 P
05. NETWORK ANALYSIS             PP   100 40   40 P      15. ELECTROMAGNETIC                 PP   100 40   62 P
06. DIGITAL LOGIC DESIGN         PP   100 40   40 P C    16. ELECTROMAGNETIC                 TW    25 10   17 P
07. DIGITAL LOGIC DESIGN         PR    50 20   35 P C    17. DATA STRUCTURES                 PP   100 40   43 P
08. POWER DEVICES AND MACHINES   PP   100 40   45 P C    18. DATA STRUCTURES                 PR    50 20   35 P
09. NETWORK AND POWER LAB.       TW    50 20   35 P C    19. COMMUNICATION THEORY            PP   100 40   41 P
10. ELECTRONIC INSTRUMENTS AND TOOLS TW    50 20   31 P C    20. COMMUNICATION THEORY            OR    50 20   27 P
                                                        21. CIRCUIT SIMULATION AND TOOLS   TW    50 20   31 P
GRAND TOTAL =  773/1500, RESULT: SECOND CLASS
ORDN. 1 MARKS :
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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.

        OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S80053038   DESHPANDE CHAITANYA DHANANJAY   SWATI   , 71200832C   , S80053038 ,   PICT   , S80053038

01. SIGNAL AND SYSTEMS	PP	100	40	74	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	90	P
02. SIGNAL AND SYSTEMS	OR	50	20	38	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	24	P
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	71	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	71	P
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	38	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	42	P
05. NETWORK ANALYSIS	PP	100	40	63	P C	15. ELECTROMAGNETIC	PP	100	40	77	P
06. DIGITAL LOGIC DESIGN	PP	100	40	56	P C	16. ELECTROMAGNETIC	TW	25	10	23	P
07. DIGITAL LOGIC DESIGN	PR	50	20	40	P C	17. DATA STRUCTURES	PP	100	40	80	P
08. POWER DEVICES AND MACHINES	PP	100	40	72	P C	18. DATA STRUCTURES	PR	50	20	46	P
09. NETWORK AND POWER LAB.	TW	50	20	43	P C	19. COMMUNICATION THEORY	PP	100	40	71	P
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	43	P C	20. COMMUNICATION THEORY	OR	50	20	40	P
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	46	P

GRAND TOTAL = 1148/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

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S80053039   DESHPANDE KAUSTUBH SURYAKANT   MANIK   , 71200833M   , S80053039 ,   PICT   , S80053039

01. SIGNAL AND SYSTEMS	PP	100	40	67	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	90	P
02. SIGNAL AND SYSTEMS	OR	50	20	34	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	21	P
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	40	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	53	P
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	33	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	37	P
05. NETWORK ANALYSIS	PP	100	40	41	P C	15. ELECTROMAGNETIC	PP	100	40	69	P
06. DIGITAL LOGIC DESIGN	PP	100	40	40	P C	16. ELECTROMAGNETIC	TW	25	10	21	P
07. DIGITAL LOGIC DESIGN	PR	50	20	38	P C	17. DATA STRUCTURES	PP	100	40	71	P
08. POWER DEVICES AND MACHINES	PP	100	40	50	P C	18. DATA STRUCTURES	PR	50	20	40	P
09. NETWORK AND POWER LAB.	TW	50	20	39	P C	19. COMMUNICATION THEORY	PP	100	40	64	P
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	35	P C	20. COMMUNICATION THEORY	OR	50	20	28	P
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	38	P

GRAND TOTAL = 949/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

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S80053040   DESHPANDE NIKITA KISHOR   NEETA   , 71200834K   , S80053040 ,   PICT   , S80053040

01. SIGNAL AND SYSTEMS	PP	100	40	59	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	55	P
02. SIGNAL AND SYSTEMS	OR	50	20	22	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	16	P
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	40	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	45	P
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	28	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	22	P
05. NETWORK ANALYSIS	PP	100	40	42	P	15. ELECTROMAGNETIC	PP	100	40	80	P
06. DIGITAL LOGIC DESIGN	PP	100	40	40	P C	16. ELECTROMAGNETIC	TW	25	10	16	P
07. DIGITAL LOGIC DESIGN	PR	50	20	20	P C	17. DATA STRUCTURES	PP	100	40	63	P
08. POWER DEVICES AND MACHINES	PP	100	40	49	P C	18. DATA STRUCTURES	PR	50	20	37	P
09. NETWORK AND POWER LAB.	TW	50	20	29	P C	19. COMMUNICATION THEORY	PP	100	40	40	P
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	31	P C	20. COMMUNICATION THEORY	OR	50	20	22	P
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	29	P

GRAND TOTAL = 785/1500, RESULT: SECOND CLASS

ORDN. 1 MARKS :

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.
        OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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S80053041   DESHPANDE SUBODH SANJAYRAO            SUPRIYA          , 71200835H    , S80053041 ,   PICT      , S80053041
01. SIGNAL AND SYSTEMS              PP   100  40   54  P C   11. ENGINEERING MATHEMATICS III      PP   100  40   60  P
02. SIGNAL AND SYSTEMS              OR    50  20   30  P C   12. ENGINEERING MATHEMATICS III      TW    25  10   18  P
03. SOLID STATES DEVICES AND CIRCUITSPP   100  40   40  P C   13. INTEGRATED CIRCUITS APPLICATIONS PP   100  40   43  P
04. SOLID STATES DEVICES AND CIRCUITSPR    50  20   25  P C   14. INTEGRATED CIRCUITS APPLICATIONS PR    50  20   38  P
05. NETWORK ANALYSIS               PP   100  40   40  P     15. ELECTROMAGNETIC                 PP   100  40   60  P
06. DIGITAL LOGIC DESIGN           PP   100  40   47  P     16. ELECTROMAGNETIC                 TW    25  10   20  P
07. DIGITAL LOGIC DESIGN           PR    50  20   37  P C   17. DATA STRUCTURES                 PP   100  40   56  P
08. POWER DEVICES AND MACHINES     PP   100  40   42  P C   18. DATA STRUCTURES                 PR    50  20   38  P
09. NETWORK AND POWER LAB.         TW    50  20   41  P C   19. COMMUNICATION THEORY            PP   100  40   51  P
10. ELECTRONIC INSTRUMENTS AND TOOLS TW   50  20   41  P C   20. COMMUNICATION THEORY            OR    50  20   27  P
                                                            21. CIRCUIT SIMULATION AND TOOLS   TW    50  20   32  P
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GRAND TOTAL = 840/1500, RESULT: HIGHER SECOND CLASS
ORDN. 1 MARKS :

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S80053042   DHAMAL NAMRATA MOHAN               SAVITA           , 71344353H    , S80053042 ,   PICT      , S80053042
01. SIGNAL AND SYSTEMS              PP   100  40   63  P C   11. ENGINEERING MATHEMATICS III      PP   100  40   65  P
02. SIGNAL AND SYSTEMS              OR    50  20   32  P C   12. ENGINEERING MATHEMATICS III      TW    25  10   21  P
03. SOLID STATES DEVICES AND CIRCUITSPP   100  40   53  P C   13. INTEGRATED CIRCUITS APPLICATIONS PP   100  40   70  P
04. SOLID STATES DEVICES AND CIRCUITSPR    50  20   36  P C   14. INTEGRATED CIRCUITS APPLICATIONS PR    50  20   36  P
05. NETWORK ANALYSIS               PP   100  40   44  P C   15. ELECTROMAGNETIC                 PP   100  40   69  P
06. DIGITAL LOGIC DESIGN           PP   100  40   45  P C   16. ELECTROMAGNETIC                 TW    25  10   22  P
07. DIGITAL LOGIC DESIGN           PR    50  20   36  P C   17. DATA STRUCTURES                 PP   100  40   76  P
08. POWER DEVICES AND MACHINES     PP   100  40   51  P C   18. DATA STRUCTURES                 PR    50  20   39  P
09. NETWORK AND POWER LAB.         TW    50  20   40  P C   19. COMMUNICATION THEORY            PP   100  40   65  P
10. ELECTRONIC INSTRUMENTS AND TOOLS TW   50  20   38  P C   20. COMMUNICATION THEORY            OR    50  20   32  P
                                                            21. CIRCUIT SIMULATION AND TOOLS   TW    50  20   43  P
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GRAND TOTAL = 976/1500, RESULT: FIRST CLASS
ORDN. 1 MARKS :

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S80053043   DHORE PRASHANT SHRIKRISHNA         RANJANA          , 71200842L    , S80053043 ,   PICT      , S80053043
01. SIGNAL AND SYSTEMS              PP   100  40   62  P C   11. ENGINEERING MATHEMATICS III      PP   100  40   44  P
02. SIGNAL AND SYSTEMS              OR    50  20   30  P C   12. ENGINEERING MATHEMATICS III      TW    25  10   14  P
03. SOLID STATES DEVICES AND CIRCUITSPP   100  40   56  P C   13. INTEGRATED CIRCUITS APPLICATIONS PP   100  40   57  P
04. SOLID STATES DEVICES AND CIRCUITSPR    50  20   33  P C   14. INTEGRATED CIRCUITS APPLICATIONS PR    50  20   32  P
05. NETWORK ANALYSIS               PP   100  40   40  P C   15. ELECTROMAGNETIC                 PP   100  40   64  P
06. DIGITAL LOGIC DESIGN           PP   100  40   58  P     16. ELECTROMAGNETIC                 TW    25  10   13  P
07. DIGITAL LOGIC DESIGN           PR    50  20   37  P C   17. DATA STRUCTURES                 PP   100  40   55  P
08. POWER DEVICES AND MACHINES     PP   100  40   56  P C   18. DATA STRUCTURES                 PR    50  20   36  P
09. NETWORK AND POWER LAB.         TW    50  20   31  P C   19. COMMUNICATION THEORY            PP   100  40   45  P
10. ELECTRONIC INSTRUMENTS AND TOOLS TW   50  20   35  P C   20. COMMUNICATION THEORY            OR    50  20   30  P
                                                            21. CIRCUIT SIMULATION AND TOOLS   TW    50  20   30  P
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GRAND TOTAL = 858/1500, RESULT: HIGHER SECOND CLASS
ORDN. 1 MARKS :

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
       OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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S80053044   DHOTEY RUTVIJ SUDHIR   SANGITA   , 71200843J   , S80053044 , PICT   , S80053044
01. SIGNAL AND SYSTEMS   PP   100 40   77 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   74 P
02. SIGNAL AND SYSTEMS   OR   50 20   30 P C   12. ENGINEERING MATHEMATICS III   TW   25 10   17 P
03. SOLID STATES DEVICES AND CIRCUITSPP 100 40   48 P C   13. INTEGRATED CIRCUITS APPLICATIONS PP   100 40   60 P
04. SOLID STATES DEVICES AND CIRCUITSPR   50 20   43 P C   14. INTEGRATED CIRCUITS APPLICATIONS PR   50 20   35 P
05. NETWORK ANALYSIS   PP   100 40   62 P C   15. ELECTROMAGNETIC   PP   100 40   59 P
06. DIGITAL LOGIC DESIGN   PP   100 40   44 P C   16. ELECTROMAGNETIC   TW   25 10   18 P
07. DIGITAL LOGIC DESIGN   PR   50 20   47 P C   17. DATA STRUCTURES   PP   100 40   84 P
08. POWER DEVICES AND MACHINES   PP   100 40   53 P C   18. DATA STRUCTURES   PR   50 20   37 P
09. NETWORK AND POWER LAB.   TW   50 20   41 P C   19. COMMUNICATION THEORY   PP   100 40   58 P
10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20   38 P C   20. COMMUNICATION THEORY   OR   50 20   35 P
                                                          21. CIRCUIT SIMULATION AND TOOLS   TW   50 20   33 P
GRAND TOTAL = 993/1500, RESULT: FIRST CLASS WITH DISTINCTION
ORDN. 1 MARKS :

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S80053045   DOKADE ABHIJIT VISHNU   BEBY   , 71200847M   , S80053045 , PICT   , S80053045
01. SIGNAL AND SYSTEMS   PP   100 40   40 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   01 F
02. SIGNAL AND SYSTEMS   OR   50 20   20 P C   12. ENGINEERING MATHEMATICS III   TW   25 10   13 P
03. SOLID STATES DEVICES AND CIRCUITSPP 100 40   19 F   13. INTEGRATED CIRCUITS APPLICATIONS PP   100 40   15 F
04. SOLID STATES DEVICES AND CIRCUITSPR   50 20   20 P   14. INTEGRATED CIRCUITS APPLICATIONS PR   50 20   20 P
05. NETWORK ANALYSIS   PP   100 40   08 F   15. ELECTROMAGNETIC   PP   100 40   22 F
06. DIGITAL LOGIC DESIGN   PP   100 40   40 P   16. ELECTROMAGNETIC   TW   25 10   12 P
07. DIGITAL LOGIC DESIGN   PR   50 20   10 F   17. DATA STRUCTURES   PP   100 40   44 P
08. POWER DEVICES AND MACHINES   PP   100 40   40 P   18. DATA STRUCTURES   PR   50 20   AA F
09. NETWORK AND POWER LAB.   TW   50 20   26 P C   19. COMMUNICATION THEORY   PP   100 40   21 F
10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20   31 P C   20. COMMUNICATION THEORY   OR   50 20   22 P
                                                          21. CIRCUIT SIMULATION AND TOOLS   TW   50 20   22 P
GRAND TOTAL = 446/1500, RESULT: FAILS   RESULT RESERVED FOR BKLG
ORDN. 1 MARKS :

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S80053046   DONDE YATHARTH ANAND   MANISHA   , 71200848K   , S80053046 , PICT   , S80053046
01. SIGNAL AND SYSTEMS   PP   100 40   40 P   11. ENGINEERING MATHEMATICS III   PP   100 40   18 F
02. SIGNAL AND SYSTEMS   OR   50 20   20 P   12. ENGINEERING MATHEMATICS III   TW   25 10   11 P
03. SOLID STATES DEVICES AND CIRCUITSPP 100 40   40 P   13. INTEGRATED CIRCUITS APPLICATIONS PP   100 40   19 F
04. SOLID STATES DEVICES AND CIRCUITSPR   50 20   20 P   14. INTEGRATED CIRCUITS APPLICATIONS PR   50 20   05 F
05. NETWORK ANALYSIS   PP   100 40   15 F   15. ELECTROMAGNETIC   PP   100 40   25 F
06. DIGITAL LOGIC DESIGN   PP   100 40   20 F   16. ELECTROMAGNETIC   TW   25 10   17 P
07. DIGITAL LOGIC DESIGN   PR   50 20   20 P C   17. DATA STRUCTURES   PP   100 40   29 F
08. POWER DEVICES AND MACHINES   PP   100 40   26 F   18. DATA STRUCTURES   PR   50 20   25 P
09. NETWORK AND POWER LAB.   TW   50 20   32 P C   19. COMMUNICATION THEORY   PP   100 40   21 F
10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20   28 P C   20. COMMUNICATION THEORY   OR   50 20   22 P
                                                          21. CIRCUIT SIMULATION AND TOOLS   TW   50 20   27 P
GRAND TOTAL = 480/1500, RESULT: FAILS   RESULT RESERVED FOR BKLG
ORDN. 1 MARKS :

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.
      OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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S80053047   GAIKWAD VAISHALI RAMESH   SHITAL   , 71344354F   , S80053047 ,   PICT   , S80053047
01. SIGNAL AND SYSTEMS   PP   100 40   57 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   58 P
02. SIGNAL AND SYSTEMS   OR   50 20   28 P C   12. ENGINEERING MATHEMATICS III   TW   25 10   22 P
03. SOLID STATES DEVICES AND CIRCUITSPP   100 40   40 P C   13. INTEGRATED CIRCUITS APPLICATIONS PP   100 40   52 P
04. SOLID STATES DEVICES AND CIRCUITSPR   50 20   20 P C   14. INTEGRATED CIRCUITS APPLICATIONS PR   50 20   23 P
05. NETWORK ANALYSIS   PP   100 40   40 P C   15. ELECTROMAGNETIC   PP   100 40   50 P
06. DIGITAL LOGIC DESIGN   PP   100 40   66 P   16. ELECTROMAGNETIC   TW   25 10   20 P
07. DIGITAL LOGIC DESIGN   PR   50 20   30 P C   17. DATA STRUCTURES   PP   100 40   54 P
08. POWER DEVICES AND MACHINES   PP   100 40   58 P C   18. DATA STRUCTURES   PR   50 20   30 P
09. NETWORK AND POWER LAB.   TW   50 20   40 P C   19. COMMUNICATION THEORY   PP   100 40   51 P
10. ELECTRONIC INSTRUMENTS AND TOOLS TW   50 20   38 P C   20. COMMUNICATION THEORY   OR   50 20   29 P
                                                         21. CIRCUIT SIMULATION AND TOOLS   TW   50 20   38 P
GRAND TOTAL = 844/1500, RESULT: HIGHER SECOND CLASS
ORDN. 1 MARKS :
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S80053048   GANGAL AKSHAY PARAG   RAJASHREE   , 71200857J   , S80053048 ,   PICT   , S80053048
01. SIGNAL AND SYSTEMS   PP   100 40   70 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   98 P
02. SIGNAL AND SYSTEMS   OR   50 20   38 P C   12. ENGINEERING MATHEMATICS III   TW   25 10   24 P
03. SOLID STATES DEVICES AND CIRCUITSPP   100 40   66 P C   13. INTEGRATED CIRCUITS APPLICATIONS PP   100 40   68 P
04. SOLID STATES DEVICES AND CIRCUITSPR   50 20   44 P C   14. INTEGRATED CIRCUITS APPLICATIONS PR   50 20   43 P
05. NETWORK ANALYSIS   PP   100 40   61 P C   15. ELECTROMAGNETIC   PP   100 40   77 P
06. DIGITAL LOGIC DESIGN   PP   100 40   54 P C   16. ELECTROMAGNETIC   TW   25 10   23 P
07. DIGITAL LOGIC DESIGN   PR   50 20   44 P C   17. DATA STRUCTURES   PP   100 40   81 P
08. POWER DEVICES AND MACHINES   PP   100 40   67 P C   18. DATA STRUCTURES   PR   50 20   45 P
09. NETWORK AND POWER LAB.   TW   50 20   45 P C   19. COMMUNICATION THEORY   PP   100 40   80 P
10. ELECTRONIC INSTRUMENTS AND TOOLS TW   50 20   45 P C   20. COMMUNICATION THEORY   OR   50 20   35 P
                                                         21. CIRCUIT SIMULATION AND TOOLS   TW   50 20   45 P
GRAND TOTAL = 1153/1500, RESULT: FIRST CLASS WITH DISTINCTION
ORDN. 1 MARKS :
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S80053049   GHADGE GITANJALI SUBHASH   MANJUSHA   , 71200863C   , S80053049 ,   PICT   , S80053049
01. SIGNAL AND SYSTEMS   PP   100 40   71 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   63 P
02. SIGNAL AND SYSTEMS   OR   50 20   35 P C   12. ENGINEERING MATHEMATICS III   TW   25 10   22 P
03. SOLID STATES DEVICES AND CIRCUITSPP   100 40   42 P C   13. INTEGRATED CIRCUITS APPLICATIONS PP   100 40   57 P
04. SOLID STATES DEVICES AND CIRCUITSPR   50 20   38 P C   14. INTEGRATED CIRCUITS APPLICATIONS PR   50 20   40 P
05. NETWORK ANALYSIS   PP   100 40   45 P C   15. ELECTROMAGNETIC   PP   100 40   64 P
06. DIGITAL LOGIC DESIGN   PP   100 40   40 P C   16. ELECTROMAGNETIC   TW   25 10   21 P
07. DIGITAL LOGIC DESIGN   PR   50 20   48 P C   17. DATA STRUCTURES   PP   100 40   65 P
08. POWER DEVICES AND MACHINES   PP   100 40   55 P C   18. DATA STRUCTURES   PR   50 20   41 P
09. NETWORK AND POWER LAB.   TW   50 20   42 P C   19. COMMUNICATION THEORY   PP   100 40   58 P
10. ELECTRONIC INSTRUMENTS AND TOOLS TW   50 20   39 P C   20. COMMUNICATION THEORY   OR   50 20   37 P
                                                         21. CIRCUIT SIMULATION AND TOOLS   TW   50 20   38 P
GRAND TOTAL = 961/1500, RESULT: FIRST CLASS
ORDN. 1 MARKS :
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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
        OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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S80053050   GIRI PRATIK CHANDRAKANT              SANDHYA GIRI          , 71344355D    , S80053050 ,   PICT     , S80053050
01. SIGNAL AND SYSTEMS          PP   100 40   57 P C    11. ENGINEERING MATHEMATICS III      PP   100 40   81 P
02. SIGNAL AND SYSTEMS          OR    50 20   21 P C    12. ENGINEERING MATHEMATICS III      TW    25 10   21 P
03. SOLID STATES DEVICES AND CIRCUITSPP  100 40   65 P C    13. INTEGRATED CIRCUITS APPLICATIONS PP   100 40   59 P
04. SOLID STATES DEVICES AND CIRCUITSPR   50 20   26 P C    14. INTEGRATED CIRCUITS APPLICATIONS PR    50 20   32 P
05. NETWORK ANALYSIS            PP   100 40   40 P C    15. ELECTROMAGNETIC                  PP   100 40   65 P
06. DIGITAL LOGIC DESIGN        PP   100 40   46 P C    16. ELECTROMAGNETIC                  TW    25 10   21 P
07. DIGITAL LOGIC DESIGN        PR    50 20   28 P C    17. DATA STRUCTURES                 PP   100 40   69 P
08. POWER DEVICES AND MACHINES  PP   100 40   61 P C    18. DATA STRUCTURES                 PR    50 20   37 P
09. NETWORK AND POWER LAB.      TW    50 20   38 P C    19. COMMUNICATION THEORY            PP   100 40   71 P
10. ELECTRONIC INSTRUMENTS AND TOOLS TW    50 20   37 P C    20. COMMUNICATION THEORY            OR    50 20   37 P
                                                       21. CIRCUIT SIMULATION AND TOOLS   TW    50 20   41 P
GRAND TOTAL =  953/1500, RESULT: FIRST CLASS
ORDN. 1 MARKS :
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S80053051   GOKHALE SATYAJIT SHASHANK            JAYASHREE            , 71200866H    , S80053051 ,   PICT     , S80053051
01. SIGNAL AND SYSTEMS          PP   100 40   75 P C    11. ENGINEERING MATHEMATICS III      PP   100 40   52 P
02. SIGNAL AND SYSTEMS          OR    50 20   29 P C    12. ENGINEERING MATHEMATICS III      TW    25 10   20 P
03. SOLID STATES DEVICES AND CIRCUITSPP  100 40   40 P C    13. INTEGRATED CIRCUITS APPLICATIONS PP   100 40   55 P
04. SOLID STATES DEVICES AND CIRCUITSPR   50 20   34 P C    14. INTEGRATED CIRCUITS APPLICATIONS PR    50 20   38 P
05. NETWORK ANALYSIS            PP   100 40   40 P C    15. ELECTROMAGNETIC                  PP   100 40   60 P
06. DIGITAL LOGIC DESIGN        PP   100 40   40 P C    16. ELECTROMAGNETIC                  TW    25 10   19 P
07. DIGITAL LOGIC DESIGN        PR    50 20   30 P C    17. DATA STRUCTURES                 PP   100 40   63 P
08. POWER DEVICES AND MACHINES  PP   100 40   64 P C    18. DATA STRUCTURES                 PR    50 20   42 P
09. NETWORK AND POWER LAB.      TW    50 20   41 P C    19. COMMUNICATION THEORY            PP   100 40   61 P
10. ELECTRONIC INSTRUMENTS AND TOOLS TW    50 20   41 P C    20. COMMUNICATION THEORY            OR    50 20   37 P
                                                       21. CIRCUIT SIMULATION AND TOOLS   TW    50 20   39 P
GRAND TOTAL =  920/1500, RESULT: FIRST CLASS
ORDN. 1 MARKS :
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S80053052   GOSAVI ABHISHEK MAHESH               MANISHA             , 71344356B    , S80053052 ,   PICT     , S80053052
01. SIGNAL AND SYSTEMS          PP   100 40   51 P C    11. ENGINEERING MATHEMATICS III      PP   100 40   47 P
02. SIGNAL AND SYSTEMS          OR    50 20   25 P C    12. ENGINEERING MATHEMATICS III      TW    25 10   19 P
03. SOLID STATES DEVICES AND CIRCUITSPP  100 40   48 P C    13. INTEGRATED CIRCUITS APPLICATIONS PP   100 40   63 P
04. SOLID STATES DEVICES AND CIRCUITSPR   50 20   24 P C    14. INTEGRATED CIRCUITS APPLICATIONS PR    50 20   32 P
05. NETWORK ANALYSIS            PP   100 40   40 P C    15. ELECTROMAGNETIC                  PP   100 40   48 P
06. DIGITAL LOGIC DESIGN        PP   100 40   45 P C    16. ELECTROMAGNETIC                  TW    25 10   18 P
07. DIGITAL LOGIC DESIGN        PR    50 20   30 P C    17. DATA STRUCTURES                 PP   100 40   69 P
08. POWER DEVICES AND MACHINES  PP   100 40   48 P C    18. DATA STRUCTURES                 PR    50 20   37 P
09. NETWORK AND POWER LAB.      TW    50 20   38 P C    19. COMMUNICATION THEORY            PP   100 40   65 P
10. ELECTRONIC INSTRUMENTS AND TOOLS TW    50 20   38 P C    20. COMMUNICATION THEORY            OR    50 20   32 P
                                                       21. CIRCUIT SIMULATION AND TOOLS   TW    50 20   32 P
GRAND TOTAL =  849/1500, RESULT: HIGHER SECOND CLASS
ORDN. 1 MARKS :
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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.
        OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S80053053   GUMBADE MAYURI RAGHUNATH   LATA   , 71344357L   , S80053053 , PICT   , S80053053

01. SIGNAL AND SYSTEMS	PP	100	40	42	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	40	P
02. SIGNAL AND SYSTEMS	OR	50	20	26	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	18	P
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	43	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	52	P
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	20	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	33	P
05. NETWORK ANALYSIS	PP	100	40	45	P	15. ELECTROMAGNETIC	PP	100	40	57	P
06. DIGITAL LOGIC DESIGN	PP	100	40	59	P	16. ELECTROMAGNETIC	TW	25	10	19	P
07. DIGITAL LOGIC DESIGN	PR	50	20	20	P C	17. DATA STRUCTURES	PP	100	40	55	P
08. POWER DEVICES AND MACHINES	PP	100	40	48	P C	18. DATA STRUCTURES	PR	50	20	25	P
09. NETWORK AND POWER LAB.	TW	50	20	38	P C	19. COMMUNICATION THEORY	PP	100	40	55	P
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	36	P C	20. COMMUNICATION THEORY	OR	50	20	27	P
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	36	P

GRAND TOTAL = 794/1500, RESULT: SECOND CLASS

ORDN. 1 MARKS :

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S80053054   GUTTE KISHOR SHIVAJI   USHABAI   , 71200874J   , S80053054 , PICT   , S80053054

01. SIGNAL AND SYSTEMS	PP	100	40	73	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	86	P
02. SIGNAL AND SYSTEMS	OR	50	20	29	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	21	P
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	48	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	59	P
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	36	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	26	P
05. NETWORK ANALYSIS	PP	100	40	46	P C	15. ELECTROMAGNETIC	PP	100	40	68	P
06. DIGITAL LOGIC DESIGN	PP	100	40	40	P C	16. ELECTROMAGNETIC	TW	25	10	22	P
07. DIGITAL LOGIC DESIGN	PR	50	20	36	P C	17. DATA STRUCTURES	PP	100	40	72	P
08. POWER DEVICES AND MACHINES	PP	100	40	58	P C	18. DATA STRUCTURES	PR	50	20	42	P
09. NETWORK AND POWER LAB.	TW	50	20	42	P C	19. COMMUNICATION THEORY	PP	100	40	63	P
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	40	P C	20. COMMUNICATION THEORY	OR	50	20	27	P
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	41	P

GRAND TOTAL = 975/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

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S80053055   HEGDE GAUTAMI GOPALKRISHNA   APARNA   , 71200880C   , S80053055 , PICT   , S80053055

01. SIGNAL AND SYSTEMS	PP	100	40	89	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	94	P
02. SIGNAL AND SYSTEMS	OR	50	20	35	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	16	P
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	56	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	64	P
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	38	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	35	P
05. NETWORK ANALYSIS	PP	100	40	70	P C	15. ELECTROMAGNETIC	PP	100	40	92	P
06. DIGITAL LOGIC DESIGN	PP	100	40	50	P C	16. ELECTROMAGNETIC	TW	25	10	19	P
07. DIGITAL LOGIC DESIGN	PR	50	20	37	P C	17. DATA STRUCTURES	PP	100	40	87	P
08. POWER DEVICES AND MACHINES	PP	100	40	69	P C	18. DATA STRUCTURES	PR	50	20	43	P
09. NETWORK AND POWER LAB.	TW	50	20	44	P C	19. COMMUNICATION THEORY	PP	100	40	76	P
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	44	P C	20. COMMUNICATION THEORY	OR	50	20	30	P
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	36	P

GRAND TOTAL = 1124/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

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   NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.
         OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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   S80053056   HINGANE SOURABH MADHUKAR   MADHURI   , 71200881M   , S80053056 ,   PICT   , S80053056
   01. SIGNAL AND SYSTEMS   PP   100 40   86 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   84 P
   02. SIGNAL AND SYSTEMS   OR   50 20   42 P C   12. ENGINEERING MATHEMATICS III   TW   25 10   22 P
   03. SOLID STATES DEVICES AND CIRCUITSPP   100 40   50 P C   13. INTEGRATED CIRCUITS APPLICATIONS PP   100 40   59 P
   04. SOLID STATES DEVICES AND CIRCUITSPR   50 20   35 P C   14. INTEGRATED CIRCUITS APPLICATIONS PR   50 20   30 P
   05. NETWORK ANALYSIS   PP   100 40   41 P C   15. ELECTROMAGNETIC   PP   100 40   80 P
   06. DIGITAL LOGIC DESIGN   PP   100 40   40 P C   16. ELECTROMAGNETIC   TW   25 10   22 P
   07. DIGITAL LOGIC DESIGN   PR   50 20   45 P C   17. DATA STRUCTURES   PP   100 40   78 P
   08. POWER DEVICES AND MACHINES   PP   100 40   66 P C   18. DATA STRUCTURES   PR   50 20   45 P
   09. NETWORK AND POWER LAB.   TW   50 20   41 P C   19. COMMUNICATION THEORY   PP   100 40   72 P
   10. ELECTRONIC INSTRUMENTS AND TOOLS TW   50 20   39 P C   20. COMMUNICATION THEORY   OR   50 20   30 P
                                                                21. CIRCUIT SIMULATION AND TOOLS   TW   50 20   36 P
GRAND TOTAL = 1043/1500, RESULT: FIRST CLASS WITH DISTINCTION
ORDN. 1 MARKS :
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   S80053057   HOWNI SAMAN WANN   LAMBOK   , 71200883H   , S80053057 ,   PICT   , S80053057
   01. SIGNAL AND SYSTEMS   PP   100 40   40 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   40 P
   02. SIGNAL AND SYSTEMS   OR   50 20   21 P C   12. ENGINEERING MATHEMATICS III   TW   25 10   14 P
   03. SOLID STATES DEVICES AND CIRCUITSPP   100 40   40 P   13. INTEGRATED CIRCUITS APPLICATIONS PP   100 40   19 F
   04. SOLID STATES DEVICES AND CIRCUITSPR   50 20   30 P   14. INTEGRATED CIRCUITS APPLICATIONS PR   50 20   08 F
   05. NETWORK ANALYSIS   PP   100 40   40 P   15. ELECTROMAGNETIC   PP   100 40   42 P
   06. DIGITAL LOGIC DESIGN   PP   100 40   40 P   16. ELECTROMAGNETIC   TW   25 10   13 P
   07. DIGITAL LOGIC DESIGN   PR   50 20   20 P C   17. DATA STRUCTURES   PP   100 40   40 P
   08. POWER DEVICES AND MACHINES   PP   100 40   40 P C   18. DATA STRUCTURES   PR   50 20   28 P
   09. NETWORK AND POWER LAB.   TW   50 20   27 P C   19. COMMUNICATION THEORY   PP   100 40   40 P
   10. ELECTRONIC INSTRUMENTS AND TOOLS TW   50 20   31 P C   20. COMMUNICATION THEORY   OR   50 20   10 F
                                                                21. CIRCUIT SIMULATION AND TOOLS   TW   50 20   25 P
GRAND TOTAL = 608/1500, RESULT: FAILS A.T.K.T.
ORDN. 1 MARKS :
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   S80053058   INGLE BHAVANA KRUSHNAJI   SHAILA   , 71344358J   , S80053058 ,   PICT   , S80053058
   01. SIGNAL AND SYSTEMS   PP   100 40   53 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   61 P
   02. SIGNAL AND SYSTEMS   OR   50 20   25 P C   12. ENGINEERING MATHEMATICS III   TW   25 10   22 P
   03. SOLID STATES DEVICES AND CIRCUITSPP   100 40   49 P C   13. INTEGRATED CIRCUITS APPLICATIONS PP   100 40   55 P
   04. SOLID STATES DEVICES AND CIRCUITSPR   50 20   21 P C   14. INTEGRATED CIRCUITS APPLICATIONS PR   50 20   31 P
   05. NETWORK ANALYSIS   PP   100 40   40 P C   15. ELECTROMAGNETIC   PP   100 40   47 P
   06. DIGITAL LOGIC DESIGN   PP   100 40   40 P C   16. ELECTROMAGNETIC   TW   25 10   21 P
   07. DIGITAL LOGIC DESIGN   PR   50 20   23 P C   17. DATA STRUCTURES   PP   100 40   52 P
   08. POWER DEVICES AND MACHINES   PP   100 40   63 P C   18. DATA STRUCTURES   PR   50 20   30 P
   09. NETWORK AND POWER LAB.   TW   50 20   39 P C   19. COMMUNICATION THEORY   PP   100 40   47 P
   10. ELECTRONIC INSTRUMENTS AND TOOLS TW   50 20   37 P C   20. COMMUNICATION THEORY   OR   50 20   40 P
                                                                21. CIRCUIT SIMULATION AND TOOLS   TW   50 20   40 P
GRAND TOTAL = 836/1500, RESULT: HIGHER SECOND CLASS
ORDN. 1 MARKS :
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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.
        OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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S80053059   JADHAV POONAM SANJAY                SANGITA           , 71344359G   , S80053059 ,  PICT     , S80053059
01. SIGNAL AND SYSTEMS              PP   100  40   46  P C    11. ENGINEERING MATHEMATICS III     PP   100  40   08  F
02. SIGNAL AND SYSTEMS              OR    50  20   22  P C    12. ENGINEERING MATHEMATICS III     TW    25  10   18  P
03. SOLID STATES DEVICES AND CIRCUITSPP   100  40   40  P C    13. INTEGRATED CIRCUITS APPLICATIONS PP   100  40   44  P
04. SOLID STATES DEVICES AND CIRCUITSPR    50  20   21  P C    14. INTEGRATED CIRCUITS APPLICATIONS PR    50  20   28  P
05. NETWORK ANALYSIS               PP   100  40   46  P      15. ELECTROMAGNETIC                 PP   100  40   42  P
06. DIGITAL LOGIC DESIGN           PP   100  40   41  P      16. ELECTROMAGNETIC                 TW    25  10   18  P
07. DIGITAL LOGIC DESIGN           PR    50  20   25  P C    17. DATA STRUCTURES                 PP   100  40   40  P
08. POWER DEVICES AND MACHINES      PP   100  40   58  P C    18. DATA STRUCTURES                 PR    50  20   32  P
09. NETWORK AND POWER LAB.          TW    50  20   39  P C    19. COMMUNICATION THEORY            PP   100  40   47  P
10. ELECTRONIC INSTRUMENTS AND TOOLS TW    50  20   37  P C    20. COMMUNICATION THEORY            OR    50  20   44  P
                                                             21. CIRCUIT SIMULATION AND TOOLS    TW    50  20   35  P
GRAND TOTAL =  731/1500, RESULT: FAILS A.T.K.T.
ORDN. 1 MARKS :
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S80053060   JAGTAP SHITAL LAXMAN                LATA              , 71344360L   , S80053060 ,  PICT     , S80053060
01. SIGNAL AND SYSTEMS              PP   100  40   53  P C    11. ENGINEERING MATHEMATICS III     PP   100  40   40  P
02. SIGNAL AND SYSTEMS              OR    50  20   23  P C    12. ENGINEERING MATHEMATICS III     TW    25  10   21  P
03. SOLID STATES DEVICES AND CIRCUITSPP   100  40   40  P C    13. INTEGRATED CIRCUITS APPLICATIONS PP   100  40   52  P
04. SOLID STATES DEVICES AND CIRCUITSPR    50  20   27  P C    14. INTEGRATED CIRCUITS APPLICATIONS PR    50  20   22  P
05. NETWORK ANALYSIS               PP   100  40   43  P      15. ELECTROMAGNETIC                 PP   100  40   47  P
06. DIGITAL LOGIC DESIGN           PP   100  40   45  P C    16. ELECTROMAGNETIC                 TW    25  10   21  P
07. DIGITAL LOGIC DESIGN           PR    50  20   22  P C    17. DATA STRUCTURES                 PP   100  40   64  P
08. POWER DEVICES AND MACHINES      PP   100  40   51  P C    18. DATA STRUCTURES                 PR    50  20   35  P
09. NETWORK AND POWER LAB.          TW    50  20   40  P C    19. COMMUNICATION THEORY            PP   100  40   58  P
10. ELECTRONIC INSTRUMENTS AND TOOLS TW    50  20   38  P C    20. COMMUNICATION THEORY            OR    50  20   34  P
                                                             21. CIRCUIT SIMULATION AND TOOLS    TW    50  20   41  P
GRAND TOTAL =  817+08/1500, RESULT: HIGHER SECOND CLASS  [O.2]
ORDN. 1 MARKS :
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S80053061   JAIN KAJAL HEMANTKUMAR              RAJANI            , 71200887L   , S80053061 ,  PICT     , S80053061
01. SIGNAL AND SYSTEMS              PP   100  40   53  P C    11. ENGINEERING MATHEMATICS III     PP   100  40   91  P
02. SIGNAL AND SYSTEMS              OR    50  20   30  P C    12. ENGINEERING MATHEMATICS III     TW    25  10   18  P
03. SOLID STATES DEVICES AND CIRCUITSPP   100  40   40  P C    13. INTEGRATED CIRCUITS APPLICATIONS PP   100  40   63  P
04. SOLID STATES DEVICES AND CIRCUITSPR    50  20   39  P C    14. INTEGRATED CIRCUITS APPLICATIONS PR    50  20   35  P
05. NETWORK ANALYSIS               PP   100  40   46  P C    15. ELECTROMAGNETIC                 PP   100  40   72  P
06. DIGITAL LOGIC DESIGN           PP   100  40   42  P C    16. ELECTROMAGNETIC                 TW    25  10   21  P
07. DIGITAL LOGIC DESIGN           PR    50  20   34  P C    17. DATA STRUCTURES                 PP   100  40   71  P
08. POWER DEVICES AND MACHINES      PP   100  40   50  P C    18. DATA STRUCTURES                 PR    50  20   38  P
09. NETWORK AND POWER LAB.          TW    50  20   38  P C    19. COMMUNICATION THEORY            PP   100  40   77  P
10. ELECTRONIC INSTRUMENTS AND TOOLS TW    50  20   35  P C    20. COMMUNICATION THEORY            OR    50  20   25  P
                                                             21. CIRCUIT SIMULATION AND TOOLS    TW    50  20   38  P
GRAND TOTAL =  956/1500, RESULT: FIRST CLASS
ORDN. 1 MARKS :
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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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S80053062   JAIN KRUNAL LALIT   MANJU   , 71200888J   , S80053062 ,   PICT   , S80053062

01. SIGNAL AND SYSTEMS	PP	100	40	81	P C		11. ENGINEERING MATHEMATICS III	PP	100	40	78	P
02. SIGNAL AND SYSTEMS	OR	50	20	38	P C		12. ENGINEERING MATHEMATICS III	TW	25	10	24	P
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	59	P C		13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	60	P
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	35	P C		14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	46	P
05. NETWORK ANALYSIS	PP	100	40	49	P C		15. ELECTROMAGNETIC	PP	100	40	86	P
06. DIGITAL LOGIC DESIGN	PP	100	40	40	P C		16. ELECTROMAGNETIC	TW	25	10	22	P
07. DIGITAL LOGIC DESIGN	PR	50	20	46	P C		17. DATA STRUCTURES	PP	100	40	56	P
08. POWER DEVICES AND MACHINES	PP	100	40	71	P C		18. DATA STRUCTURES	PR	50	20	43	P
09. NETWORK AND POWER LAB.	TW	50	20	44	P C		19. COMMUNICATION THEORY	PP	100	40	68	P
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	43	P C		20. COMMUNICATION THEORY	OR	50	20	37	P
							21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	45	P

GRAND TOTAL = 1071/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :
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S80053063   JAVANJAL MAUSAMI SHIVSHANKAR   SANDHYA   , 71344361J   , S80053063 ,   PICT   , S80053063

01. SIGNAL AND SYSTEMS	PP	100	40	77	P C		11. ENGINEERING MATHEMATICS III	PP	100	40	86	P
02. SIGNAL AND SYSTEMS	OR	50	20	38	P C		12. ENGINEERING MATHEMATICS III	TW	25	10	19	P
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	47	P C		13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	46	P
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	34	P C		14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	32	P
05. NETWORK ANALYSIS	PP	100	40	62	P C		15. ELECTROMAGNETIC	PP	100	40	69	P
06. DIGITAL LOGIC DESIGN	PP	100	40	45	P C		16. ELECTROMAGNETIC	TW	25	10	19	P
07. DIGITAL LOGIC DESIGN	PR	50	20	37	P C		17. DATA STRUCTURES	PP	100	40	64	P
08. POWER DEVICES AND MACHINES	PP	100	40	61	P C		18. DATA STRUCTURES	PR	50	20	38	P
09. NETWORK AND POWER LAB.	TW	50	20	40	P C		19. COMMUNICATION THEORY	PP	100	40	58	P
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	37	P C		20. COMMUNICATION THEORY	OR	50	20	33	P
							21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	34	P

GRAND TOTAL = 976/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :
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S80053064   JAWALIKAR AMEY RAVINDRA   NIRMALA RAVINDRA JA , 71344362G   , S80053064 ,   PICT   , S80053064

01. SIGNAL AND SYSTEMS	PP	100	40	64	P		11. ENGINEERING MATHEMATICS III	PP	100	40	64	P
02. SIGNAL AND SYSTEMS	OR	50	20	20	P C		12. ENGINEERING MATHEMATICS III	TW	25	10	17	P
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	42	P C		13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	51	P
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	30	P C		14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	40	P
05. NETWORK ANALYSIS	PP	100	40	48	P		15. ELECTROMAGNETIC	PP	100	40	73	P
06. DIGITAL LOGIC DESIGN	PP	100	40	63	P		16. ELECTROMAGNETIC	TW	25	10	19	P
07. DIGITAL LOGIC DESIGN	PR	50	20	27	P C		17. DATA STRUCTURES	PP	100	40	61	P
08. POWER DEVICES AND MACHINES	PP	100	40	58	P C		18. DATA STRUCTURES	PR	50	20	42	P
09. NETWORK AND POWER LAB.	TW	50	20	39	P C		19. COMMUNICATION THEORY	PP	100	40	55	P
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	39	P C		20. COMMUNICATION THEORY	OR	50	20	33	P
							21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	28	P

GRAND TOTAL = 913/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :
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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.
      OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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S80053065   JOSHI NEEL RAJEEV                   PADMAJA           , 71200894C    , S80053065 ,   PICT     , S80053065
01. SIGNAL AND SYSTEMS            PP   100 40   53 P C    11. ENGINEERING MATHEMATICS III    PP   100 40   60 P
02. SIGNAL AND SYSTEMS            OR    50 20   35 P C    12. ENGINEERING MATHEMATICS III    TW    25 10   18 P
03. SOLID STATES DEVICES AND CIRCUITSPP 100 40  47 P C    13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 71 P
04. SOLID STATES DEVICES AND CIRCUITSPR  50 20  40 P C    14. INTEGRATED CIRCUITS APPLICATIONS PR  50 20 40 P
05. NETWORK ANALYSIS             PP   100 40   40 P C    15. ELECTROMAGNETIC               PP   100 40   64 P
06. DIGITAL LOGIC DESIGN         PP   100 40   40 P C    16. ELECTROMAGNETIC               TW    25 10   19 P
07. DIGITAL LOGIC DESIGN         PR    50 20   42 P C    17. DATA STRUCTURES              PP   100 40   69 P
08. POWER DEVICES AND MACHINES   PP   100 40   54 P C    18. DATA STRUCTURES              PR    50 20   41 P
09. NETWORK AND POWER LAB.       TW    50 20   42 P C    19. COMMUNICATION THEORY         PP   100 40   57 P
10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20  40 P C    20. COMMUNICATION THEORY         OR    50 20   37 P
                                                         21. CIRCUIT SIMULATION AND TOOLS TW    50 20   37 P
GRAND TOTAL =  946/1500, RESULT: FIRST CLASS
ORDN. 1 MARKS :
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S80053066   JOSHI NIKITA SANJAY                 MADHAVI           , 71200895M    , S80053066 ,   PICT     , S80053066
01. SIGNAL AND SYSTEMS            PP   100 40   66 P C    11. ENGINEERING MATHEMATICS III    PP   100 40   53 P
02. SIGNAL AND SYSTEMS            OR    50 20   30 P C    12. ENGINEERING MATHEMATICS III    TW    25 10   20 P
03. SOLID STATES DEVICES AND CIRCUITSPP 100 40  40 P      13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 40 P
04. SOLID STATES DEVICES AND CIRCUITSPR  50 20  35 P C    14. INTEGRATED CIRCUITS APPLICATIONS PR  50 20 39 P
05. NETWORK ANALYSIS             PP   100 40   40 P C    15. ELECTROMAGNETIC               PP   100 40   54 P
06. DIGITAL LOGIC DESIGN         PP   100 40   45 P      16. ELECTROMAGNETIC               TW    25 10   20 P
07. DIGITAL LOGIC DESIGN         PR    50 20   32 P C    17. DATA STRUCTURES              PP   100 40   45 P
08. POWER DEVICES AND MACHINES   PP   100 40   40 P C    18. DATA STRUCTURES              PR    50 20   35 P
09. NETWORK AND POWER LAB.       TW    50 20   38 P C    19. COMMUNICATION THEORY         PP   100 40   48 P
10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20  42 P C    20. COMMUNICATION THEORY         OR    50 20   35 P
                                                         21. CIRCUIT SIMULATION AND TOOLS TW    50 20   39 P
GRAND TOTAL =  836/1500, RESULT: HIGHER SECOND CLASS
ORDN. 1 MARKS :
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S80053067   KADAM AKSHAY VITTHALRAO             LALITA            , 71200898F    , S80053067 ,   PICT     , S80053067
01. SIGNAL AND SYSTEMS            PP   100 40   70 P C    11. ENGINEERING MATHEMATICS III    PP   100 40   85 P
02. SIGNAL AND SYSTEMS            OR    50 20   32 P C    12. ENGINEERING MATHEMATICS III    TW    25 10   22 P
03. SOLID STATES DEVICES AND CIRCUITSPP 100 40  50 P C    13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 64 P
04. SOLID STATES DEVICES AND CIRCUITSPR  50 20  34 P C    14. INTEGRATED CIRCUITS APPLICATIONS PR  50 20 22 P
05. NETWORK ANALYSIS             PP   100 40   66 P C    15. ELECTROMAGNETIC               PP   100 40   77 P
06. DIGITAL LOGIC DESIGN         PP   100 40   47 P C    16. ELECTROMAGNETIC               TW    25 10   22 P
07. DIGITAL LOGIC DESIGN         PR    50 20   32 P C    17. DATA STRUCTURES              PP   100 40   74 P
08. POWER DEVICES AND MACHINES   PP   100 40   65 P C    18. DATA STRUCTURES              PR    50 20   37 P
09. NETWORK AND POWER LAB.       TW    50 20   42 P C    19. COMMUNICATION THEORY         PP   100 40   70 P
10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20  41 P C    20. COMMUNICATION THEORY         OR    50 20   33 P
                                                         21. CIRCUIT SIMULATION AND TOOLS TW    50 20   43 P
GRAND TOTAL = 1028/1500, RESULT: FIRST CLASS WITH DISTINCTION
ORDN. 1 MARKS :
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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S80053068   KALE PRASHANT SUBHASH   SHARDA   , 71200902H   , S80053068 ,   PICT   , S80053068

01. SIGNAL AND SYSTEMS	PP	100	40	82	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	69	P	
02. SIGNAL AND SYSTEMS	OR	50	20	32	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	19	P	
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	42	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	43	P
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	37	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	21	P
05. NETWORK ANALYSIS	PP	100	40	48	P C	15. ELECTROMAGNETIC	PP	100	40	71	P	
06. DIGITAL LOGIC DESIGN	PP	100	40	70	P	16. ELECTROMAGNETIC	TW	25	10	19	P	
07. DIGITAL LOGIC DESIGN	PR	50	20	27	P C	17. DATA STRUCTURES	PP	100	40	46	P	
08. POWER DEVICES AND MACHINES	PP	100	40	49	P C	18. DATA STRUCTURES	PR	50	20	36	P	
09. NETWORK AND POWER LAB.	TW	50	20	35	P C	19. COMMUNICATION THEORY	PP	100	40	53	P	
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	37	P C	20. COMMUNICATION THEORY	OR	50	20	34	P
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	36	P	

GRAND TOTAL = 906/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

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S80053069   KAMBLE SANJEEVANI RAMESH   JAYASHREE   , 71200908G   , S80053069 ,   PICT   , S80053069

01. SIGNAL AND SYSTEMS	PP	100	40	40	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	40	P	
02. SIGNAL AND SYSTEMS	OR	50	20	21	P	12. ENGINEERING MATHEMATICS III	TW	25	10	19	P	
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	40	P	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	40	P
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	33	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	20	P
05. NETWORK ANALYSIS	PP	100	40	22	F	15. ELECTROMAGNETIC	PP	100	40	40	P	
06. DIGITAL LOGIC DESIGN	PP	100	40	40	P C	16. ELECTROMAGNETIC	TW	25	10	21	P	
07. DIGITAL LOGIC DESIGN	PR	50	20	27	P C	17. DATA STRUCTURES	PP	100	40	48	P	
08. POWER DEVICES AND MACHINES	PP	100	40	40	P	18. DATA STRUCTURES	PR	50	20	22	P	
09. NETWORK AND POWER LAB.	TW	50	20	37	P C	19. COMMUNICATION THEORY	PP	100	40	40	P	
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	36	P C	20. COMMUNICATION THEORY	OR	50	20	40	P
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	39	P	

GRAND TOTAL = 705/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

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S80053070   KAMBLE VIJAY SANJAY   SAVITRI   , 71200909E   , S80053070 ,   PICT   , S80053070

01. SIGNAL AND SYSTEMS	PP	100	40	68	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	69	P	
02. SIGNAL AND SYSTEMS	OR	50	20	33	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	20	P	
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	61	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	56	P
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	35	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	36	P
05. NETWORK ANALYSIS	PP	100	40	58	P C	15. ELECTROMAGNETIC	PP	100	40	80	P	
06. DIGITAL LOGIC DESIGN	PP	100	40	48	P C	16. ELECTROMAGNETIC	TW	25	10	21	P	
07. DIGITAL LOGIC DESIGN	PR	50	20	32	P C	17. DATA STRUCTURES	PP	100	40	75	P	
08. POWER DEVICES AND MACHINES	PP	100	40	62	P C	18. DATA STRUCTURES	PR	50	20	38	P	
09. NETWORK AND POWER LAB.	TW	50	20	39	P C	19. COMMUNICATION THEORY	PP	100	40	61	P	
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	40	P C	20. COMMUNICATION THEORY	OR	50	20	30	P
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	40	P	

GRAND TOTAL = 1002/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.
         OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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S80053071   KAPRE SURAJ DNYANOBA   JAYSHREE   , 71200911G   , S80053071 ,   PICT   , S80053071
01. SIGNAL AND SYSTEMS   PP   100 40   68 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   57 P
02. SIGNAL AND SYSTEMS   OR   50 20   30 P C   12. ENGINEERING MATHEMATICS III   TW   25 10   18 P
03. SOLID STATES DEVICES AND CIRCUITSPP   100 40   40 P C   13. INTEGRATED CIRCUITS APPLICATIONS PP   100 40   52 P
04. SOLID STATES DEVICES AND CIRCUITSPR   50 20   30 P C   14. INTEGRATED CIRCUITS APPLICATIONS PR   50 20   24 P
05. NETWORK ANALYSIS   PP   100 40   40 P C   15. ELECTROMAGNETIC   PP   100 40   51 P
06. DIGITAL LOGIC DESIGN   PP   100 40   68 P   16. ELECTROMAGNETIC   TW   25 10   19 P
07. DIGITAL LOGIC DESIGN   PR   50 20   29 P C   17. DATA STRUCTURES   PP   100 40   67 P
08. POWER DEVICES AND MACHINES   PP   100 40   47 P C   18. DATA STRUCTURES   PR   50 20   36 P
09. NETWORK AND POWER LAB.   TW   50 20   37 P C   19. COMMUNICATION THEORY   PP   100 40   63 P
10. ELECTRONIC INSTRUMENTS AND TOOLS TW   50 20   34 P C   20. COMMUNICATION THEORY   OR   50 20   30 P
                                                         21. CIRCUIT SIMULATION AND TOOLS TW   50 20   33 P
GRAND TOTAL = 873/1500, RESULT: HIGHER SECOND CLASS
ORDN. 1 MARKS :
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S80053072   KASAR SNEHAL NIVRUTTI   SUREKHA   , 71212196L   , S80053072 ,   PICT   , S80053072
01. SIGNAL AND SYSTEMS   PP   100 40   64 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   86 P
02. SIGNAL AND SYSTEMS   OR   50 20   35 P C   12. ENGINEERING MATHEMATICS III   TW   25 10   21 P
03. SOLID STATES DEVICES AND CIRCUITSPP   100 40   44 P C   13. INTEGRATED CIRCUITS APPLICATIONS PP   100 40   42 P
04. SOLID STATES DEVICES AND CIRCUITSPR   50 20   29 P C   14. INTEGRATED CIRCUITS APPLICATIONS PR   50 20   34 P
05. NETWORK ANALYSIS   PP   100 40   46 P C   15. ELECTROMAGNETIC   PP   100 40   72 P
06. DIGITAL LOGIC DESIGN   PP   100 40   46 P C   16. ELECTROMAGNETIC   TW   25 10   20 P
07. DIGITAL LOGIC DESIGN   PR   50 20   32 P C   17. DATA STRUCTURES   PP   100 40   79 P
08. POWER DEVICES AND MACHINES   PP   100 40   58 P C   18. DATA STRUCTURES   PR   50 20   32 P
09. NETWORK AND POWER LAB.   TW   50 20   40 P C   19. COMMUNICATION THEORY   PP   100 40   76 P
10. ELECTRONIC INSTRUMENTS AND TOOLS TW   50 20   39 P C   20. COMMUNICATION THEORY   OR   50 20   30 P
                                                         21. CIRCUIT SIMULATION AND TOOLS TW   50 20   36 P
GRAND TOTAL = 961/1500, RESULT: FIRST CLASS
ORDN. 1 MARKS :
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S80053073   KASLIKAR RASHMI MILIND   SWATI   , 71200915K   , S80053073 ,   PICT   , S80053073
01. SIGNAL AND SYSTEMS   PP   100 40   63 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   87 P
02. SIGNAL AND SYSTEMS   OR   50 20   39 P C   12. ENGINEERING MATHEMATICS III   TW   25 10   21 P
03. SOLID STATES DEVICES AND CIRCUITSPP   100 40   51 P C   13. INTEGRATED CIRCUITS APPLICATIONS PP   100 40   48 P
04. SOLID STATES DEVICES AND CIRCUITSPR   50 20   35 P C   14. INTEGRATED CIRCUITS APPLICATIONS PR   50 20   37 P
05. NETWORK ANALYSIS   PP   100 40   56 P C   15. ELECTROMAGNETIC   PP   100 40   67 P
06. DIGITAL LOGIC DESIGN   PP   100 40   40 P C   16. ELECTROMAGNETIC   TW   25 10   20 P
07. DIGITAL LOGIC DESIGN   PR   50 20   28 P C   17. DATA STRUCTURES   PP   100 40   71 P
08. POWER DEVICES AND MACHINES   PP   100 40   54 P C   18. DATA STRUCTURES   PR   50 20   37 P
09. NETWORK AND POWER LAB.   TW   50 20   38 P C   19. COMMUNICATION THEORY   PP   100 40   71 P
10. ELECTRONIC INSTRUMENTS AND TOOLS TW   50 20   40 P C   20. COMMUNICATION THEORY   OR   50 20   30 P
                                                         21. CIRCUIT SIMULATION AND TOOLS TW   50 20   36 P
GRAND TOTAL = 969/1500, RESULT: FIRST CLASS
ORDN. 1 MARKS :
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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.
      OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S80053074   KHAIRE SHUBHAM SUNIL   SANGITA   , 71200921D   , S80053074 ,   PICT   , S80053074

#	HEAD OF PASSING		MAX	MIN	OBT	P/F	C	#	HEAD OF PASSING		MAX	MIN	OBT	P/F
01.	SIGNAL AND SYSTEMS	PP	100	40	54	P	C	11.	ENGINEERING MATHEMATICS III	PP	100	40	79	P
02.	SIGNAL AND SYSTEMS	OR	50	20	34	P	C	12.	ENGINEERING MATHEMATICS III	TW	25	10	16	P
03.	SOLID STATES DEVICES AND CIRCUITS	PP	100	40	49	P	C	13.	INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	52	P
04.	SOLID STATES DEVICES AND CIRCUITS	PR	50	20	37	P	C	14.	INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	33	P
05.	NETWORK ANALYSIS	PP	100	40	46	P	C	15.	ELECTROMAGNETIC	PP	100	40	63	P
06.	DIGITAL LOGIC DESIGN	PP	100	40	47	P	C	16.	ELECTROMAGNETIC	TW	25	10	17	P
07.	DIGITAL LOGIC DESIGN	PR	50	20	30	P	C	17.	DATA STRUCTURES	PP	100	40	54	P
08.	POWER DEVICES AND MACHINES	PP	100	40	51	P	C	18.	DATA STRUCTURES	PR	50	20	37	P
09.	NETWORK AND POWER LAB.	TW	50	20	36	P	C	19.	COMMUNICATION THEORY	PP	100	40	76	P
10.	ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	34	P	C	20.	COMMUNICATION THEORY	OR	50	20	30	P
								21.	CIRCUIT SIMULATION AND TOOLS	TW	50	20	30	P

GRAND TOTAL = 905/1500, RESULT: FIRST CLASS
ORDN. 1 MARKS :

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S80053075   KONDHAWALE PRATIBHA MHATARABA   MANGAL   , 71200929K   , S80053075 ,   PICT   , S80053075

#	HEAD OF PASSING		MAX	MIN	OBT	P/F	C	#	HEAD OF PASSING		MAX	MIN	OBT	P/F
01.	SIGNAL AND SYSTEMS	PP	100	40	47	P	C	11.	ENGINEERING MATHEMATICS III	PP	100	40	40	P
02.	SIGNAL AND SYSTEMS	OR	50	20	24	P	C	12.	ENGINEERING MATHEMATICS III	TW	25	10	20	P
03.	SOLID STATES DEVICES AND CIRCUITS	PP	100	40	48	P		13.	INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	40	P
04.	SOLID STATES DEVICES AND CIRCUITS	PR	50	20	27	P	C	14.	INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	27	P
05.	NETWORK ANALYSIS	PP	100	40	21	F		15.	ELECTROMAGNETIC	PP	100	40	51	P
06.	DIGITAL LOGIC DESIGN	PP	100	40	40	P	C	16.	ELECTROMAGNETIC	TW	25	10	21	P
07.	DIGITAL LOGIC DESIGN	PR	50	20	25	P	C	17.	DATA STRUCTURES	PP	100	40	56	P
08.	POWER DEVICES AND MACHINES	PP	100	40	40	P		18.	DATA STRUCTURES	PR	50	20	35	P
09.	NETWORK AND POWER LAB.	TW	50	20	39	P	C	19.	COMMUNICATION THEORY	PP	100	40	51	P
10.	ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	38	P	C	20.	COMMUNICATION THEORY	OR	50	20	24	P
								21.	CIRCUIT SIMULATION AND TOOLS	TW	50	20	40	P

GRAND TOTAL = 754/1500, RESULT: FAILS A.T.K.T.   RESULT RESERVED FOR BKLG
ORDN. 1 MARKS :

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S80053076   KORANNE RENUKA SUNIL   SHUBHADA KORANNE   , 71344363E   , S80053076 ,   PICT   , S80053076

#	HEAD OF PASSING		MAX	MIN	OBT	P/F	C	#	HEAD OF PASSING		MAX	MIN	OBT	P/F
01.	SIGNAL AND SYSTEMS	PP	100	40	55	P	C	11.	ENGINEERING MATHEMATICS III	PP	100	40	53	P
02.	SIGNAL AND SYSTEMS	OR	50	20	28	P	C	12.	ENGINEERING MATHEMATICS III	TW	25	10	19	P
03.	SOLID STATES DEVICES AND CIRCUITS	PP	100	40	47	P	C	13.	INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	44	P
04.	SOLID STATES DEVICES AND CIRCUITS	PR	50	20	38	P	C	14.	INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	37	P
05.	NETWORK ANALYSIS	PP	100	40	40	P	C	15.	ELECTROMAGNETIC	PP	100	40	42	P
06.	DIGITAL LOGIC DESIGN	PP	100	40	54	P	C	16.	ELECTROMAGNETIC	TW	25	10	22	P
07.	DIGITAL LOGIC DESIGN	PR	50	20	35	P	C	17.	DATA STRUCTURES	PP	100	40	65	P
08.	POWER DEVICES AND MACHINES	PP	100	40	49	P	C	18.	DATA STRUCTURES	PR	50	20	40	P
09.	NETWORK AND POWER LAB.	TW	50	20	40	P	C	19.	COMMUNICATION THEORY	PP	100	40	50	P
10.	ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	37	P	C	20.	COMMUNICATION THEORY	OR	50	20	40	P
								21.	CIRCUIT SIMULATION AND TOOLS	TW	50	20	37	P

GRAND TOTAL = 872/1500, RESULT: HIGHER SECOND CLASS
ORDN. 1 MARKS :

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.
        OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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S80053077   KOTHARI GAURAO RAJENDRA   VIJAYA   , 71200932K   , S80053077 ,   PICT   , S80053077
01. SIGNAL AND SYSTEMS   PP   100 40   48 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   74 P
02. SIGNAL AND SYSTEMS   OR   50 20   39 P C   12. ENGINEERING MATHEMATICS III   TW   25 10   20 P
03. SOLID STATES DEVICES AND CIRCUITSPP   100 40   48 P C   13. INTEGRATED CIRCUITS APPLICATIONS PP   100 40   41 P
04. SOLID STATES DEVICES AND CIRCUITSPR   50 20   36 P C   14. INTEGRATED CIRCUITS APPLICATIONS PR   50 20   33 P
05. NETWORK ANALYSIS   PP   100 40   42 P C   15. ELECTROMAGNETIC   PP   100 40   79 P
06. DIGITAL LOGIC DESIGN   PP   100 40   54 P C   16. ELECTROMAGNETIC   TW   25 10   21 P
07. DIGITAL LOGIC DESIGN   PR   50 20   32 P C   17. DATA STRUCTURES   PP   100 40   78 P
08. POWER DEVICES AND MACHINES   PP   100 40   53 P C   18. DATA STRUCTURES   PR   50 20   38 P
09. NETWORK AND POWER LAB.   TW   50 20   39 P C   19. COMMUNICATION THEORY   PP   100 40   61 P
10. ELECTRONIC INSTRUMENTS AND TOOLS TW   50 20   40 P C   20. COMMUNICATION THEORY   OR   50 20   29 P
                                                             21. CIRCUIT SIMULATION AND TOOLS   TW   50 20   40 P
GRAND TOTAL = 945/1500, RESULT: FIRST CLASS
ORDN. 1 MARKS :
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S80053078   KRITI KESARWANI   ALKA   , 71200933H   , S80053078 ,   PICT   , S80053078
01. SIGNAL AND SYSTEMS   PP   100 40   58 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   61 P
02. SIGNAL AND SYSTEMS   OR   50 20   20 P C   12. ENGINEERING MATHEMATICS III   TW   25 10   16 P
03. SOLID STATES DEVICES AND CIRCUITSPP   100 40   44 P C   13. INTEGRATED CIRCUITS APPLICATIONS PP   100 40   40 P
04. SOLID STATES DEVICES AND CIRCUITSPR   50 20   34 P C   14. INTEGRATED CIRCUITS APPLICATIONS PR   50 20   28 P
05. NETWORK ANALYSIS   PP   100 40   43 P C   15. ELECTROMAGNETIC   PP   100 40   59 P
06. DIGITAL LOGIC DESIGN   PP   100 40   40 P C   16. ELECTROMAGNETIC   TW   25 10   17 P
07. DIGITAL LOGIC DESIGN   PR   50 20   28 P C   17. DATA STRUCTURES   PP   100 40   69 P
08. POWER DEVICES AND MACHINES   PP   100 40   42 P C   18. DATA STRUCTURES   PR   50 20   35 P
09. NETWORK AND POWER LAB.   TW   50 20   38 P C   19. COMMUNICATION THEORY   PP   100 40   60 P
10. ELECTRONIC INSTRUMENTS AND TOOLS TW   50 20   35 P C   20. COMMUNICATION THEORY   OR   50 20   27 P
                                                             21. CIRCUIT SIMULATION AND TOOLS   TW   50 20   32 P
GRAND TOTAL = 826/1500, RESULT: HIGHER SECOND CLASS
ORDN. 1 MARKS :
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S80053079   KULKARNI PARIMAL SHRIKRISHNA   VASUDHA   , 71200936B   , S80053079 ,   PICT   , S80053079
01. SIGNAL AND SYSTEMS   PP   100 40   80 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   87 P
02. SIGNAL AND SYSTEMS   OR   50 20   42 P C   12. ENGINEERING MATHEMATICS III   TW   25 10   24 P
03. SOLID STATES DEVICES AND CIRCUITSPP   100 40   64 P C   13. INTEGRATED CIRCUITS APPLICATIONS PP   100 40   54 P
04. SOLID STATES DEVICES AND CIRCUITSPR   50 20   38 P C   14. INTEGRATED CIRCUITS APPLICATIONS PR   50 20   43 P
05. NETWORK ANALYSIS   PP   100 40   76 P C   15. ELECTROMAGNETIC   PP   100 40   84 P
06. DIGITAL LOGIC DESIGN   PP   100 40   52 P C   16. ELECTROMAGNETIC   TW   25 10   23 P
07. DIGITAL LOGIC DESIGN   PR   50 20   40 P C   17. DATA STRUCTURES   PP   100 40   72 P
08. POWER DEVICES AND MACHINES   PP   100 40   74 P C   18. DATA STRUCTURES   PR   50 20   45 P
09. NETWORK AND POWER LAB.   TW   50 20   44 P C   19. COMMUNICATION THEORY   PP   100 40   72 P
10. ELECTRONIC INSTRUMENTS AND TOOLS TW   50 20   43 P C   20. COMMUNICATION THEORY   OR   50 20   37 P
                                                             21. CIRCUIT SIMULATION AND TOOLS   TW   50 20   46 P
GRAND TOTAL = 1140/1500, RESULT: FIRST CLASS WITH DISTINCTION
ORDN. 1 MARKS :
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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

          OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S80053080   KULKARNI VISHAL VINAYAK   VARSHA   , 71200939G   , S80053080 ,   PICT   , S80053080
01. SIGNAL AND SYSTEMS   PP   100 40 71 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   68 P
02. SIGNAL AND SYSTEMS   OR   50 20 37 P C   12. ENGINEERING MATHEMATICS III   TW   25 10   24 P
03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 51 P C   13. INTEGRATED CIRCUITS APPLICATIONS PP   100 40   56 P
04. SOLID STATES DEVICES AND CIRCUITSPR   50 20 35 P C   14. INTEGRATED CIRCUITS APPLICATIONS PR   50 20   36 P
05. NETWORK ANALYSIS   PP   100 40 43 P C   15. ELECTROMAGNETIC   PP   100 40   70 P
06. DIGITAL LOGIC DESIGN   PP   100 40 61 P C   16. ELECTROMAGNETIC   TW   25 10   23 P
07. DIGITAL LOGIC DESIGN   PR   50 20 44 P C   17. DATA STRUCTURES   PP   100 40   83 P
08. POWER DEVICES AND MACHINES   PP   100 40 46 P C   18. DATA STRUCTURES   PR   50 20   46 P
09. NETWORK AND POWER LAB.   TW   50 20 37 P C   19. COMMUNICATION THEORY   PP   100 40   75 P
10. ELECTRONIC INSTRUMENTS AND TOOLS TW   50 20 36 P C   20. COMMUNICATION THEORY   OR   50 20   32 P
                                                         21. CIRCUIT SIMULATION AND TOOLS   TW   50 20   45 P
GRAND TOTAL = 1019/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

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S80053081   LADIKKAR CHAITALI AVINASH   NEETA   , 71200942G   , S80053081 ,   PICT   , S80053081
01. SIGNAL AND SYSTEMS   PP   100 40 67 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   66 P
02. SIGNAL AND SYSTEMS   OR   50 20 30 P C   12. ENGINEERING MATHEMATICS III   TW   25 10   22 P
03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 40 P C   13. INTEGRATED CIRCUITS APPLICATIONS PP   100 40   40 P
04. SOLID STATES DEVICES AND CIRCUITSPR   50 20 41 P C   14. INTEGRATED CIRCUITS APPLICATIONS PR   50 20   37 P
05. NETWORK ANALYSIS   PP   100 40 53 P C   15. ELECTROMAGNETIC   PP   100 40   63 P
06. DIGITAL LOGIC DESIGN   PP   100 40 44 P C   16. ELECTROMAGNETIC   TW   25 10   22 P
07. DIGITAL LOGIC DESIGN   PR   50 20 45 P C   17. DATA STRUCTURES   PP   100 40   73 P
08. POWER DEVICES AND MACHINES   PP   100 40 40 P C   18. DATA STRUCTURES   PR   50 20   43 P
09. NETWORK AND POWER LAB.   TW   50 20 44 P C   19. COMMUNICATION THEORY   PP   100 40   43 P
10. ELECTRONIC INSTRUMENTS AND TOOLS TW   50 20 44 P C   20. COMMUNICATION THEORY   OR   50 20   33 P
                                                         21. CIRCUIT SIMULATION AND TOOLS   TW   50 20   44 P
GRAND TOTAL = 934/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

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S80053082   LASRADO VINOD WILFRED   MARIA   , 71200944C   , S80053082 ,   PICT   , S80053082
01. SIGNAL AND SYSTEMS   PP   100 40 68 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   88 P
02. SIGNAL AND SYSTEMS   OR   50 20 45 P C   12. ENGINEERING MATHEMATICS III   TW   25 10   23 P
03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 70 P C   13. INTEGRATED CIRCUITS APPLICATIONS PP   100 40   57 P
04. SOLID STATES DEVICES AND CIRCUITSPR   50 20 35 P C   14. INTEGRATED CIRCUITS APPLICATIONS PR   50 20   42 P
05. NETWORK ANALYSIS   PP   100 40 83 P C   15. ELECTROMAGNETIC   PP   100 40   90 P
06. DIGITAL LOGIC DESIGN   PP   100 40 56 P C   16. ELECTROMAGNETIC   TW   25 10   23 P
07. DIGITAL LOGIC DESIGN   PR   50 20 45 P C   17. DATA STRUCTURES   PP   100 40   76 P
08. POWER DEVICES AND MACHINES   PP   100 40 79 P C   18. DATA STRUCTURES   PR   50 20   45 P
09. NETWORK AND POWER LAB.   TW   50 20 46 P C   19. COMMUNICATION THEORY   PP   100 40   79 P
10. ELECTRONIC INSTRUMENTS AND TOOLS TW   50 20 43 P C   20. COMMUNICATION THEORY   OR   50 20   34 P
                                                         21. CIRCUIT SIMULATION AND TOOLS   TW   50 20   44 P
GRAND TOTAL = 1171/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

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  NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.
        OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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  S80053083   LOHIYA KRISHNA SUBHASH   SUNITA   , 71200945M   , S80053083 ,   PICT   , S80053083
  01. SIGNAL AND SYSTEMS   PP   100 40   40 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   59 P
  02. SIGNAL AND SYSTEMS   OR   50 20   25 P   12. ENGINEERING MATHEMATICS III   TW   25 10   14 P
  03. SOLID STATES DEVICES AND CIRCUITSPP   100 40   55 P   13. INTEGRATED CIRCUITS APPLICATIONS PP   100 40   41 P
  04. SOLID STATES DEVICES AND CIRCUITSPR   50 20   30 P C   14. INTEGRATED CIRCUITS APPLICATIONS PR   50 20   23 P
  05. NETWORK ANALYSIS   PP   100 40   40 P   15. ELECTROMAGNETIC   PP   100 40   55 P
  06. DIGITAL LOGIC DESIGN   PP   100 40   49 P   16. ELECTROMAGNETIC   TW   25 10   14 P
  07. DIGITAL LOGIC DESIGN   PR   50 20   26 P C   17. DATA STRUCTURES   PP   100 40   63 P
  08. POWER DEVICES AND MACHINES   PP   100 40   40 P C   18. DATA STRUCTURES   PR   50 20   40 P
  09. NETWORK AND POWER LAB.   TW   50 20   27 P C   19. COMMUNICATION THEORY   PP   100 40   40 P
  10. ELECTRONIC INSTRUMENTS AND TOOLS TW   50 20   32 P C   20. COMMUNICATION THEORY   OR   50 20   34 P
                                                               21. CIRCUIT SIMULATION AND TOOLS   TW   50 20   28 P
GRAND TOTAL = 775/1500, RESULT: SECOND CLASS
ORDN. 1 MARKS :
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  S80053084   MANE MADHURI DILIP   SHEELA   , 71200954L   , S80053084 ,   PICT   , S80053084
  01. SIGNAL AND SYSTEMS   PP   100 40   76 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   71 P
  02. SIGNAL AND SYSTEMS   OR   50 20   36 P C   12. ENGINEERING MATHEMATICS III   TW   25 10   23 P
  03. SOLID STATES DEVICES AND CIRCUITSPP   100 40   63 P C   13. INTEGRATED CIRCUITS APPLICATIONS PP   100 40   50 P
  04. SOLID STATES DEVICES AND CIRCUITSPR   50 20   41 P C   14. INTEGRATED CIRCUITS APPLICATIONS PR   50 20   31 P
  05. NETWORK ANALYSIS   PP   100 40   63 P C   15. ELECTROMAGNETIC   PP   100 40   76 P
  06. DIGITAL LOGIC DESIGN   PP   100 40   43 P C   16. ELECTROMAGNETIC   TW   25 10   23 P
  07. DIGITAL LOGIC DESIGN   PR   50 20   34 P C   17. DATA STRUCTURES   PP   100 40   74 P
  08. POWER DEVICES AND MACHINES   PP   100 40   76 P C   18. DATA STRUCTURES   PR   50 20   45 P
  09. NETWORK AND POWER LAB.   TW   50 20   44 P C   19. COMMUNICATION THEORY   PP   100 40   64 P
  10. ELECTRONIC INSTRUMENTS AND TOOLS TW   50 20   43 P C   20. COMMUNICATION THEORY   OR   50 20   32 P
                                                               21. CIRCUIT SIMULATION AND TOOLS   TW   50 20   45 P
GRAND TOTAL = 1053/1500, RESULT: FIRST CLASS WITH DISTINCTION
ORDN. 1 MARKS :
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  S80053085   MANU MAKKAR   INDU   , 71200958C   , S80053085 ,   PICT   , S80053085
  01. SIGNAL AND SYSTEMS   PP   100 40   54 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   47 P
  02. SIGNAL AND SYSTEMS   OR   50 20   29 P   12. ENGINEERING MATHEMATICS III   TW   25 10   17 P
  03. SOLID STATES DEVICES AND CIRCUITSPP   100 40   40 P C   13. INTEGRATED CIRCUITS APPLICATIONS PP   100 40   52 P
  04. SOLID STATES DEVICES AND CIRCUITSPR   50 20   24 P C   14. INTEGRATED CIRCUITS APPLICATIONS PR   50 20   32 P
  05. NETWORK ANALYSIS   PP   100 40   40 P C   15. ELECTROMAGNETIC   PP   100 40   57 P
  06. DIGITAL LOGIC DESIGN   PP   100 40   65 P   16. ELECTROMAGNETIC   TW   25 10   19 P
  07. DIGITAL LOGIC DESIGN   PR   50 20   29 P C   17. DATA STRUCTURES   PP   100 40   59 P
  08. POWER DEVICES AND MACHINES   PP   100 40   58 P C   18. DATA STRUCTURES   PR   50 20   30 P
  09. NETWORK AND POWER LAB.   TW   50 20   36 P C   19. COMMUNICATION THEORY   PP   100 40   73 P
  10. ELECTRONIC INSTRUMENTS AND TOOLS TW   50 20   32 P C   20. COMMUNICATION THEORY   OR   50 20   30 P
                                                               21. CIRCUIT SIMULATION AND TOOLS   TW   50 20   36 P
GRAND TOTAL = 859/1500, RESULT: HIGHER SECOND CLASS
ORDN. 1 MARKS :
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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.

        OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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    S80053086   MHETRE ROSHANEE RAJENDRA   SANGITA   , 71200960E   , S80053086 ,   PICT   , S80053086

    01. SIGNAL AND SYSTEMS   PP   100 40   61 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   90 P
    02. SIGNAL AND SYSTEMS   OR   50 20   38 P C   12. ENGINEERING MATHEMATICS III   TW   25 10   21 P
    03. SOLID STATES DEVICES AND CIRCUITSPP   100 40   51 P C   13. INTEGRATED CIRCUITS APPLICATIONS PP   100 40   60 P
    04. SOLID STATES DEVICES AND CIRCUITSPR   50 20   41 P C   14. INTEGRATED CIRCUITS APPLICATIONS PR   50 20   21 P
    05. NETWORK ANALYSIS   PP   100 40   60 P C   15. ELECTROMAGNETIC   PP   100 40   68 P
    06. DIGITAL LOGIC DESIGN   PP   100 40   56 P C   16. ELECTROMAGNETIC   TW   25 10   21 P
    07. DIGITAL LOGIC DESIGN   PR   50 20   35 P C   17. DATA STRUCTURES   PP   100 40   65 P
    08. POWER DEVICES AND MACHINES   PP   100 40   63 P C   18. DATA STRUCTURES   PR   50 20   44 P
    09. NETWORK AND POWER LAB.   TW   50 20   38 P C   19. COMMUNICATION THEORY   PP   100 40   69 P
    10. ELECTRONIC INSTRUMENTS AND TOOLS TW   50 20   40 P C   20. COMMUNICATION THEORY   OR   50 20   32 P
                                                                  21. CIRCUIT SIMULATION AND TOOLS   TW   50 20   39 P

GRAND TOTAL = 1013/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

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    S80053087   MIHIR BIDWAI   URMILA   , 71200961C   , S80053087 ,   PICT   , S80053087

    01. SIGNAL AND SYSTEMS   PP   100 40   43 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   40 P
    02. SIGNAL AND SYSTEMS   OR   50 20   21 P C   12. ENGINEERING MATHEMATICS III   TW   25 10   13 P
    03. SOLID STATES DEVICES AND CIRCUITSPP   100 40   40 P C   13. INTEGRATED CIRCUITS APPLICATIONS PP   100 40   28 F
    04. SOLID STATES DEVICES AND CIRCUITSPR   50 20   34 P C   14. INTEGRATED CIRCUITS APPLICATIONS PR   50 20   25 P
    05. NETWORK ANALYSIS   PP   100 40   40 P C   15. ELECTROMAGNETIC   PP   100 40   56 P
    06. DIGITAL LOGIC DESIGN   PP   100 40   40 P C   16. ELECTROMAGNETIC   TW   25 10   13 P
    07. DIGITAL LOGIC DESIGN   PR   50 20   37 P C   17. DATA STRUCTURES   PP   100 40   40 P
    08. POWER DEVICES AND MACHINES   PP   100 40   40 P C   18. DATA STRUCTURES   PR   50 20   30 P
    09. NETWORK AND POWER LAB.   TW   50 20   33 P C   19. COMMUNICATION THEORY   PP   100 40   53 P
    10. ELECTRONIC INSTRUMENTS AND TOOLS TW   50 20   27 P C   20. COMMUNICATION THEORY   OR   50 20   09 F
                                                                  21. CIRCUIT SIMULATION AND TOOLS   TW   50 20   23 P

GRAND TOTAL = 685/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

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    S80053088   MUNNA KUMAR   URMILADEVI   , 71200970B   , S80053088 ,   PICT   , S80053088

    01. SIGNAL AND SYSTEMS   PP   100 40   71 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   66 P
    02. SIGNAL AND SYSTEMS   OR   50 20   35 P C   12. ENGINEERING MATHEMATICS III   TW   25 10   18 P
    03. SOLID STATES DEVICES AND CIRCUITSPP   100 40   48 P C   13. INTEGRATED CIRCUITS APPLICATIONS PP   100 40   55 P
    04. SOLID STATES DEVICES AND CIRCUITSPR   50 20   37 P C   14. INTEGRATED CIRCUITS APPLICATIONS PR   50 20   28 P
    05. NETWORK ANALYSIS   PP   100 40   52 P C   15. ELECTROMAGNETIC   PP   100 40   70 P
    06. DIGITAL LOGIC DESIGN   PP   100 40   45 P C   16. ELECTROMAGNETIC   TW   25 10   17 P
    07. DIGITAL LOGIC DESIGN   PR   50 20   38 P C   17. DATA STRUCTURES   PP   100 40   71 P
    08. POWER DEVICES AND MACHINES   PP   100 40   66 P C   18. DATA STRUCTURES   PR   50 20   35 P
    09. NETWORK AND POWER LAB.   TW   50 20   38 P C   19. COMMUNICATION THEORY   PP   100 40   64 P
    10. ELECTRONIC INSTRUMENTS AND TOOLS TW   50 20   39 P C   20. COMMUNICATION THEORY   OR   50 20   22 P
                                                                  21. CIRCUIT SIMULATION AND TOOLS   TW   50 20   32 P

GRAND TOTAL = 947/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

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    NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.
          OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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    S80053089   NAGARE PRERANA EKNATH   PUSHPA   , 71200975C   , S80053089 ,   PICT   , S80053089
    01. SIGNAL AND SYSTEMS   PP   100 40   64 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   66 P
    02. SIGNAL AND SYSTEMS   OR   50 20   25 P C   12. ENGINEERING MATHEMATICS III   TW   25 10   18 P
    03. SOLID STATES DEVICES AND CIRCUITSPP   100 40   51 P C   13. INTEGRATED CIRCUITS APPLICATIONS PP   100 40   60 P
    04. SOLID STATES DEVICES AND CIRCUITSPR   50 20   36 P C   14. INTEGRATED CIRCUITS APPLICATIONS PR   50 20   21 P
    05. NETWORK ANALYSIS   PP   100 40   56 P C   15. ELECTROMAGNETIC   PP   100 40   66 P
    06. DIGITAL LOGIC DESIGN   PP   100 40   42 P C   16. ELECTROMAGNETIC   TW   25 10   19 P
    07. DIGITAL LOGIC DESIGN   PR   50 20   34 P C   17. DATA STRUCTURES   PP   100 40   71 P
    08. POWER DEVICES AND MACHINES   PP   100 40   52 P C   18. DATA STRUCTURES   PR   50 20   33 P
    09. NETWORK AND POWER LAB.   TW   50 20   38 P C   19. COMMUNICATION THEORY   PP   100 40   54 P
    10. ELECTRONIC INSTRUMENTS AND TOOLS TW   50 20   36 P C   20. COMMUNICATION THEORY   OR   50 20   30 P
                                                                     21. CIRCUIT SIMULATION AND TOOLS   TW   50 20   36 P
  GRAND TOTAL = 908/1500, RESULT: FIRST CLASS
  ORDN. 1 MARKS :
. . . .
    S80053090   NAIR PRATHMESH PRAKASH   PRIYA   , 71344364C   , S80053090 ,   PICT   , S80053090
    01. SIGNAL AND SYSTEMS   PP   100 40   57 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   43 P
    02. SIGNAL AND SYSTEMS   OR   50 20   23 P C   12. ENGINEERING MATHEMATICS III   TW   25 10   20 P
    03. SOLID STATES DEVICES AND CIRCUITSPP   100 40   44 P C   13. INTEGRATED CIRCUITS APPLICATIONS PP   100 40   40 P
    04. SOLID STATES DEVICES AND CIRCUITSPR   50 20   33 P C   14. INTEGRATED CIRCUITS APPLICATIONS PR   50 20   30 P
    05. NETWORK ANALYSIS   PP   100 40   43 P C   15. ELECTROMAGNETIC   PP   100 40   46 P
    06. DIGITAL LOGIC DESIGN   PP   100 40   68 P   16. ELECTROMAGNETIC   TW   25 10   20 P
    07. DIGITAL LOGIC DESIGN   PR   50 20   35 P   17. DATA STRUCTURES   PP   100 40   53 P
    08. POWER DEVICES AND MACHINES   PP   100 40   40 P C   18. DATA STRUCTURES   PR   50 20   25 P
    09. NETWORK AND POWER LAB.   TW   50 20   39 P C   19. COMMUNICATION THEORY   PP   100 40   40 P
    10. ELECTRONIC INSTRUMENTS AND TOOLS TW   50 20   38 P C   20. COMMUNICATION THEORY   OR   50 20   34 P
                                                                     21. CIRCUIT SIMULATION AND TOOLS   TW   50 20   36 P
  GRAND TOTAL = 807/1500, RESULT: SECOND CLASS
  ORDN. 1 MARKS :
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    S80053091   NAWATHALE MANOJ PANDURANG   RAJANI   , 71200982F   , S80053091 ,   PICT   , S80053091
    01. SIGNAL AND SYSTEMS   PP   100 40   62 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   72 P
    02. SIGNAL AND SYSTEMS   OR   50 20   25 P C   12. ENGINEERING MATHEMATICS III   TW   25 10   23 P
    03. SOLID STATES DEVICES AND CIRCUITSPP   100 40   57 P C   13. INTEGRATED CIRCUITS APPLICATIONS PP   100 40   62 P
    04. SOLID STATES DEVICES AND CIRCUITSPR   50 20   28 P C   14. INTEGRATED CIRCUITS APPLICATIONS PR   50 20   38 P
    05. NETWORK ANALYSIS   PP   100 40   54 P C   15. ELECTROMAGNETIC   PP   100 40   74 P
    06. DIGITAL LOGIC DESIGN   PP   100 40   41 P C   16. ELECTROMAGNETIC   TW   25 10   22 P
    07. DIGITAL LOGIC DESIGN   PR   50 20   38 P C   17. DATA STRUCTURES   PP   100 40   80 P
    08. POWER DEVICES AND MACHINES   PP   100 40   56 P C   18. DATA STRUCTURES   PR   50 20   35 P
    09. NETWORK AND POWER LAB.   TW   50 20   38 P C   19. COMMUNICATION THEORY   PP   100 40   67 P
    10. ELECTRONIC INSTRUMENTS AND TOOLS TW   50 20   37 P C   20. COMMUNICATION THEORY   OR   50 20   30 P
                                                                     21. CIRCUIT SIMULATION AND TOOLS   TW   50 20   43 P
  GRAND TOTAL = 982/1500, RESULT: FIRST CLASS
  ORDN. 1 MARKS :
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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.
        OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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S80053092   NEHA NIKUM   KAUSHALYA   , 71200984B   , S80053092 ,   PICT   , S80053092

01. SIGNAL AND SYSTEMS	PP	100	40	40	P C		11. ENGINEERING MATHEMATICS III	PP	100	40	25	F
02. SIGNAL AND SYSTEMS	OR	50	20	23	P		12. ENGINEERING MATHEMATICS III	TW	25	10	11	P
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	40	P		13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	19	F
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	20	P		14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	05	F
05. NETWORK ANALYSIS	PP	100	40	24	F		15. ELECTROMAGNETIC	PP	100	40	40	P
06. DIGITAL LOGIC DESIGN	PP	100	40	25	F		16. ELECTROMAGNETIC	TW	25	10	10	P
07. DIGITAL LOGIC DESIGN	PR	50	20	20	P C		17. DATA STRUCTURES	PP	100	40	20	F
08. POWER DEVICES AND MACHINES	PP	100	40	14	F		18. DATA STRUCTURES	PR	50	20	AA	F
09. NETWORK AND POWER LAB.	TW	50	20	28	P C		19. COMMUNICATION THEORY	PP	100	40	12	F
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	32	P C		20. COMMUNICATION THEORY	OR	50	20	07	F
							21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	22	P

GRAND TOTAL = 437/1500, RESULT: FAILS
ORDN. 1 MARKS :
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S80053093   NEVPURKAR MANDAR SANTOSH   ANAGHA   , 71200985L   , S80053093 ,   PICT   , S80053093

01. SIGNAL AND SYSTEMS	PP	100	40	62	P C		11. ENGINEERING MATHEMATICS III	PP	100	40	67	P
02. SIGNAL AND SYSTEMS	OR	50	20	41	P C		12. ENGINEERING MATHEMATICS III	TW	25	10	21	P
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	66	P C		13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	45	P
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	41	P C		14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	37	P
05. NETWORK ANALYSIS	PP	100	40	57	P C		15. ELECTROMAGNETIC	PP	100	40	76	P
06. DIGITAL LOGIC DESIGN	PP	100	40	53	P C		16. ELECTROMAGNETIC	TW	25	10	22	P
07. DIGITAL LOGIC DESIGN	PR	50	20	46	P C		17. DATA STRUCTURES	PP	100	40	78	P
08. POWER DEVICES AND MACHINES	PP	100	40	59	P C		18. DATA STRUCTURES	PR	50	20	44	P
09. NETWORK AND POWER LAB.	TW	50	20	44	P C		19. COMMUNICATION THEORY	PP	100	40	77	P
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	42	P C		20. COMMUNICATION THEORY	OR	50	20	32	P
							21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	42	P

GRAND TOTAL = 1052/1500, RESULT: FIRST CLASS WITH DISTINCTION
ORDN. 1 MARKS :
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S80053094   NIMISHA NITIN MISHRA   KIRTI   , 71205847J   , S80053094 ,   PICT   , S80053094

01. SIGNAL AND SYSTEMS	PP	100	40	77	P C		11. ENGINEERING MATHEMATICS III	PP	100	40	82	P
02. SIGNAL AND SYSTEMS	OR	50	20	40	P C		12. ENGINEERING MATHEMATICS III	TW	25	10	19	P
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	69	P C		13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	54	P
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	42	P C		14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	42	P
05. NETWORK ANALYSIS	PP	100	40	61	P C		15. ELECTROMAGNETIC	PP	100	40	75	P
06. DIGITAL LOGIC DESIGN	PP	100	40	42	P C		16. ELECTROMAGNETIC	TW	25	10	19	P
07. DIGITAL LOGIC DESIGN	PR	50	20	47	P C		17. DATA STRUCTURES	PP	100	40	69	P
08. POWER DEVICES AND MACHINES	PP	100	40	74	P C		18. DATA STRUCTURES	PR	50	20	45	P
09. NETWORK AND POWER LAB.	TW	50	20	39	P C		19. COMMUNICATION THEORY	PP	100	40	74	P
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	38	P C		20. COMMUNICATION THEORY	OR	50	20	39	P
							21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	35	P

GRAND TOTAL = 1082/1500, RESULT: FIRST CLASS WITH DISTINCTION
ORDN. 1 MARKS :
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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.
        OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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S80053095   OMKAR NAGESHKAR   KRUPA   , 71200991E   , S80053095 ,   PICT   , S80053095

01. SIGNAL AND SYSTEMS	PP	100	40	84	P C		11. ENGINEERING MATHEMATICS III	PP	100	40	62	P	
02. SIGNAL AND SYSTEMS	OR	50	20	43	P C		12. ENGINEERING MATHEMATICS III	TW	25	10	20	P	
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	43	P C		13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	40	P
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	41	P C		14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	36	P
05. NETWORK ANALYSIS	PP	100	40	56	P C		15. ELECTROMAGNETIC	PP	100	40	74	P	
06. DIGITAL LOGIC DESIGN	PP	100	40	40	P C		16. ELECTROMAGNETIC	TW	25	10	21	P	
07. DIGITAL LOGIC DESIGN	PR	50	20	42	P C		17. DATA STRUCTURES	PP	100	40	61	P	
08. POWER DEVICES AND MACHINES	PP	100	40	58	P C		18. DATA STRUCTURES	PR	50	20	42	P	
09. NETWORK AND POWER LAB.	TW	50	20	41	P C		19. COMMUNICATION THEORY	PP	100	40	57	P	
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	37	P C		20. COMMUNICATION THEORY	OR	50	20	40	P	
							21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	40	P	

GRAND TOTAL = 978/1500, RESULT: FIRST CLASS
ORDN. 1 MARKS :
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S80053096   OSWAL ARCHA AMRUT   ALKA OSWAL   , 71344365M   , S80053096 ,   PICT   , S80053096

01. SIGNAL AND SYSTEMS	PP	100	40	46	P		11. ENGINEERING MATHEMATICS III	PP	100	40	17	F	
02. SIGNAL AND SYSTEMS	OR	50	20	27	P		12. ENGINEERING MATHEMATICS III	TW	25	10	17	P	
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	40	P		13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	40	P
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	24	P		14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	11	F
05. NETWORK ANALYSIS	PP	100	40	43	P		15. ELECTROMAGNETIC	PP	100	40	47	P	
06. DIGITAL LOGIC DESIGN	PP	100	40	29	F		16. ELECTROMAGNETIC	TW	25	10	17	P	
07. DIGITAL LOGIC DESIGN	PR	50	20	30	P C		17. DATA STRUCTURES	PP	100	40	40	P	
08. POWER DEVICES AND MACHINES	PP	100	40	19	F		18. DATA STRUCTURES	PR	50	20	33	P	
09. NETWORK AND POWER LAB.	TW	50	20	40	P C		19. COMMUNICATION THEORY	PP	100	40	40	P	
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	38	P C		20. COMMUNICATION THEORY	OR	50	20	26	P	
							21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	32	P	

GRAND TOTAL = 656/1500, RESULT: FAILS A.T.K.T.
ORDN. 1 MARKS :
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S80053097   PANMAND SUHAS DASHRATH   HIRA   , 71344366K   , S80053097 ,   PICT   , S80053097

01. SIGNAL AND SYSTEMS	PP	100	40	63	P C		11. ENGINEERING MATHEMATICS III	PP	100	40	87	P	
02. SIGNAL AND SYSTEMS	OR	50	20	31	P C		12. ENGINEERING MATHEMATICS III	TW	25	10	24	P	
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	71	P C		13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	57	P
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	30	P C		14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	37	P
05. NETWORK ANALYSIS	PP	100	40	54	P C		15. ELECTROMAGNETIC	PP	100	40	83	P	
06. DIGITAL LOGIC DESIGN	PP	100	40	48	P C		16. ELECTROMAGNETIC	TW	25	10	22	P	
07. DIGITAL LOGIC DESIGN	PR	50	20	28	P C		17. DATA STRUCTURES	PP	100	40	77	P	
08. POWER DEVICES AND MACHINES	PP	100	40	73	P C		18. DATA STRUCTURES	PR	50	20	35	P	
09. NETWORK AND POWER LAB.	TW	50	20	41	P C		19. COMMUNICATION THEORY	PP	100	40	75	P	
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	42	P C		20. COMMUNICATION THEORY	OR	50	20	41	P	
							21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	44	P	

GRAND TOTAL = 1063/1500, RESULT: FIRST CLASS WITH DISTINCTION
ORDN. 1 MARKS :
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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.
      OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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S80053098   PARAKH BHUSHAN RAJENDRA              PUSHPA              , 71344367H    , S80053098 ,   PICT     , S80053098
01. SIGNAL AND SYSTEMS              PP   100 40   54  P C   11. ENGINEERING MATHEMATICS III       PP   100 40   66  P
02. SIGNAL AND SYSTEMS              OR    50 20   20  P C   12. ENGINEERING MATHEMATICS III       TW    25 10   22  P
03. SOLID STATES DEVICES AND CIRCUITSPP   100 40   40  P C   13. INTEGRATED CIRCUITS APPLICATIONS PP   100 40   62  P
04. SOLID STATES DEVICES AND CIRCUITSPR    50 20   28  P C   14. INTEGRATED CIRCUITS APPLICATIONS PR    50 20   40  P
05. NETWORK ANALYSIS               PP   100 40   40  P C   15. ELECTROMAGNETIC                   PP   100 40   73  P
06. DIGITAL LOGIC DESIGN           PP   100 40   40  P C   16. ELECTROMAGNETIC                   TW    25 10   21  P
07. DIGITAL LOGIC DESIGN           PR    50 20   30  P     17. DATA STRUCTURES                  PP   100 40   77  P
08. POWER DEVICES AND MACHINES     PP   100 40   54  P C   18. DATA STRUCTURES                  PR    50 20   39  P
09. NETWORK AND POWER LAB.         TW    50 20   38  P C   19. COMMUNICATION THEORY             PP   100 40   73  P
10. ELECTRONIC INSTRUMENTS AND TOOLS TW    50 20   37  P C   20. COMMUNICATION THEORY             OR    50 20   35  P
                                                           21. CIRCUIT SIMULATION AND TOOLS    TW    50 20   41  P
GRAND TOTAL =  930/1500, RESULT: FIRST CLASS
ORDN. 1 MARKS :
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S80053099   PATEL KINNARY VIPIN               VINAYA PATEL        , 71344368F    , S80053099 ,   PICT     , S80053099
01. SIGNAL AND SYSTEMS              PP   100 40   83  P C   11. ENGINEERING MATHEMATICS III       PP   100 40   90  P
02. SIGNAL AND SYSTEMS              OR    50 20   24  P C   12. ENGINEERING MATHEMATICS III       TW    25 10   22  P
03. SOLID STATES DEVICES AND CIRCUITSPP   100 40   79  P C   13. INTEGRATED CIRCUITS APPLICATIONS PP   100 40   63  P
04. SOLID STATES DEVICES AND CIRCUITSPR    50 20   36  P C   14. INTEGRATED CIRCUITS APPLICATIONS PR    50 20   33  P
05. NETWORK ANALYSIS               PP   100 40   63  P C   15. ELECTROMAGNETIC                   PP   100 40   77  P
06. DIGITAL LOGIC DESIGN           PP   100 40   58  P C   16. ELECTROMAGNETIC                   TW    25 10   21  P
07. DIGITAL LOGIC DESIGN           PR    50 20   39  P C   17. DATA STRUCTURES                  PP   100 40   85  P
08. POWER DEVICES AND MACHINES     PP   100 40   63  P C   18. DATA STRUCTURES                  PR    50 20   41  P
09. NETWORK AND POWER LAB.         TW    50 20   40  P C   19. COMMUNICATION THEORY             PP   100 40   76  P
10. ELECTRONIC INSTRUMENTS AND TOOLS TW    50 20   39  P C   20. COMMUNICATION THEORY             OR    50 20   40  P
                                                           21. CIRCUIT SIMULATION AND TOOLS    TW    50 20   43  P
GRAND TOTAL = 1115/1500, RESULT: FIRST CLASS WITH DISTINCTION
ORDN. 1 MARKS :
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S80053100   PATIL CHINAR SAMPATRAO            PATIL SUVARNA SAMPAT , 71344369D    , S80053100 ,   PICT     , S80053100
01. SIGNAL AND SYSTEMS              PP   100 40   40  P C   11. ENGINEERING MATHEMATICS III       PP   100 40   57  P
02. SIGNAL AND SYSTEMS              OR    50 20   21  P C   12. ENGINEERING MATHEMATICS III       TW    25 10   20  P
03. SOLID STATES DEVICES AND CIRCUITSPP   100 40   40  P C   13. INTEGRATED CIRCUITS APPLICATIONS PP   100 40   53  P
04. SOLID STATES DEVICES AND CIRCUITSPR    50 20   27  P C   14. INTEGRATED CIRCUITS APPLICATIONS PR    50 20   38  P
05. NETWORK ANALYSIS               PP   100 40   40  P C   15. ELECTROMAGNETIC                   PP   100 40   48  P
06. DIGITAL LOGIC DESIGN           PP   100 40   55  P C   16. ELECTROMAGNETIC                   TW    25 10   20  P
07. DIGITAL LOGIC DESIGN           PR    50 20   32  P C   17. DATA STRUCTURES                  PP   100 40   82  P
08. POWER DEVICES AND MACHINES     PP   100 40   45  P C   18. DATA STRUCTURES                  PR    50 20   41  P
09. NETWORK AND POWER LAB.         TW    50 20   34  P C   19. COMMUNICATION THEORY             PP   100 40   50  P
10. ELECTRONIC INSTRUMENTS AND TOOLS TW    50 20   32  P C   20. COMMUNICATION THEORY             OR    50 20   45  P
                                                           21. CIRCUIT SIMULATION AND TOOLS    TW    50 20   38  P
GRAND TOTAL =  858/1500, RESULT: HIGHER SECOND CLASS
ORDN. 1 MARKS :
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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.
       OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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 S80053101   PATIL SAYALI ABHAY   NILIMA   , 71201009C   , S80053101 ,   PICT   , S80053101
 01. SIGNAL AND SYSTEMS   PP   100 40   54 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   93 P
 02. SIGNAL AND SYSTEMS   OR   50 20   41 P C   12. ENGINEERING MATHEMATICS III   TW   25 10   20 P
 03. SOLID STATES DEVICES AND CIRCUITSPP   100 40   55 P C   13. INTEGRATED CIRCUITS APPLICATIONS PP   100 40   56 P
 04. SOLID STATES DEVICES AND CIRCUITSPR   50 20   36 P C   14. INTEGRATED CIRCUITS APPLICATIONS PR   50 20   37 P
 05. NETWORK ANALYSIS   PP   100 40   52 P C   15. ELECTROMAGNETIC   PP   100 40   67 P
 06. DIGITAL LOGIC DESIGN   PP   100 40   45 P C   16. ELECTROMAGNETIC   TW   25 10   21 P
 07. DIGITAL LOGIC DESIGN   PR   50 20   44 P C   17. DATA STRUCTURES   PP   100 40   79 P
 08. POWER DEVICES AND MACHINES   PP   100 40   50 P C   18. DATA STRUCTURES   PR   50 20   42 P
 09. NETWORK AND POWER LAB.   TW   50 20   42 P C   19. COMMUNICATION THEORY   PP   100 40   77 P
 10. ELECTRONIC INSTRUMENTS AND TOOLS TW   50 20   39 P C   20. COMMUNICATION THEORY   OR   50 20   37 P
                                                             21. CIRCUIT SIMULATION AND TOOLS   TW   50 20   40 P
GRAND TOTAL = 1027/1500, RESULT: FIRST CLASS WITH DISTINCTION
ORDN. 1 MARKS :
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 S80053102   PATIL TUSHAR TUKARAM   KAMAL   , 71201010G   , S80053102 ,   PICT   , S80053102
 01. SIGNAL AND SYSTEMS   PP   100 40   62 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   99 P
 02. SIGNAL AND SYSTEMS   OR   50 20   43 P C   12. ENGINEERING MATHEMATICS III   TW   25 10   23 P
 03. SOLID STATES DEVICES AND CIRCUITSPP   100 40   66 P C   13. INTEGRATED CIRCUITS APPLICATIONS PP   100 40   51 P
 04. SOLID STATES DEVICES AND CIRCUITSPR   50 20   39 P C   14. INTEGRATED CIRCUITS APPLICATIONS PR   50 20   40 P
 05. NETWORK ANALYSIS   PP   100 40   62 P C   15. ELECTROMAGNETIC   PP   100 40   88 P
 06. DIGITAL LOGIC DESIGN   PP   100 40   64 P   16. ELECTROMAGNETIC   TW   25 10   22 P
 07. DIGITAL LOGIC DESIGN   PR   50 20   44 P C   17. DATA STRUCTURES   PP   100 40   76 P
 08. POWER DEVICES AND MACHINES   PP   100 40   59 P C   18. DATA STRUCTURES   PR   50 20   40 P
 09. NETWORK AND POWER LAB.   TW   50 20   42 P C   19. COMMUNICATION THEORY   PP   100 40   73 P
 10. ELECTRONIC INSTRUMENTS AND TOOLS TW   50 20   42 P C   20. COMMUNICATION THEORY   OR   50 20   35 P
                                                             21. CIRCUIT SIMULATION AND TOOLS   TW   50 20   44 P
GRAND TOTAL = 1114/1500, RESULT: FIRST CLASS WITH DISTINCTION
ORDN. 1 MARKS :
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 S80053103   PAWAR GAURAV GURUNATH   JYOTSNA   , 71201012C   , S80053103 ,   PICT   , S80053103
 01. SIGNAL AND SYSTEMS   PP   100 40   55 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   70 P
 02. SIGNAL AND SYSTEMS   OR   50 20   23 P C   12. ENGINEERING MATHEMATICS III   TW   25 10   21 P
 03. SOLID STATES DEVICES AND CIRCUITSPP   100 40   57 P C   13. INTEGRATED CIRCUITS APPLICATIONS PP   100 40   40 P
 04. SOLID STATES DEVICES AND CIRCUITSPR   50 20   25 P C   14. INTEGRATED CIRCUITS APPLICATIONS PR   50 20   28 P
 05. NETWORK ANALYSIS   PP   100 40   42 P C   15. ELECTROMAGNETIC   PP   100 40   63 P
 06. DIGITAL LOGIC DESIGN   PP   100 40   44 P C   16. ELECTROMAGNETIC   TW   25 10   21 P
 07. DIGITAL LOGIC DESIGN   PR   50 20   40 P C   17. DATA STRUCTURES   PP   100 40   63 P
 08. POWER DEVICES AND MACHINES   PP   100 40   54 P C   18. DATA STRUCTURES   PR   50 20   25 P
 09. NETWORK AND POWER LAB.   TW   50 20   41 P C   19. COMMUNICATION THEORY   PP   100 40   57 P
 10. ELECTRONIC INSTRUMENTS AND TOOLS TW   50 20   39 P C   20. COMMUNICATION THEORY   OR   50 20   32 P
                                                             21. CIRCUIT SIMULATION AND TOOLS   TW   50 20   41 P
GRAND TOTAL = 881/1500, RESULT: HIGHER SECOND CLASS
ORDN. 1 MARKS :
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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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S80053104   PHADKE ASHWINI SHEKHAR   ANJALI   , 71201013M   , S80053104 ,   PICT   , S80053104

01. SIGNAL AND SYSTEMS	PP	100	40	42	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	63	P
02. SIGNAL AND SYSTEMS	OR	50	20	33	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	19	P
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	40	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	41	P
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	30	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	34	P
05. NETWORK ANALYSIS	PP	100	40	44	P C	15. ELECTROMAGNETIC	PP	100	40	68	P
06. DIGITAL LOGIC DESIGN	PP	100	40	40	P C	16. ELECTROMAGNETIC	TW	25	10	18	P
07. DIGITAL LOGIC DESIGN	PR	50	20	35	P C	17. DATA STRUCTURES	PP	100	40	61	P
08. POWER DEVICES AND MACHINES	PP	100	40	40	P C	18. DATA STRUCTURES	PR	50	20	38	P
09. NETWORK AND POWER LAB.	TW	50	20	40	P C	19. COMMUNICATION THEORY	PP	100	40	55	P
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	42	P C	20. COMMUNICATION THEORY	OR	50	20	28	P
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	35	P

GRAND TOTAL = 846/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :
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S80053105   PRANITA PRAMOD GHOLE   SANGITA   , 71201020D   , S80053105 ,   PICT   , S80053105

01. SIGNAL AND SYSTEMS	PP	100	40	89	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	93	P
02. SIGNAL AND SYSTEMS	OR	50	20	44	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	21	P
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	59	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	40	P
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	40	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	38	P
05. NETWORK ANALYSIS	PP	100	40	56	P C	15. ELECTROMAGNETIC	PP	100	40	72	P
06. DIGITAL LOGIC DESIGN	PP	100	40	51	P C	16. ELECTROMAGNETIC	TW	25	10	19	P
07. DIGITAL LOGIC DESIGN	PR	50	20	43	P C	17. DATA STRUCTURES	PP	100	40	68	P
08. POWER DEVICES AND MACHINES	PP	100	40	70	P C	18. DATA STRUCTURES	PR	50	20	36	P
09. NETWORK AND POWER LAB.	TW	50	20	40	P C	19. COMMUNICATION THEORY	PP	100	40	57	P
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	39	P C	20. COMMUNICATION THEORY	OR	50	20	48	P
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	38	P

GRAND TOTAL = 1061/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :
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S80053106   PRASHANT KUMAR   LALITA DEVI   , 71201021B   , S80053106 ,   PICT   , S80053106

01. SIGNAL AND SYSTEMS	PP	100	40	55	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	78	P
02. SIGNAL AND SYSTEMS	OR	50	20	30	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	19	P
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	47	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	41	P
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	37	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	35	P
05. NETWORK ANALYSIS	PP	100	40	40	P C	15. ELECTROMAGNETIC	PP	100	40	59	P
06. DIGITAL LOGIC DESIGN	PP	100	40	40	P C	16. ELECTROMAGNETIC	TW	25	10	17	P
07. DIGITAL LOGIC DESIGN	PR	50	20	35	P C	17. DATA STRUCTURES	PP	100	40	53	P
08. POWER DEVICES AND MACHINES	PP	100	40	51	P C	18. DATA STRUCTURES	PR	50	20	37	P
09. NETWORK AND POWER LAB.	TW	50	20	38	P C	19. COMMUNICATION THEORY	PP	100	40	41	P
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	38	P C	20. COMMUNICATION THEORY	OR	50	20	35	P
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	32	P

GRAND TOTAL = 858/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :
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  NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.
        OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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  S80053107   PRIYANKA BALRAJ   R MANORAMA   , 71201024G   , S80053107 ,   PICT   , S80053107
  01. SIGNAL AND SYSTEMS   PP   100 40   66 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   84 P
  02. SIGNAL AND SYSTEMS   OR   50 20   41 P C   12. ENGINEERING MATHEMATICS III   TW   25 10   20 P
  03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 72 P C   13. INTEGRATED CIRCUITS APPLICATIONS PP   100 40   58 P
  04. SOLID STATES DEVICES AND CIRCUITSPR   50 20 35 P C   14. INTEGRATED CIRCUITS APPLICATIONS PR   50 20   38 P
  05. NETWORK ANALYSIS   PP   100 40   58 P C   15. ELECTROMAGNETIC   PP   100 40   67 P
  06. DIGITAL LOGIC DESIGN   PP   100 40   81 P C   16. ELECTROMAGNETIC   TW   25 10   20 P
  07. DIGITAL LOGIC DESIGN   PR   50 20   39 P C   17. DATA STRUCTURES   PP   100 40   76 P
  08. POWER DEVICES AND MACHINES   PP   100 40   74 P C   18. DATA STRUCTURES   PR   50 20   26 P
  09. NETWORK AND POWER LAB.   TW   50 20   38 P C   19. COMMUNICATION THEORY   PP   100 40   66 P
  10. ELECTRONIC INSTRUMENTS AND TOOLS TW   50 20 37 P C   20. COMMUNICATION THEORY   OR   50 20   41 P
                                                            21. CIRCUIT SIMULATION AND TOOLS   TW   50 20   40 P
GRAND TOTAL = 1077/1500, RESULT: FIRST CLASS WITH DISTINCTION
ORDN. 1 MARKS :
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  S80053108   PULEKAR ADITYA CHANDRASHEKHAR   MADHAVI   , 71201025E   , S80053108 ,   PICT   , S80053108
  01. SIGNAL AND SYSTEMS   PP   100 40   74 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   82 P
  02. SIGNAL AND SYSTEMS   OR   50 20   38 P C   12. ENGINEERING MATHEMATICS III   TW   25 10   20 P
  03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 68 P C   13. INTEGRATED CIRCUITS APPLICATIONS PP   100 40   45 P
  04. SOLID STATES DEVICES AND CIRCUITSPR   50 20 36 P C   14. INTEGRATED CIRCUITS APPLICATIONS PR   50 20   40 P
  05. NETWORK ANALYSIS   PP   100 40   53 P C   15. ELECTROMAGNETIC   PP   100 40   67 P
  06. DIGITAL LOGIC DESIGN   PP   100 40   40 P C   16. ELECTROMAGNETIC   TW   25 10   18 P
  07. DIGITAL LOGIC DESIGN   PR   50 20   42 P C   17. DATA STRUCTURES   PP   100 40   67 P
  08. POWER DEVICES AND MACHINES   PP   100 40   63 P C   18. DATA STRUCTURES   PR   50 20   39 P
  09. NETWORK AND POWER LAB.   TW   50 20   37 P C   19. COMMUNICATION THEORY   PP   100 40   61 P
  10. ELECTRONIC INSTRUMENTS AND TOOLS TW   50 20 37 P C   20. COMMUNICATION THEORY   OR   50 20   42 P
                                                            21. CIRCUIT SIMULATION AND TOOLS   TW   50 20   39 P
GRAND TOTAL = 1008/1500, RESULT: FIRST CLASS WITH DISTINCTION
ORDN. 1 MARKS :
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  S80053109   R GOKUL KRISHNAN   R RAJESWARI   , 71201026C   , S80053109 ,   PICT   , S80053109
  01. SIGNAL AND SYSTEMS   PP   100 40   66 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   79 P
  02. SIGNAL AND SYSTEMS   OR   50 20   40 P C   12. ENGINEERING MATHEMATICS III   TW   25 10   21 P
  03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 50 P C   13. INTEGRATED CIRCUITS APPLICATIONS PP   100 40   42 P
  04. SOLID STATES DEVICES AND CIRCUITSPR   50 20 38 P C   14. INTEGRATED CIRCUITS APPLICATIONS PR   50 20   45 P
  05. NETWORK ANALYSIS   PP   100 40   48 P C   15. ELECTROMAGNETIC   PP   100 40   76 P
  06. DIGITAL LOGIC DESIGN   PP   100 40   54 P C   16. ELECTROMAGNETIC   TW   25 10   23 P
  07. DIGITAL LOGIC DESIGN   PR   50 20   43 P C   17. DATA STRUCTURES   PP   100 40   65 P
  08. POWER DEVICES AND MACHINES   PP   100 40   50 P C   18. DATA STRUCTURES   PR   50 20   41 P
  09. NETWORK AND POWER LAB.   TW   50 20   41 P C   19. COMMUNICATION THEORY   PP   100 40   55 P
  10. ELECTRONIC INSTRUMENTS AND TOOLS TW   50 20 39 P C   20. COMMUNICATION THEORY   OR   50 20   38 P
                                                            21. CIRCUIT SIMULATION AND TOOLS   TW   50 20   44 P
GRAND TOTAL = 998/1500, RESULT: FIRST CLASS WITH DISTINCTION
ORDN. 1 MARKS :
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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.

          OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S80053110   RADHIKA RAJE   VIDYA   , 71201027M   , S80053110 ,   PICT   , S80053110

01. SIGNAL AND SYSTEMS	PP	100	40	59	P C	11. ENGINEERING MATHEMATICS III	PP	100 40 64 P
02. SIGNAL AND SYSTEMS	OR	50	20	25	P C	12. ENGINEERING MATHEMATICS III	TW	25 10 20 P
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	52	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100 40 55 P
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	35	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50 20 27 P
05. NETWORK ANALYSIS	PP	100	40	40	P C	15. ELECTROMAGNETIC	PP	100 40 69 P
06. DIGITAL LOGIC DESIGN	PP	100	40	40	P C	16. ELECTROMAGNETIC	TW	25 10 19 P
07. DIGITAL LOGIC DESIGN	PR	50	20	37	P C	17. DATA STRUCTURES	PP	100 40 68 P
08. POWER DEVICES AND MACHINES	PP	100	40	58	P C	18. DATA STRUCTURES	PR	50 20 38 P
09. NETWORK AND POWER LAB.	TW	50	20	40	P C	19. COMMUNICATION THEORY	PP	100 40 76 P
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	40	P C	20. COMMUNICATION THEORY	OR	50 20 34 P
						21. CIRCUIT SIMULATION AND TOOLS	TW	50 20 37 P

GRAND TOTAL = 933/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

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S80053111   RAHEGAONKAR RUSHIKESH VIJAY   LALITA   , 71201028K   , S80053111 ,   PICT   , S80053111

01. SIGNAL AND SYSTEMS	PP	100	40	75	P C	11. ENGINEERING MATHEMATICS III	PP	100 40 90 P
02. SIGNAL AND SYSTEMS	OR	50	20	29	P C	12. ENGINEERING MATHEMATICS III	TW	25 10 22 P
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	68	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100 40 55 P
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	33	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50 20 29 P
05. NETWORK ANALYSIS	PP	100	40	40	P C	15. ELECTROMAGNETIC	PP	100 40 68 P
06. DIGITAL LOGIC DESIGN	PP	100	40	60	P C	16. ELECTROMAGNETIC	TW	25 10 23 P
07. DIGITAL LOGIC DESIGN	PR	50	20	39	P C	17. DATA STRUCTURES	PP	100 40 73 P
08. POWER DEVICES AND MACHINES	PP	100	40	71	P C	18. DATA STRUCTURES	PR	50 20 36 P
09. NETWORK AND POWER LAB.	TW	50	20	41	P C	19. COMMUNICATION THEORY	PP	100 40 82 P
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	38	P C	20. COMMUNICATION THEORY	OR	50 20 32 P
						21. CIRCUIT SIMULATION AND TOOLS	TW	50 20 43 P

GRAND TOTAL = 1047/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

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S80053112   RAHUL MAHIPATI KALE   SUVARNA   , 71201030M   , S80053112 ,   PICT   , S80053112

01. SIGNAL AND SYSTEMS	PP	100	40	59	P C	11. ENGINEERING MATHEMATICS III	PP	100 40 75 P
02. SIGNAL AND SYSTEMS	OR	50	20	32	P C	12. ENGINEERING MATHEMATICS III	TW	25 10 22 P
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	68	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100 40 48 P
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	30	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50 20 34 P
05. NETWORK ANALYSIS	PP	100	40	40	P C	15. ELECTROMAGNETIC	PP	100 40 70 P
06. DIGITAL LOGIC DESIGN	PP	100	40	53	P C	16. ELECTROMAGNETIC	TW	25 10 21 P
07. DIGITAL LOGIC DESIGN	PR	50	20	42	P C	17. DATA STRUCTURES	PP	100 40 70 P
08. POWER DEVICES AND MACHINES	PP	100	40	58	P C	18. DATA STRUCTURES	PR	50 20 35 P
09. NETWORK AND POWER LAB.	TW	50	20	39	P C	19. COMMUNICATION THEORY	PP	100 40 75 P
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	38	P C	20. COMMUNICATION THEORY	OR	50 20 30 P
						21. CIRCUIT SIMULATION AND TOOLS	TW	50 20 42 P

GRAND TOTAL = 981/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S80053113   RATHOD ABHIJIT KALURAM   ALKA   , 71201034D   , S80053113 ,   PICT   , S80053113
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01. SIGNAL AND SYSTEMS            PP   100  40   49  P C   11. ENGINEERING MATHEMATICS III      PP   100  40   63  P
02. SIGNAL AND SYSTEMS            OR    50  20   23  P C   12. ENGINEERING MATHEMATICS III      TW    25  10   19  P
03. SOLID STATES DEVICES AND CIRCUITSPP 100  40   40  P C   13. INTEGRATED CIRCUITS APPLICATIONS PP   100  40   40  P
04. SOLID STATES DEVICES AND CIRCUITSPR  50  20   33  P C   14. INTEGRATED CIRCUITS APPLICATIONS PR    50  20   32  P
05. NETWORK ANALYSIS             PP   100  40   45  P     15. ELECTROMAGNETIC                  PP   100  40   42  P
06. DIGITAL LOGIC DESIGN         PP   100  40   56  P     16. ELECTROMAGNETIC                  TW    25  10   20  P
07. DIGITAL LOGIC DESIGN         PR    50  20   34  P C   17. DATA STRUCTURES                 PP   100  40   46  P
08. POWER DEVICES AND MACHINES   PP   100  40   40  P     18. DATA STRUCTURES                 PR    50  20   42  P
09. NETWORK AND POWER LAB.       TW    50  20   40  P C   19. COMMUNICATION THEORY            PP   100  40   40  P
10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50  20   36  P C   20. COMMUNICATION THEORY            OR    50  20   31  P
                                                          21. CIRCUIT SIMULATION AND TOOLS   TW    50  20   39  P
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GRAND TOTAL = 810/1500, RESULT: SECOND CLASS

ORDN. 1 MARKS :

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S80053114   RATHOD PRIYANKA NEMICHAND   SHOBHA   , 71201036L   , S80053114 ,   PICT   , S80053114
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01. SIGNAL AND SYSTEMS            PP   100  40   40  P C   11. ENGINEERING MATHEMATICS III      PP   100  40   63  P
02. SIGNAL AND SYSTEMS            OR    50  20   30  P     12. ENGINEERING MATHEMATICS III      TW    25  10   19  P
03. SOLID STATES DEVICES AND CIRCUITSPP 100  40   40  P     13. INTEGRATED CIRCUITS APPLICATIONS PP   100  40   40  P
04. SOLID STATES DEVICES AND CIRCUITSPR  50  20   24  P C   14. INTEGRATED CIRCUITS APPLICATIONS PR    50  20   22  P
05. NETWORK ANALYSIS             PP   100  40   40  P     15. ELECTROMAGNETIC                  PP   100  40   58  P
06. DIGITAL LOGIC DESIGN         PP   100  40   41  P C   16. ELECTROMAGNETIC                  TW    25  10   18  P
07. DIGITAL LOGIC DESIGN         PR    50  20   30  P C   17. DATA STRUCTURES                 PP   100  40   57  P
08. POWER DEVICES AND MACHINES   PP   100  40   40  P C   18. DATA STRUCTURES                 PR    50  20   37  P
09. NETWORK AND POWER LAB.       TW    50  20   37  P C   19. COMMUNICATION THEORY            PP   100  40   40  P
10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50  20   35  P C   20. COMMUNICATION THEORY            OR    50  20   30  P
                                                          21. CIRCUIT SIMULATION AND TOOLS   TW    50  20   34  P
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GRAND TOTAL = 775/1500, RESULT: SECOND CLASS

ORDN. 1 MARKS :

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S80053115   RITAPURE KIRAN KAMLAKAR   SUVARNA   , 71344370H   , S80053115 ,   PICT   , S80053115
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01. SIGNAL AND SYSTEMS            PP   100  40   45  P C   11. ENGINEERING MATHEMATICS III      PP   100  40   65  P
02. SIGNAL AND SYSTEMS            OR    50  20   25  P C   12. ENGINEERING MATHEMATICS III      TW    25  10   22  P
03. SOLID STATES DEVICES AND CIRCUITSPP 100  40   53  P C   13. INTEGRATED CIRCUITS APPLICATIONS PP   100  40   57  P
04. SOLID STATES DEVICES AND CIRCUITSPR  50  20   38  P C   14. INTEGRATED CIRCUITS APPLICATIONS PR    50  20   30  P
05. NETWORK ANALYSIS             PP   100  40   40  P     15. ELECTROMAGNETIC                  PP   100  40   60  P
06. DIGITAL LOGIC DESIGN         PP   100  40   45  P C   16. ELECTROMAGNETIC                  TW    25  10   21  P
07. DIGITAL LOGIC DESIGN         PR    50  20   40  P C   17. DATA STRUCTURES                 PP   100  40   66  P
08. POWER DEVICES AND MACHINES   PP   100  40   51  P C   18. DATA STRUCTURES                 PR    50  20   41  P
09. NETWORK AND POWER LAB.       TW    50  20   40  P C   19. COMMUNICATION THEORY            PP   100  40   68  P
10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50  20   38  P C   20. COMMUNICATION THEORY            OR    50  20   35  P
                                                          21. CIRCUIT SIMULATION AND TOOLS   TW    50  20   38  P
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GRAND TOTAL = 918/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

        OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S80053116   ROHANKAR AKSHAY ANILRAO   REKHA   , 71344371F   , S80053116 ,   PICT   , S80053116
01. SIGNAL AND SYSTEMS   PP   100 40   46 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   43 P
02. SIGNAL AND SYSTEMS   OR   50 20   23 P C   12. ENGINEERING MATHEMATICS III   TW   25 10   19 P
03. SOLID STATES DEVICES AND CIRCUITSPP   100 40   40 P C   13. INTEGRATED CIRCUITS APPLICATIONS PP   100 40   40 P
04. SOLID STATES DEVICES AND CIRCUITSPR   50 20   34 P C   14. INTEGRATED CIRCUITS APPLICATIONS PR   50 20   28 P
05. NETWORK ANALYSIS   PP   100 40   40 P   15. ELECTROMAGNETIC   PP   100 40   40 P
06. DIGITAL LOGIC DESIGN   PP   100 40   42 P C   16. ELECTROMAGNETIC   TW   25 10   19 P
07. DIGITAL LOGIC DESIGN   PR   50 20   42 P C   17. DATA STRUCTURES   PP   100 40   57 P
08. POWER DEVICES AND MACHINES   PP   100 40   46 P C   18. DATA STRUCTURES   PR   50 20   22 P
09. NETWORK AND POWER LAB.   TW   50 20   40 P C   19. COMMUNICATION THEORY   PP   100 40   40 P
10. ELECTRONIC INSTRUMENTS AND TOOLS TW   50 20   38 P C   20. COMMUNICATION THEORY   OR   50 20   35 P
                                                          21. CIRCUIT SIMULATION AND TOOLS TW   50 20   36 P
GRAND TOTAL = 770/1500, RESULT: SECOND CLASS
ORDN. 1 MARKS :

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S80053117   ROHIT GUPTA   GEETA   , 71201042E   , S80053117 ,   PICT   , S80053117
01. SIGNAL AND SYSTEMS   PP   100 40   55 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   79 P
02. SIGNAL AND SYSTEMS   OR   50 20   31 P   12. ENGINEERING MATHEMATICS III   TW   25 10   11 P
03. SOLID STATES DEVICES AND CIRCUITSPP   100 40   49 P C   13. INTEGRATED CIRCUITS APPLICATIONS PP   100 40   40 P
04. SOLID STATES DEVICES AND CIRCUITSPR   50 20   21 P C   14. INTEGRATED CIRCUITS APPLICATIONS PR   50 20   11 F
05. NETWORK ANALYSIS   PP   100 40   40 P C   15. ELECTROMAGNETIC   PP   100 40   46 P
06. DIGITAL LOGIC DESIGN   PP   100 40   40 P C   16. ELECTROMAGNETIC   TW   25 10   10 P
07. DIGITAL LOGIC DESIGN   PR   50 20   30 P C   17. DATA STRUCTURES   PP   100 40   57 P
08. POWER DEVICES AND MACHINES   PP   100 40   52 P C   18. DATA STRUCTURES   PR   50 20   05 F
09. NETWORK AND POWER LAB.   TW   50 20   29 P C   19. COMMUNICATION THEORY   PP   100 40   43 P
10. ELECTRONIC INSTRUMENTS AND TOOLS TW   50 20   31 P C   20. COMMUNICATION THEORY   OR   50 20   10 F
                                                          21. CIRCUIT SIMULATION AND TOOLS TW   50 20   20 P
GRAND TOTAL = 710/1500, RESULT: FAILS A.T.K.T.
ORDN. 1 MARKS :

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S80053118   ROKADE SUYOG SUDARSHAN   SUREKHA   , 71201043C   , S80053118 ,   PICT   , S80053118
01. SIGNAL AND SYSTEMS   PP   100 40   44 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   29 F
02. SIGNAL AND SYSTEMS   OR   50 20   35 P   12. ENGINEERING MATHEMATICS III   TW   25 10   18 P
03. SOLID STATES DEVICES AND CIRCUITSPP   100 40   40 P   13. INTEGRATED CIRCUITS APPLICATIONS PP   100 40   40 P
04. SOLID STATES DEVICES AND CIRCUITSPR   50 20   27 P C   14. INTEGRATED CIRCUITS APPLICATIONS PR   50 20   28 P
05. NETWORK ANALYSIS   PP   100 40   20 F   15. ELECTROMAGNETIC   PP   100 40   27 F
06. DIGITAL LOGIC DESIGN   PP   100 40   40 P C   16. ELECTROMAGNETIC   TW   25 10   20 P
07. DIGITAL LOGIC DESIGN   PR   50 20   30 P C   17. DATA STRUCTURES   PP   100 40   57 P
08. POWER DEVICES AND MACHINES   PP   100 40   46 P   18. DATA STRUCTURES   PR   50 20   07 F
09. NETWORK AND POWER LAB.   TW   50 20   39 P C   19. COMMUNICATION THEORY   PP   100 40   44 P
10. ELECTRONIC INSTRUMENTS AND TOOLS TW   50 20   37 P C   20. COMMUNICATION THEORY   OR   50 20   22 P
                                                          21. CIRCUIT SIMULATION AND TOOLS TW   50 20   39 P
GRAND TOTAL = 689/1500, RESULT: FAILS A.T.K.T.
ORDN. 1 MARKS :

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.
         OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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S80053119   RUTUJA S JADHAV   BHARATI   , 71201045K   , S80053119 ,   PICT   , S80053119
01. SIGNAL AND SYSTEMS   PP   100 40   47 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   78 P
02. SIGNAL AND SYSTEMS   OR   50 20   32 P C   12. ENGINEERING MATHEMATICS III   TW   25 10   19 P
03. SOLID STATES DEVICES AND CIRCUITSPP 100 40   46 P C   13. INTEGRATED CIRCUITS APPLICATIONS PP   100 40   51 P
04. SOLID STATES DEVICES AND CIRCUITSPR   50 20   34 P C   14. INTEGRATED CIRCUITS APPLICATIONS PR   50 20   34 P
05. NETWORK ANALYSIS   PP   100 40   40 P C   15. ELECTROMAGNETIC   PP   100 40   53 P
06. DIGITAL LOGIC DESIGN   PP   100 40   57 P C   16. ELECTROMAGNETIC   TW   25 10   18 P
07. DIGITAL LOGIC DESIGN   PR   50 20   46 P C   17. DATA STRUCTURES   PP   100 40   77 P
08. POWER DEVICES AND MACHINES   PP   100 40   46 P C   18. DATA STRUCTURES   PR   50 20   38 P
09. NETWORK AND POWER LAB.   TW   50 20   40 P C   19. COMMUNICATION THEORY   PP   100 40   60 P
10. ELECTRONIC INSTRUMENTS AND TOOLS TW   50 20   40 P C   20. COMMUNICATION THEORY   OR   50 20   23 P
                                                            21. CIRCUIT SIMULATION AND TOOLS   TW   50 20   33 P
GRAND TOTAL = 912/1500, RESULT: FIRST CLASS
ORDN. 1 MARKS :
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S80053120   SALVE PRAJAKTA BHAUSAHEB   MANGAL   , 71201051D   , S80053120 ,   PICT   , S80053120
01. SIGNAL AND SYSTEMS   PP   100 40   52 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   68 P
02. SIGNAL AND SYSTEMS   OR   50 20   28 P C   12. ENGINEERING MATHEMATICS III   TW   25 10   20 P
03. SOLID STATES DEVICES AND CIRCUITSPP 100 40   46 P C   13. INTEGRATED CIRCUITS APPLICATIONS PP   100 40   42 P
04. SOLID STATES DEVICES AND CIRCUITSPR   50 20   35 P C   14. INTEGRATED CIRCUITS APPLICATIONS PR   50 20   21 P
05. NETWORK ANALYSIS   PP   100 40   40 P   15. ELECTROMAGNETIC   PP   100 40   44 P
06. DIGITAL LOGIC DESIGN   PP   100 40   42 P C   16. ELECTROMAGNETIC   TW   25 10   21 P
07. DIGITAL LOGIC DESIGN   PR   50 20   38 P C   17. DATA STRUCTURES   PP   100 40   59 P
08. POWER DEVICES AND MACHINES   PP   100 40   40 P C   18. DATA STRUCTURES   PR   50 20   25 P
09. NETWORK AND POWER LAB.   TW   50 20   40 P C   19. COMMUNICATION THEORY   PP   100 40   61 P
10. ELECTRONIC INSTRUMENTS AND TOOLS TW   50 20   37 P C   20. COMMUNICATION THEORY   OR   50 20   37 P
                                                            21. CIRCUIT SIMULATION AND TOOLS   TW   50 20   41 P
GRAND TOTAL = 837/1500, RESULT: HIGHER SECOND CLASS
ORDN. 1 MARKS :
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S80053121   SATPUTE SHIVRAJ PRAKASH   VIJAYA   , 71201055G   , S80053121 ,   PICT   , S80053121
01. SIGNAL AND SYSTEMS   PP   100 40   60 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   70 P
02. SIGNAL AND SYSTEMS   OR   50 20   33 P C   12. ENGINEERING MATHEMATICS III   TW   25 10   21 P
03. SOLID STATES DEVICES AND CIRCUITSPP 100 40   40 P C   13. INTEGRATED CIRCUITS APPLICATIONS PP   100 40   50 P
04. SOLID STATES DEVICES AND CIRCUITSPR   50 20   27 P C   14. INTEGRATED CIRCUITS APPLICATIONS PR   50 20   21 P
05. NETWORK ANALYSIS   PP   100 40   44 P C   15. ELECTROMAGNETIC   PP   100 40   61 P
06. DIGITAL LOGIC DESIGN   PP   100 40   42 P C   16. ELECTROMAGNETIC   TW   25 10   20 P
07. DIGITAL LOGIC DESIGN   PR   50 20   37 P C   17. DATA STRUCTURES   PP   100 40   72 P
08. POWER DEVICES AND MACHINES   PP   100 40   66 P C   18. DATA STRUCTURES   PR   50 20   32 P
09. NETWORK AND POWER LAB.   TW   50 20   39 P C   19. COMMUNICATION THEORY   PP   100 40   59 P
10. ELECTRONIC INSTRUMENTS AND TOOLS TW   50 20   37 P C   20. COMMUNICATION THEORY   OR   50 20   30 P
                                                            21. CIRCUIT SIMULATION AND TOOLS   TW   50 20   37 P
GRAND TOTAL = 898+02/1500, RESULT: FIRST CLASS [O.2]
ORDN. 1 MARKS :
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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.

          OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S80053122   SAURABH NANGLIA   SANDHYA   , 71201056E   , S80053122 ,   PICT   , S80053122

01. SIGNAL AND SYSTEMS	PP	100	40	56	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	58	P	
02. SIGNAL AND SYSTEMS	OR	50	20	23	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	14	P	
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	53	P	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	40	P
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	20	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	22	P
05. NETWORK ANALYSIS	PP	100	40	40	P C	15. ELECTROMAGNETIC	PP	100	40	AA	F
06. DIGITAL LOGIC DESIGN	PP	100	40	45	P C	16. ELECTROMAGNETIC	TW	25	10	16	P
07. DIGITAL LOGIC DESIGN	PR	50	20	28	P C	17. DATA STRUCTURES	PP	100	40	56	P
08. POWER DEVICES AND MACHINES	PP	100	40	42	P C	18. DATA STRUCTURES	PR	50	20	23	P
09. NETWORK AND POWER LAB.	TW	50	20	34	P C	19. COMMUNICATION THEORY	PP	100	40	52	P
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	30	P C	20. COMMUNICATION THEORY	OR	50	20	22	P
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	28	P

GRAND TOTAL = 702/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

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S80053123   SAURAV KUMAR   RAUSHAN DEVI   , 71201057C   , S80053123 ,   PICT   , S80053123

01. SIGNAL AND SYSTEMS	PP	100	40	60	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	78	P	
02. SIGNAL AND SYSTEMS	OR	50	20	35	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	18	P	
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	51	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	49	P
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	35	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	21	P
05. NETWORK ANALYSIS	PP	100	40	41	P C	15. ELECTROMAGNETIC	PP	100	40	61	P
06. DIGITAL LOGIC DESIGN	PP	100	40	46	P C	16. ELECTROMAGNETIC	TW	25	10	18	P
07. DIGITAL LOGIC DESIGN	PR	50	20	39	P C	17. DATA STRUCTURES	PP	100	40	72	P
08. POWER DEVICES AND MACHINES	PP	100	40	56	P C	18. DATA STRUCTURES	PR	50	20	35	P
09. NETWORK AND POWER LAB.	TW	50	20	39	P C	19. COMMUNICATION THEORY	PP	100	40	75	P
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	40	P C	20. COMMUNICATION THEORY	OR	50	20	32	P
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	33	P

GRAND TOTAL = 934/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

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S80053124   SHAH PARIN LITESH   BHAVYA   , 71201060C   , S80053124 ,   PICT   , S80053124

01. SIGNAL AND SYSTEMS	PP	100	40	56	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	83	P	
02. SIGNAL AND SYSTEMS	OR	50	20	28	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	16	P	
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	56	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	63	P
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	34	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	35	P
05. NETWORK ANALYSIS	PP	100	40	43	P C	15. ELECTROMAGNETIC	PP	100	40	71	P
06. DIGITAL LOGIC DESIGN	PP	100	40	52	P C	16. ELECTROMAGNETIC	TW	25	10	17	P
07. DIGITAL LOGIC DESIGN	PR	50	20	42	P C	17. DATA STRUCTURES	PP	100	40	81	P
08. POWER DEVICES AND MACHINES	PP	100	40	63	P C	18. DATA STRUCTURES	PR	50	20	33	P
09. NETWORK AND POWER LAB.	TW	50	20	40	P C	19. COMMUNICATION THEORY	PP	100	40	72	P
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	38	P C	20. COMMUNICATION THEORY	OR	50	20	27	P
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	32	P

GRAND TOTAL = 982/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.
       OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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 S80053125   SHANTANU SHEKAR JAGTAP   SHUBHANGI   , 71201063H   , S80053125 ,   PICT   , S80053125
 01. SIGNAL AND SYSTEMS   PP   100 40   56 P
 02. SIGNAL AND SYSTEMS   OR   50 20   22 P
 03. SOLID STATES DEVICES AND CIRCUITSPP   100 40   40 P
 04. SOLID STATES DEVICES AND CIRCUITSPR   50 20   20 P
 05. NETWORK ANALYSIS   PP   100 40   40 P
 06. DIGITAL LOGIC DESIGN   PP   100 40   40 P
 07. DIGITAL LOGIC DESIGN   PR   50 20   08 F
 08. POWER DEVICES AND MACHINES   PP   100 40   27 F
 09. NETWORK AND POWER LAB.   TW   50 20   23 P C
 10. ELECTRONIC INSTRUMENTS AND TOOLS TW   50 20   20 P C
 FIRST TERM TOTAL = 296/750.
ORDN. 1 MARKS :

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 S80053126   SHELAR RAHUL BAPU   MEENA   , 71201065D   , S80053126 ,   PICT   , S80053126
 01. SIGNAL AND SYSTEMS   PP   100 40   48 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   61 P
 02. SIGNAL AND SYSTEMS   OR   50 20   23 P C   12. ENGINEERING MATHEMATICS III   TW   25 10   18 P
 03. SOLID STATES DEVICES AND CIRCUITSPP   100 40   58 P C   13. INTEGRATED CIRCUITS APPLICATIONS PP   100 40   50 P
 04. SOLID STATES DEVICES AND CIRCUITSPR   50 20   35 P C   14. INTEGRATED CIRCUITS APPLICATIONS PR   50 20   36 P
 05. NETWORK ANALYSIS   PP   100 40   40 P C   15. ELECTROMAGNETIC   PP   100 40   72 P
 06. DIGITAL LOGIC DESIGN   PP   100 40   59 P C   16. ELECTROMAGNETIC   TW   25 10   20 P
 07. DIGITAL LOGIC DESIGN   PR   50 20   42 P C   17. DATA STRUCTURES   PP   100 40   78 P
 08. POWER DEVICES AND MACHINES   PP   100 40   51 P C   18. DATA STRUCTURES   PR   50 20   35 P
 09. NETWORK AND POWER LAB.   TW   50 20   39 P C   19. COMMUNICATION THEORY   PP   100 40   75 P
 10. ELECTRONIC INSTRUMENTS AND TOOLS TW   50 20   39 P C   20. COMMUNICATION THEORY   OR   50 20   27 P
                                                               21. CIRCUIT SIMULATION AND TOOLS   TW   50 20   38 P
GRAND TOTAL = 944/1500, RESULT: FIRST CLASS
ORDN. 1 MARKS :

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 S80053127   SHELAR SAGAR PRAKASH   REKHA   , 71201066B   , S80053127 ,   PICT   , S80053127
 01. SIGNAL AND SYSTEMS   PP   100 40   78 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   83 P
 02. SIGNAL AND SYSTEMS   OR   50 20   25 P C   12. ENGINEERING MATHEMATICS III   TW   25 10   20 P
 03. SOLID STATES DEVICES AND CIRCUITSPP   100 40   66 P C   13. INTEGRATED CIRCUITS APPLICATIONS PP   100 40   40 P
 04. SOLID STATES DEVICES AND CIRCUITSPR   50 20   37 P C   14. INTEGRATED CIRCUITS APPLICATIONS PR   50 20   30 P
 05. NETWORK ANALYSIS   PP   100 40   45 P   15. ELECTROMAGNETIC   PP   100 40   80 P
 06. DIGITAL LOGIC DESIGN   PP   100 40   63 P C   16. ELECTROMAGNETIC   TW   25 10   22 P
 07. DIGITAL LOGIC DESIGN   PR   50 20   44 P C   17. DATA STRUCTURES   PP   100 40   72 P
 08. POWER DEVICES AND MACHINES   PP   100 40   56 P C   18. DATA STRUCTURES   PR   50 20   36 P
 09. NETWORK AND POWER LAB.   TW   50 20   40 P C   19. COMMUNICATION THEORY   PP   100 40   68 P
 10. ELECTRONIC INSTRUMENTS AND TOOLS TW   50 20   41 P C   20. COMMUNICATION THEORY   OR   50 20   27 P
                                                               21. CIRCUIT SIMULATION AND TOOLS   TW   50 20   39 P
GRAND TOTAL = 1012/1500, RESULT: FIRST CLASS WITH DISTINCTION
ORDN. 1 MARKS :

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.
       OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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S80053128   SHELKE AMOL MARUTI   SHARADA   , 71344372D   , S80053128 ,   PICT   , S80053128
01. SIGNAL AND SYSTEMS   PP   100 40 25 F   11. ENGINEERING MATHEMATICS III   PP   100 40 23 F
02. SIGNAL AND SYSTEMS   OR   50 20 20 P   12. ENGINEERING MATHEMATICS III   TW   25 10 19 P
03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 25 F   13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 28 F
04. SOLID STATES DEVICES AND CIRCUITSPR   50 20 25 P C   14. INTEGRATED CIRCUITS APPLICATIONS PR   50 20 07 F
05. NETWORK ANALYSIS   PP   100 40 24 F   15. ELECTROMAGNETIC   PP   100 40 20 F
06. DIGITAL LOGIC DESIGN   PP   100 40 58 P   16. ELECTROMAGNETIC   TW   25 10 20 P
07. DIGITAL LOGIC DESIGN   PR   50 20 34 P C   17. DATA STRUCTURES   PP   100 40 42 P
08. POWER DEVICES AND MACHINES   PP   100 40 24 F   18. DATA STRUCTURES   PR   50 20 26 P
09. NETWORK AND POWER LAB.   TW   50 20 35 P C   19. COMMUNICATION THEORY   PP   100 40 40 P
10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 32 P C   20. COMMUNICATION THEORY   OR   50 20 29 P
                                                           21. CIRCUIT SIMULATION AND TOOLS   TW   50 20 38 P
GRAND TOTAL = 594/1500, RESULT: FAILS
ORDN. 1 MARKS :

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 S80053129   SHEP VISHWAJIT CHANDRAKANT   URMILA   , 71201068J   , S80053129 ,   PICT   , S80053129
01. SIGNAL AND SYSTEMS   PP   100 40 54 P C   11. ENGINEERING MATHEMATICS III   PP   100 40 62 P
02. SIGNAL AND SYSTEMS   OR   50 20 22 P C   12. ENGINEERING MATHEMATICS III   TW   25 10 16 P
03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 40 P C   13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 40 P
04. SOLID STATES DEVICES AND CIRCUITSPR   50 20 22 P C   14. INTEGRATED CIRCUITS APPLICATIONS PR   50 20 08 F
05. NETWORK ANALYSIS   PP   100 40 41 P   15. ELECTROMAGNETIC   PP   100 40 51 P
06. DIGITAL LOGIC DESIGN   PP   100 40 54 P   16. ELECTROMAGNETIC   TW   25 10 17 P
07. DIGITAL LOGIC DESIGN   PR   50 20 32 P C   17. DATA STRUCTURES   PP   100 40 61 P
08. POWER DEVICES AND MACHINES   PP   100 40 40 P C   18. DATA STRUCTURES   PR   50 20 06 F
09. NETWORK AND POWER LAB.   TW   50 20 35 P C   19. COMMUNICATION THEORY   PP   100 40 48 P
10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 31 P C   20. COMMUNICATION THEORY   OR   50 20 30 P
                                                           21. CIRCUIT SIMULATION AND TOOLS   TW   50 20 32 P
GRAND TOTAL = 742/1500, RESULT: FAILS A.T.K.T.
ORDN. 1 MARKS :

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 S80053130   SHINDE SANIKA HANMANT   SANGEETA   , 71201071J   , S80053130 ,   PICT   , S80053130
01. SIGNAL AND SYSTEMS   PP   100 40 88 P C   11. ENGINEERING MATHEMATICS III   PP   100 40 91 P
02. SIGNAL AND SYSTEMS   OR   50 20 40 P C   12. ENGINEERING MATHEMATICS III   TW   25 10 21 P
03. SOLID STATES DEVICES AND CIRCUITSPP 100 40 74 P C   13. INTEGRATED CIRCUITS APPLICATIONS PP 100 40 44 P
04. SOLID STATES DEVICES AND CIRCUITSPR   50 20 38 P C   14. INTEGRATED CIRCUITS APPLICATIONS PR   50 20 33 P
05. NETWORK ANALYSIS   PP   100 40 42 P C   15. ELECTROMAGNETIC   PP   100 40 55 P
06. DIGITAL LOGIC DESIGN   PP   100 40 56 P C   16. ELECTROMAGNETIC   TW   25 10 20 P
07. DIGITAL LOGIC DESIGN   PR   50 20 36 P C   17. DATA STRUCTURES   PP   100 40 69 P
08. POWER DEVICES AND MACHINES   PP   100 40 57 P C   18. DATA STRUCTURES   PR   50 20 43 P
09. NETWORK AND POWER LAB.   TW   50 20 43 P C   19. COMMUNICATION THEORY   PP   100 40 60 P
10. ELECTRONIC INSTRUMENTS AND TOOLS TW 50 20 43 P C   20. COMMUNICATION THEORY   OR   50 20 30 P
                                                           21. CIRCUIT SIMULATION AND TOOLS   TW   50 20 31 P
GRAND TOTAL = 1014/1500, RESULT: FIRST CLASS WITH DISTINCTION
ORDN. 1 MARKS :

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
       OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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  S80053131    SHINDE SIDDESH BALASAHEB            ASHA              , 71201072G    , S80053131 ,   PICT     , S80053131
  01. SIGNAL AND SYSTEMS              PP    100  40   73  P C    11. ENGINEERING MATHEMATICS III      PP    100  40   76  P
  02. SIGNAL AND SYSTEMS              OR     50  20   45  P C    12. ENGINEERING MATHEMATICS III      TW     25  10   22  P
  03. SOLID STATES DEVICES AND CIRCUITSPP    100  40   63  P C    13. INTEGRATED CIRCUITS APPLICATIONS PP    100  40   59  P
  04. SOLID STATES DEVICES AND CIRCUITSPR     50  20   44  P C    14. INTEGRATED CIRCUITS APPLICATIONS PR     50  20   42  P
  05. NETWORK ANALYSIS               PP    100  40   67  P C    15. ELECTROMAGNETIC                 PP    100  40   80  P
  06. DIGITAL LOGIC DESIGN           PP    100  40   48  P C    16. ELECTROMAGNETIC                 TW     25  10   23  P
  07. DIGITAL LOGIC DESIGN           PR     50  20   42  P C    17. DATA STRUCTURES                 PP    100  40   86  P
  08. POWER DEVICES AND MACHINES     PP    100  40   64  P C    18. DATA STRUCTURES                 PR     50  20   40  P
  09. NETWORK AND POWER LAB.         TW     50  20   43  P C    19. COMMUNICATION THEORY            PP    100  40   53  P
  10. ELECTRONIC INSTRUMENTS AND TOOLS TW     50  20   43  P C    20. COMMUNICATION THEORY            OR     50  20   40  P
                                                                21. CIRCUIT SIMULATION AND TOOLS    TW     50  20   41  P
GRAND TOTAL = 1094/1500, RESULT: FIRST CLASS WITH DISTINCTION
ORDN. 1 MARKS :
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  S80053132    SHINDE VAISHALI GULAB              SHOBHA            , 71344373B    , S80053132 ,   PICT     , S80053132
  01. SIGNAL AND SYSTEMS              PP    100  40   64  P C    11. ENGINEERING MATHEMATICS III      PP    100  40   65  P
  02. SIGNAL AND SYSTEMS              OR     50  20   27  P C    12. ENGINEERING MATHEMATICS III      TW     25  10   20  P
  03. SOLID STATES DEVICES AND CIRCUITSPP    100  40   54  P C    13. INTEGRATED CIRCUITS APPLICATIONS PP    100  40   50  P
  04. SOLID STATES DEVICES AND CIRCUITSPR     50  20   28  P C    14. INTEGRATED CIRCUITS APPLICATIONS PR     50  20   32  P
  05. NETWORK ANALYSIS               PP    100  40   40  P C    15. ELECTROMAGNETIC                 PP    100  40   63  P
  06. DIGITAL LOGIC DESIGN           PP    100  40   53  P C    16. ELECTROMAGNETIC                 TW     25  10   20  P
  07. DIGITAL LOGIC DESIGN           PR     50  20   39  P C    17. DATA STRUCTURES                 PP    100  40   77  P
  08. POWER DEVICES AND MACHINES     PP    100  40   44  P C    18. DATA STRUCTURES                 PR     50  20   36  P
  09. NETWORK AND POWER LAB.         TW     50  20   39  P C    19. COMMUNICATION THEORY            PP    100  40   62  P
  10. ELECTRONIC INSTRUMENTS AND TOOLS TW     50  20   36  P C    20. COMMUNICATION THEORY            OR     50  20   30  P
                                                                21. CIRCUIT SIMULATION AND TOOLS    TW     50  20   38  P
GRAND TOTAL =  917/1500, RESULT: FIRST CLASS
ORDN. 1 MARKS :
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  S80053133    SONAWANE YASHWANT DEEPAK           SEEMA             , 71201079D    , S80053133 ,   PICT     , S80053133
  01. SIGNAL AND SYSTEMS              PP    100  40   43  P C    11. ENGINEERING MATHEMATICS III      PP    100  40   62  P
  02. SIGNAL AND SYSTEMS              OR     50  20   25  P C    12. ENGINEERING MATHEMATICS III      TW     25  10   17  P
  03. SOLID STATES DEVICES AND CIRCUITSPP    100  40   41  P C    13. INTEGRATED CIRCUITS APPLICATIONS PP    100  40   26  F
  04. SOLID STATES DEVICES AND CIRCUITSPR     50  20   20  P C    14. INTEGRATED CIRCUITS APPLICATIONS PR     50  20   38  P
  05. NETWORK ANALYSIS               PP    100  40   40  P      15. ELECTROMAGNETIC                 PP    100  40   53  P
  06. DIGITAL LOGIC DESIGN           PP    100  40   40  P C    16. ELECTROMAGNETIC                 TW     25  10   14  P
  07. DIGITAL LOGIC DESIGN           PR     50  20   40  P C    17. DATA STRUCTURES                 PP    100  40   58  P
  08. POWER DEVICES AND MACHINES     PP    100  40   40  P C    18. DATA STRUCTURES                 PR     50  20   31  P
  09. NETWORK AND POWER LAB.         TW     50  20   31  P C    19. COMMUNICATION THEORY            PP    100  40   61  P
  10. ELECTRONIC INSTRUMENTS AND TOOLS TW     50  20   33  P C    20. COMMUNICATION THEORY            OR     50  20   27  P
                                                                21. CIRCUIT SIMULATION AND TOOLS    TW     50  20   21  P
GRAND TOTAL =  761/1500, RESULT: FAILS A.T.K.T.
ORDN. 1 MARKS :
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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.
        OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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S80053134   SORATE TEJASWI TANAJI   NALINI   , 71344374L   , S80053134 ,   PICT   , S80053134

#	Subject	Head	Max	Min	Obt	P	C	#	Subject	Head	Max	Min	Obt	P
01.	SIGNAL AND SYSTEMS	PP	100	40	52	P	C	11.	ENGINEERING MATHEMATICS III	PP	100	40	66	P
02.	SIGNAL AND SYSTEMS	OR	50	20	25	P	C	12.	ENGINEERING MATHEMATICS III	TW	25	10	20	P
03.	SOLID STATES DEVICES AND CIRCUITS	PP	100	40	40	P	C	13.	INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	51	P
04.	SOLID STATES DEVICES AND CIRCUITS	PR	50	20	30	P	C	14.	INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	30	P
05.	NETWORK ANALYSIS	PP	100	40	40	P	C	15.	ELECTROMAGNETIC	PP	100	40	47	P
06.	DIGITAL LOGIC DESIGN	PP	100	40	46	P	C	16.	ELECTROMAGNETIC	TW	25	10	20	P
07.	DIGITAL LOGIC DESIGN	PR	50	20	39	P	C	17.	DATA STRUCTURES	PP	100	40	64	P
08.	POWER DEVICES AND MACHINES	PP	100	40	58	P	C	18.	DATA STRUCTURES	PR	50	20	35	P
09.	NETWORK AND POWER LAB.	TW	50	20	40	P	C	19.	COMMUNICATION THEORY	PP	100	40	70	P
10.	ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	38	P	C	20.	COMMUNICATION THEORY	OR	50	20	36	P
								21.	CIRCUIT SIMULATION AND TOOLS	TW	50	20	39	P

GRAND TOTAL = 886/1500, RESULT: HIGHER SECOND CLASS
ORDN. 1 MARKS :
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S80053135   SRISHTI GANJOO   NISHA   , 71201080H   , S80053135 ,   PICT   , S80053135

#	Subject	Head	Max	Min	Obt	P	C	#	Subject	Head	Max	Min	Obt	P
01.	SIGNAL AND SYSTEMS	PP	100	40	62	P	C	11.	ENGINEERING MATHEMATICS III	PP	100	40	73	P
02.	SIGNAL AND SYSTEMS	OR	50	20	35	P	C	12.	ENGINEERING MATHEMATICS III	TW	25	10	17	P
03.	SOLID STATES DEVICES AND CIRCUITS	PP	100	40	56	P	C	13.	INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	51	P
04.	SOLID STATES DEVICES AND CIRCUITS	PR	50	20	44	P	C	14.	INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	32	P
05.	NETWORK ANALYSIS	PP	100	40	40	P	C	15.	ELECTROMAGNETIC	PP	100	40	68	P
06.	DIGITAL LOGIC DESIGN	PP	100	40	54	P	C	16.	ELECTROMAGNETIC	TW	25	10	18	P
07.	DIGITAL LOGIC DESIGN	PR	50	20	40	P	C	17.	DATA STRUCTURES	PP	100	40	78	P
08.	POWER DEVICES AND MACHINES	PP	100	40	44	P	C	18.	DATA STRUCTURES	PR	50	20	39	P
09.	NETWORK AND POWER LAB.	TW	50	20	38	P	C	19.	COMMUNICATION THEORY	PP	100	40	65	P
10.	ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	36	P	C	20.	COMMUNICATION THEORY	OR	50	20	41	P
								21.	CIRCUIT SIMULATION AND TOOLS	TW	50	20	34	P

GRAND TOTAL = 965/1500, RESULT: FIRST CLASS
ORDN. 1 MARKS :
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S80053136   SUSHIL KUMAR   SHAKUNTALA DEVI   , 71201085J   , S80053136 ,   PICT   , S80053136

#	Subject	Head	Max	Min	Obt	P	C	#	Subject	Head	Max	Min	Obt	P
01.	SIGNAL AND SYSTEMS	PP	100	40	51	P	C	11.	ENGINEERING MATHEMATICS III	PP	100	40	79	P
02.	SIGNAL AND SYSTEMS	OR	50	20	33	P	C	12.	ENGINEERING MATHEMATICS III	TW	25	10	19	P
03.	SOLID STATES DEVICES AND CIRCUITS	PP	100	40	51	P	C	13.	INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	40	P
04.	SOLID STATES DEVICES AND CIRCUITS	PR	50	20	20	P	C	14.	INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	25	P
05.	NETWORK ANALYSIS	PP	100	40	59	P	C	15.	ELECTROMAGNETIC	PP	100	40	72	P
06.	DIGITAL LOGIC DESIGN	PP	100	40	45	P	C	16.	ELECTROMAGNETIC	TW	25	10	19	P
07.	DIGITAL LOGIC DESIGN	PR	50	20	30	P	C	17.	DATA STRUCTURES	PP	100	40	48	P
08.	POWER DEVICES AND MACHINES	PP	100	40	46	P	C	18.	DATA STRUCTURES	PR	50	20	27	P
09.	NETWORK AND POWER LAB.	TW	50	20	37	P	C	19.	COMMUNICATION THEORY	PP	100	40	66	P
10.	ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	36	P	C	20.	COMMUNICATION THEORY	OR	50	20	27	P
								21.	CIRCUIT SIMULATION AND TOOLS	TW	50	20	36	P

GRAND TOTAL = 866/1500, RESULT: HIGHER SECOND CLASS
ORDN. 1 MARKS :
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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
      OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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S80053137   TAMBE MAITREYEE MILIND   MRUDULA   , 71201090E   , S80053137 ,   PICT   , S80053137
01. SIGNAL AND SYSTEMS   PP   100 40   69 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   86 P
02. SIGNAL AND SYSTEMS   OR   50 20   40 P C   12. ENGINEERING MATHEMATICS III   TW   25 10   23 P
03. SOLID STATES DEVICES AND CIRCUITSPP   100 40   74 P C   13. INTEGRATED CIRCUITS APPLICATIONS PP   100 40   40 P
04. SOLID STATES DEVICES AND CIRCUITSPR   50 20   45 P C   14. INTEGRATED CIRCUITS APPLICATIONS PR   50 20   36 P
05. NETWORK ANALYSIS   PP   100 40   56 P C   15. ELECTROMAGNETIC   PP   100 40   72 P
06. DIGITAL LOGIC DESIGN   PP   100 40   54 P C   16. ELECTROMAGNETIC   TW   25 10   24 P
07. DIGITAL LOGIC DESIGN   PR   50 20   42 P C   17. DATA STRUCTURES   PP   100 40   73 P
08. POWER DEVICES AND MACHINES   PP   100 40   49 P C   18. DATA STRUCTURES   PR   50 20   45 P
09. NETWORK AND POWER LAB.   TW   50 20   45 P C   19. COMMUNICATION THEORY   PP   100 40   69 P
10. ELECTRONIC INSTRUMENTS AND TOOLS TW   50 20   45 P C   20. COMMUNICATION THEORY   OR   50 20   44 P
                                                             21. CIRCUIT SIMULATION AND TOOLS   TW   50 20   45 P
GRAND TOTAL = 1076/1500, RESULT: FIRST CLASS WITH DISTINCTION
ORDN. 1 MARKS :
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S80053138   TANDEL PRASAD SADANAND   SNEHA   , 71201092M   , S80053138 ,   PICT   , S80053138
01. SIGNAL AND SYSTEMS   PP   100 40   76 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   96 P
02. SIGNAL AND SYSTEMS   OR   50 20   28 P C   12. ENGINEERING MATHEMATICS III   TW   25 10   20 P
03. SOLID STATES DEVICES AND CIRCUITSPP   100 40   71 P C   13. INTEGRATED CIRCUITS APPLICATIONS PP   100 40   59 P
04. SOLID STATES DEVICES AND CIRCUITSPR   50 20   30 P C   14. INTEGRATED CIRCUITS APPLICATIONS PR   50 20   42 P
05. NETWORK ANALYSIS   PP   100 40   55 P C   15. ELECTROMAGNETIC   PP   100 40   63 P
06. DIGITAL LOGIC DESIGN   PP   100 40   60 P C   16. ELECTROMAGNETIC   TW   25 10   21 P
07. DIGITAL LOGIC DESIGN   PR   50 20   42 P C   17. DATA STRUCTURES   PP   100 40   78 P
08. POWER DEVICES AND MACHINES   PP   100 40   72 P C   18. DATA STRUCTURES   PR   50 20   31 P
09. NETWORK AND POWER LAB.   TW   50 20   41 P C   19. COMMUNICATION THEORY   PP   100 40   87 P
10. ELECTRONIC INSTRUMENTS AND TOOLS TW   50 20   41 P C   20. COMMUNICATION THEORY   OR   50 20   42 P
                                                             21. CIRCUIT SIMULATION AND TOOLS   TW   50 20   38 P
GRAND TOTAL = 1093/1500, RESULT: FIRST CLASS WITH DISTINCTION
ORDN. 1 MARKS :
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S80053139   TATHE SAKET NANDKISHOR   MEENA   , 71201093K   , S80053139 ,   PICT   , S80053139
01. SIGNAL AND SYSTEMS   PP   100 40   54 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   70 P
02. SIGNAL AND SYSTEMS   OR   50 20   26 P C   12. ENGINEERING MATHEMATICS III   TW   25 10   16 P
03. SOLID STATES DEVICES AND CIRCUITSPP   100 40   57 P C   13. INTEGRATED CIRCUITS APPLICATIONS PP   100 40   49 P
04. SOLID STATES DEVICES AND CIRCUITSPR   50 20   20 P C   14. INTEGRATED CIRCUITS APPLICATIONS PR   50 20   36 P
05. NETWORK ANALYSIS   PP   100 40   47 P   15. ELECTROMAGNETIC   PP   100 40   52 P
06. DIGITAL LOGIC DESIGN   PP   100 40   40 P C   16. ELECTROMAGNETIC   TW   25 10   15 P
07. DIGITAL LOGIC DESIGN   PR   50 20   39 P C   17. DATA STRUCTURES   PP   100 40   69 P
08. POWER DEVICES AND MACHINES   PP   100 40   54 P C   18. DATA STRUCTURES   PR   50 20   29 P
09. NETWORK AND POWER LAB.   TW   50 20   36 P C   19. COMMUNICATION THEORY   PP   100 40   55 P
10. ELECTRONIC INSTRUMENTS AND TOOLS TW   50 20   37 P C   20. COMMUNICATION THEORY   OR   50 20   27 P
                                                             21. CIRCUIT SIMULATION AND TOOLS   TW   50 20   28 P
GRAND TOTAL = 856/1500, RESULT: HIGHER SECOND CLASS
ORDN. 1 MARKS :
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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.
       OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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S80053140   TIKHE SHIRIN ABHAY   SWATI   , 71201096D   , S80053140 ,   PICT   , S80053140
01. SIGNAL AND SYSTEMS   PP   100 40   66 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   86 P
02. SIGNAL AND SYSTEMS   OR   50 20   24 P C   12. ENGINEERING MATHEMATICS III   TW   25 10   17 P
03. SOLID STATES DEVICES AND CIRCUITSPP   100 40   40 P C   13. INTEGRATED CIRCUITS APPLICATIONS PP   100 40   40 P
04. SOLID STATES DEVICES AND CIRCUITSPR   50 20   32 P C   14. INTEGRATED CIRCUITS APPLICATIONS PR   50 20   34 P
05. NETWORK ANALYSIS   PP   100 40   47 P C   15. ELECTROMAGNETIC   PP   100 40   66 P
06. DIGITAL LOGIC DESIGN   PP   100 40   40 P C   16. ELECTROMAGNETIC   TW   25 10   18 P
07. DIGITAL LOGIC DESIGN   PR   50 20   42 P C   17. DATA STRUCTURES   PP   100 40   61 P
08. POWER DEVICES AND MACHINES   PP   100 40   52 P   18. DATA STRUCTURES   PR   50 20   37 P
09. NETWORK AND POWER LAB.   TW   50 20   41 P C   19. COMMUNICATION THEORY   PP   100 40   70 P
10. ELECTRONIC INSTRUMENTS AND TOOLS TW   50 20   38 P C   20. COMMUNICATION THEORY   OR   50 20   27 P
                                                             21. CIRCUIT SIMULATION AND TOOLS   TW   50 20   36 P
GRAND TOTAL = 914/1500, RESULT: FIRST CLASS
ORDN. 1 MARKS :
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S80053141   TUPE NITIN BABASAHEB   SANJIVANI   , 71201098L   , S80053141 ,   PICT   , S80053141
01. SIGNAL AND SYSTEMS   PP   100 40   52 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   21 F
02. SIGNAL AND SYSTEMS   OR   50 20   21 P C   12. ENGINEERING MATHEMATICS III   TW   25 10   19 P
03. SOLID STATES DEVICES AND CIRCUITSPP   100 40   47 P C   13. INTEGRATED CIRCUITS APPLICATIONS PP   100 40   40 P
04. SOLID STATES DEVICES AND CIRCUITSPR   50 20   22 P C   14. INTEGRATED CIRCUITS APPLICATIONS PR   50 20   13 F
05. NETWORK ANALYSIS   PP   100 40   40 P C   15. ELECTROMAGNETIC   PP   100 40   57 P
06. DIGITAL LOGIC DESIGN   PP   100 40   50 P C   16. ELECTROMAGNETIC   TW   25 10   19 P
07. DIGITAL LOGIC DESIGN   PR   50 20   38 P   17. DATA STRUCTURES   PP   100 40   AA F
08. POWER DEVICES AND MACHINES   PP   100 40   40 P C   18. DATA STRUCTURES   PR   50 20   AA F
09. NETWORK AND POWER LAB.   TW   50 20   37 P C   19. COMMUNICATION THEORY   PP   100 40   40 P
10. ELECTRONIC INSTRUMENTS AND TOOLS TW   50 20   41 P C   20. COMMUNICATION THEORY   OR   50 20   29 P
                                                             21. CIRCUIT SIMULATION AND TOOLS   TW   50 20   27 P
GRAND TOTAL = 653/1500, RESULT: FAILS A.T.K.T.
ORDN. 1 MARKS :
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S80053142   UNDE MADHURI BALASAHEB   LAXMI   , 71344375J   , S80053142 ,   PICT   , S80053142
01. SIGNAL AND SYSTEMS   PP   100 40   40 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   40 P
02. SIGNAL AND SYSTEMS   OR   50 20   21 P   12. ENGINEERING MATHEMATICS III   TW   25 10   17 P
03. SOLID STATES DEVICES AND CIRCUITSPP   100 40   54 P   13. INTEGRATED CIRCUITS APPLICATIONS PP   100 40   40 P
04. SOLID STATES DEVICES AND CIRCUITSPR   50 20   21 P C   14. INTEGRATED CIRCUITS APPLICATIONS PR   50 20   25 P
05. NETWORK ANALYSIS   PP   100 40   40 P   15. ELECTROMAGNETIC   PP   100 40   41 P
06. DIGITAL LOGIC DESIGN   PP   100 40   58 P   16. ELECTROMAGNETIC   TW   25 10   18 P
07. DIGITAL LOGIC DESIGN   PR   50 20   25 P   17. DATA STRUCTURES   PP   100 40   40 P
08. POWER DEVICES AND MACHINES   PP   100 40   46 P   18. DATA STRUCTURES   PR   50 20   22 P
09. NETWORK AND POWER LAB.   TW   50 20   39 P C   19. COMMUNICATION THEORY   PP   100 40   40 P
10. ELECTRONIC INSTRUMENTS AND TOOLS TW   50 20   38 P C   20. COMMUNICATION THEORY   OR   50 20   27 P
                                                             21. CIRCUIT SIMULATION AND TOOLS   TW   50 20   31 P
GRAND TOTAL = 723/1500, RESULT: PASS CLASS
ORDN. 1 MARKS :
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  NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.
        OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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  S80053143   UPADHYE VEDA UDAY   MANISHA   , 71201101D   , S80053143 ,   PICT   , S80053143
  01. SIGNAL AND SYSTEMS   PP   100 40   64 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   90 P
  02. SIGNAL AND SYSTEMS   OR   50 20   35 P C   12. ENGINEERING MATHEMATICS III   TW   25 10   19 P
  03. SOLID STATES DEVICES AND CIRCUITSPP   100 40   62 P C   13. INTEGRATED CIRCUITS APPLICATIONS PP   100 40   45 P
  04. SOLID STATES DEVICES AND CIRCUITSPR   50 20   36 P C   14. INTEGRATED CIRCUITS APPLICATIONS PR   50 20   45 P
  05. NETWORK ANALYSIS   PP   100 40   77 P C   15. ELECTROMAGNETIC   PP   100 40   75 P
  06. DIGITAL LOGIC DESIGN   PP   100 40   60 P C   16. ELECTROMAGNETIC   TW   25 10   21 P
  07. DIGITAL LOGIC DESIGN   PR   50 20   36 P C   17. DATA STRUCTURES   PP   100 40   71 P
  08. POWER DEVICES AND MACHINES   PP   100 40   49 P C   18. DATA STRUCTURES   PR   50 20   39 P
  09. NETWORK AND POWER LAB.   TW   50 20   39 P C   19. COMMUNICATION THEORY   PP   100 40   88 P
  10. ELECTRONIC INSTRUMENTS AND TOOLS TW   50 20   38 P C   20. COMMUNICATION THEORY   OR   50 20   30 P
                                                                 21. CIRCUIT SIMULATION AND TOOLS   TW   50 20   39 P
GRAND TOTAL = 1058/1500, RESULT: FIRST CLASS WITH DISTINCTION
ORDN. 1 MARKS :
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  S80053144   USTAD SABA ABDUL SHAKOOR   ATIYA   , 71201102B   , S80053144 ,   PICT   , S80053144
  01. SIGNAL AND SYSTEMS   PP   100 40   75 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   97 P
  02. SIGNAL AND SYSTEMS   OR   50 20   43 P C   12. ENGINEERING MATHEMATICS III   TW   25 10   23 P
  03. SOLID STATES DEVICES AND CIRCUITSPP   100 40   76 P C   13. INTEGRATED CIRCUITS APPLICATIONS PP   100 40   52 P
  04. SOLID STATES DEVICES AND CIRCUITSPR   50 20   42 P C   14. INTEGRATED CIRCUITS APPLICATIONS PR   50 20   35 P
  05. NETWORK ANALYSIS   PP   100 40   66 P C   15. ELECTROMAGNETIC   PP   100 40   92 P
  06. DIGITAL LOGIC DESIGN   PP   100 40   70 P C   16. ELECTROMAGNETIC   TW   25 10   23 P
  07. DIGITAL LOGIC DESIGN   PR   50 20   38 P C   17. DATA STRUCTURES   PP   100 40   78 P
  08. POWER DEVICES AND MACHINES   PP   100 40   45 P C   18. DATA STRUCTURES   PR   50 20   36 P
  09. NETWORK AND POWER LAB.   TW   50 20   43 P C   19. COMMUNICATION THEORY   PP   100 40   67 P
  10. ELECTRONIC INSTRUMENTS AND TOOLS TW   50 20   44 P C   20. COMMUNICATION THEORY   OR   50 20   40 P
                                                                 21. CIRCUIT SIMULATION AND TOOLS   TW   50 20   44 P
GRAND TOTAL = 1129/1500, RESULT: FIRST CLASS WITH DISTINCTION
ORDN. 1 MARKS :
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  S80053145   VADGAONKAR MANALI ABHAY   ROOPA   , 71201103L   , S80053145 ,   PICT   , S80053145
  01. SIGNAL AND SYSTEMS   PP   100 40   49 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   16 F
  02. SIGNAL AND SYSTEMS   OR   50 20   28 P C   12. ENGINEERING MATHEMATICS III   TW   25 10   13 P
  03. SOLID STATES DEVICES AND CIRCUITSPP   100 40   40 P C   13. INTEGRATED CIRCUITS APPLICATIONS PP   100 40   17 F
  04. SOLID STATES DEVICES AND CIRCUITSPR   50 20   35 P C   14. INTEGRATED CIRCUITS APPLICATIONS PR   50 20   12 F
  05. NETWORK ANALYSIS   PP   100 40   AA F   15. ELECTROMAGNETIC   PP   100 40   27 F
  06. DIGITAL LOGIC DESIGN   PP   100 40   28 F   16. ELECTROMAGNETIC   TW   25 10   15 P
  07. DIGITAL LOGIC DESIGN   PR   50 20   38 P C   17. DATA STRUCTURES   PP   100 40   40 P
  08. POWER DEVICES AND MACHINES   PP   100 40   10 F   18. DATA STRUCTURES   PR   50 20   36 P
  09. NETWORK AND POWER LAB.   TW   50 20   37 P C   19. COMMUNICATION THEORY   PP   100 40   25 F
  10. ELECTRONIC INSTRUMENTS AND TOOLS TW   50 20   34 P C   20. COMMUNICATION THEORY   OR   50 20   25 P
                                                                 21. CIRCUIT SIMULATION AND TOOLS   TW   50 20   32 P
GRAND TOTAL = 557/1500, RESULT: FAILS
ORDN. 1 MARKS :
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NOTE: FIRST LINE : SEAT NO.,  NAME OF THE CANDIDATE,  MOTHER,  PERMANENT REG. NO.,  PREVIOUS SEAT NO.,  COLLEGE,   SEAT NO.
       OTHER LINES: HEAD OF PASSING,  MAX. MARKS,  MIN. PASS MARKS,  MARKS OBTAINED,  P/F:PASS/FAIL,  C:PREVIOUS CARRY OVER
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  S80053146   VIKRANT SINGH                    SUMITRA           , 71201110C    , S80053146 ,   PICT     , S80053146
  01. SIGNAL AND SYSTEMS             PP    100 40   59  P C    11. ENGINEERING MATHEMATICS III     PP    100 40   AA  F
  02. SIGNAL AND SYSTEMS             OR     50 20   30  P C    12. ENGINEERING MATHEMATICS III     TW     25 10   12  P
  03. SOLID STATES DEVICES AND CIRCUITSPP   100 40   51  P C    13. INTEGRATED CIRCUITS APPLICATIONS PP   100 40   24  F
  04. SOLID STATES DEVICES AND CIRCUITSPR    50 20   20  P C    14. INTEGRATED CIRCUITS APPLICATIONS PR    50 20   24  P
  05. NETWORK ANALYSIS               PP    100 40   40  P C    15. ELECTROMAGNETIC                  PP    100 40   49  P
  06. DIGITAL LOGIC DESIGN           PP    100 40   45  P      16. ELECTROMAGNETIC                  TW     25 10   11  P
  07. DIGITAL LOGIC DESIGN           PR     50 20   32  P C    17. DATA STRUCTURES                 PP    100 40   22  F
  08. POWER DEVICES AND MACHINES     PP    100 40   43  P C    18. DATA STRUCTURES                 PR     50 20   30  P
  09. NETWORK AND POWER LAB.         TW     50 20   32  P C    19. COMMUNICATION THEORY            PP    100 40   69  P
  10. ELECTRONIC INSTRUMENTS AND TOOLS TW   50 20   32  P C    20. COMMUNICATION THEORY            OR     50 20   10  F
                                                              21. CIRCUIT SIMULATION AND TOOLS   TW     50 20   20  P
GRAND TOTAL =  655/1500, RESULT: FAILS A.T.K.T.
ORDN. 1 MARKS :
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  S80053147   VISHAL VATSA                     ASHA              , 71201111M    , S80053147 ,   PICT     , S80053147
  01. SIGNAL AND SYSTEMS             PP    100 40   61  P C    11. ENGINEERING MATHEMATICS III     PP    100 40   83  P
  02. SIGNAL AND SYSTEMS             OR     50 20   29  P C    12. ENGINEERING MATHEMATICS III     TW     25 10   17  P
  03. SOLID STATES DEVICES AND CIRCUITSPP   100 40   48  P C    13. INTEGRATED CIRCUITS APPLICATIONS PP   100 40   45  P
  04. SOLID STATES DEVICES AND CIRCUITSPR    50 20   36  P C    14. INTEGRATED CIRCUITS APPLICATIONS PR    50 20   40  P
  05. NETWORK ANALYSIS               PP    100 40   54  P C    15. ELECTROMAGNETIC                  PP    100 40   68  P
  06. DIGITAL LOGIC DESIGN           PP    100 40   48  P C    16. ELECTROMAGNETIC                  TW     25 10   17  P
  07. DIGITAL LOGIC DESIGN           PR     50 20   41  P C    17. DATA STRUCTURES                 PP    100 40   62  P
  08. POWER DEVICES AND MACHINES     PP    100 40   40  P C    18. DATA STRUCTURES                 PR     50 20   33  P
  09. NETWORK AND POWER LAB.         TW     50 20   39  P C    19. COMMUNICATION THEORY            PP    100 40   50  P
  10. ELECTRONIC INSTRUMENTS AND TOOLS TW   50 20   35  P C    20. COMMUNICATION THEORY            OR     50 20   20  P
                                                              21. CIRCUIT SIMULATION AND TOOLS   TW     50 20   35  P
GRAND TOTAL =  901/1500, RESULT: FIRST CLASS
ORDN. 1 MARKS :
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  S80053148   WADIKAR MUGDHA RAVINDRA          MEENAL            , 71201113H    , S80053148 ,   PICT     , S80053148
  01. SIGNAL AND SYSTEMS             PP    100 40   63  P C    11. ENGINEERING MATHEMATICS III     PP    100 40   78  P
  02. SIGNAL AND SYSTEMS             OR     50 20   31  P C    12. ENGINEERING MATHEMATICS III     TW     25 10   18  P
  03. SOLID STATES DEVICES AND CIRCUITSPP   100 40   52  P C    13. INTEGRATED CIRCUITS APPLICATIONS PP   100 40   40  P
  04. SOLID STATES DEVICES AND CIRCUITSPR    50 20   35  P C    14. INTEGRATED CIRCUITS APPLICATIONS PR    50 20   35  P
  05. NETWORK ANALYSIS               PP    100 40   46  P C    15. ELECTROMAGNETIC                  PP    100 40   72  P
  06. DIGITAL LOGIC DESIGN           PP    100 40   40  P C    16. ELECTROMAGNETIC                  TW     25 10   17  P
  07. DIGITAL LOGIC DESIGN           PR     50 20   37  P C    17. DATA STRUCTURES                 PP    100 40   74  P
  08. POWER DEVICES AND MACHINES     PP    100 40   46  P C    18. DATA STRUCTURES                 PR     50 20   39  P
  09. NETWORK AND POWER LAB.         TW     50 20   34  P C    19. COMMUNICATION THEORY            PP    100 40   54  P
  10. ELECTRONIC INSTRUMENTS AND TOOLS TW   50 20   37  P C    20. COMMUNICATION THEORY            OR     50 20   30  P
                                                              21. CIRCUIT SIMULATION AND TOOLS   TW     50 20   33  P
GRAND TOTAL =  911/1500, RESULT: FIRST CLASS
ORDN. 1 MARKS :
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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.
        OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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 S80053149   WARPE HRUSHEEKESH SUNIL              SMITA              , 71201119G   , S80053149 ,   PICT     , S80053149
 01. SIGNAL AND SYSTEMS              PP   100  40   59  P C     11. ENGINEERING MATHEMATICS III     PP   100  40   78  P
 02. SIGNAL AND SYSTEMS              OR    50  20   35  P C     12. ENGINEERING MATHEMATICS III     TW    25  10   20  P
 03. SOLID STATES DEVICES AND CIRCUITSPP   100  40   66  P C     13. INTEGRATED CIRCUITS APPLICATIONS PP   100  40   47  P
 04. SOLID STATES DEVICES AND CIRCUITSPR    50  20   32  P C     14. INTEGRATED CIRCUITS APPLICATIONS PR    50  20   34  P
 05. NETWORK ANALYSIS               PP   100  40   48  P C     15. ELECTROMAGNETIC                 PP   100  40   75  P
 06. DIGITAL LOGIC DESIGN           PP   100  40   45  P C     16. ELECTROMAGNETIC                 TW    25  10   19  P
 07. DIGITAL LOGIC DESIGN           PR    50  20   40  P C     17. DATA STRUCTURES                 PP   100  40   60  P
 08. POWER DEVICES AND MACHINES     PP   100  40   51  P C     18. DATA STRUCTURES                 PR    50  20   34  P
 09. NETWORK AND POWER LAB.         TW    50  20   40  P C     19. COMMUNICATION THEORY            PP   100  40   55  P
 10. ELECTRONIC INSTRUMENTS AND TOOLS TW    50  20   41  P C     20. COMMUNICATION THEORY            OR    50  20   38  P
                                                              21. CIRCUIT SIMULATION AND TOOLS   TW    50  20   33  P
GRAND TOTAL =  950/1500, RESULT: FIRST CLASS
ORDN. 1 MARKS :
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 S80053150   WAYKOS ABHIJIT JINCHANDRA            NAYANA             , 71201120L   , S80053150 ,   PICT     , S80053150
 01. SIGNAL AND SYSTEMS              PP   100  40   81  P C     11. ENGINEERING MATHEMATICS III     PP   100  40   86  P
 02. SIGNAL AND SYSTEMS              OR    50  20   38  P C     12. ENGINEERING MATHEMATICS III     TW    25  10   19  P
 03. SOLID STATES DEVICES AND CIRCUITSPP   100  40   58  P C     13. INTEGRATED CIRCUITS APPLICATIONS PP   100  40   57  P
 04. SOLID STATES DEVICES AND CIRCUITSPR    50  20   33  P C     14. INTEGRATED CIRCUITS APPLICATIONS PR    50  20   38  P
 05. NETWORK ANALYSIS               PP   100  40   55  P C     15. ELECTROMAGNETIC                 PP   100  40   67  P
 06. DIGITAL LOGIC DESIGN           PP   100  40   40  P C     16. ELECTROMAGNETIC                 TW    25  10   19  P
 07. DIGITAL LOGIC DESIGN           PR    50  20   40  P C     17. DATA STRUCTURES                 PP   100  40   72  P
 08. POWER DEVICES AND MACHINES     PP   100  40   55  P C     18. DATA STRUCTURES                 PR    50  20   37  P
 09. NETWORK AND POWER LAB.         TW    50  20   40  P C     19. COMMUNICATION THEORY            PP   100  40   79  P
 10. ELECTRONIC INSTRUMENTS AND TOOLS TW    50  20   38  P C     20. COMMUNICATION THEORY            OR    50  20   35  P
                                                              21. CIRCUIT SIMULATION AND TOOLS   TW    50  20   39  P
GRAND TOTAL = 1026/1500, RESULT: FIRST CLASS WITH DISTINCTION
ORDN. 1 MARKS :
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 S80053151   YEMULA AKHIL                         MANJULA            , 71201123E   , S80053151 ,   PICT     , S80053151
 01. SIGNAL AND SYSTEMS              PP   100  40   68  P C     11. ENGINEERING MATHEMATICS III     PP   100  40   66  P
 02. SIGNAL AND SYSTEMS              OR    50  20   21  P C     12. ENGINEERING MATHEMATICS III     TW    25  10   17  P
 03. SOLID STATES DEVICES AND CIRCUITSPP   100  40   40  P C     13. INTEGRATED CIRCUITS APPLICATIONS PP   100  40   40  P
 04. SOLID STATES DEVICES AND CIRCUITSPR    50  20   20  P C     14. INTEGRATED CIRCUITS APPLICATIONS PR    50  20   24  P
 05. NETWORK ANALYSIS               PP   100  40   40  P C     15. ELECTROMAGNETIC                 PP   100  40   66  P
 06. DIGITAL LOGIC DESIGN           PP   100  40   42  P C     16. ELECTROMAGNETIC                 TW    25  10   17  P
 07. DIGITAL LOGIC DESIGN           PR    50  20   32  P C     17. DATA STRUCTURES                 PP   100  40   65  P
 08. POWER DEVICES AND MACHINES     PP   100  40   40  P C     18. DATA STRUCTURES                 PR    50  20   36  P
 09. NETWORK AND POWER LAB.         TW    50  20   35  P C     19. COMMUNICATION THEORY            PP   100  40   52  P
 10. ELECTRONIC INSTRUMENTS AND TOOLS TW    50  20   36  P C     20. COMMUNICATION THEORY            OR    50  20   25  P
                                                              21. CIRCUIT SIMULATION AND TOOLS   TW    50  20   29  P
GRAND TOTAL =  811/1500, RESULT: SECOND CLASS
ORDN. 1 MARKS :
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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.
        OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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S80053153   AGRAWAL PRANAT ADITYA   ASHA   , 71100721H   , S80053153 ,   PICT   , S80053153

01. SIGNAL AND SYSTEMS	PP	100	40	47	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	68	P	
02. SIGNAL AND SYSTEMS	OR	50	20	28	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	17	P C	
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	40	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	42	P C	
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	20	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	23	P C	
05. NETWORK ANALYSIS	PP	100	40	40	P C	15. ELECTROMAGNETIC	PP	100	40	40	P C	
06. DIGITAL LOGIC DESIGN	PP	100	40	40	P C	16. ELECTROMAGNETIC	TW	25	10	17	P C	
07. DIGITAL LOGIC DESIGN	PR	50	20	30	P C	17. DATA STRUCTURES	PP	100	40	45	P C	
08. POWER DEVICES AND MACHINES	PP	100	40	40	P C	18. DATA STRUCTURES	PR	50	20	37	P C	
09. NETWORK AND POWER LAB.	TW	50	20	33	P C	19. COMMUNICATION THEORY	PP	100	40	66	P	
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	30	P C	20. COMMUNICATION THEORY	OR	50	20	21	P C	
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	36	P C	

GRAND TOTAL = 760/1500, RESULT: SECOND CLASS
ORDN. 1 MARKS :
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S80053154   ALI ASGER MODI   ZEENAT   , 71100724B   , S80053154 ,   PICT   , S80053154

01. SIGNAL AND SYSTEMS	PP	100	40	41	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	40	P C	
02. SIGNAL AND SYSTEMS	OR	50	20	22	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	14	P C	
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	40	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	40	P C	
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	30	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	25	P C	
05. NETWORK ANALYSIS	PP	100	40	40	P C	15. ELECTROMAGNETIC	PP	100	40	26	F	
06. DIGITAL LOGIC DESIGN	PP	100	40	42	P C	16. ELECTROMAGNETIC	TW	25	10	15	P C	
07. DIGITAL LOGIC DESIGN	PR	50	20	25	P C	17. DATA STRUCTURES	PP	100	40	54	P C	
08. POWER DEVICES AND MACHINES	PP	100	40	43	P C	18. DATA STRUCTURES	PR	50	20	37	P C	
09. NETWORK AND POWER LAB.	TW	50	20	34	P C	19. COMMUNICATION THEORY	PP	100	40	40	P C	
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	30	P C	20. COMMUNICATION THEORY	OR	50	20	25	P C	
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	30	P C	

GRAND TOTAL = 693/1500, RESULT: FAILS A.T.K.T.
ORDN. 1 MARKS :
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S80053156   BORSE SWATI NANA   KALPANA   , 71100754D   , S80053156 ,   PICT   , S80053156

01. SIGNAL AND SYSTEMS	PP	100	40	46	P	11. ENGINEERING MATHEMATICS III	PP	100	40	40	P C	
02. SIGNAL AND SYSTEMS	OR	50	20	38	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	18	P C	
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	43	P	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	40	P C	
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	23	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	20	P C	
05. NETWORK ANALYSIS	PP	100	40	AA	F	15. ELECTROMAGNETIC	PP	100	40	40	P	
06. DIGITAL LOGIC DESIGN	PP	100	40	40	P C	16. ELECTROMAGNETIC	TW	25	10	17	P C	
07. DIGITAL LOGIC DESIGN	PR	50	20	20	P C	17. DATA STRUCTURES	PP	100	40	45	P C	
08. POWER DEVICES AND MACHINES	PP	100	40	40	P C	18. DATA STRUCTURES	PR	50	20	32	P C	
09. NETWORK AND POWER LAB.	TW	50	20	27	P C	19. COMMUNICATION THEORY	PP	100	40	44	P C	
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	32	P C	20. COMMUNICATION THEORY	OR	50	20	20	P C	
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	33	P C	

GRAND TOTAL = 658/1500, RESULT: FAILS A.T.K.T.
ORDN. 1 MARKS :
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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.
        OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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 S80053157   BUDHWANT UMA SHYAMRAO                 NANDINI          , 71100757J    , S80053157 ,   PICT     , S80053157
 01. SIGNAL AND SYSTEMS              PP   100  40   41  P C     11. ENGINEERING MATHEMATICS III     PP   100  40   56  P
 02. SIGNAL AND SYSTEMS              OR    50  20   27  P C     12. ENGINEERING MATHEMATICS III     TW    25  10   16  P C
 03. SOLID STATES DEVICES AND CIRCUITSPP  100  40   45  P C     13. INTEGRATED CIRCUITS APPLICATIONS PP   100  40   40  P C
 04. SOLID STATES DEVICES AND CIRCUITSPR   50  20   28  P C     14. INTEGRATED CIRCUITS APPLICATIONS PR    50  20   28  P C
 05. NETWORK ANALYSIS                PP   100  40   40  P C     15. ELECTROMAGNETIC                 PP   100  40   40  P C
 06. DIGITAL LOGIC DESIGN            PP   100  40   43  P C     16. ELECTROMAGNETIC                 TW    25  10   17  P C
 07. DIGITAL LOGIC DESIGN            PR    50  20   28  P C     17. DATA STRUCTURES                 PP   100  40   51  P C
 08. POWER DEVICES AND MACHINES      PP   100  40   41  P C     18. DATA STRUCTURES                 PR    50  20   33  P C
 09. NETWORK AND POWER LAB.          TW    50  20   38  P C     19. COMMUNICATION THEORY            PP   100  40   44  P C
 10. ELECTRONIC INSTRUMENTS AND TOOLS TW   50  20   34  P C     20. COMMUNICATION THEORY            OR    50  20   25  P C
                                                               21. CIRCUIT SIMULATION AND TOOLS   TW    50  20   32  P C
GRAND TOTAL =  747+03/1500, RESULT: SECOND CLASS  [O.2]
ORDN. 1 MARKS :
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 S80053158   CHANDAK ANKIT PANDURANGJI             MAYA             , 71100760J    , S80053158 ,   PICT     , S80053158
 01. SIGNAL AND SYSTEMS              PP   100  40   45  P C     11. ENGINEERING MATHEMATICS III     PP   100  40   44  P
 02. SIGNAL AND SYSTEMS              OR    50  20   20  P C     12. ENGINEERING MATHEMATICS III     TW    25  10   18  P C
 03. SOLID STATES DEVICES AND CIRCUITSPP  100  40   48  P C     13. INTEGRATED CIRCUITS APPLICATIONS PP   100  40   40  P C
 04. SOLID STATES DEVICES AND CIRCUITSPR   50  20   40  P C     14. INTEGRATED CIRCUITS APPLICATIONS PR    50  20   36  P C
 05. NETWORK ANALYSIS                PP   100  40   25  F       15. ELECTROMAGNETIC                 PP   100  40   40  P
 06. DIGITAL LOGIC DESIGN            PP   100  40   60  P C     16. ELECTROMAGNETIC                 TW    25  10   18  P C
 07. DIGITAL LOGIC DESIGN            PR    50  20   35  P C     17. DATA STRUCTURES                 PP   100  40   43  P C
 08. POWER DEVICES AND MACHINES      PP   100  40   23  F       18. DATA STRUCTURES                 PR    50  20   25  P C
 09. NETWORK AND POWER LAB.          TW    50  20   37  P C     19. COMMUNICATION THEORY            PP   100  40   45  P C
 10. ELECTRONIC INSTRUMENTS AND TOOLS TW   50  20   35  P C     20. COMMUNICATION THEORY            OR    50  20   21  P C
                                                               21. CIRCUIT SIMULATION AND TOOLS   TW    50  20   37  P C
GRAND TOTAL =  735/1500, RESULT: FAILS A.T.K.T.
ORDN. 1 MARKS :
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 S80053159   CHANDAN PANCHLORIA                    SUMITRADEVI      , 71100761G    , S80053159 ,   PICT     , S80053159
 01. SIGNAL AND SYSTEMS              PP   100  40   40  P C     11. ENGINEERING MATHEMATICS III     PP   100  40   59  P C
 02. SIGNAL AND SYSTEMS              OR    50  20   30  P C     12. ENGINEERING MATHEMATICS III     TW    25  10   19  P C
 03. SOLID STATES DEVICES AND CIRCUITSPP  100  40   40  P C     13. INTEGRATED CIRCUITS APPLICATIONS PP   100  40   48  P C
 04. SOLID STATES DEVICES AND CIRCUITSPR   50  20   30  P C     14. INTEGRATED CIRCUITS APPLICATIONS PR    50  20   34  P C
 05. NETWORK ANALYSIS                PP   100  40   40  P C     15. ELECTROMAGNETIC                 PP   100  40   45  P
 06. DIGITAL LOGIC DESIGN            PP   100  40   45  P C     16. ELECTROMAGNETIC                 TW    25  10   20  P C
 07. DIGITAL LOGIC DESIGN            PR    50  20   22  P C     17. DATA STRUCTURES                 PP   100  40   57  P C
 08. POWER DEVICES AND MACHINES      PP   100  40   40  P C     18. DATA STRUCTURES                 PR    50  20   36  P C
 09. NETWORK AND POWER LAB.          TW    50  20   39  P C     19. COMMUNICATION THEORY            PP   100  40   40  P C
 10. ELECTRONIC INSTRUMENTS AND TOOLS TW   50  20   39  P C     20. COMMUNICATION THEORY            OR    50  20   30  P C
                                                               21. CIRCUIT SIMULATION AND TOOLS   TW    50  20   38  P C
GRAND TOTAL =  791/1500, RESULT: SECOND CLASS
ORDN. 1 MARKS :
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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.

          OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S80053160   DESHMUKH ASHUTOSH VILASRAO            SUNITA            , 71100780C    , S80053160 ,   PICT     , S80053160
01. SIGNAL AND SYSTEMS              PP    100  40   49  P C   11. ENGINEERING MATHEMATICS III      PP    100  40   54  P C
02. SIGNAL AND SYSTEMS              OR     50  20   22  P C   12. ENGINEERING MATHEMATICS III      TW     25  10   11  P C
03. SOLID STATES DEVICES AND CIRCUITSPP    100  40   41  P C   13. INTEGRATED CIRCUITS APPLICATIONS PP    100  40   40  P
04. SOLID STATES DEVICES AND CIRCUITSPR     50  20   24  P C   14. INTEGRATED CIRCUITS APPLICATIONS PR     50  20   20  P
05. NETWORK ANALYSIS               PP    100  40   46  P C   15. ELECTROMAGNETIC                  PP    100  40   48  P
06. DIGITAL LOGIC DESIGN           PP    100  40   40  P C   16. ELECTROMAGNETIC                  TW     25  10   11  P C
07. DIGITAL LOGIC DESIGN           PR     50  20   27  P C   17. DATA STRUCTURES                  PP    100  40   AA  F
08. POWER DEVICES AND MACHINES     PP    100  40   40  P C   18. DATA STRUCTURES                  PR     50  20   30  P C
09. NETWORK AND POWER LAB.         TW     50  20   29  P C   19. COMMUNICATION THEORY             PP    100  40   40  P C
10. ELECTRONIC INSTRUMENTS AND TOOLS TW     50  20   25  P C   20. COMMUNICATION THEORY             OR     50  20   20  P C
                                                             21. CIRCUIT SIMULATION AND TOOLS    TW     50  20   24  P C
GRAND TOTAL =  641/1500, RESULT: FAILS A.T.K.T.
ORDN. 1 MARKS :
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S80053161   DHODI SHRADDHA MADHU                 ARUNA             , 71045415F    , S80053161 ,   PICT     , S80053161
01. SIGNAL AND SYSTEMS              PP    100  40   49  P C   11. ENGINEERING MATHEMATICS III      PP    100  40   40  P C
02. SIGNAL AND SYSTEMS              OR     50  20   25  P C   12. ENGINEERING MATHEMATICS III      TW     25  10   18  P C
03. SOLID STATES DEVICES AND CIRCUITSPP    100  40   40  P     13. INTEGRATED CIRCUITS APPLICATIONS PP    100  40   23  F
04. SOLID STATES DEVICES AND CIRCUITSPR     50  20   24  P C   14. INTEGRATED CIRCUITS APPLICATIONS PR     50  20   27  P C
05. NETWORK ANALYSIS               PP    100  40   40  P     15. ELECTROMAGNETIC                  PP    100  40   40  P
06. DIGITAL LOGIC DESIGN           PP    100  40   46  P C   16. ELECTROMAGNETIC                  TW     25  10   16  P C
07. DIGITAL LOGIC DESIGN           PR     50  20   22  P C   17. DATA STRUCTURES                  PP    100  40   40  P C
08. POWER DEVICES AND MACHINES     PP    100  40   17  F     18. DATA STRUCTURES                  PR     50  20   39  P C
09. NETWORK AND POWER LAB.         TW     50  20   20  P C   19. COMMUNICATION THEORY             PP    100  40   40  P C
10. ELECTRONIC INSTRUMENTS AND TOOLS TW     50  20   25  P C   20. COMMUNICATION THEORY             OR     50  20   20  P C
                                                             21. CIRCUIT SIMULATION AND TOOLS    TW     50  20   32  P C
GRAND TOTAL =  643/1500, RESULT: FAILS A.T.K.T.
ORDN. 1 MARKS :
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S80053162   DOLAS LUBDHA RAJU                    SUNITA            , 71236310G    , S80053162 ,   PICT     , S80053162
01. SIGNAL AND SYSTEMS              PP    100  40   40  P C   11. ENGINEERING MATHEMATICS III      PP    100  40   23  F
02. SIGNAL AND SYSTEMS              OR     50  20   22  P C   12. ENGINEERING MATHEMATICS III      TW     25  10   14  P C
03. SOLID STATES DEVICES AND CIRCUITSPP    100  40   41  P C   13. INTEGRATED CIRCUITS APPLICATIONS PP    100  40   40  P C
04. SOLID STATES DEVICES AND CIRCUITSPR     50  20   21  P C   14. INTEGRATED CIRCUITS APPLICATIONS PR     50  20   20  P C
05. NETWORK ANALYSIS               PP    100  40   40  P C   15. ELECTROMAGNETIC                  PP    100  40   40  P
06. DIGITAL LOGIC DESIGN           PP    100  40   40  P C   16. ELECTROMAGNETIC                  TW     25  10   14  P C
07. DIGITAL LOGIC DESIGN           PR     50  20   28  P C   17. DATA STRUCTURES                  PP    100  40   40  P C
08. POWER DEVICES AND MACHINES     PP    100  40   40  P C   18. DATA STRUCTURES                  PR     50  20   36  P C
09. NETWORK AND POWER LAB.         TW     50  20   32  P C   19. COMMUNICATION THEORY             PP    100  40   40  P C
10. ELECTRONIC INSTRUMENTS AND TOOLS TW     50  20   36  P C   20. COMMUNICATION THEORY             OR     50  20   29  P C
                                                             21. CIRCUIT SIMULATION AND TOOLS    TW     50  20   30  P C
GRAND TOTAL =  666/1500, RESULT: FAILS A.T.K.T.
ORDN. 1 MARKS :
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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.
       OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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S80053164   GAIKWAD GAYATRI DILIP   DIPALI   , 71100792G   , S80053164 ,   PICT   , S80053164
01. SIGNAL AND SYSTEMS   PP   100 40   40 P   11. ENGINEERING MATHEMATICS III   PP   100 40   40 P C
02. SIGNAL AND SYSTEMS   OR   50 20   29 P C   12. ENGINEERING MATHEMATICS III   TW   25 10   18 P C
03. SOLID STATES DEVICES AND CIRCUITSPP 100 40   40 P C   13. INTEGRATED CIRCUITS APPLICATIONS PP   100 40   40 P
04. SOLID STATES DEVICES AND CIRCUITSPR   50 20   25 P C   14. INTEGRATED CIRCUITS APPLICATIONS PR   50 20   25 P C
05. NETWORK ANALYSIS   PP   100 40   40 P   15. ELECTROMAGNETIC   PP   100 40   22 F
06. DIGITAL LOGIC DESIGN   PP   100 40   47 P C   16. ELECTROMAGNETIC   TW   25 10   18 P C
07. DIGITAL LOGIC DESIGN   PR   50 20   20 P C   17. DATA STRUCTURES   PP   100 40   44 P C
08. POWER DEVICES AND MACHINES   PP   100 40   23 F   18. DATA STRUCTURES   PR   50 20   41 P C
09. NETWORK AND POWER LAB.   TW   50 20   30 P C   19. COMMUNICATION THEORY   PP   100 40   40 P C
10. ELECTRONIC INSTRUMENTS AND TOOLS TW   50 20   31 P C   20. COMMUNICATION THEORY   OR   50 20   22 P C
                                                            21. CIRCUIT SIMULATION AND TOOLS   TW   50 20   36 P C
GRAND TOTAL = 671/1500, RESULT: FAILS A.T.K.T.
ORDN. 1 MARKS :
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S80053165   GENGAJE PRAJAKTA SHANTARAM   SUREKHA   , 71236312C   , S80053165 ,   PICT   , S80053165
01. SIGNAL AND SYSTEMS   PP   100 40   49 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   28 F
02. SIGNAL AND SYSTEMS   OR   50 20   25 P C   12. ENGINEERING MATHEMATICS III   TW   25 10   21 P C
03. SOLID STATES DEVICES AND CIRCUITSPP 100 40   40 P C   13. INTEGRATED CIRCUITS APPLICATIONS PP   100 40   40 P C
04. SOLID STATES DEVICES AND CIRCUITSPR   50 20   25 P C   14. INTEGRATED CIRCUITS APPLICATIONS PR   50 20   22 P C
05. NETWORK ANALYSIS   PP   100 40   40 P   15. ELECTROMAGNETIC   PP   100 40   40 P
06. DIGITAL LOGIC DESIGN   PP   100 40   40 P C   16. ELECTROMAGNETIC   TW   25 10   17 P C
07. DIGITAL LOGIC DESIGN   PR   50 20   22 P C   17. DATA STRUCTURES   PP   100 40   45 P C
08. POWER DEVICES AND MACHINES   PP   100 40   48 P C   18. DATA STRUCTURES   PR   50 20   38 P C
09. NETWORK AND POWER LAB.   TW   50 20   40 P C   19. COMMUNICATION THEORY   PP   100 40   45 P C
10. ELECTRONIC INSTRUMENTS AND TOOLS TW   50 20   40 P C   20. COMMUNICATION THEORY   OR   50 20   23 P C
                                                            21. CIRCUIT SIMULATION AND TOOLS   TW   50 20   40 P C
GRAND TOTAL = 728/1500, RESULT: FAILS A.T.K.T.
ORDN. 1 MARKS :
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S80053172   KOLI VIJAYA VYANKATRAO   SEETA   , 71100866D   , S80053172 ,   PICT   , S80053172
01. SIGNAL AND SYSTEMS   PP   100 40   40 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   40 P
02. SIGNAL AND SYSTEMS   OR   50 20   22 P C   12. ENGINEERING MATHEMATICS III   TW   25 10   17 P C
03. SOLID STATES DEVICES AND CIRCUITSPP 100 40   40 P   13. INTEGRATED CIRCUITS APPLICATIONS PP   100 40   40 P C
04. SOLID STATES DEVICES AND CIRCUITSPR   50 20   20 P C   14. INTEGRATED CIRCUITS APPLICATIONS PR   50 20   07 F
05. NETWORK ANALYSIS   PP   100 40   17 F   15. ELECTROMAGNETIC   PP   100 40   40 P C
06. DIGITAL LOGIC DESIGN   PP   100 40   23 F   16. ELECTROMAGNETIC   TW   25 10   18 P C
07. DIGITAL LOGIC DESIGN   PR   50 20   30 P C   17. DATA STRUCTURES   PP   100 40   43 P C
08. POWER DEVICES AND MACHINES   PP   100 40   22 F   18. DATA STRUCTURES   PR   50 20   32 P C
09. NETWORK AND POWER LAB.   TW   50 20   27 P C   19. COMMUNICATION THEORY   PP   100 40   40 P C
10. ELECTRONIC INSTRUMENTS AND TOOLS TW   50 20   30 P C   20. COMMUNICATION THEORY   OR   50 20   10 F
                                                            21. CIRCUIT SIMULATION AND TOOLS   TW   50 20   32 P C
GRAND TOTAL = 590/1500, RESULT: FAILS A.T.K.T.   RESULT RESERVED FOR BKLG
ORDN. 1 MARKS :
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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S80053173   MORE ANKUR SURESHRAO   LAXMI   , 71100900H   , S80053173 ,   PICT   , S80053173

01. SIGNAL AND SYSTEMS	PP	100	40	40	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	44	P C
02. SIGNAL AND SYSTEMS	OR	50	20	26	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	12	P C
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	AA	F	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	AA	F
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	20	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	AA	F
05. NETWORK ANALYSIS	PP	100	40	17	F	15. ELECTROMAGNETIC	PP	100	40	10	F
06. DIGITAL LOGIC DESIGN	PP	100	40	AA	F	16. ELECTROMAGNETIC	TW	25	10	11	P C
07. DIGITAL LOGIC DESIGN	PR	50	20	25	P C	17. DATA STRUCTURES	PP	100	40	40	P
08. POWER DEVICES AND MACHINES	PP	100	40	40	P	18. DATA STRUCTURES	PR	50	20	32	P C
09. NETWORK AND POWER LAB.	TW	50	20	24	P C	19. COMMUNICATION THEORY	PP	100	40	16	F
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	27	P C	20. COMMUNICATION THEORY	OR	50	20	AA	F
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	23	P C

GRAND TOTAL = 407/1500, RESULT: FAILS

ORDN. 1 MARKS :

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S80053176   NANDNAWARE AKASH JAGNNATH   DRAUPADA   , 71100910E   , S80053176 ,   PICT   , S80053176

01. SIGNAL AND SYSTEMS	PP	100	40	22	F	11. ENGINEERING MATHEMATICS III	PP	100	40	40	P C
02. SIGNAL AND SYSTEMS	OR	50	20	26	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	11	P C
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	40	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	40	P
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	25	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	AA	F
05. NETWORK ANALYSIS	PP	100	40	12	F	15. ELECTROMAGNETIC	PP	100	40	13	F
06. DIGITAL LOGIC DESIGN	PP	100	40	49	P C	16. ELECTROMAGNETIC	TW	25	10	10	P C
07. DIGITAL LOGIC DESIGN	PR	50	20	AA	F	17. DATA STRUCTURES	PP	100	40	40	P C
08. POWER DEVICES AND MACHINES	PP	100	40	16	F	18. DATA STRUCTURES	PR	50	20	35	P C
09. NETWORK AND POWER LAB.	TW	50	20	30	P C	19. COMMUNICATION THEORY	PP	100	40	22	F
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	28	P C	20. COMMUNICATION THEORY	OR	50	20	30	P C
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	25	P C

GRAND TOTAL = 514/1500, RESULT: FAILS

ORDN. 1 MARKS :

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S80053179   PAVAN TENKALE   SHIVKANTA   , 71100946F   , S80053179 ,   PICT   , S80053179

01. SIGNAL AND SYSTEMS	PP	100	40	54	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	61	P C
02. SIGNAL AND SYSTEMS	OR	50	20	32	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	17	P C
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	48	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	56	P C
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	35	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	20	P C
05. NETWORK ANALYSIS	PP	100	40	42	P C	15. ELECTROMAGNETIC	PP	100	40	40	P
06. DIGITAL LOGIC DESIGN	PP	100	40	46	P C	16. ELECTROMAGNETIC	TW	25	10	13	P C
07. DIGITAL LOGIC DESIGN	PR	50	20	23	P C	17. DATA STRUCTURES	PP	100	40	43	P C
08. POWER DEVICES AND MACHINES	PP	100	40	40	P C	18. DATA STRUCTURES	PR	50	20	25	P C
09. NETWORK AND POWER LAB.	TW	50	20	38	P C	19. COMMUNICATION THEORY	PP	100	40	53	P C
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	32	P C	20. COMMUNICATION THEORY	OR	50	20	23	P C
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	30	P C

GRAND TOTAL = 771/1500, RESULT: SECOND CLASS

ORDN. 1 MARKS :

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.

      OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S80053185   SARODE BHUSHAN CHANDRAKANT   ALKA   , 71236328K   , S80053185 ,   PICT   , S80053185
01. SIGNAL AND SYSTEMS   PP   100 40   48 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   59 P
02. SIGNAL AND SYSTEMS   OR   50 20   37 P C   12. ENGINEERING MATHEMATICS III   TW   25 10   20 P C
03. SOLID STATES DEVICES AND CIRCUITSPP   100 40   40 P C   13. INTEGRATED CIRCUITS APPLICATIONS PP   100 40   63 P C
04. SOLID STATES DEVICES AND CIRCUITSPR   50 20   35 P C   14. INTEGRATED CIRCUITS APPLICATIONS PR   50 20   38 P C
05. NETWORK ANALYSIS   PP   100 40   40 P C   15. ELECTROMAGNETIC   PP   100 40   40 P C
06. DIGITAL LOGIC DESIGN   PP   100 40   69 P C   16. ELECTROMAGNETIC   TW   25 10   19 P C
07. DIGITAL LOGIC DESIGN   PR   50 20   22 P C   17. DATA STRUCTURES   PP   100 40   67 P C
08. POWER DEVICES AND MACHINES   PP   100 40   54 P C   18. DATA STRUCTURES   PR   50 20   45 P C
09. NETWORK AND POWER LAB.   TW   50 20   41 P C   19. COMMUNICATION THEORY   PP   100 40   70 P C
10. ELECTRONIC INSTRUMENTS AND TOOLS TW   50 20   42 P C   20. COMMUNICATION THEORY   OR   50 20   35 P C
                                                              21. CIRCUIT SIMULATION AND TOOLS   TW   50 20   38 P C
GRAND TOTAL = 922/1500, RESULT: FIRST CLASS
ORDN. 1 MARKS :

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S80053186   SARWAR ABHIJIT ASHOK   MEERA   , 71100988M   , S80053186 ,   PICT   , S80053186
01. SIGNAL AND SYSTEMS   PP   100 40   40 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   52 P
02. SIGNAL AND SYSTEMS   OR   50 20   20 P C   12. ENGINEERING MATHEMATICS III   TW   25 10   12 P C
03. SOLID STATES DEVICES AND CIRCUITSPP   100 40   46 P C   13. INTEGRATED CIRCUITS APPLICATIONS PP   100 40   45 P C
04. SOLID STATES DEVICES AND CIRCUITSPR   50 20   33 P C   14. INTEGRATED CIRCUITS APPLICATIONS PR   50 20   30 P C
05. NETWORK ANALYSIS   PP   100 40   40 P C   15. ELECTROMAGNETIC   PP   100 40   41 P C
06. DIGITAL LOGIC DESIGN   PP   100 40   52 P C   16. ELECTROMAGNETIC   TW   25 10   12 P C
07. DIGITAL LOGIC DESIGN   PR   50 20   32 P C   17. DATA STRUCTURES   PP   100 40   51 P C
08. POWER DEVICES AND MACHINES   PP   100 40   40 P C   18. DATA STRUCTURES   PR   50 20   34 P C
09. NETWORK AND POWER LAB.   TW   50 20   39 P C   19. COMMUNICATION THEORY   PP   100 40   46 P C
10. ELECTRONIC INSTRUMENTS AND TOOLS TW   50 20   35 P C   20. COMMUNICATION THEORY   OR   50 20   30 P C
                                                              21. CIRCUIT SIMULATION AND TOOLS   TW   50 20   30 P C
GRAND TOTAL = 760/1500, RESULT: SECOND CLASS
ORDN. 1 MARKS :

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S80053187   SHAHA SANKET MANOJKUMAR   NEETA   , 71100997L   , S80053187 ,   PICT   , S80053187
01. SIGNAL AND SYSTEMS   PP   100 40   51 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   72 P
02. SIGNAL AND SYSTEMS   OR   50 20   24 P C   12. ENGINEERING MATHEMATICS III   TW   25 10   23 P
03. SOLID STATES DEVICES AND CIRCUITSPP   100 40   40 P C   13. INTEGRATED CIRCUITS APPLICATIONS PP   100 40   40 P
04. SOLID STATES DEVICES AND CIRCUITSPR   50 20   22 P C   14. INTEGRATED CIRCUITS APPLICATIONS PR   50 20   38 P
05. NETWORK ANALYSIS   PP   100 40   58 P C   15. ELECTROMAGNETIC   PP   100 40   81 P
06. DIGITAL LOGIC DESIGN   PP   100 40   48 P C   16. ELECTROMAGNETIC   TW   25 10   23 P
07. DIGITAL LOGIC DESIGN   PR   50 20   39 P C   17. DATA STRUCTURES   PP   100 40   55 P
08. POWER DEVICES AND MACHINES   PP   100 40   40 P C   18. DATA STRUCTURES   PR   50 20   35 P
09. NETWORK AND POWER LAB.   TW   50 20   20 P C   19. COMMUNICATION THEORY   PP   100 40   40 P
10. ELECTRONIC INSTRUMENTS AND TOOLS TW   50 20   22 P C   20. COMMUNICATION THEORY   OR   50 20   21 P
                                                              21. CIRCUIT SIMULATION AND TOOLS   TW   50 20   45 P
GRAND TOTAL = 837/1500, RESULT: HIGHER SECOND CLASS
ORDN. 1 MARKS :

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.
        OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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  S80053188   SHIKHA SUMAN SINHA                    SAVITA              , 71101004J    , S80053188 ,   PICT     , S80053188
  01. SIGNAL AND SYSTEMS            PP   100  40   42  P C     11. ENGINEERING MATHEMATICS III     PP   100  40   69  P
  02. SIGNAL AND SYSTEMS            OR    50  20   33  P C     12. ENGINEERING MATHEMATICS III     TW    25  10   19  P C
  03. SOLID STATES DEVICES AND CIRCUITSPP   100  40   40  P C     13. INTEGRATED CIRCUITS APPLICATIONS PP   100  40   47  P C
  04. SOLID STATES DEVICES AND CIRCUITSPR    50  20   25  P C     14. INTEGRATED CIRCUITS APPLICATIONS PR    50  20   20  P C
  05. NETWORK ANALYSIS             PP   100  40   40  P C     15. ELECTROMAGNETIC                 PP   100  40   45  P C
  06. DIGITAL LOGIC DESIGN         PP   100  40   61  P C     16. ELECTROMAGNETIC                 TW    25  10   17  P C
  07. DIGITAL LOGIC DESIGN         PR    50  20   24  P C     17. DATA STRUCTURES                 PP   100  40   61  P C
  08. POWER DEVICES AND MACHINES   PP   100  40   40  P C     18. DATA STRUCTURES                 PR    50  20   40  P C
  09. NETWORK AND POWER LAB.       TW    50  20   39  P C     19. COMMUNICATION THEORY            PP   100  40   51  P C
  10. ELECTRONIC INSTRUMENTS AND TOOLS TW    50  20   35  P C     20. COMMUNICATION THEORY            OR    50  20   24  P C
                                                             21. CIRCUIT SIMULATION AND TOOLS   TW    50  20   36  P C
GRAND TOTAL =  808/1500, RESULT: SECOND CLASS
ORDN. 1 MARKS :
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  S80053189   SIDDHANT KHURANA                      SAROJ              , 71101016B    , S80053189 ,   PICT     , S80053189
  01. SIGNAL AND SYSTEMS            PP   100  40   43  P C     11. ENGINEERING MATHEMATICS III     PP   100  40   44  P C
  02. SIGNAL AND SYSTEMS            OR    50  20   36  P C     12. ENGINEERING MATHEMATICS III     TW    25  10   17  P C
  03. SOLID STATES DEVICES AND CIRCUITSPP   100  40   40  P C     13. INTEGRATED CIRCUITS APPLICATIONS PP   100  40   28  F
  04. SOLID STATES DEVICES AND CIRCUITSPR    50  20   34  P C     14. INTEGRATED CIRCUITS APPLICATIONS PR    50  20   20  P C
  05. NETWORK ANALYSIS             PP   100  40   40  P C     15. ELECTROMAGNETIC                 PP   100  40   42  P C
  06. DIGITAL LOGIC DESIGN         PP   100  40   40  P C     16. ELECTROMAGNETIC                 TW    25  10   14  P C
  07. DIGITAL LOGIC DESIGN         PR    50  20   25  P C     17. DATA STRUCTURES                 PP   100  40   53  P C
  08. POWER DEVICES AND MACHINES   PP   100  40   40  P C     18. DATA STRUCTURES                 PR    50  20   46  P C
  09. NETWORK AND POWER LAB.       TW    50  20   38  P C     19. COMMUNICATION THEORY            PP   100  40   40  P C
  10. ELECTRONIC INSTRUMENTS AND TOOLS TW    50  20   35  P C     20. COMMUNICATION THEORY            OR    50  20   27  P C
                                                             21. CIRCUIT SIMULATION AND TOOLS   TW    50  20   33  P C
GRAND TOTAL =  735/1500, RESULT: FAILS A.T.K.T.
ORDN. 1 MARKS :
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  S80053190   SONAWANE NIKHIL BHIMRAO               SUNANDA            , 71045632J    , S80053190 ,   PICT     , S80053190
  01. SIGNAL AND SYSTEMS            PP   100  40   AA  F       11. ENGINEERING MATHEMATICS III     PP   100  40   AA  F
  02. SIGNAL AND SYSTEMS            OR    50  20   24  P C     12. ENGINEERING MATHEMATICS III     TW    25  10   10  P C
  03. SOLID STATES DEVICES AND CIRCUITSPP   100  40   AA  F       13. INTEGRATED CIRCUITS APPLICATIONS PP   100  40   AA  F
  04. SOLID STATES DEVICES AND CIRCUITSPR    50  20   AA  F       14. INTEGRATED CIRCUITS APPLICATIONS PR    50  20   AA  F
  05. NETWORK ANALYSIS             PP   100  40   AA  F       15. ELECTROMAGNETIC                 PP   100  40   AA  F
  06. DIGITAL LOGIC DESIGN         PP   100  40   40  P C     16. ELECTROMAGNETIC                 TW    25  10   10  P C
  07. DIGITAL LOGIC DESIGN         PR    50  20   30  P C     17. DATA STRUCTURES                 PP   100  40   AA  F
  08. POWER DEVICES AND MACHINES   PP   100  40   40  P C     18. DATA STRUCTURES                 PR    50  20   AA  F
  09. NETWORK AND POWER LAB.       TW    50  20   27  P C     19. COMMUNICATION THEORY            PP   100  40   AA  F
  10. ELECTRONIC INSTRUMENTS AND TOOLS TW    50  20   30  P C     20. COMMUNICATION THEORY            OR    50  20   AA  F
                                                             21. CIRCUIT SIMULATION AND TOOLS   TW    50  20   20  P C
GRAND TOTAL =  231/1500, RESULT: FAILS
ORDN. 1 MARKS :
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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.
         OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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S80053192   TANNA VATSAL AJAY   VAISHALI   , 71236329H   , S80053192 ,   PICT   , S80053192
01. SIGNAL AND SYSTEMS   PP   100 40   40 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   44 P
02. SIGNAL AND SYSTEMS   OR   50 20   30 P C   12. ENGINEERING MATHEMATICS III   TW   25 10   16 P C
03. SOLID STATES DEVICES AND CIRCUITSPP   100 40   46 P C   13. INTEGRATED CIRCUITS APPLICATIONS PP   100 40   40 P C
04. SOLID STATES DEVICES AND CIRCUITSPR   50 20   39 P C   14. INTEGRATED CIRCUITS APPLICATIONS PR   50 20   27 P C
05. NETWORK ANALYSIS   PP   100 40   43 P C   15. ELECTROMAGNETIC   PP   100 40   40 P C
06. DIGITAL LOGIC DESIGN   PP   100 40   53 P C   16. ELECTROMAGNETIC   TW   25 10   13 P C
07. DIGITAL LOGIC DESIGN   PR   50 20   23 P C   17. DATA STRUCTURES   PP   100 40   54 P C
08. POWER DEVICES AND MACHINES   PP   100 40   44 P C   18. DATA STRUCTURES   PR   50 20   33 P C
09. NETWORK AND POWER LAB.   TW   50 20   37 P C   19. COMMUNICATION THEORY   PP   100 40   48 P C
10. ELECTRONIC INSTRUMENTS AND TOOLS TW   50 20   36 P C   20. COMMUNICATION THEORY   OR   50 20   40 P C
                                                              21. CIRCUIT SIMULATION AND TOOLS   TW   50 20   32 P C
GRAND TOTAL = 778/1500, RESULT: SECOND CLASS
ORDN. 1 MARKS :

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S80053194   THAKARE APURV VIJAY   MEENA   , 71101031F   , S80053194 ,   PICT   , S80053194
01. SIGNAL AND SYSTEMS   PP   100 40   40 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   51 P
02. SIGNAL AND SYSTEMS   OR   50 20   26 P C   12. ENGINEERING MATHEMATICS III   TW   25 10   11 P C
03. SOLID STATES DEVICES AND CIRCUITSPP   100 40   56 P   13. INTEGRATED CIRCUITS APPLICATIONS PP   100 40   40 P C
04. SOLID STATES DEVICES AND CIRCUITSPR   50 20   21 P C   14. INTEGRATED CIRCUITS APPLICATIONS PR   50 20   22 P C
05. NETWORK ANALYSIS   PP   100 40   40 P C   15. ELECTROMAGNETIC   PP   100 40   53 P C
06. DIGITAL LOGIC DESIGN   PP   100 40   40 P C   16. ELECTROMAGNETIC   TW   25 10   11 P C
07. DIGITAL LOGIC DESIGN   PR   50 20   20 P C   17. DATA STRUCTURES   PP   100 40   46 P C
08. POWER DEVICES AND MACHINES   PP   100 40   40 P C   18. DATA STRUCTURES   PR   50 20   33 P C
09. NETWORK AND POWER LAB.   TW   50 20   20 P C   19. COMMUNICATION THEORY   PP   100 40   40 P C
10. ELECTRONIC INSTRUMENTS AND TOOLS TW   50 20   22 P C   20. COMMUNICATION THEORY   OR   50 20   26 P C
                                                              21. CIRCUIT SIMULATION AND TOOLS   TW   50 20   23 P C
GRAND TOTAL = 681/1500, RESULT: PASS CLASS
ORDN. 1 MARKS :

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S80053196   VARADE RUSHIKESH DILIP   CHHAYA   , 71101045F   , S80053196 ,   PICT   , S80053196
01. SIGNAL AND SYSTEMS   PP   100 40   42 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   40 P C
02. SIGNAL AND SYSTEMS   OR   50 20   30 P C   12. ENGINEERING MATHEMATICS III   TW   25 10   12 P C
03. SOLID STATES DEVICES AND CIRCUITSPP   100 40   49 P C   13. INTEGRATED CIRCUITS APPLICATIONS PP   100 40   51 P C
04. SOLID STATES DEVICES AND CIRCUITSPR   50 20   36 P C   14. INTEGRATED CIRCUITS APPLICATIONS PR   50 20   33 P C
05. NETWORK ANALYSIS   PP   100 40   40 P C   15. ELECTROMAGNETIC   PP   100 40   40 P
06. DIGITAL LOGIC DESIGN   PP   100 40   53 P C   16. ELECTROMAGNETIC   TW   25 10   11 P C
07. DIGITAL LOGIC DESIGN   PR   50 20   32 P C   17. DATA STRUCTURES   PP   100 40   41 P C
08. POWER DEVICES AND MACHINES   PP   100 40   46 P C   18. DATA STRUCTURES   PR   50 20   32 P C
09. NETWORK AND POWER LAB.   TW   50 20   28 P C   19. COMMUNICATION THEORY   PP   100 40   40 P C
10. ELECTRONIC INSTRUMENTS AND TOOLS TW   50 20   27 P C   20. COMMUNICATION THEORY   OR   50 20   28 P C
                                                              21. CIRCUIT SIMULATION AND TOOLS   TW   50 20   25 P C
GRAND TOTAL = 736/1500, RESULT: PASS CLASS
ORDN. 1 MARKS :

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NOTE: FIRST LINE : SEAT NO.,  NAME OF THE CANDIDATE,  MOTHER,  PERMANENT REG. NO.,  PREVIOUS SEAT NO.,  COLLEGE,   SEAT NO.
       OTHER LINES: HEAD OF PASSING,  MAX. MARKS,  MIN. PASS MARKS,  MARKS OBTAINED,  P/F:PASS/FAIL,  C:PREVIOUS CARRY OVER
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S80053197   YEILE PRAJKT PRAMOD                    JYOTI              , 71236332H    , S80053197 ,   PICT     , S80053197
01. SIGNAL AND SYSTEMS            PP    100  40   41  P C   11. ENGINEERING MATHEMATICS III     PP    100  40   40  P
02. SIGNAL AND SYSTEMS            OR     50  20   23  P C   12. ENGINEERING MATHEMATICS III     TW     25  10   15  P C
03. SOLID STATES DEVICES AND CIRCUITSPP  100  40   40  P C   13. INTEGRATED CIRCUITS APPLICATIONS PP   100  40   40  P C
04. SOLID STATES DEVICES AND CIRCUITSPR   50  20   26  P C   14. INTEGRATED CIRCUITS APPLICATIONS PR    50  20   20  P C
05. NETWORK ANALYSIS             PP    100  40   40  P     15. ELECTROMAGNETIC                 PP    100  40   28  F
06. DIGITAL LOGIC DESIGN         PP    100  40   43  P     16. ELECTROMAGNETIC                 TW     25  10   12  P C
07. DIGITAL LOGIC DESIGN         PR     50  20   30  P C   17. DATA STRUCTURES                 PP    100  40   40  P C
08. POWER DEVICES AND MACHINES   PP    100  40   41  P C   18. DATA STRUCTURES                 PR     50  20   35  P C
09. NETWORK AND POWER LAB.       TW     50  20   35  P C   19. COMMUNICATION THEORY            PP    100  40   43  P
10. ELECTRONIC INSTRUMENTS AND TOOLS TW  50  20   29  P C   20. COMMUNICATION THEORY            OR     50  20   28  P C
                                                           21. CIRCUIT SIMULATION AND TOOLS    TW     50  20   31  P C
GRAND TOTAL =  680/1500, RESULT: FAILS A.T.K.T.
ORDN. 1 MARKS :
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S80053199   KHANDELWAL DIVYANG KAMALKISHOR          SANGEETA           , 71100863K     ,              ,   PICT     , S80053199
01. SIGNAL AND SYSTEMS            PP    100  40   43  P C
02. SIGNAL AND SYSTEMS            OR     50  20   20  P C
03. SOLID STATES DEVICES AND CIRCUITSPP  100  40   AA  F
04. SOLID STATES DEVICES AND CIRCUITSPR   50  20   AA  F
05. NETWORK ANALYSIS             PP    100  40   AA  F
06. DIGITAL LOGIC DESIGN         PP    100  40   AA  F
07. DIGITAL LOGIC DESIGN         PR     50  20   29  P C
08. POWER DEVICES AND MACHINES   PP    100  40   AA  F
09. NETWORK AND POWER LAB.       TW     50  20   21  P C
10. ELECTRONIC INSTRUMENTS AND TOOLS TW  50  20   23  P C
FIRST TERM TOTAL =  136/750.
ORDN. 1 MARKS :
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S80053200   PITRODA UTSAV RAJENDRA                 KAILASH            , 71129936G     ,              ,   PICT     , S80053200
01. SIGNAL AND SYSTEMS            PP    100  40   40  P C   11. ENGINEERING MATHEMATICS III     PP    100  40   AA  F
02. SIGNAL AND SYSTEMS            OR     50  20   21  P C   12. ENGINEERING MATHEMATICS III     TW     25  10   18  P C
03. SOLID STATES DEVICES AND CIRCUITSPP  100  40   49  P C   13. INTEGRATED CIRCUITS APPLICATIONS PP   100  40   51  P C
04. SOLID STATES DEVICES AND CIRCUITSPR   50  20   28  P C   14. INTEGRATED CIRCUITS APPLICATIONS PR    50  20   30  P C
05. NETWORK ANALYSIS             PP    100  40   40  P C   15. ELECTROMAGNETIC                 PP    100  40   40  P C
06. DIGITAL LOGIC DESIGN         PP    100  40   40  P C   16. ELECTROMAGNETIC                 TW     25  10   16  P C
07. DIGITAL LOGIC DESIGN         PR     50  20   24  P C   17. DATA STRUCTURES                 PP    100  40   43  P C
08. POWER DEVICES AND MACHINES   PP    100  40   48  P C   18. DATA STRUCTURES                 PR     50  20   45  P C
09. NETWORK AND POWER LAB.       TW     50  20   41  P C   19. COMMUNICATION THEORY            PP    100  40   40  P C
10. ELECTRONIC INSTRUMENTS AND TOOLS TW  50  20   38  P C   20. COMMUNICATION THEORY            OR     50  20   20  P C
                                                           21. CIRCUIT SIMULATION AND TOOLS    TW     50  20   42  P C
GRAND TOTAL =  714/1500, RESULT: FAILS A.T.K.T.
ORDN. 1 MARKS :
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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.
      OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S80054201   AADITYA RAMESH                    DINAZ              , 71200745J    , S80054201 ,  PICT     , S80054201
 01. DISCRETE STRUCTURES          PP   100  40   79  P C    11. ENGINEERING MATHEMATICS III     PP   100  40   52  P
 02. PROGRAMMING & PROBLEM SOLVING PP   100  40   57  P C    12. MICROPROC. & INTERFACING TECHNIQ.PP   100  40   65  P
 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100  40   64  P C    13. DATA STRUCTURES                  PP   100  40   73  P
 04. DATA STRUCTURES AND ALGORITHMS PP  100  40   59  P C    14. COMPUTER GRAPHICS                PP   100  40   70  P
 05. HUMANITIES AND SOCIAL SCIENCE PP   100  40   57  P C    15. COMPUTER ORGANIZATION            PP   100  40   73  P
 06. PROGRAMMING LABORATORY        TW    25  10   20  P C    16. O. O. PROG. & COMP. GRAPH. LAB   TW    50  20   39  P
 07. PROGRAMMING LABORATORY        PR    50  20   43  P C    17. O. O. PROG. & COMP. GRAPH. LAB   PR    50  20   43  P
 08. DIGITAL ELECTRONICS LABORATORY TW   25  10   18  P C    18. MICROPROCESSORS & INTERFACING LABTW    50  20   37  P
 09. DIGITAL ELECTRONICS LABORATORY PR   50  20   37  P C    19. MICROPROCESSORS & INTERFACING LABPR    50  20   43  P
 10. SOFT SKILLS                   TW    50  20   35  P C    20. DATA STRUCTURES LABORATORY       TW    50  20   40  P
                                                             21. DATA STRUCTURES LABORATORY       PR    50  20   40  P
GRAND TOTAL = 1044/1500, RESULT: FIRST CLASS WITH DISTINCTION
ORDN. 1 MARKS :
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S80054202   ABHINAV KAUL                      SUREKHA            , 71200747E    , S80054202 ,  PICT     , S80054202
 01. DISCRETE STRUCTURES          PP   100  40   53  P C    11. ENGINEERING MATHEMATICS III     PP   100  40   40  P
 02. PROGRAMMING & PROBLEM SOLVING PP   100  40   40  P C    12. MICROPROC. & INTERFACING TECHNIQ.PP   100  40   14  F
 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100  40   40  P C    13. DATA STRUCTURES                  PP   100  40   40  P
 04. DATA STRUCTURES AND ALGORITHMS PP  100  40   40  P C    14. COMPUTER GRAPHICS                PP   100  40   48  P
 05. HUMANITIES AND SOCIAL SCIENCE PP   100  40   47  P C    15. COMPUTER ORGANIZATION            PP   100  40   29  F
 06. PROGRAMMING LABORATORY        TW    25  10   18  P C    16. O. O. PROG. & COMP. GRAPH. LAB   TW    50  20   33  P
 07. PROGRAMMING LABORATORY        PR    50  20   13  F      17. O. O. PROG. & COMP. GRAPH. LAB   PR    50  20   38  P
 08. DIGITAL ELECTRONICS LABORATORY TW   25  10   20  P C    18. MICROPROCESSORS & INTERFACING LABTW    50  20   35  P
 09. DIGITAL ELECTRONICS LABORATORY PR   50  20   32  P C    19. MICROPROCESSORS & INTERFACING LABPR    50  20   24  P
 10. SOFT SKILLS                   TW    50  20   32  P C    20. DATA STRUCTURES LABORATORY       TW    50  20   35  P
                                                             21. DATA STRUCTURES LABORATORY       PR    50  20   31  P
GRAND TOTAL =  702/1500, RESULT: FAILS A.T.K.T.
ORDN. 1 MARKS :
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S80054203   ABHYANKAR RUCHA RHISHIKESH        ARTI               , 71200749M    , S80054203 ,  PICT     , S80054203
 01. DISCRETE STRUCTURES          PP   100  40   71  P C    11. ENGINEERING MATHEMATICS III     PP   100  40   58  P
 02. PROGRAMMING & PROBLEM SOLVING PP   100  40   58  P C    12. MICROPROC. & INTERFACING TECHNIQ.PP   100  40   57  P
 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100  40   73  P C    13. DATA STRUCTURES                  PP   100  40   65  P
 04. DATA STRUCTURES AND ALGORITHMS PP  100  40   61  P C    14. COMPUTER GRAPHICS                PP   100  40   72  P
 05. HUMANITIES AND SOCIAL SCIENCE PP   100  40   64  P C    15. COMPUTER ORGANIZATION            PP   100  40   65  P
 06. PROGRAMMING LABORATORY        TW    25  10   22  P C    16. O. O. PROG. & COMP. GRAPH. LAB   TW    50  20   45  P
 07. PROGRAMMING LABORATORY        PR    50  20   40  P C    17. O. O. PROG. & COMP. GRAPH. LAB   PR    50  20   40  P
 08. DIGITAL ELECTRONICS LABORATORY TW   25  10   20  P C    18. MICROPROCESSORS & INTERFACING LABTW    50  20   44  P
 09. DIGITAL ELECTRONICS LABORATORY PR   50  20   35  P C    19. MICROPROCESSORS & INTERFACING LABPR    50  20   39  P
 10. SOFT SKILLS                   TW    50  20   40  P C    20. DATA STRUCTURES LABORATORY       TW    50  20   45  P
                                                             21. DATA STRUCTURES LABORATORY       PR    50  20   41  P
GRAND TOTAL = 1055/1500, RESULT: FIRST CLASS WITH DISTINCTION
ORDN. 1 MARKS :
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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
        OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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S80054204   AJINKYA DNYANESHWAR RAJPUT   MEENAKSHI   , 71200753K   , S80054204 , PICT   , S80054204
01. DISCRETE STRUCTURES   PP   100 40   50 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   40 P
02. PROGRAMMING & PROBLEM SOLVING PP   100 40   50 P C   12. MICROPROC. & INTERFACING TECHNIQ.PP   100 40   64 P
03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 40   66 P C   13. DATA STRUCTURES   PP   100 40   58 P
04. DATA STRUCTURES AND ALGORITHMS PP   100 40   48 P C   14. COMPUTER GRAPHICS   PP   100 40   65 P
05. HUMANITIES AND SOCIAL SCIENCE PP   100 40   53 P C   15. COMPUTER ORGANIZATION   PP   100 40   54 P
06. PROGRAMMING LABORATORY   TW   25 10   20 P C   16. O. O. PROG. & COMP. GRAPH. LAB   TW   50 20   41 P
07. PROGRAMMING LABORATORY   PR   50 20   41 P C   17. O. O. PROG. & COMP. GRAPH. LAB   PR   50 20   42 P
08. DIGITAL ELECTRONICS LABORATORY TW   25 10   20 P C   18. MICROPROCESSORS & INTERFACING LABTW   50 20   38 P
09. DIGITAL ELECTRONICS LABORATORY PR   50 20   35 P C   19. MICROPROCESSORS & INTERFACING LABPR   50 20   27 P
10. SOFT SKILLS   TW   50 20   31 P C   20. DATA STRUCTURES LABORATORY   TW   50 20   39 P
                                                           21. DATA STRUCTURES LABORATORY   PR   50 20   39 P
GRAND TOTAL = 921/1500, RESULT: FIRST CLASS
ORDN. 1 MARKS :
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S80054205   AKASH MAHAPATRA   SUJATA   , 71200754H   , S80054205 , PICT   , S80054205
01. DISCRETE STRUCTURES   PP   100 40   63 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   58 P
02. PROGRAMMING & PROBLEM SOLVING PP   100 40   53 P C   12. MICROPROC. & INTERFACING TECHNIQ.PP   100 40   49 P
03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 40   66 P C   13. DATA STRUCTURES   PP   100 40   55 P
04. DATA STRUCTURES AND ALGORITHMS PP   100 40   51 P C   14. COMPUTER GRAPHICS   PP   100 40   45 P
05. HUMANITIES AND SOCIAL SCIENCE PP   100 40   54 P C   15. COMPUTER ORGANIZATION   PP   100 40   56 P
06. PROGRAMMING LABORATORY   TW   25 10   21 P C   16. O. O. PROG. & COMP. GRAPH. LAB   TW   50 20   40 P
07. PROGRAMMING LABORATORY   PR   50 20   38 P C   17. O. O. PROG. & COMP. GRAPH. LAB   PR   50 20   42 P
08. DIGITAL ELECTRONICS LABORATORY TW   25 10   20 P C   18. MICROPROCESSORS & INTERFACING LABTW   50 20   40 P
09. DIGITAL ELECTRONICS LABORATORY PR   50 20   36 P C   19. MICROPROCESSORS & INTERFACING LABPR   50 20   25 P
10. SOFT SKILLS   TW   50 20   41 P C   20. DATA STRUCTURES LABORATORY   TW   50 20   43 P
                                                           21. DATA STRUCTURES LABORATORY   PR   50 20   35 P
GRAND TOTAL = 931/1500, RESULT: FIRST CLASS
ORDN. 1 MARKS :
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S80054206   AMAN RATHI   GEETA   , 71200759J   , S80054206 , PICT   , S80054206
01. DISCRETE STRUCTURES   PP   100 40   45 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   21 F
02. PROGRAMMING & PROBLEM SOLVING PP   100 40   47 P C   12. MICROPROC. & INTERFACING TECHNIQ.PP   100 40   50 P
03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 40   55 P C   13. DATA STRUCTURES   PP   100 40   49 P
04. DATA STRUCTURES AND ALGORITHMS PP   100 40   47 P C   14. COMPUTER GRAPHICS   PP   100 40   60 P
05. HUMANITIES AND SOCIAL SCIENCE PP   100 40   66 P C   15. COMPUTER ORGANIZATION   PP   100 40   58 P
06. PROGRAMMING LABORATORY   TW   25 10   21 P C   16. O. O. PROG. & COMP. GRAPH. LAB   TW   50 20   38 P
07. PROGRAMMING LABORATORY   PR   50 20   21 P C   17. O. O. PROG. & COMP. GRAPH. LAB   PR   50 20   46 P
08. DIGITAL ELECTRONICS LABORATORY TW   25 10   22 P C   18. MICROPROCESSORS & INTERFACING LABTW   50 20   36 P
09. DIGITAL ELECTRONICS LABORATORY PR   50 20   34 P C   19. MICROPROCESSORS & INTERFACING LABPR   50 20   06 F
10. SOFT SKILLS   TW   50 20   41 P C   20. DATA STRUCTURES LABORATORY   TW   50 20   38 P
                                                           21. DATA STRUCTURES LABORATORY   PR   50 20   40 P
GRAND TOTAL = 841/1500, RESULT: FAILS A.T.K.T.
ORDN. 1 MARKS :
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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
       OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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  S80054207   AMAN SINGH   KIRAN   , 71200760B   , S80054207 ,   PICT   , S80054207
  01. DISCRETE STRUCTURES   PP   100 40   45 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   40 P
  02. PROGRAMMING & PROBLEM SOLVING PP   100 40   51 P C   12. MICROPROC. & INTERFACING TECHNIQ.PP   100 40   41 P
  03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 40   47 P C   13. DATA STRUCTURES   PP   100 40   50 P
  04. DATA STRUCTURES AND ALGORITHMS PP   100 40   40 P C   14. COMPUTER GRAPHICS   PP   100 40   54 P
  05. HUMANITIES AND SOCIAL SCIENCE PP   100 40   56 P C   15. COMPUTER ORGANIZATION   PP   100 40   42 P
  06. PROGRAMMING LABORATORY   TW   25 10   12 P C   16. O. O. PROG. & COMP. GRAPH. LAB   TW   50 20   22 P
  07. PROGRAMMING LABORATORY   PR   50 20   40 P C   17. O. O. PROG. & COMP. GRAPH. LAB   PR   50 20   35 P
  08. DIGITAL ELECTRONICS LABORATORY TW   25 10   15 P C   18. MICROPROCESSORS & INTERFACING LABTW   50 20   22 P
  09. DIGITAL ELECTRONICS LABORATORY PR   50 20   33 P C   19. MICROPROCESSORS & INTERFACING LABPR   50 20   02 F
  10. SOFT SKILLS   TW   50 20   30 P C   20. DATA STRUCTURES LABORATORY   TW   50 20   25 P
                                                               21. DATA STRUCTURES LABORATORY   PR   50 20   25 P
GRAND TOTAL = 727/1500, RESULT: FAILS A.T.K.T.
ORDN. 1 MARKS :
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  S80054208   ANIRUDH SUDARSHAN   PADMAPRIYA   , 71200762J   , S80054208 ,   PICT   , S80054208
  01. DISCRETE STRUCTURES   PP   100 40   81 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   58 P
  02. PROGRAMMING & PROBLEM SOLVING PP   100 40   67 P C   12. MICROPROC. & INTERFACING TECHNIQ.PP   100 40   59 P
  03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 40   56 P C   13. DATA STRUCTURES   PP   100 40   64 P
  04. DATA STRUCTURES AND ALGORITHMS PP   100 40   56 P C   14. COMPUTER GRAPHICS   PP   100 40   62 P
  05. HUMANITIES AND SOCIAL SCIENCE PP   100 40   62 P C   15. COMPUTER ORGANIZATION   PP   100 40   75 P
  06. PROGRAMMING LABORATORY   TW   25 10   23 P C   16. O. O. PROG. & COMP. GRAPH. LAB   TW   50 20   38 P
  07. PROGRAMMING LABORATORY   PR   50 20   41 P C   17. O. O. PROG. & COMP. GRAPH. LAB   PR   50 20   43 P
  08. DIGITAL ELECTRONICS LABORATORY TW   25 10   22 P C   18. MICROPROCESSORS & INTERFACING LABTW   50 20   38 P
  09. DIGITAL ELECTRONICS LABORATORY PR   50 20   32 P C   19. MICROPROCESSORS & INTERFACING LABPR   50 20   43 P
  10. SOFT SKILLS   TW   50 20   40 P C   20. DATA STRUCTURES LABORATORY   TW   50 20   40 P
                                                               21. DATA STRUCTURES LABORATORY   PR   50 20   41 P
GRAND TOTAL = 1041/1500, RESULT: FIRST CLASS WITH DISTINCTION
ORDN. 1 MARKS :
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  S80054209   ANTHONY ELIZABETH XAVIER   ANJALI   , 71347755F   , S80054209 ,   PICT   , S80054209
  01. DISCRETE STRUCTURES   PP   100 40   57 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   41 P
  02. PROGRAMMING & PROBLEM SOLVING PP   100 40   49 P C   12. MICROPROC. & INTERFACING TECHNIQ.PP   100 40   51 P
  03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 40   72 P C   13. DATA STRUCTURES   PP   100 40   58 P
  04. DATA STRUCTURES AND ALGORITHMS PP   100 40   56 P C   14. COMPUTER GRAPHICS   PP   100 40   63 P
  05. HUMANITIES AND SOCIAL SCIENCE PP   100 40   51 P C   15. COMPUTER ORGANIZATION   PP   100 40   56 P
  06. PROGRAMMING LABORATORY   TW   25 10   20 P C   16. O. O. PROG. & COMP. GRAPH. LAB   TW   50 20   42 P
  07. PROGRAMMING LABORATORY   PR   50 20   23 P C   17. O. O. PROG. & COMP. GRAPH. LAB   PR   50 20   40 P
  08. DIGITAL ELECTRONICS LABORATORY TW   25 10   20 P C   18. MICROPROCESSORS & INTERFACING LABTW   50 20   40 P
  09. DIGITAL ELECTRONICS LABORATORY PR   50 20   35 P C   19. MICROPROCESSORS & INTERFACING LABPR   50 20   20 P
  10. SOFT SKILLS   TW   50 20   42 P C   20. DATA STRUCTURES LABORATORY   TW   50 20   40 P
                                                               21. DATA STRUCTURES LABORATORY   PR   50 20   39 P
GRAND TOTAL = 915/1500, RESULT: FIRST CLASS
ORDN. 1 MARKS :
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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.
       OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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  S80054210   ANVEKAR ASHWINI KISHOR   KANCHANA   , 71200763G   , S80054210 , PICT   , S80054210
  01. DISCRETE STRUCTURES   PP   100 40   73 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   46 P
  02. PROGRAMMING & PROBLEM SOLVING PP   100 40   51 P C   12. MICROPROC. & INTERFACING TECHNIQ.PP   100 40   47 P
  03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 40   56 P C   13. DATA STRUCTURES   PP   100 40   58 P
  04. DATA STRUCTURES AND ALGORITHMS PP   100 40   40 P C   14. COMPUTER GRAPHICS   PP   100 40   53 P
  05. HUMANITIES AND SOCIAL SCIENCE PP   100 40   57 P C   15. COMPUTER ORGANIZATION   PP   100 40   58 P
  06. PROGRAMMING LABORATORY   TW   25 10   20 P C   16. O. O. PROG. & COMP. GRAPH. LAB   TW   50 20   35 P
  07. PROGRAMMING LABORATORY   PR   50 20   22 P C   17. O. O. PROG. & COMP. GRAPH. LAB   PR   50 20   40 P
  08. DIGITAL ELECTRONICS LABORATORY TW   25 10   21 P C   18. MICROPROCESSORS & INTERFACING LABTW   50 20   35 P
  09. DIGITAL ELECTRONICS LABORATORY PR   50 20   32 P C   19. MICROPROCESSORS & INTERFACING LABPR   50 20   33 P
  10. SOFT SKILLS   TW   50 20   40 P C   20. DATA STRUCTURES LABORATORY   TW   50 20   35 P
                                                               21. DATA STRUCTURES LABORATORY   PR   50 20   37 P
GRAND TOTAL = 889/1500, RESULT: HIGHER SECOND CLASS
ORDN. 1 MARKS :
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  S80054211   BAGDIYA TUSHAR VIJAYCHAND   VEENA   , 71200771H   , S80054211 , PICT   , S80054211
  01. DISCRETE STRUCTURES   PP   100 40   73 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   68 P
  02. PROGRAMMING & PROBLEM SOLVING PP   100 40   49 P C   12. MICROPROC. & INTERFACING TECHNIQ.PP   100 40   47 P
  03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 40   58 P C   13. DATA STRUCTURES   PP   100 40   55 P
  04. DATA STRUCTURES AND ALGORITHMS PP   100 40   49 P C   14. COMPUTER GRAPHICS   PP   100 40   50 P
  05. HUMANITIES AND SOCIAL SCIENCE PP   100 40   43 P C   15. COMPUTER ORGANIZATION   PP   100 40   54 P
  06. PROGRAMMING LABORATORY   TW   25 10   21 P C   16. O. O. PROG. & COMP. GRAPH. LAB   TW   50 20   39 P
  07. PROGRAMMING LABORATORY   PR   50 20   43 P C   17. O. O. PROG. & COMP. GRAPH. LAB   PR   50 20   40 P
  08. DIGITAL ELECTRONICS LABORATORY TW   25 10   21 P C   18. MICROPROCESSORS & INTERFACING LABTW   50 20   41 P
  09. DIGITAL ELECTRONICS LABORATORY PR   50 20   30 P C   19. MICROPROCESSORS & INTERFACING LABPR   50 20   40 P
  10. SOFT SKILLS   TW   50 20   35 P C   20. DATA STRUCTURES LABORATORY   TW   50 20   39 P
                                                               21. DATA STRUCTURES LABORATORY   PR   50 20   41 P
GRAND TOTAL = 936/1500, RESULT: FIRST CLASS
ORDN. 1 MARKS :
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  S80054212   BAGUL PALLAVI DEEPAK   MADHAVI   , 71200772F   , S80054212 , PICT   , S80054212
  01. DISCRETE STRUCTURES   PP   100 40   73 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   52 P
  02. PROGRAMMING & PROBLEM SOLVING PP   100 40   43 P C   12. MICROPROC. & INTERFACING TECHNIQ.PP   100 40   57 P
  03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 40   46 P C   13. DATA STRUCTURES   PP   100 40   50 P
  04. DATA STRUCTURES AND ALGORITHMS PP   100 40   40 P C   14. COMPUTER GRAPHICS   PP   100 40   51 P
  05. HUMANITIES AND SOCIAL SCIENCE PP   100 40   45 P C   15. COMPUTER ORGANIZATION   PP   100 40   62 P
  06. PROGRAMMING LABORATORY   TW   25 10   19 P C   16. O. O. PROG. & COMP. GRAPH. LAB   TW   50 20   39 P
  07. PROGRAMMING LABORATORY   PR   50 20   36 P C   17. O. O. PROG. & COMP. GRAPH. LAB   PR   50 20   34 P
  08. DIGITAL ELECTRONICS LABORATORY TW   25 10   19 P C   18. MICROPROCESSORS & INTERFACING LABTW   50 20   40 P
  09. DIGITAL ELECTRONICS LABORATORY PR   50 20   29 P C   19. MICROPROCESSORS & INTERFACING LABPR   50 20   35 P
  10. SOFT SKILLS   TW   50 20   35 P C   20. DATA STRUCTURES LABORATORY   TW   50 20   40 P
                                                               21. DATA STRUCTURES LABORATORY   PR   50 20   32 P
GRAND TOTAL = 877/1500, RESULT: HIGHER SECOND CLASS
ORDN. 1 MARKS :
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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
      OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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  S80054213   BAHETI BHUSHAN RAJGOPAL   KIRAN   , 71200773D   , S80054213 , PICT   , S80054213
  01. DISCRETE STRUCTURES   PP   100 40   66 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   78 P
  02. PROGRAMMING & PROBLEM SOLVING PP   100 40   56 P C   12. MICROPROC. & INTERFACING TECHNIQ.PP   100 40   56 P
  03. DIGIT. ELECTRONICS & LOGIC DESIGNPP   100 40   69 P C   13. DATA STRUCTURES   PP   100 40   51 P
  04. DATA STRUCTURES AND ALGORITHMS PP   100 40   40 P C   14. COMPUTER GRAPHICS   PP   100 40   56 P
  05. HUMANITIES AND SOCIAL SCIENCE PP   100 40   53 P C   15. COMPUTER ORGANIZATION   PP   100 40   58 P
  06. PROGRAMMING LABORATORY   TW   25 10   19 P C   16. O. O. PROG. & COMP. GRAPH. LAB TW   50 20   30 P
  07. PROGRAMMING LABORATORY   PR   50 20   41 P C   17. O. O. PROG. & COMP. GRAPH. LAB PR   50 20   39 P
  08. DIGITAL ELECTRONICS LABORATORY TW   25 10   20 P C   18. MICROPROCESSORS & INTERFACING LABTW   50 20   32 P
  09. DIGITAL ELECTRONICS LABORATORY PR   50 20   35 P C   19. MICROPROCESSORS & INTERFACING LABPR   50 20   39 P
  10. SOFT SKILLS   TW   50 20   36 P C   20. DATA STRUCTURES LABORATORY   TW   50 20   32 P
                                                              21. DATA STRUCTURES LABORATORY   PR   50 20   38 P
GRAND TOTAL = 944/1500, RESULT: FIRST CLASS
ORDN. 1 MARKS :
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  S80054214   BAJAJ KRISHNA ANJANIKUMAR   VARSHA   , 71200775L   , S80054214 , PICT   , S80054214
  01. DISCRETE STRUCTURES   PP   100 40   74 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   70 P
  02. PROGRAMMING & PROBLEM SOLVING PP   100 40   52 P C   12. MICROPROC. & INTERFACING TECHNIQ.PP   100 40   69 P
  03. DIGIT. ELECTRONICS & LOGIC DESIGNPP   100 40   65 P C   13. DATA STRUCTURES   PP   100 40   69 P
  04. DATA STRUCTURES AND ALGORITHMS PP   100 40   65 P C   14. COMPUTER GRAPHICS   PP   100 40   64 P
  05. HUMANITIES AND SOCIAL SCIENCE PP   100 40   62 P C   15. COMPUTER ORGANIZATION   PP   100 40   72 P
  06. PROGRAMMING LABORATORY   TW   25 10   21 P C   16. O. O. PROG. & COMP. GRAPH. LAB TW   50 20   41 P
  07. PROGRAMMING LABORATORY   PR   50 20   33 P C   17. O. O. PROG. & COMP. GRAPH. LAB PR   50 20   39 P
  08. DIGITAL ELECTRONICS LABORATORY TW   25 10   19 P C   18. MICROPROCESSORS & INTERFACING LABTW   50 20   43 P
  09. DIGITAL ELECTRONICS LABORATORY PR   50 20   42 P C   19. MICROPROCESSORS & INTERFACING LABPR   50 20   41 P
  10. SOFT SKILLS   TW   50 20   37 P C   20. DATA STRUCTURES LABORATORY   TW   50 20   44 P
                                                              21. DATA STRUCTURES LABORATORY   PR   50 20   44 P
GRAND TOTAL = 1066/1500, RESULT: FIRST CLASS WITH DISTINCTION
ORDN. 1 MARKS :
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  S80054215   BANDEWAR AKASH DEVIDAS   MANISHA   , 71200779C   , S80054215 , PICT   , S80054215
  01. DISCRETE STRUCTURES   PP   100 40   40 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   40 P
  02. PROGRAMMING & PROBLEM SOLVING PP   100 40   52 P   12. MICROPROC. & INTERFACING TECHNIQ.PP   100 40   25 F
  03. DIGIT. ELECTRONICS & LOGIC DESIGNPP   100 40   49 P C   13. DATA STRUCTURES   PP   100 40   AA F
  04. DATA STRUCTURES AND ALGORITHMS PP   100 40   40 P   14. COMPUTER GRAPHICS   PP   100 40   29 F
  05. HUMANITIES AND SOCIAL SCIENCE PP   100 40   43 P C   15. COMPUTER ORGANIZATION   PP   100 40   AA F
  06. PROGRAMMING LABORATORY   TW   25 10   15 P C   16. O. O. PROG. & COMP. GRAPH. LAB TW   50 20   20 P
  07. PROGRAMMING LABORATORY   PR   50 20   25 P C   17. O. O. PROG. & COMP. GRAPH. LAB PR   50 20   AA F
  08. DIGITAL ELECTRONICS LABORATORY TW   25 10   18 P C   18. MICROPROCESSORS & INTERFACING LABTW   50 20   20 P
  09. DIGITAL ELECTRONICS LABORATORY PR   50 20   28 P C   19. MICROPROCESSORS & INTERFACING LABPR   50 20   02 F
  10. SOFT SKILLS   TW   50 20   28 P C   20. DATA STRUCTURES LABORATORY   TW   50 20   20 P
                                                              21. DATA STRUCTURES LABORATORY   PR   50 20   22 P
GRAND TOTAL = 516/1500, RESULT: FAILS
ORDN. 1 MARKS :
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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.
       OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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 S80054216   BANG VIJAY SANTOSHKUMAR   JYOTI   , 71200780G   , S80054216 , PICT   , S80054216
 01. DISCRETE STRUCTURES   PP   100 40   77 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   48 P
 02. PROGRAMMING & PROBLEM SOLVING PP 100 40   56 P C   12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40   58 P
 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 40 67 P C   13. DATA STRUCTURES   PP   100 40   67 P
 04. DATA STRUCTURES AND ALGORITHMS PP 100 40   59 P C   14. COMPUTER GRAPHICS   PP   100 40   65 P
 05. HUMANITIES AND SOCIAL SCIENCE PP 100 40   62 P C   15. COMPUTER ORGANIZATION   PP   100 40   65 P
 06. PROGRAMMING LABORATORY   TW   25 10   21 P C   16. O. O. PROG. & COMP. GRAPH. LAB TW   50 20   41 P
 07. PROGRAMMING LABORATORY   PR   50 20   46 P C   17. O. O. PROG. & COMP. GRAPH. LAB PR   50 20   38 P
 08. DIGITAL ELECTRONICS LABORATORY TW   25 10   21 P C   18. MICROPROCESSORS & INTERFACING LABTW 50 20   38 P
 09. DIGITAL ELECTRONICS LABORATORY PR   50 20   41 P C   19. MICROPROCESSORS & INTERFACING LABPR 50 20   40 P
 10. SOFT SKILLS   TW   50 20   39 P C   20. DATA STRUCTURES LABORATORY   TW   50 20   42 P
                                                            21. DATA STRUCTURES LABORATORY   PR   50 20   44 P
GRAND TOTAL = 1035/1500, RESULT: FIRST CLASS WITH DISTINCTION
ORDN. 1 MARKS :
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 S80054217   BANGAD ANKITA ISHWARPRASAD   USHA   , 71200781E   , S80054217 , PICT   , S80054217
 01. DISCRETE STRUCTURES   PP   100 40   78 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   79 P
 02. PROGRAMMING & PROBLEM SOLVING PP 100 40   47 P C   12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40   61 P
 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 40 66 P C   13. DATA STRUCTURES   PP   100 40   62 P
 04. DATA STRUCTURES AND ALGORITHMS PP 100 40   53 P C   14. COMPUTER GRAPHICS   PP   100 40   68 P
 05. HUMANITIES AND SOCIAL SCIENCE PP 100 40   62 P C   15. COMPUTER ORGANIZATION   PP   100 40   65 P
 06. PROGRAMMING LABORATORY   TW   25 10   19 P C   16. O. O. PROG. & COMP. GRAPH. LAB TW   50 20   38 P
 07. PROGRAMMING LABORATORY   PR   50 20   42 P C   17. O. O. PROG. & COMP. GRAPH. LAB PR   50 20   42 P
 08. DIGITAL ELECTRONICS LABORATORY TW   25 10   20 P C   18. MICROPROCESSORS & INTERFACING LABTW 50 20   40 P
 09. DIGITAL ELECTRONICS LABORATORY PR   50 20   33 P C   19. MICROPROCESSORS & INTERFACING LABPR 50 20   44 P
 10. SOFT SKILLS   TW   50 20   37 P C   20. DATA STRUCTURES LABORATORY   TW   50 20   40 P
                                                            21. DATA STRUCTURES LABORATORY   PR   50 20   41 P
GRAND TOTAL = 1037/1500, RESULT: FIRST CLASS WITH DISTINCTION
ORDN. 1 MARKS :
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 S80054218   BAT PANKAJ RAMSINGH   PUSHPA   , 71200786F   , S80054218 , PICT   , S80054218
 01. DISCRETE STRUCTURES   PP   100 40   50 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   54 P
 02. PROGRAMMING & PROBLEM SOLVING PP 100 40   40 P C   12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40   42 P
 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 40 74 P C   13. DATA STRUCTURES   PP   100 40   49 P
 04. DATA STRUCTURES AND ALGORITHMS PP 100 40   41 P C   14. COMPUTER GRAPHICS   PP   100 40   27 F
 05. HUMANITIES AND SOCIAL SCIENCE PP 100 40   41 P C   15. COMPUTER ORGANIZATION   PP   100 40   58 P
 06. PROGRAMMING LABORATORY   TW   25 10   18 P C   16. O. O. PROG. & COMP. GRAPH. LAB TW   50 20   30 P
 07. PROGRAMMING LABORATORY   PR   50 20   39 P C   17. O. O. PROG. & COMP. GRAPH. LAB PR   50 20   35 P
 08. DIGITAL ELECTRONICS LABORATORY TW   25 10   16 P C   18. MICROPROCESSORS & INTERFACING LABTW 50 20   25 P
 09. DIGITAL ELECTRONICS LABORATORY PR   50 20   30 P C   19. MICROPROCESSORS & INTERFACING LABPR 50 20   35 P
 10. SOFT SKILLS   TW   50 20   32 P C   20. DATA STRUCTURES LABORATORY   TW   50 20   28 P
                                                            21. DATA STRUCTURES LABORATORY   PR   50 20   32 P
GRAND TOTAL = 796/1500, RESULT: FAILS A.T.K.T.
ORDN. 1 MARKS :
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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
          OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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 S80054219   BELE ANAND PURUSHOTTAM   UJJWALA   , 71347756D   , S80054219 ,   PICT   , S80054219
 01. DISCRETE STRUCTURES   PP   100 40   58 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   40 P
 02. PROGRAMMING & PROBLEM SOLVING PP   100 40   59 P C   12. MICROPROC. & INTERFACING TECHNIQ.PP   100 40   64 P
 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 40   66 P C   13. DATA STRUCTURES   PP   100 40   66 P
 04. DATA STRUCTURES AND ALGORITHMS PP   100 40   51 P C   14. COMPUTER GRAPHICS   PP   100 40   67 P
 05. HUMANITIES AND SOCIAL SCIENCE PP   100 40   59 P C   15. COMPUTER ORGANIZATION   PP   100 40   73 P
 06. PROGRAMMING LABORATORY   TW   25 10   20 P C   16. O. O. PROG. & COMP. GRAPH. LAB   TW   50 20   39 P
 07. PROGRAMMING LABORATORY   PR   50 20   26 P C   17. O. O. PROG. & COMP. GRAPH. LAB   PR   50 20   40 P
 08. DIGITAL ELECTRONICS LABORATORY TW   25 10   18 P C   18. MICROPROCESSORS & INTERFACING LABTW   50 20   39 P
 09. DIGITAL ELECTRONICS LABORATORY PR   50 20   30 P C   19. MICROPROCESSORS & INTERFACING LABPR   50 20   45 P
 10. SOFT SKILLS   TW   50 20   39 P C   20. DATA STRUCTURES LABORATORY   TW   50 20   41 P
                                                              21. DATA STRUCTURES LABORATORY   PR   50 20   42 P
GRAND TOTAL = 982/1500, RESULT: FIRST CLASS
ORDN. 1 MARKS :
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 S80054220   BHADKAMKAR SIDDHARTH CHARUDATTA   SNEHA   , 71200789L   , S80054220 ,   PICT   , S80054220
 01. DISCRETE STRUCTURES   PP   100 40   64 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   62 P
 02. PROGRAMMING & PROBLEM SOLVING PP   100 40   51 P C   12. MICROPROC. & INTERFACING TECHNIQ.PP   100 40   54 P
 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 40   69 P C   13. DATA STRUCTURES   PP   100 40   62 P
 04. DATA STRUCTURES AND ALGORITHMS PP   100 40   53 P C   14. COMPUTER GRAPHICS   PP   100 40   64 P
 05. HUMANITIES AND SOCIAL SCIENCE PP   100 40   56 P C   15. COMPUTER ORGANIZATION   PP   100 40   72 P
 06. PROGRAMMING LABORATORY   TW   25 10   23 P C   16. O. O. PROG. & COMP. GRAPH. LAB   TW   50 20   38 P
 07. PROGRAMMING LABORATORY   PR   50 20   38 P C   17. O. O. PROG. & COMP. GRAPH. LAB   PR   50 20   28 P
 08. DIGITAL ELECTRONICS LABORATORY TW   25 10   22 P C   18. MICROPROCESSORS & INTERFACING LABTW   50 20   40 P
 09. DIGITAL ELECTRONICS LABORATORY PR   50 20   34 P C   19. MICROPROCESSORS & INTERFACING LABPR   50 20   35 P
 10. SOFT SKILLS   TW   50 20   36 P C   20. DATA STRUCTURES LABORATORY   TW   50 20   40 P
                                                              21. DATA STRUCTURES LABORATORY   PR   50 20   39 P
GRAND TOTAL = 980/1500, RESULT: FIRST CLASS
ORDN. 1 MARKS :
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 S80054221   BHALERAO SHRADDHA VINOD   SUNITA   , 71347757B   , S80054221 ,   PICT   , S80054221
 01. DISCRETE STRUCTURES   PP   100 40   40 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   27 F
 02. PROGRAMMING & PROBLEM SOLVING PP   100 40   49 P C   12. MICROPROC. & INTERFACING TECHNIQ.PP   100 40   54 P
 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 40   50 P C   13. DATA STRUCTURES   PP   100 40   53 P
 04. DATA STRUCTURES AND ALGORITHMS PP   100 40   40 P   14. COMPUTER GRAPHICS   PP   100 40   50 P
 05. HUMANITIES AND SOCIAL SCIENCE PP   100 40   59 P C   15. COMPUTER ORGANIZATION   PP   100 40   59 P
 06. PROGRAMMING LABORATORY   TW   25 10   21 P C   16. O. O. PROG. & COMP. GRAPH. LAB   TW   50 20   40 P
 07. PROGRAMMING LABORATORY   PR   50 20   20 P C   17. O. O. PROG. & COMP. GRAPH. LAB   PR   50 20   25 P
 08. DIGITAL ELECTRONICS LABORATORY TW   25 10   20 P C   18. MICROPROCESSORS & INTERFACING LABTW   50 20   39 P
 09. DIGITAL ELECTRONICS LABORATORY PR   50 20   20 P C   19. MICROPROCESSORS & INTERFACING LABPR   50 20   25 P
 10. SOFT SKILLS   TW   50 20   42 P C   20. DATA STRUCTURES LABORATORY   TW   50 20   37 P
                                                              21. DATA STRUCTURES LABORATORY   PR   50 20   34 P
GRAND TOTAL = 804/1500, RESULT: FAILS A.T.K.T.
ORDN. 1 MARKS :
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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.
        OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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  S80054222   BHAWSAR SHOBHANA JAYVANT   JAYSHREE   , 71347758L   , S80054222 ,   PICT   , S80054222
  01. DISCRETE STRUCTURES   PP   100 40   47 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   51 P
  02. PROGRAMMING & PROBLEM SOLVING PP   100 40   59 P C   12. MICROPROC. & INTERFACING TECHNIQ.PP   100 40   55 P
  03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 40   56 P C   13. DATA STRUCTURES   PP   100 40   63 P
  04. DATA STRUCTURES AND ALGORITHMS PP   100 40   49 P C   14. COMPUTER GRAPHICS   PP   100 40   47 P
  05. HUMANITIES AND SOCIAL SCIENCE PP   100 40   57 P C   15. COMPUTER ORGANIZATION   PP   100 40   72 P
  06. PROGRAMMING LABORATORY   TW   25 10   20 P C   16. O. O. PROG. & COMP. GRAPH. LAB   TW   50 20   38 P
  07. PROGRAMMING LABORATORY   PR   50 20   25 P C   17. O. O. PROG. & COMP. GRAPH. LAB   PR   50 20   38 P
  08. DIGITAL ELECTRONICS LABORATORY TW   25 10   21 P C   18. MICROPROCESSORS & INTERFACING LABTW   50 20   38 P
  09. DIGITAL ELECTRONICS LABORATORY PR   50 20   28 P C   19. MICROPROCESSORS & INTERFACING LABPR   50 20   37 P
  10. SOFT SKILLS   TW   50 20   37 P C   20. DATA STRUCTURES LABORATORY   TW   50 20   40 P
                                                              21. DATA STRUCTURES LABORATORY   PR   50 20   39 P
GRAND TOTAL = 917/1500, RESULT: FIRST CLASS
ORDN. 1 MARKS :
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  S80054223   BHUTADA RUTUSHA KAMALKISHOR   SUNITA   , 71200798K   , S80054223 ,   PICT   , S80054223
  01. DISCRETE STRUCTURES   PP   100 40   69 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   66 P
  02. PROGRAMMING & PROBLEM SOLVING PP   100 40   51 P C   12. MICROPROC. & INTERFACING TECHNIQ.PP   100 40   58 P
  03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 40   63 P C   13. DATA STRUCTURES   PP   100 40   63 P
  04. DATA STRUCTURES AND ALGORITHMS PP   100 40   53 P C   14. COMPUTER GRAPHICS   PP   100 40   65 P
  05. HUMANITIES AND SOCIAL SCIENCE PP   100 40   64 P C   15. COMPUTER ORGANIZATION   PP   100 40   69 P
  06. PROGRAMMING LABORATORY   TW   25 10   22 P C   16. O. O. PROG. & COMP. GRAPH. LAB   TW   50 20   44 P
  07. PROGRAMMING LABORATORY   PR   50 20   41 P C   17. O. O. PROG. & COMP. GRAPH. LAB   PR   50 20   32 P
  08. DIGITAL ELECTRONICS LABORATORY TW   25 10   21 P C   18. MICROPROCESSORS & INTERFACING LABTW   50 20   43 P
  09. DIGITAL ELECTRONICS LABORATORY PR   50 20   38 P C   19. MICROPROCESSORS & INTERFACING LABPR   50 20   40 P
  10. SOFT SKILLS   TW   50 20   37 P C   20. DATA STRUCTURES LABORATORY   TW   50 20   43 P
                                                              21. DATA STRUCTURES LABORATORY   PR   50 20   40 P
GRAND TOTAL = 1022/1500, RESULT: FIRST CLASS WITH DISTINCTION
ORDN. 1 MARKS :
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  S80054224   BINO JOSEPH   JASMINE   , 71200800E   , S80054224 ,   PICT   , S80054224
  01. DISCRETE STRUCTURES   PP   100 40   79 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   81 P
  02. PROGRAMMING & PROBLEM SOLVING PP   100 40   63 P C   12. MICROPROC. & INTERFACING TECHNIQ.PP   100 40   66 P
  03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 40   75 P C   13. DATA STRUCTURES   PP   100 40   60 P
  04. DATA STRUCTURES AND ALGORITHMS PP   100 40   57 P C   14. COMPUTER GRAPHICS   PP   100 40   59 P
  05. HUMANITIES AND SOCIAL SCIENCE PP   100 40   67 P C   15. COMPUTER ORGANIZATION   PP   100 40   76 P
  06. PROGRAMMING LABORATORY   TW   25 10   23 P C   16. O. O. PROG. & COMP. GRAPH. LAB   TW   50 20   38 P
  07. PROGRAMMING LABORATORY   PR   50 20   42 P C   17. O. O. PROG. & COMP. GRAPH. LAB   PR   50 20   32 P
  08. DIGITAL ELECTRONICS LABORATORY TW   25 10   21 P C   18. MICROPROCESSORS & INTERFACING LABTW   50 20   40 P
  09. DIGITAL ELECTRONICS LABORATORY PR   50 20   40 P C   19. MICROPROCESSORS & INTERFACING LABPR   50 20   40 P
  10. SOFT SKILLS   TW   50 20   38 P C   20. DATA STRUCTURES LABORATORY   TW   50 20   40 P
                                                              21. DATA STRUCTURES LABORATORY   PR   50 20   38 P
GRAND TOTAL = 1075/1500, RESULT: FIRST CLASS WITH DISTINCTION
ORDN. 1 MARKS :
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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
       OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S80054225   BORSE AKSHAY RAJENDRA   SUREKHA   , 71200804H   , S80054225 , PICT   , S80054225
01. DISCRETE STRUCTURES   PP   100 40   56 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   52 P
02. PROGRAMMING & PROBLEM SOLVING PP   100 40   45 P C   12. MICROPROC. & INTERFACING TECHNIQ.PP   100 40   51 P
03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 40   65 P C   13. DATA STRUCTURES   PP   100 40   49 P
04. DATA STRUCTURES AND ALGORITHMS PP   100 40   45 P C   14. COMPUTER GRAPHICS   PP   100 40   54 P
05. HUMANITIES AND SOCIAL SCIENCE PP   100 40   49 P C   15. COMPUTER ORGANIZATION   PP   100 40   64 P
06. PROGRAMMING LABORATORY   TW   25 10   15 P C   16. O. O. PROG. & COMP. GRAPH. LAB   TW   50 20   37 P
07. PROGRAMMING LABORATORY   PR   50 20   41 P C   17. O. O. PROG. & COMP. GRAPH. LAB   PR   50 20   40 P
08. DIGITAL ELECTRONICS LABORATORY TW   25 10   15 P C   18. MICROPROCESSORS & INTERFACING LABTW   50 20   35 P
09. DIGITAL ELECTRONICS LABORATORY PR   50 20   37 P C   19. MICROPROCESSORS & INTERFACING LABPR   50 20   30 P
10. SOFT SKILLS   TW   50 20   33 P C   20. DATA STRUCTURES LABORATORY   TW   50 20   37 P
                                                             21. DATA STRUCTURES LABORATORY   PR   50 20   36 P
GRAND TOTAL = 886/1500, RESULT: HIGHER SECOND CLASS
ORDN. 1 MARKS :
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S80054226   BUDHAWANT MAYUR SHIVAJI   HIRABAI   , 71200806D   , S80054226 , PICT   , S80054226
01. DISCRETE STRUCTURES   PP   100 40   66 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   49 P
02. PROGRAMMING & PROBLEM SOLVING PP   100 40   46 P C   12. MICROPROC. & INTERFACING TECHNIQ.PP   100 40   52 P
03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 40   60 P C   13. DATA STRUCTURES   PP   100 40   55 P
04. DATA STRUCTURES AND ALGORITHMS PP   100 40   48 P C   14. COMPUTER GRAPHICS   PP   100 40   47 P
05. HUMANITIES AND SOCIAL SCIENCE PP   100 40   63 P C   15. COMPUTER ORGANIZATION   PP   100 40   64 P
06. PROGRAMMING LABORATORY   TW   25 10   22 P C   16. O. O. PROG. & COMP. GRAPH. LAB   TW   50 20   39 P
07. PROGRAMMING LABORATORY   PR   50 20   43 P C   17. O. O. PROG. & COMP. GRAPH. LAB   PR   50 20   41 P
08. DIGITAL ELECTRONICS LABORATORY TW   25 10   22 P C   18. MICROPROCESSORS & INTERFACING LABTW   50 20   40 P
09. DIGITAL ELECTRONICS LABORATORY PR   50 20   38 P C   19. MICROPROCESSORS & INTERFACING LABPR   50 20   40 P
10. SOFT SKILLS   TW   50 20   44 P C   20. DATA STRUCTURES LABORATORY   TW   50 20   39 P
                                                             21. DATA STRUCTURES LABORATORY   PR   50 20   38 P
GRAND TOTAL = 956/1500, RESULT: FIRST CLASS
ORDN. 1 MARKS :
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S80054228   CHAUHAN KRUTI SANDEEP   REETA   , 71200813G   , S80054228 , PICT   , S80054228
01. DISCRETE STRUCTURES   PP   100 40   81 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   62 P
02. PROGRAMMING & PROBLEM SOLVING PP   100 40   55 P C   12. MICROPROC. & INTERFACING TECHNIQ.PP   100 40   71 P
03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 40   81 P C   13. DATA STRUCTURES   PP   100 40   65 P
04. DATA STRUCTURES AND ALGORITHMS PP   100 40   47 P C   14. COMPUTER GRAPHICS   PP   100 40   61 P
05. HUMANITIES AND SOCIAL SCIENCE PP   100 40   62 P C   15. COMPUTER ORGANIZATION   PP   100 40   70 P
06. PROGRAMMING LABORATORY   TW   25 10   22 P C   16. O. O. PROG. & COMP. GRAPH. LAB   TW   50 20   42 P
07. PROGRAMMING LABORATORY   PR   50 20   41 P C   17. O. O. PROG. & COMP. GRAPH. LAB   PR   50 20   29 P
08. DIGITAL ELECTRONICS LABORATORY TW   25 10   20 P C   18. MICROPROCESSORS & INTERFACING LABTW   50 20   41 P
09. DIGITAL ELECTRONICS LABORATORY PR   50 20   43 P C   19. MICROPROCESSORS & INTERFACING LABPR   50 20   40 P
10. SOFT SKILLS   TW   50 20   40 P C   20. DATA STRUCTURES LABORATORY   TW   50 20   42 P
                                                             21. DATA STRUCTURES LABORATORY   PR   50 20   40 P
GRAND TOTAL = 1055/1500, RESULT: FIRST CLASS WITH DISTINCTION
ORDN. 1 MARKS :
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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
      OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S80054229   CHAVAN GAURAV SANDIP   MEENAKSHI   , 71200814E   , S80054229 , PICT   , S80054229
 01. DISCRETE STRUCTURES   PP   100 40   79 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   65 P
 02. PROGRAMMING & PROBLEM SOLVING PP   100 40   62 P C   12. MICROPROC. & INTERFACING TECHNIQ.PP   100 40   58 P
 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 40   64 P C   13. DATA STRUCTURES   PP   100 40   54 P
 04. DATA STRUCTURES AND ALGORITHMS PP   100 40   52 P C   14. COMPUTER GRAPHICS   PP   100 40   60 P
 05. HUMANITIES AND SOCIAL SCIENCE PP   100 40   62 P C   15. COMPUTER ORGANIZATION   PP   100 40   65 P
 06. PROGRAMMING LABORATORY   TW   25 10   22 P C   16. O. O. PROG. & COMP. GRAPH. LAB   TW   50 20   43 P
 07. PROGRAMMING LABORATORY   PR   50 20   45 P C   17. O. O. PROG. & COMP. GRAPH. LAB   PR   50 20   43 P
 08. DIGITAL ELECTRONICS LABORATORY TW   25 10   22 P C   18. MICROPROCESSORS & INTERFACING LABTW   50 20   43 P
 09. DIGITAL ELECTRONICS LABORATORY PR   50 20   46 P C   19. MICROPROCESSORS & INTERFACING LABPR   50 20   38 P
 10. SOFT SKILLS   TW   50 20   39 P C   20. DATA STRUCTURES LABORATORY   TW   50 20   44 P
                                                             21. DATA STRUCTURES LABORATORY   PR   50 20   39 P
GRAND TOTAL = 1045/1500, RESULT: FIRST CLASS WITH DISTINCTION
ORDN. 1 MARKS :

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S80054230   CHAVAN PRACHI RAMKRISHNA   SADHANA   , 71200815C   , S80054230 , PICT   , S80054230
 01. DISCRETE STRUCTURES   PP   100 40   75 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   52 P
 02. PROGRAMMING & PROBLEM SOLVING PP   100 40   66 P C   12. MICROPROC. & INTERFACING TECHNIQ.PP   100 40   82 P
 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 40   71 P C   13. DATA STRUCTURES   PP   100 40   56 P
 04. DATA STRUCTURES AND ALGORITHMS PP   100 40   55 P C   14. COMPUTER GRAPHICS   PP   100 40   61 P
 05. HUMANITIES AND SOCIAL SCIENCE PP   100 40   62 P C   15. COMPUTER ORGANIZATION   PP   100 40   75 P
 06. PROGRAMMING LABORATORY   TW   25 10   21 P C   16. O. O. PROG. & COMP. GRAPH. LAB   TW   50 20   41 P
 07. PROGRAMMING LABORATORY   PR   50 20   35 P C   17. O. O. PROG. & COMP. GRAPH. LAB   PR   50 20   33 P
 08. DIGITAL ELECTRONICS LABORATORY TW   25 10   20 P C   18. MICROPROCESSORS & INTERFACING LABTW   50 20   41 P
 09. DIGITAL ELECTRONICS LABORATORY PR   50 20   35 P C   19. MICROPROCESSORS & INTERFACING LABPR   50 20   40 P
 10. SOFT SKILLS   TW   50 20   41 P C   20. DATA STRUCTURES LABORATORY   TW   50 20   43 P
                                                             21. DATA STRUCTURES LABORATORY   PR   50 20   38 P
GRAND TOTAL = 1043/1500, RESULT: FIRST CLASS WITH DISTINCTION
ORDN. 1 MARKS :

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S80054231   CHAVHAN KUNAL RAVINDRA   LALITA   , 71200819F   , S80054231 , PICT   , S80054231
 01. DISCRETE STRUCTURES   PP   100 40   50 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   21 F
 02. PROGRAMMING & PROBLEM SOLVING PP   100 40   44 P C   12. MICROPROC. & INTERFACING TECHNIQ.PP   100 40   46 P
 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 40   40 P C   13. DATA STRUCTURES   PP   100 40   51 P
 04. DATA STRUCTURES AND ALGORITHMS PP   100 40   40 P   14. COMPUTER GRAPHICS   PP   100 40   40 P
 05. HUMANITIES AND SOCIAL SCIENCE PP   100 40   52 P C   15. COMPUTER ORGANIZATION   PP   100 40   40 P
 06. PROGRAMMING LABORATORY   TW   25 10   15 P C   16. O. O. PROG. & COMP. GRAPH. LAB   TW   50 20   30 P
 07. PROGRAMMING LABORATORY   PR   50 20   28 P C   17. O. O. PROG. & COMP. GRAPH. LAB   PR   50 20   35 P
 08. DIGITAL ELECTRONICS LABORATORY TW   25 10   17 P C   18. MICROPROCESSORS & INTERFACING LABTW   50 20   30 P
 09. DIGITAL ELECTRONICS LABORATORY PR   50 20   25 P   19. MICROPROCESSORS & INTERFACING LABPR   50 20   24 P
 10. SOFT SKILLS   TW   50 20   28 P C   20. DATA STRUCTURES LABORATORY   TW   50 20   30 P
                                                             21. DATA STRUCTURES LABORATORY   PR   50 20   25 P
GRAND TOTAL = 711/1500, RESULT: FAILS A.T.K.T.
ORDN. 1 MARKS :

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
       OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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 S80054232   DATIR DEEPAK HARIBHAU   MANDA   , 71200828E   , S80054232 , PICT   , S80054232
 01. DISCRETE STRUCTURES   PP   100 40   52 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   24 F
 02. PROGRAMMING & PROBLEM SOLVING PP   100 40   45 P   12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40   05 F
 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 40   40 P   13. DATA STRUCTURES   PP   100 40   25 F
 04. DATA STRUCTURES AND ALGORITHMS PP   100 40   15 F   14. COMPUTER GRAPHICS   PP   100 40   40 P
 05. HUMANITIES AND SOCIAL SCIENCE PP   100 40   42 P   15. COMPUTER ORGANIZATION   PP   100 40   28 F
 06. PROGRAMMING LABORATORY   TW   25 10   14 P C   16. O. O. PROG. & COMP. GRAPH. LAB   TW   50 20   30 P
 07. PROGRAMMING LABORATORY   PR   50 20   28 P C   17. O. O. PROG. & COMP. GRAPH. LAB   PR   50 20   30 P
 08. DIGITAL ELECTRONICS LABORATORY TW   25 10   14 P C   18. MICROPROCESSORS & INTERFACING LABTW   50 20   24 P
 09. DIGITAL ELECTRONICS LABORATORY PR   50 20   21 P   19. MICROPROCESSORS & INTERFACING LABPR   50 20   04 F
 10. SOFT SKILLS   TW   50 20   28 P C   20. DATA STRUCTURES LABORATORY   TW   50 20   28 P
                                                            21. DATA STRUCTURES LABORATORY   PR   50 20   10 F
GRAND TOTAL = 547/1500, RESULT: FAILS
ORDN. 1 MARKS :
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 S80054233   DESAI SHAILEE MIHIR   PALLAVI   , 71200830G   , S80054233 , PICT   , S80054233
 01. DISCRETE STRUCTURES   PP   100 40   64 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   46 P
 02. PROGRAMMING & PROBLEM SOLVING PP   100 40   51 P C   12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40   67 P
 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 40   57 P C 13. DATA STRUCTURES   PP   100 40   58 P
 04. DATA STRUCTURES AND ALGORITHMS PP   100 40   55 P C 14. COMPUTER GRAPHICS   PP   100 40   62 P
 05. HUMANITIES AND SOCIAL SCIENCE PP   100 40   62 P C   15. COMPUTER ORGANIZATION   PP   100 40   63 P
 06. PROGRAMMING LABORATORY   TW   25 10   19 P C   16. O. O. PROG. & COMP. GRAPH. LAB   TW   50 20   38 P
 07. PROGRAMMING LABORATORY   PR   50 20   24 P C   17. O. O. PROG. & COMP. GRAPH. LAB   PR   50 20   25 P
 08. DIGITAL ELECTRONICS LABORATORY TW   25 10   22 P C   18. MICROPROCESSORS & INTERFACING LABTW   50 20   39 P
 09. DIGITAL ELECTRONICS LABORATORY PR   50 20   25 P C   19. MICROPROCESSORS & INTERFACING LABPR   50 20   28 P
 10. SOFT SKILLS   TW   50 20   41 P C   20. DATA STRUCTURES LABORATORY   TW   50 20   38 P
                                                            21. DATA STRUCTURES LABORATORY   PR   50 20   25 P
GRAND TOTAL = 909/1500, RESULT: FIRST CLASS
ORDN. 1 MARKS :
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 S80054234   DESHMUKH POOJA DEEPAK   KANCHAN   , 71347760B   , S80054234 , PICT   , S80054234
 01. DISCRETE STRUCTURES   PP   100 40   51 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   40 P
 02. PROGRAMMING & PROBLEM SOLVING PP   100 40   49 P C   12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40   63 P
 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 40   60 P C 13. DATA STRUCTURES   PP   100 40   55 P
 04. DATA STRUCTURES AND ALGORITHMS PP   100 40   42 P C 14. COMPUTER GRAPHICS   PP   100 40   59 P
 05. HUMANITIES AND SOCIAL SCIENCE PP   100 40   53 P C   15. COMPUTER ORGANIZATION   PP   100 40   71 P
 06. PROGRAMMING LABORATORY   TW   25 10   20 P C   16. O. O. PROG. & COMP. GRAPH. LAB   TW   50 20   44 P
 07. PROGRAMMING LABORATORY   PR   50 20   20 P C   17. O. O. PROG. & COMP. GRAPH. LAB   PR   50 20   35 P
 08. DIGITAL ELECTRONICS LABORATORY TW   25 10   20 P C   18. MICROPROCESSORS & INTERFACING LABTW   50 20   42 P
 09. DIGITAL ELECTRONICS LABORATORY PR   50 20   30 P C   19. MICROPROCESSORS & INTERFACING LABPR   50 20   38 P
 10. SOFT SKILLS   TW   50 20   44 P C   20. DATA STRUCTURES LABORATORY   TW   50 20   44 P
                                                            21. DATA STRUCTURES LABORATORY   PR   50 20   40 P
GRAND TOTAL = 920/1500, RESULT: FIRST CLASS
ORDN. 1 MARKS :
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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.
          OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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  S80054235   DHAGE SHRADDHA BHAUSAHEB   SUMAN   , 71200837D   , S80054235 ,   PICT   , S80054235
  01. DISCRETE STRUCTURES   PP   100 40   66 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   54 P
  02. PROGRAMMING & PROBLEM SOLVING PP   100 40   56 P C   12. MICROPROC. & INTERFACING TECHNIQ.PP   100 40   67 P
  03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 40   59 P C   13. DATA STRUCTURES   PP   100 40   60 P
  04. DATA STRUCTURES AND ALGORITHMS PP   100 40   50 P C   14. COMPUTER GRAPHICS   PP   100 40   61 P
  05. HUMANITIES AND SOCIAL SCIENCE PP   100 40   63 P C   15. COMPUTER ORGANIZATION   PP   100 40   72 P
  06. PROGRAMMING LABORATORY   TW   25 10   21 P C   16. O. O. PROG. & COMP. GRAPH. LAB   TW   50 20   41 P
  07. PROGRAMMING LABORATORY   PR   50 20   29 P C   17. O. O. PROG. & COMP. GRAPH. LAB   PR   50 20   33 P
  08. DIGITAL ELECTRONICS LABORATORY TW   25 10   19 P C   18. MICROPROCESSORS & INTERFACING LABTW   50 20   43 P
  09. DIGITAL ELECTRONICS LABORATORY PR   50 20   34 P C   19. MICROPROCESSORS & INTERFACING LABPR   50 20   37 P
  10. SOFT SKILLS   TW   50 20   37 P C   20. DATA STRUCTURES LABORATORY   TW   50 20   42 P
                                                                21. DATA STRUCTURES LABORATORY   PR   50 20   38 P
GRAND TOTAL = 982/1500, RESULT: FIRST CLASS
ORDN. 1 MARKS :
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  S80054236   DHAVALIKAR RAHUL DEEPAK   SMITA   , 71200839L   , S80054236 ,   PICT   , S80054236
  01. DISCRETE STRUCTURES   PP   100 40   74 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   70 P
  02. PROGRAMMING & PROBLEM SOLVING PP   100 40   68 P C   12. MICROPROC. & INTERFACING TECHNIQ.PP   100 40   71 P
  03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 40   60 P C   13. DATA STRUCTURES   PP   100 40   58 P
  04. DATA STRUCTURES AND ALGORITHMS PP   100 40   60 P C   14. COMPUTER GRAPHICS   PP   100 40   61 P
  05. HUMANITIES AND SOCIAL SCIENCE PP   100 40   56 P C   15. COMPUTER ORGANIZATION   PP   100 40   67 P
  06. PROGRAMMING LABORATORY   TW   25 10   23 P C   16. O. O. PROG. & COMP. GRAPH. LAB   TW   50 20   45 P
  07. PROGRAMMING LABORATORY   PR   50 20   42 P C   17. O. O. PROG. & COMP. GRAPH. LAB   PR   50 20   44 P
  08. DIGITAL ELECTRONICS LABORATORY TW   25 10   21 P C   18. MICROPROCESSORS & INTERFACING LABTW   50 20   45 P
  09. DIGITAL ELECTRONICS LABORATORY PR   50 20   38 P C   19. MICROPROCESSORS & INTERFACING LABPR   50 20   44 P
  10. SOFT SKILLS   TW   50 20   38 P C   20. DATA STRUCTURES LABORATORY   TW   50 20   45 P
                                                                21. DATA STRUCTURES LABORATORY   PR   50 20   45 P
GRAND TOTAL = 1075/1500, RESULT: FIRST CLASS WITH DISTINCTION
ORDN. 1 MARKS :
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  S80054237   DHEKALE AMRUTA RAMBHAU   KANTABAI   , 71200840D   , S80054237 ,   PICT   , S80054237
  01. DISCRETE STRUCTURES   PP   100 40   81 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   42 P
  02. PROGRAMMING & PROBLEM SOLVING PP   100 40   49 P C   12. MICROPROC. & INTERFACING TECHNIQ.PP   100 40   59 P
  03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 40   56 P C   13. DATA STRUCTURES   PP   100 40   60 P
  04. DATA STRUCTURES AND ALGORITHMS PP   100 40   41 P C   14. COMPUTER GRAPHICS   PP   100 40   63 P
  05. HUMANITIES AND SOCIAL SCIENCE PP   100 40   60 P C   15. COMPUTER ORGANIZATION   PP   100 40   55 P
  06. PROGRAMMING LABORATORY   TW   25 10   20 P C   16. O. O. PROG. & COMP. GRAPH. LAB   TW   50 20   40 P
  07. PROGRAMMING LABORATORY   PR   50 20   38 P C   17. O. O. PROG. & COMP. GRAPH. LAB   PR   50 20   35 P
  08. DIGITAL ELECTRONICS LABORATORY TW   25 10   18 P C   18. MICROPROCESSORS & INTERFACING LABTW   50 20   42 P
  09. DIGITAL ELECTRONICS LABORATORY PR   50 20   30 P C   19. MICROPROCESSORS & INTERFACING LABPR   50 20   35 P
  10. SOFT SKILLS   TW   50 20   36 P C   20. DATA STRUCTURES LABORATORY   TW   50 20   43 P
                                                                21. DATA STRUCTURES LABORATORY   PR   50 20   32 P
GRAND TOTAL = 935/1500, RESULT: FIRST CLASS
ORDN. 1 MARKS :
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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.
        OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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  S80054238   DHOLE ANIKET RAJENDRA   SUNITA   , 71200841B   , S80054238 ,   PICT   , S80054238
  01. DISCRETE STRUCTURES   PP   100 40   74 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   65 P
  02. PROGRAMMING & PROBLEM SOLVING PP   100 40   61 P C   12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40   54 P
  03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 40   67 P C   13. DATA STRUCTURES   PP   100 40   61 P
  04. DATA STRUCTURES AND ALGORITHMS PP 100 40   55 P C   14. COMPUTER GRAPHICS   PP   100 40   54 P
  05. HUMANITIES AND SOCIAL SCIENCE PP   100 40   60 P C   15. COMPUTER ORGANIZATION   PP   100 40   49 P
  06. PROGRAMMING LABORATORY   TW   25 10   23 P C   16. O. O. PROG. & COMP. GRAPH. LAB   TW   50 20   32 P
  07. PROGRAMMING LABORATORY   PR   50 20   44 P C   17. O. O. PROG. & COMP. GRAPH. LAB   PR   50 20   39 P
  08. DIGITAL ELECTRONICS LABORATORY TW   25 10   23 P C   18. MICROPROCESSORS & INTERFACING LABTW   50 20   32 P
  09. DIGITAL ELECTRONICS LABORATORY PR   50 20   37 P C   19. MICROPROCESSORS & INTERFACING LABPR   50 20   44 P
  10. SOFT SKILLS   TW   50 20   34 P C   20. DATA STRUCTURES LABORATORY   TW   50 20   33 P
                                                             21. DATA STRUCTURES LABORATORY   PR   50 20   41 P
GRAND TOTAL = 982/1500, RESULT: FIRST CLASS
ORDN. 1 MARKS :
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  S80054239   DINGANE RASHMI SUNIL   CHITRA   , 71200844G   , S80054239 ,   PICT   , S80054239
  01. DISCRETE STRUCTURES   PP   100 40   78 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   70 P
  02. PROGRAMMING & PROBLEM SOLVING PP   100 40   68 P C   12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40   82 P
  03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 40   71 P C   13. DATA STRUCTURES   PP   100 40   56 P
  04. DATA STRUCTURES AND ALGORITHMS PP 100 40   48 P C   14. COMPUTER GRAPHICS   PP   100 40   66 P
  05. HUMANITIES AND SOCIAL SCIENCE PP   100 40   63 P C   15. COMPUTER ORGANIZATION   PP   100 40   71 P
  06. PROGRAMMING LABORATORY   TW   25 10   21 P C   16. O. O. PROG. & COMP. GRAPH. LAB   TW   50 20   43 P
  07. PROGRAMMING LABORATORY   PR   50 20   39 P C   17. O. O. PROG. & COMP. GRAPH. LAB   PR   50 20   42 P
  08. DIGITAL ELECTRONICS LABORATORY TW   25 10   20 P C   18. MICROPROCESSORS & INTERFACING LABTW   50 20   38 P
  09. DIGITAL ELECTRONICS LABORATORY PR   50 20   36 P C   19. MICROPROCESSORS & INTERFACING LABPR   50 20   46 P
  10. SOFT SKILLS   TW   50 20   40 P C   20. DATA STRUCTURES LABORATORY   TW   50 20   43 P
                                                             21. DATA STRUCTURES LABORATORY   PR   50 20   43 P
GRAND TOTAL = 1084/1500, RESULT: FIRST CLASS WITH DISTINCTION
ORDN. 1 MARKS :
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  S80054240   GADGE PRASHIK SUNIL   MALA   , 71200851K   , S80054240 ,   PICT   , S80054240
  01. DISCRETE STRUCTURES   PP   100 40   54 P   11. ENGINEERING MATHEMATICS III   PP   100 40   02 F
  02. PROGRAMMING & PROBLEM SOLVING PP   100 40   40 P C   12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40   08 F
  03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 40   40 P C   13. DATA STRUCTURES   PP   100 40   40 P
  04. DATA STRUCTURES AND ALGORITHMS PP 100 40   40 P   14. COMPUTER GRAPHICS   PP   100 40   50 P
  05. HUMANITIES AND SOCIAL SCIENCE PP   100 40   57 P C   15. COMPUTER ORGANIZATION   PP   100 40   40 P
  06. PROGRAMMING LABORATORY   TW   25 10   14 P C   16. O. O. PROG. & COMP. GRAPH. LAB   TW   50 20   35 P
  07. PROGRAMMING LABORATORY   PR   50 20   02 F   17. O. O. PROG. & COMP. GRAPH. LAB   PR   50 20   17 F
  08. DIGITAL ELECTRONICS LABORATORY TW   25 10   17 P C   18. MICROPROCESSORS & INTERFACING LABTW   50 20   37 P
  09. DIGITAL ELECTRONICS LABORATORY PR   50 20   35 P   19. MICROPROCESSORS & INTERFACING LABPR   50 20   20 P
  10. SOFT SKILLS   TW   50 20   31 P C   20. DATA STRUCTURES LABORATORY   TW   50 20   37 P
                                                             21. DATA STRUCTURES LABORATORY   PR   50 20   21 P
GRAND TOTAL = 637/1500, RESULT: FAILS A.T.K.T.   RESULT RESERVED FOR BKLG
ORDN. 1 MARKS :
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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.
       OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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S80054241   GAIKWAD KAJAL PANDURANG   SINDHU   , 71200852H   , S80054241 ,   PICT   , S80054241
01. DISCRETE STRUCTURES   PP   100 40   62 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   67 P
02. PROGRAMMING & PROBLEM SOLVING PP   100 40   63 P C   12. MICROPROC. & INTERFACING TECHNIQ.PP   100 40   61 P
03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 40   73 P C   13. DATA STRUCTURES   PP   100 40   60 P
04. DATA STRUCTURES AND ALGORITHMS PP   100 40   63 P C   14. COMPUTER GRAPHICS   PP   100 40   63 P
05. HUMANITIES AND SOCIAL SCIENCE PP   100 40   60 P C   15. COMPUTER ORGANIZATION   PP   100 40   67 P
06. PROGRAMMING LABORATORY   TW   25 10   22 P C   16. O. O. PROG. & COMP. GRAPH. LAB   TW   50 20   42 P
07. PROGRAMMING LABORATORY   PR   50 20   40 P C   17. O. O. PROG. & COMP. GRAPH. LAB   PR   50 20   38 P
08. DIGITAL ELECTRONICS LABORATORY TW   25 10   24 P C   18. MICROPROCESSORS & INTERFACING LABTW   50 20   41 P
09. DIGITAL ELECTRONICS LABORATORY PR   50 20   39 P C   19. MICROPROCESSORS & INTERFACING LABPR   50 20   40 P
10. SOFT SKILLS   TW   50 20   41 P C   20. DATA STRUCTURES LABORATORY   TW   50 20   41 P
                                                            21. DATA STRUCTURES LABORATORY   PR   50 20   40 P
GRAND TOTAL = 1047/1500, RESULT: FIRST CLASS WITH DISTINCTION
ORDN. 1 MARKS :
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S80054242   GAIKWAD POOJA ANIL   SWATI   , 71347761L   , S80054242 ,   PICT   , S80054242
01. DISCRETE STRUCTURES   PP   100 40   59 P   11. ENGINEERING MATHEMATICS III   PP   100 40   07 F
02. PROGRAMMING & PROBLEM SOLVING PP   100 40   50 P C   12. MICROPROC. & INTERFACING TECHNIQ.PP   100 40   60 P
03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 40   54 P C   13. DATA STRUCTURES   PP   100 40   45 P
04. DATA STRUCTURES AND ALGORITHMS PP   100 40   40 P C   14. COMPUTER GRAPHICS   PP   100 40   47 P
05. HUMANITIES AND SOCIAL SCIENCE PP   100 40   54 P C   15. COMPUTER ORGANIZATION   PP   100 40   57 P
06. PROGRAMMING LABORATORY   TW   25 10   22 P C   16. O. O. PROG. & COMP. GRAPH. LAB   TW   50 20   40 P
07. PROGRAMMING LABORATORY   PR   50 20   35 P   17. O. O. PROG. & COMP. GRAPH. LAB   PR   50 20   37 P
08. DIGITAL ELECTRONICS LABORATORY TW   25 10   23 P C   18. MICROPROCESSORS & INTERFACING LABTW   50 20   40 P
09. DIGITAL ELECTRONICS LABORATORY PR   50 20   20 P C   19. MICROPROCESSORS & INTERFACING LABPR   50 20   25 P
10. SOFT SKILLS   TW   50 20   44 P C   20. DATA STRUCTURES LABORATORY   TW   50 20   40 P
                                                            21. DATA STRUCTURES LABORATORY   PR   50 20   30 P
GRAND TOTAL = 829/1500, RESULT: FAILS A.T.K.T.
ORDN. 1 MARKS :
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S80054243   GAURAV MISHRA   MAMATA   , 71200858G   , S80054243 ,   PICT   , S80054243
01. DISCRETE STRUCTURES   PP   100 40   77 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   40 P
02. PROGRAMMING & PROBLEM SOLVING PP   100 40   54 P C   12. MICROPROC. & INTERFACING TECHNIQ.PP   100 40   65 P
03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 40   65 P C   13. DATA STRUCTURES   PP   100 40   54 P
04. DATA STRUCTURES AND ALGORITHMS PP   100 40   48 P C   14. COMPUTER GRAPHICS   PP   100 40   54 P
05. HUMANITIES AND SOCIAL SCIENCE PP   100 40   62 P C   15. COMPUTER ORGANIZATION   PP   100 40   59 P
06. PROGRAMMING LABORATORY   TW   25 10   20 P C   16. O. O. PROG. & COMP. GRAPH. LAB   TW   50 20   41 P
07. PROGRAMMING LABORATORY   PR   50 20   28 P C   17. O. O. PROG. & COMP. GRAPH. LAB   PR   50 20   41 P
08. DIGITAL ELECTRONICS LABORATORY TW   25 10   20 P C   18. MICROPROCESSORS & INTERFACING LABTW   50 20   40 P
09. DIGITAL ELECTRONICS LABORATORY PR   50 20   38 P C   19. MICROPROCESSORS & INTERFACING LABPR   50 20   34 P
10. SOFT SKILLS   TW   50 20   41 P C   20. DATA STRUCTURES LABORATORY   TW   50 20   43 P
                                                            21. DATA STRUCTURES LABORATORY   PR   50 20   34 P
GRAND TOTAL = 958/1500, RESULT: FIRST CLASS
ORDN. 1 MARKS :
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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
      OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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 S80054244   GAURI SANDEEP DESHMUKH              AARTI            , 71200859E    , S80054244 ,  PICT     , S80054244
 01. DISCRETE STRUCTURES          PP   100  40   74  P C   11. ENGINEERING MATHEMATICS III     PP   100  40   57  P
 02. PROGRAMMING & PROBLEM SOLVING  PP   100  40   56  P C   12. MICROPROC. & INTERFACING TECHNIQ.PP   100  40   63  P
 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100  40   75  P C   13. DATA STRUCTURES               PP   100  40   57  P
 04. DATA STRUCTURES AND ALGORITHMS PP   100  40   47  P C   14. COMPUTER GRAPHICS             PP   100  40   64  P
 05. HUMANITIES AND SOCIAL SCIENCE  PP   100  40   64  P C   15. COMPUTER ORGANIZATION         PP   100  40   65  P
 06. PROGRAMMING LABORATORY       TW    25  10   22  P C   16. O. O. PROG. & COMP. GRAPH. LAB  TW    50  20   41  P
 07. PROGRAMMING LABORATORY       PR    50  20   40  P C   17. O. O. PROG. & COMP. GRAPH. LAB  PR    50  20   32  P
 08. DIGITAL ELECTRONICS LABORATORY TW  25  10   22  P C   18. MICROPROCESSORS & INTERFACING LABTW    50  20   41  P
 09. DIGITAL ELECTRONICS LABORATORY PR  50  20   40  P C   19. MICROPROCESSORS & INTERFACING LABPR    50  20   40  P
 10. SOFT SKILLS                  TW    50  20   41  P C   20. DATA STRUCTURES LABORATORY     TW    50  20   41  P
                                                          21. DATA STRUCTURES LABORATORY     PR    50  20   42  P
GRAND TOTAL = 1024/1500, RESULT: FIRST CLASS WITH DISTINCTION
ORDN. 1 MARKS :
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 S80054245   GAWALI PRAJAKTA PANDURANG           SHOBHA           , 71200861G    , S80054245 ,  PICT     , S80054245
 01. DISCRETE STRUCTURES          PP   100  40   40  P C   11. ENGINEERING MATHEMATICS III     PP   100  40   25  F
 02. PROGRAMMING & PROBLEM SOLVING  PP   100  40   58  P     12. MICROPROC. & INTERFACING TECHNIQ.PP   100  40   46  P
 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100  40   40  P C   13. DATA STRUCTURES               PP   100  40   40  P
 04. DATA STRUCTURES AND ALGORITHMS PP   100  40   28  F     14. COMPUTER GRAPHICS             PP   100  40   41  P
 05. HUMANITIES AND SOCIAL SCIENCE  PP   100  40   55  P C   15. COMPUTER ORGANIZATION         PP   100  40   40  P
 06. PROGRAMMING LABORATORY       TW    25  10   18  P C   16. O. O. PROG. & COMP. GRAPH. LAB  TW    50  20   39  P
 07. PROGRAMMING LABORATORY       PR    50  20   20  P C   17. O. O. PROG. & COMP. GRAPH. LAB  PR    50  20   25  P
 08. DIGITAL ELECTRONICS LABORATORY TW  25  10   19  P C   18. MICROPROCESSORS & INTERFACING LABTW    50  20   39  P
 09. DIGITAL ELECTRONICS LABORATORY PR  50  20   25  P C   19. MICROPROCESSORS & INTERFACING LABPR    50  20   32  P
 10. SOFT SKILLS                  TW    50  20   40  P C   20. DATA STRUCTURES LABORATORY     TW    50  20   39  P
                                                          21. DATA STRUCTURES LABORATORY     PR    50  20   20  P
GRAND TOTAL =  729/1500, RESULT: FAILS A.T.K.T.
ORDN. 1 MARKS :
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 S80054246   GAWANDE KETKI NANDKISHORE           SUSHAMA          , 71200862E    , S80054246 ,  PICT     , S80054246
 01. DISCRETE STRUCTURES          PP   100  40   54  P C   11. ENGINEERING MATHEMATICS III     PP   100  40   40  P
 02. PROGRAMMING & PROBLEM SOLVING  PP   100  40   43  P C   12. MICROPROC. & INTERFACING TECHNIQ.PP   100  40   49  P
 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100  40   66  P C   13. DATA STRUCTURES               PP   100  40   48  P
 04. DATA STRUCTURES AND ALGORITHMS PP   100  40   40  P C   14. COMPUTER GRAPHICS             PP   100  40   60  P
 05. HUMANITIES AND SOCIAL SCIENCE  PP   100  40   59  P C   15. COMPUTER ORGANIZATION         PP   100  40   63  P
 06. PROGRAMMING LABORATORY       TW    25  10   20  P C   16. O. O. PROG. & COMP. GRAPH. LAB  TW    50  20   37  P
 07. PROGRAMMING LABORATORY       PR    50  20   34  P C   17. O. O. PROG. & COMP. GRAPH. LAB  PR    50  20   39  P
 08. DIGITAL ELECTRONICS LABORATORY TW  25  10   21  P C   18. MICROPROCESSORS & INTERFACING LABTW    50  20   35  P
 09. DIGITAL ELECTRONICS LABORATORY PR  50  20   34  P C   19. MICROPROCESSORS & INTERFACING LABPR    50  20   25  P
 10. SOFT SKILLS                  TW    50  20   38  P C   20. DATA STRUCTURES LABORATORY     TW    50  20   38  P
                                                          21. DATA STRUCTURES LABORATORY     PR    50  20   34  P
GRAND TOTAL =  877/1500, RESULT: HIGHER SECOND CLASS
ORDN. 1 MARKS :
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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO.,   PREVIOUS SEAT NO.,   COLLEGE,   SEAT NO.
         OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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  S80054247   GOVIND BAPU CHAUDHARY   SUMAN   , 71200870F   , S80054247 ,   PICT   , S80054247
  01. DISCRETE STRUCTURES   PP   100 40   57 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   54 P
  02. PROGRAMMING & PROBLEM SOLVING PP 100 40   54 P C   12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40   67 P
  03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 40   66 P C 13. DATA STRUCTURES   PP   100 40   62 P
  04. DATA STRUCTURES AND ALGORITHMS PP 100 40   45 P C   14. COMPUTER GRAPHICS   PP   100 40   51 P
  05. HUMANITIES AND SOCIAL SCIENCE PP   100 40   65 P C   15. COMPUTER ORGANIZATION   PP   100 40   68 P
  06. PROGRAMMING LABORATORY   TW   25 10   22 P C   16. O. O. PROG. & COMP. GRAPH. LAB   TW   50 20   44 P
  07. PROGRAMMING LABORATORY   PR   50 20   40 P C   17. O. O. PROG. & COMP. GRAPH. LAB   PR   50 20   46 P
  08. DIGITAL ELECTRONICS LABORATORY TW   25 10   22 P C   18. MICROPROCESSORS & INTERFACING LABTW   50 20   45 P
  09. DIGITAL ELECTRONICS LABORATORY PR   50 20   38 P C   19. MICROPROCESSORS & INTERFACING LABPR   50 20   44 P
  10. SOFT SKILLS   TW   50 20   43 P C   20. DATA STRUCTURES LABORATORY   TW   50 20   43 P
                                                             21. DATA STRUCTURES LABORATORY   PR   50 20   38 P
GRAND TOTAL = 1014/1500, RESULT: FIRST CLASS WITH DISTINCTION
ORDN. 1 MARKS :
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  S80054248   HALKARE PRAJAKTA DNYANDEO   SANGITA   , 71200876E   , S80054248 ,   PICT   , S80054248
  01. DISCRETE STRUCTURES   PP   100 40   58 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   78 P
  02. PROGRAMMING & PROBLEM SOLVING PP 100 40   65 P C   12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40   64 P
  03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 40   58 P C 13. DATA STRUCTURES   PP   100 40   55 P
  04. DATA STRUCTURES AND ALGORITHMS PP 100 40   59 P C   14. COMPUTER GRAPHICS   PP   100 40   53 P
  05. HUMANITIES AND SOCIAL SCIENCE PP   100 40   56 P C   15. COMPUTER ORGANIZATION   PP   100 40   64 P
  06. PROGRAMMING LABORATORY   TW   25 10   20 P C   16. O. O. PROG. & COMP. GRAPH. LAB   TW   50 20   44 P
  07. PROGRAMMING LABORATORY   PR   50 20   38 P C   17. O. O. PROG. & COMP. GRAPH. LAB   PR   50 20   41 P
  08. DIGITAL ELECTRONICS LABORATORY TW   25 10   20 P C   18. MICROPROCESSORS & INTERFACING LABTW   50 20   45 P
  09. DIGITAL ELECTRONICS LABORATORY PR   50 20   39 P C   19. MICROPROCESSORS & INTERFACING LABPR   50 20   42 P
  10. SOFT SKILLS   TW   50 20   41 P C   20. DATA STRUCTURES LABORATORY   TW   50 20   41 P
                                                             21. DATA STRUCTURES LABORATORY   PR   50 20   39 P
GRAND TOTAL = 1020/1500, RESULT: FIRST CLASS WITH DISTINCTION
ORDN. 1 MARKS :
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  S80054249   HOOLI MAYURESH BASAVARAJ   SHAILA   , 71200882K   , S80054249 ,   PICT   , S80054249
  01. DISCRETE STRUCTURES   PP   100 40   54 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   40 P
  02. PROGRAMMING & PROBLEM SOLVING PP 100 40   40 P C   12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40   60 P
  03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 40   57 P C 13. DATA STRUCTURES   PP   100 40   42 P
  04. DATA STRUCTURES AND ALGORITHMS PP 100 40   49 P C   14. COMPUTER GRAPHICS   PP   100 40   47 P
  05. HUMANITIES AND SOCIAL SCIENCE PP   100 40   57 P C   15. COMPUTER ORGANIZATION   PP   100 40   45 P
  06. PROGRAMMING LABORATORY   TW   25 10   23 P C   16. O. O. PROG. & COMP. GRAPH. LAB   TW   50 20   40 P
  07. PROGRAMMING LABORATORY   PR   50 20   40 P C   17. O. O. PROG. & COMP. GRAPH. LAB   PR   50 20   39 P
  08. DIGITAL ELECTRONICS LABORATORY TW   25 10   21 P C   18. MICROPROCESSORS & INTERFACING LABTW   50 20   35 P
  09. DIGITAL ELECTRONICS LABORATORY PR   50 20   32 P C   19. MICROPROCESSORS & INTERFACING LABPR   50 20   05 F
  10. SOFT SKILLS   TW   50 20   38 P C   20. DATA STRUCTURES LABORATORY   TW   50 20   40 P
                                                             21. DATA STRUCTURES LABORATORY   PR   50 20   40 P
GRAND TOTAL = 844/1500, RESULT: FAILS A.T.K.T.
ORDN. 1 MARKS :
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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
         OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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 S80054250   JADHAV RUTUJA RAMESH                  NANDA              , 71347762J    , S80054250 ,  PICT    , S80054250
 01. DISCRETE STRUCTURES          PP   100  40   62  P C    11. ENGINEERING MATHEMATICS III     PP   100  40   63  P
 02. PROGRAMMING & PROBLEM SOLVING  PP   100  40   63  P C    12. MICROPROC. & INTERFACING TECHNIQ.PP   100  40   73  P
 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP   100  40   71  P C    13. DATA STRUCTURES               PP   100  40   63  P
 04. DATA STRUCTURES AND ALGORITHMS  PP   100  40   56  P C    14. COMPUTER GRAPHICS             PP   100  40   68  P
 05. HUMANITIES AND SOCIAL SCIENCE  PP   100  40   58  P C    15. COMPUTER ORGANIZATION         PP   100  40   76  P
 06. PROGRAMMING LABORATORY        TW    25  10   21  P C    16. O. O. PROG. & COMP. GRAPH. LAB   TW    50  20   39  P
 07. PROGRAMMING LABORATORY        PR    50  20   38  P C    17. O. O. PROG. & COMP. GRAPH. LAB   PR    50  20   39  P
 08. DIGITAL ELECTRONICS LABORATORY  TW    25  10   21  P C    18. MICROPROCESSORS & INTERFACING LABTW    50  20   40  P
 09. DIGITAL ELECTRONICS LABORATORY  PR    50  20   33  P C    19. MICROPROCESSORS & INTERFACING LABPR    50  20   40  P
 10. SOFT SKILLS                   TW    50  20   40  P C    20. DATA STRUCTURES LABORATORY      TW    50  20   40  P
                                                             21. DATA STRUCTURES LABORATORY      PR    50  20   30  P
GRAND TOTAL = 1034/1500, RESULT: FIRST CLASS WITH DISTINCTION
ORDN. 1 MARKS :
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 S80054251   JAGTAP AMRUTA SATISH                  SANJIVANI          , 71200885D    , S80054251 ,  PICT    , S80054251
 01. DISCRETE STRUCTURES          PP   100  40   77  P C    11. ENGINEERING MATHEMATICS III     PP   100  40   71  P
 02. PROGRAMMING & PROBLEM SOLVING  PP   100  40   60  P C    12. MICROPROC. & INTERFACING TECHNIQ.PP   100  40   71  P
 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP   100  40   70  P C    13. DATA STRUCTURES               PP   100  40   64  P
 04. DATA STRUCTURES AND ALGORITHMS  PP   100  40   58  P C    14. COMPUTER GRAPHICS             PP   100  40   71  P
 05. HUMANITIES AND SOCIAL SCIENCE  PP   100  40   71  P C    15. COMPUTER ORGANIZATION         PP   100  40   77  P
 06. PROGRAMMING LABORATORY        TW    25  10   23  P C    16. O. O. PROG. & COMP. GRAPH. LAB   TW    50  20   45  P
 07. PROGRAMMING LABORATORY        PR    50  20   27  P C    17. O. O. PROG. & COMP. GRAPH. LAB   PR    50  20   40  P
 08. DIGITAL ELECTRONICS LABORATORY  TW    25  10   23  P C    18. MICROPROCESSORS & INTERFACING LABTW    50  20   45  P
 09. DIGITAL ELECTRONICS LABORATORY  PR    50  20   41  P C    19. MICROPROCESSORS & INTERFACING LABPR    50  20   43  P
 10. SOFT SKILLS                   TW    50  20   43  P C    20. DATA STRUCTURES LABORATORY      TW    50  20   43  P
                                                             21. DATA STRUCTURES LABORATORY      PR    50  20   44  P
GRAND TOTAL = 1107/1500, RESULT: FIRST CLASS WITH DISTINCTION
ORDN. 1 MARKS :
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 S80054252   JOSHI AKSHAY ANIL                     JYOTI              , 71200892G    , S80054252 ,  PICT    , S80054252
 01. DISCRETE STRUCTURES          PP   100  40   71  P C    11. ENGINEERING MATHEMATICS III     PP   100  40   58  P
 02. PROGRAMMING & PROBLEM SOLVING  PP   100  40   45  P C    12. MICROPROC. & INTERFACING TECHNIQ.PP   100  40   65  P
 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP   100  40   74  P C    13. DATA STRUCTURES               PP   100  40   56  P
 04. DATA STRUCTURES AND ALGORITHMS  PP   100  40   48  P C    14. COMPUTER GRAPHICS             PP   100  40   58  P
 05. HUMANITIES AND SOCIAL SCIENCE  PP   100  40   60  P C    15. COMPUTER ORGANIZATION         PP   100  40   73  P
 06. PROGRAMMING LABORATORY        TW    25  10   18  P C    16. O. O. PROG. & COMP. GRAPH. LAB   TW    50  20   43  P
 07. PROGRAMMING LABORATORY        PR    50  20   35  P C    17. O. O. PROG. & COMP. GRAPH. LAB   PR    50  20   46  P
 08. DIGITAL ELECTRONICS LABORATORY  TW    25  10   21  P C    18. MICROPROCESSORS & INTERFACING LABTW    50  20   44  P
 09. DIGITAL ELECTRONICS LABORATORY  PR    50  20   42  P C    19. MICROPROCESSORS & INTERFACING LABPR    50  20   28  P
 10. SOFT SKILLS                   TW    50  20   40  P C    20. DATA STRUCTURES LABORATORY      TW    50  20   42  P
                                                             21. DATA STRUCTURES LABORATORY      PR    50  20   41  P
GRAND TOTAL = 1008/1500, RESULT: FIRST CLASS WITH DISTINCTION
ORDN. 1 MARKS :
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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.
         OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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 S80054253   JOSHI AVANI AVINASH                 ASHWINI            , 71200893E    , S80054253 ,   PICT     , S80054253
 01. DISCRETE STRUCTURES          PP   100  40   44  P C   11. ENGINEERING MATHEMATICS III      PP   100  40   14  F
 02. PROGRAMMING & PROBLEM SOLVING PP   100  40   45  P C   12. MICROPROC. & INTERFACING TECHNIQ.PP  100  40   50  P
 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100  40   40  P C   13. DATA STRUCTURES                 PP   100  40   44  P
 04. DATA STRUCTURES AND ALGORITHMS PP  100  40   44  P C   14. COMPUTER GRAPHICS               PP   100  40   58  P
 05. HUMANITIES AND SOCIAL SCIENCE PP   100  40   54  P C   15. COMPUTER ORGANIZATION           PP   100  40   47  P
 06. PROGRAMMING LABORATORY        TW    25  10   20  P C   16. O. O. PROG. & COMP. GRAPH. LAB   TW    50  20   30  P
 07. PROGRAMMING LABORATORY        PR    50  20   20  P C   17. O. O. PROG. & COMP. GRAPH. LAB   PR    50  20   20$ P
 08. DIGITAL ELECTRONICS LABORATORY TW   25  10   21  P C   18. MICROPROCESSORS & INTERFACING LABTW    50  20   28  P
 09. DIGITAL ELECTRONICS LABORATORY PR   50  20   30  P C   19. MICROPROCESSORS & INTERFACING LABPR    50  20   05  F
 10. SOFT SKILLS                   TW    50  20   37  P C   20. DATA STRUCTURES LABORATORY       TW    50  20   28  P
                                                            21. DATA STRUCTURES LABORATORY       PR    50  20   12  F
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GRAND TOTAL = 691/1500, RESULT: FAILS A.T.K.T.   [$ O.1]
ORDN. 1 MARKS : (17)2,

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 S80054254   JOSHIPURA RUJUTA SHISHIR            MEGHAL             , 71200897H    , S80054254 ,   PICT     , S80054254
 01. DISCRETE STRUCTURES          PP   100  40   67  P C   11. ENGINEERING MATHEMATICS III      PP   100  40   62  P
 02. PROGRAMMING & PROBLEM SOLVING PP   100  40   48  P C   12. MICROPROC. & INTERFACING TECHNIQ.PP  100  40   72  P
 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100  40   80  P C   13. DATA STRUCTURES                 PP   100  40   62  P
 04. DATA STRUCTURES AND ALGORITHMS PP  100  40   54  P C   14. COMPUTER GRAPHICS               PP   100  40   59  P
 05. HUMANITIES AND SOCIAL SCIENCE PP   100  40   62  P C   15. COMPUTER ORGANIZATION           PP   100  40   69  P
 06. PROGRAMMING LABORATORY        TW    25  10   18  P C   16. O. O. PROG. & COMP. GRAPH. LAB   TW    50  20   39  P
 07. PROGRAMMING LABORATORY        PR    50  20   40  P C   17. O. O. PROG. & COMP. GRAPH. LAB   PR    50  20   41  P
 08. DIGITAL ELECTRONICS LABORATORY TW   25  10   19  P C   18. MICROPROCESSORS & INTERFACING LABTW    50  20   40  P
 09. DIGITAL ELECTRONICS LABORATORY PR   50  20   41  P C   19. MICROPROCESSORS & INTERFACING LABPR    50  20   40  P
 10. SOFT SKILLS                   TW    50  20   39  P C   20. DATA STRUCTURES LABORATORY       TW    50  20   40  P
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GRAND TOTAL = 1032/1500, RESULT: FIRST CLASS WITH DISTINCTION
ORDN. 1 MARKS :

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 S80054255   KADAM VIJAY BALASAHEB               SUREKHA            , 71347763G    , S80054255 ,   PICT     , S80054255
 01. DISCRETE STRUCTURES          PP   100  40   40  P C   11. ENGINEERING MATHEMATICS III      PP   100  40   18  F
 02. PROGRAMMING & PROBLEM SOLVING PP   100  40   40  P C   12. MICROPROC. & INTERFACING TECHNIQ.PP  100  40   59  P
 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100  40   56  P C   13. DATA STRUCTURES                 PP   100  40   53  P
 04. DATA STRUCTURES AND ALGORITHMS PP  100  40   44  P C   14. COMPUTER GRAPHICS               PP   100  40   54  P
 05. HUMANITIES AND SOCIAL SCIENCE PP   100  40   57  P C   15. COMPUTER ORGANIZATION           PP   100  40   61  P
 06. PROGRAMMING LABORATORY        TW    25  10   21  P C   16. O. O. PROG. & COMP. GRAPH. LAB   TW    50  20   38  P
 07. PROGRAMMING LABORATORY        PR    50  20   28  P C   17. O. O. PROG. & COMP. GRAPH. LAB   PR    50  20   35  P
 08. DIGITAL ELECTRONICS LABORATORY TW   25  10   20  P C   18. MICROPROCESSORS & INTERFACING LABTW    50  20   40  P
 09. DIGITAL ELECTRONICS LABORATORY PR   50  20   33  P C   19. MICROPROCESSORS & INTERFACING LABPR    50  20   36  P
 10. SOFT SKILLS                   TW    50  20   33  P C   20. DATA STRUCTURES LABORATORY       TW    50  20   39  P
                                                            21. DATA STRUCTURES LABORATORY       PR    50  20   37  P
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GRAND TOTAL = 842/1500, RESULT: FAILS A.T.K.T.
ORDN. 1 MARKS :

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.
         OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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  S80054256   KALBHOR KOMAL JALINDAR   KALBHOR RAJASHREE   , 71347764E   , S80054256 ,   PICT   , S80054256
  01. DISCRETE STRUCTURES   PP   100 40   65 P   11. ENGINEERING MATHEMATICS III   PP   100 40   40 P
  02. PROGRAMMING & PROBLEM SOLVING PP   100 40   56 P C   12. MICROPROC. & INTERFACING TECHNIQ.PP   100 40   61 P
  03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 40   58 P C   13. DATA STRUCTURES   PP   100 40   51 P
  04. DATA STRUCTURES AND ALGORITHMS PP   100 40   45 P C   14. COMPUTER GRAPHICS   PP   100 40   55 P
  05. HUMANITIES AND SOCIAL SCIENCE PP   100 40   56 P C   15. COMPUTER ORGANIZATION   PP   100 40   65 P
  06. PROGRAMMING LABORATORY   TW   25 10   20 P C   16. O. O. PROG. & COMP. GRAPH. LAB   TW   50 20   41 P
  07. PROGRAMMING LABORATORY   PR   50 20   25 P C   17. O. O. PROG. & COMP. GRAPH. LAB   PR   50 20   35 P
  08. DIGITAL ELECTRONICS LABORATORY TW   25 10   20 P C   18. MICROPROCESSORS & INTERFACING LABTW   50 20   40 P
  09. DIGITAL ELECTRONICS LABORATORY PR   50 20   23 P   19. MICROPROCESSORS & INTERFACING LABPR   50 20   25 P
  10. SOFT SKILLS   TW   50 20   40 P C   20. DATA STRUCTURES LABORATORY   TW   50 20   41 P
                                                                  21. DATA STRUCTURES LABORATORY   PR   50 20   26 P
GRAND TOTAL = 888/1500, RESULT: HIGHER SECOND CLASS
ORDN. 1 MARKS :
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  S80054257   KALBHOR SHILPA SOPAN   KALBHOR ANJALI   , 71347765C   , S80054257 ,   PICT   , S80054257
  01. DISCRETE STRUCTURES   PP   100 40   68 P   11. ENGINEERING MATHEMATICS III   PP   100 40   20 F
  02. PROGRAMMING & PROBLEM SOLVING PP   100 40   65 P C   12. MICROPROC. & INTERFACING TECHNIQ.PP   100 40   68 P
  03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 40   68 P C   13. DATA STRUCTURES   PP   100 40   59 P
  04. DATA STRUCTURES AND ALGORITHMS PP   100 40   52 P C   14. COMPUTER GRAPHICS   PP   100 40   67 P
  05. HUMANITIES AND SOCIAL SCIENCE PP   100 40   60 P C   15. COMPUTER ORGANIZATION   PP   100 40   70 P
  06. PROGRAMMING LABORATORY   TW   25 10   22 P C   16. O. O. PROG. & COMP. GRAPH. LAB   TW   50 20   40 P
  07. PROGRAMMING LABORATORY   PR   50 20   38 P C   17. O. O. PROG. & COMP. GRAPH. LAB   PR   50 20   36 P
  08. DIGITAL ELECTRONICS LABORATORY TW   25 10   23 P C   18. MICROPROCESSORS & INTERFACING LABTW   50 20   40 P
  09. DIGITAL ELECTRONICS LABORATORY PR   50 20   38 P C   19. MICROPROCESSORS & INTERFACING LABPR   50 20   22 P
  10. SOFT SKILLS   TW   50 20   41 P C   20. DATA STRUCTURES LABORATORY   TW   50 20   42 P
                                                                  21. DATA STRUCTURES LABORATORY   PR   50 20   37 P
GRAND TOTAL = 976/1500, RESULT: FAILS A.T.K.T.
ORDN. 1 MARKS :
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  S80054258   KALDATE SHASHANK RAMDAS   JAYASHRI   , 71200901K   , S80054258 ,   PICT   , S80054258
  01. DISCRETE STRUCTURES   PP   100 40   47 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   40 P
  02. PROGRAMMING & PROBLEM SOLVING PP   100 40   45 P C   12. MICROPROC. & INTERFACING TECHNIQ.PP   100 40   54 P
  03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 40   46 P C   13. DATA STRUCTURES   PP   100 40   53 P
  04. DATA STRUCTURES AND ALGORITHMS PP   100 40   43 P C   14. COMPUTER GRAPHICS   PP   100 40   56 P
  05. HUMANITIES AND SOCIAL SCIENCE PP   100 40   57 P C   15. COMPUTER ORGANIZATION   PP   100 40   56 P
  06. PROGRAMMING LABORATORY   TW   25 10   22 P C   16. O. O. PROG. & COMP. GRAPH. LAB   TW   50 20   36 P
  07. PROGRAMMING LABORATORY   PR   50 20   35 P C   17. O. O. PROG. & COMP. GRAPH. LAB   PR   50 20   30 P
  08. DIGITAL ELECTRONICS LABORATORY TW   25 10   22 P C   18. MICROPROCESSORS & INTERFACING LABTW   50 20   25 P
  09. DIGITAL ELECTRONICS LABORATORY PR   50 20   39 P C   19. MICROPROCESSORS & INTERFACING LABPR   50 20   20 P
  10. SOFT SKILLS   TW   50 20   37 P C   20. DATA STRUCTURES LABORATORY   TW   50 20   28 P
                                                                  21. DATA STRUCTURES LABORATORY   PR   50 20   35 P
GRAND TOTAL = 826/1500, RESULT: HIGHER SECOND CLASS
ORDN. 1 MARKS :
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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.
      OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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  S80054259   KALUSKAR SANIYA VIKRAM   PALLAVEE   , 71200904D   , S80054259 ,   PICT   , S80054259
  01. DISCRETE STRUCTURES   PP   100 40   75 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   67 P
  02. PROGRAMMING & PROBLEM SOLVING PP   100 40   61 P C   12. MICROPROC. & INTERFACING TECHNIQ.PP   100 40   75 P
  03. DIGIT. ELECTRONICS & LOGIC DESIGNPP   100 40   57 P C   13. DATA STRUCTURES   PP   100 40   53 P
  04. DATA STRUCTURES AND ALGORITHMS PP   100 40   40 P C   14. COMPUTER GRAPHICS   PP   100 40   69 P
  05. HUMANITIES AND SOCIAL SCIENCE PP   100 40   57 P C   15. COMPUTER ORGANIZATION   PP   100 40   68 P
  06. PROGRAMMING LABORATORY   TW   25 10   17 P C   16. O. O. PROG. & COMP. GRAPH. LAB   TW   50 20   40 P
  07. PROGRAMMING LABORATORY   PR   50 20   41 P C   17. O. O. PROG. & COMP. GRAPH. LAB   PR   50 20   41 P
  08. DIGITAL ELECTRONICS LABORATORY TW   25 10   19 P C   18. MICROPROCESSORS & INTERFACING LABTW   50 20   43 P
  09. DIGITAL ELECTRONICS LABORATORY PR   50 20   39 P C   19. MICROPROCESSORS & INTERFACING LABPR   50 20   40 P
  10. SOFT SKILLS   TW   50 20   39 P C   20. DATA STRUCTURES LABORATORY   TW   50 20   40 P
                                                            21. DATA STRUCTURES LABORATORY   PR   50 20   41 P
GRAND TOTAL = 1022/1500, RESULT: FIRST CLASS WITH DISTINCTION
ORDN. 1 MARKS :
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  S80054260   KAMBALE NILESH VIJAY   JAYMALA   , 71347766M   , S80054260 ,   PICT   , S80054260
  01. DISCRETE STRUCTURES   PP   100 40   40 P   11. ENGINEERING MATHEMATICS III   PP   100 40   09 F
  02. PROGRAMMING & PROBLEM SOLVING PP   100 40   60 P   12. MICROPROC. & INTERFACING TECHNIQ.PP   100 40   25 F
  03. DIGIT. ELECTRONICS & LOGIC DESIGNPP   100 40   42 P   13. DATA STRUCTURES   PP   100 40   29 F
  04. DATA STRUCTURES AND ALGORITHMS PP   100 40   42 P   14. COMPUTER GRAPHICS   PP   100 40   45 P
  05. HUMANITIES AND SOCIAL SCIENCE PP   100 40   52 P C   15. COMPUTER ORGANIZATION   PP   100 40   47 P
  06. PROGRAMMING LABORATORY   TW   25 10   20 P C   16. O. O. PROG. & COMP. GRAPH. LAB   TW   50 20   28 P
  07. PROGRAMMING LABORATORY   PR   50 20   AA F   17. O. O. PROG. & COMP. GRAPH. LAB   PR   50 20   23 P
  08. DIGITAL ELECTRONICS LABORATORY TW   25 10   18 P C   18. MICROPROCESSORS & INTERFACING LABTW   50 20   30 P
  09. DIGITAL ELECTRONICS LABORATORY PR   50 20   22 P C   19. MICROPROCESSORS & INTERFACING LABPR   50 20   20 P
  10. SOFT SKILLS   TW   50 20   29 P C   20. DATA STRUCTURES LABORATORY   TW   50 20   30 P
                                                            21. DATA STRUCTURES LABORATORY   PR   50 20   22 P
GRAND TOTAL = 633/1500, RESULT: FAILS A.T.K.T.
ORDN. 1 MARKS :
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  S80054261   KAMBLE ASHAY UTTAM   LATA   , 71200905B   , S80054261 ,   PICT   , S80054261
  01. DISCRETE STRUCTURES   PP   100 40   44 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   42 P
  02. PROGRAMMING & PROBLEM SOLVING PP   100 40   50 P C   12. MICROPROC. & INTERFACING TECHNIQ.PP   100 40   50 P
  03. DIGIT. ELECTRONICS & LOGIC DESIGNPP   100 40   59 P C   13. DATA STRUCTURES   PP   100 40   55 P
  04. DATA STRUCTURES AND ALGORITHMS PP   100 40   47 P C   14. COMPUTER GRAPHICS   PP   100 40   61 P
  05. HUMANITIES AND SOCIAL SCIENCE PP   100 40   61 P C   15. COMPUTER ORGANIZATION   PP   100 40   56 P
  06. PROGRAMMING LABORATORY   TW   25 10   20 P C   16. O. O. PROG. & COMP. GRAPH. LAB   TW   50 20   30 P
  07. PROGRAMMING LABORATORY   PR   50 20   41 P C   17. O. O. PROG. & COMP. GRAPH. LAB   PR   50 20   38 P
  08. DIGITAL ELECTRONICS LABORATORY TW   25 10   18 P C   18. MICROPROCESSORS & INTERFACING LABTW   50 20   27 P
  09. DIGITAL ELECTRONICS LABORATORY PR   50 20   34 P C   19. MICROPROCESSORS & INTERFACING LABPR   50 20   38 P
  10. SOFT SKILLS   TW   50 20   35 P C   20. DATA STRUCTURES LABORATORY   TW   50 20   27 P
                                                            21. DATA STRUCTURES LABORATORY   PR   50 20   34 P
GRAND TOTAL = 867/1500, RESULT: HIGHER SECOND CLASS
ORDN. 1 MARKS :
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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.
        OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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  S80054262   KAPILESHWAR KAUSTUBH DATTATRAYA   KETAKI   , 71200910J   , S80054262 , PICT   , S80054262
  01. DISCRETE STRUCTURES   PP   100 40   67 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   76 P
  02. PROGRAMMING & PROBLEM SOLVING   PP   100 40   60 P C   12. MICROPROC. & INTERFACING TECHNIQ.PP   100 40   63 P
  03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 40   71 P C   13. DATA STRUCTURES   PP   100 40   51 P
  04. DATA STRUCTURES AND ALGORITHMS PP   100 40   49 P C   14. COMPUTER GRAPHICS   PP   100 40   65 P
  05. HUMANITIES AND SOCIAL SCIENCE   PP   100 40   56 P C   15. COMPUTER ORGANIZATION   PP   100 40   62 P
  06. PROGRAMMING LABORATORY   TW   25 10   20 P C   16. O. O. PROG. & COMP. GRAPH. LAB   TW   50 20   42 P
  07. PROGRAMMING LABORATORY   PR   50 20   43 P C   17. O. O. PROG. & COMP. GRAPH. LAB   PR   50 20   42 P
  08. DIGITAL ELECTRONICS LABORATORY TW   25 10   22 P C   18. MICROPROCESSORS & INTERFACING LABTW   50 20   42 P
  09. DIGITAL ELECTRONICS LABORATORY PR   50 20   40 P C   19. MICROPROCESSORS & INTERFACING LABPR   50 20   40 P
  10. SOFT SKILLS   TW   50 20   42 P C   20. DATA STRUCTURES LABORATORY   TW   50 20   42 P
                                                               21. DATA STRUCTURES LABORATORY   PR   50 20   41 P
GRAND TOTAL = 1036/1500, RESULT: FIRST CLASS WITH DISTINCTION
ORDN. 1 MARKS :
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  S80054263   KARPE AKASH TUKARAM   ASHA   , 71200912E   , S80054263 , PICT   , S80054263
  01. DISCRETE STRUCTURES   PP   100 40   55 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   41 P
  02. PROGRAMMING & PROBLEM SOLVING   PP   100 40   52 P C   12. MICROPROC. & INTERFACING TECHNIQ.PP   100 40   43 P
  03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 40   61 P C   13. DATA STRUCTURES   PP   100 40   53 P
  04. DATA STRUCTURES AND ALGORITHMS PP   100 40   58 P C   14. COMPUTER GRAPHICS   PP   100 40   58 P
  05. HUMANITIES AND SOCIAL SCIENCE   PP   100 40   59 P C   15. COMPUTER ORGANIZATION   PP   100 40   70 P
  06. PROGRAMMING LABORATORY   TW   25 10   21 P C   16. O. O. PROG. & COMP. GRAPH. LAB   TW   50 20   39 P
  07. PROGRAMMING LABORATORY   PR   50 20   35 P C   17. O. O. PROG. & COMP. GRAPH. LAB   PR   50 20   31 P
  08. DIGITAL ELECTRONICS LABORATORY TW   25 10   22 P C   18. MICROPROCESSORS & INTERFACING LABTW   50 20   37 P
  09. DIGITAL ELECTRONICS LABORATORY PR   50 20   35 P C   19. MICROPROCESSORS & INTERFACING LABPR   50 20   22 P
  10. SOFT SKILLS   TW   50 20   38 P C   20. DATA STRUCTURES LABORATORY   TW   50 20   40 P
                                                               21. DATA STRUCTURES LABORATORY   PR   50 20   39 P
GRAND TOTAL = 909/1500, RESULT: FIRST CLASS
ORDN. 1 MARKS :
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  S80054264   KARTIK ANIL REDDY   NAMRATA   , 71200913C   , S80054264 , PICT   , S80054264
  01. DISCRETE STRUCTURES   PP   100 40   58 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   40 P
  02. PROGRAMMING & PROBLEM SOLVING   PP   100 40   61 P C   12. MICROPROC. & INTERFACING TECHNIQ.PP   100 40   58 P
  03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 40   57 P C   13. DATA STRUCTURES   PP   100 40   57 P
  04. DATA STRUCTURES AND ALGORITHMS PP   100 40   47 P C   14. COMPUTER GRAPHICS   PP   100 40   63 P
  05. HUMANITIES AND SOCIAL SCIENCE   PP   100 40   57 P C   15. COMPUTER ORGANIZATION   PP   100 40   69 P
  06. PROGRAMMING LABORATORY   TW   25 10   17 P C   16. O. O. PROG. & COMP. GRAPH. LAB   TW   50 20   40 P
  07. PROGRAMMING LABORATORY   PR   50 20   40 P C   17. O. O. PROG. & COMP. GRAPH. LAB   PR   50 20   42 P
  08. DIGITAL ELECTRONICS LABORATORY TW   25 10   18 P C   18. MICROPROCESSORS & INTERFACING LABTW   50 20   38 P
  09. DIGITAL ELECTRONICS LABORATORY PR   50 20   32 P C   19. MICROPROCESSORS & INTERFACING LABPR   50 20   34 P
  10. SOFT SKILLS   TW   50 20   35 P C   20. DATA STRUCTURES LABORATORY   TW   50 20   39 P
                                                               21. DATA STRUCTURES LABORATORY   PR   50 20   37 P
GRAND TOTAL = 939/1500, RESULT: FIRST CLASS
ORDN. 1 MARKS :
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NOTE: FIRST LINE : SEAT NO.,  NAME OF THE CANDIDATE,  MOTHER,  PERMANENT REG. NO.,  PREVIOUS SEAT NO.,  COLLEGE,   SEAT NO.
        OTHER LINES: HEAD OF PASSING,  MAX. MARKS,  MIN. PASS MARKS,  MARKS OBTAINED,  P/F:PASS/FAIL,  C:PREVIOUS CARRY OVER
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 S80054265    KASAT SARVESH BALKISAN              SWARUPA              , 71200914M    , S80054265 ,   PICT     , S80054265
 01. DISCRETE STRUCTURES            PP   100  40   65  P C    11. ENGINEERING MATHEMATICS III      PP   100  40   70  P
 02. PROGRAMMING & PROBLEM SOLVING  PP   100  40   53  P C    12. MICROPROC. & INTERFACING TECHNIQ.PP   100  40   60  P
 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100  40   69  P C    13. DATA STRUCTURES                  PP   100  40   60  P
 04. DATA STRUCTURES AND ALGORITHMS PP   100  40   55  P C    14. COMPUTER GRAPHICS                PP   100  40   65  P
 05. HUMANITIES AND SOCIAL SCIENCE  PP   100  40   66  P C    15. COMPUTER ORGANIZATION            PP   100  40   70  P
 06. PROGRAMMING LABORATORY         TW    25  10   23  P C    16. O. O. PROG. & COMP. GRAPH. LAB   TW    50  20   41  P
 07. PROGRAMMING LABORATORY         PR    50  20   41  P C    17. O. O. PROG. & COMP. GRAPH. LAB   PR    50  20   42  P
 08. DIGITAL ELECTRONICS LABORATORY TW    25  10   22  P C    18. MICROPROCESSORS & INTERFACING LABTW    50  20   39  P
 09. DIGITAL ELECTRONICS LABORATORY PR    50  20   40  P C    19. MICROPROCESSORS & INTERFACING LABPR    50  20   30  P
 10. SOFT SKILLS                    TW    50  20   40  P C    20. DATA STRUCTURES LABORATORY       TW    50  20   39  P
                                                             21. DATA STRUCTURES LABORATORY       PR    50  20   39  P
GRAND TOTAL = 1029/1500, RESULT: FIRST CLASS WITH DISTINCTION
ORDN. 1 MARKS :
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 S80054266    KHADE SOURABH DURYODHAN             SANGITA              , 71200919B    , S80054266 ,   PICT     , S80054266
 01. DISCRETE STRUCTURES            PP   100  40   56  P C    11. ENGINEERING MATHEMATICS III      PP   100  40   14  F
 02. PROGRAMMING & PROBLEM SOLVING  PP   100  40   45  P C    12. MICROPROC. & INTERFACING TECHNIQ.PP   100  40   43  P
 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100  40   62  P C    13. DATA STRUCTURES                  PP   100  40   49  P
 04. DATA STRUCTURES AND ALGORITHMS PP   100  40   51  P C    14. COMPUTER GRAPHICS                PP   100  40   58  P
 05. HUMANITIES AND SOCIAL SCIENCE  PP   100  40   48  P C    15. COMPUTER ORGANIZATION            PP   100  40   49  P
 06. PROGRAMMING LABORATORY         TW    25  10   16  P C    16. O. O. PROG. & COMP. GRAPH. LAB   TW    50  20   30  P
 07. PROGRAMMING LABORATORY         PR    50  20   36  P C    17. O. O. PROG. & COMP. GRAPH. LAB   PR    50  20   32  P
 08. DIGITAL ELECTRONICS LABORATORY TW    25  10   17  P C    18. MICROPROCESSORS & INTERFACING LABTW    50  20   32  P
 09. DIGITAL ELECTRONICS LABORATORY PR    50  20   42  P C    19. MICROPROCESSORS & INTERFACING LABPR    50  20   30  P
 10. SOFT SKILLS                    TW    50  20   34  P C    20. DATA STRUCTURES LABORATORY       TW    50  20   33  P
                                                             21. DATA STRUCTURES LABORATORY       PR    50  20   30  P
GRAND TOTAL =  807/1500, RESULT: FAILS A.T.K.T.
ORDN. 1 MARKS :
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 S80054267    KHADKE SRUSHTI SANJEEV              USHA                 , 71200920F    , S80054267 ,   PICT     , S80054267
 01. DISCRETE STRUCTURES            PP   100  40   50  P C    11. ENGINEERING MATHEMATICS III      PP   100  40   40  P
 02. PROGRAMMING & PROBLEM SOLVING  PP   100  40   46  P C    12. MICROPROC. & INTERFACING TECHNIQ.PP   100  40   47  P
 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100  40   57  P C    13. DATA STRUCTURES                  PP   100  40   42  P
 04. DATA STRUCTURES AND ALGORITHMS PP   100  40   43  P C    14. COMPUTER GRAPHICS                PP   100  40   50  P
 05. HUMANITIES AND SOCIAL SCIENCE  PP   100  40   55  P C    15. COMPUTER ORGANIZATION            PP   100  40   59  P
 06. PROGRAMMING LABORATORY         TW    25  10   21  P C    16. O. O. PROG. & COMP. GRAPH. LAB   TW    50  20   38  P
 07. PROGRAMMING LABORATORY         PR    50  20   40  P C    17. O. O. PROG. & COMP. GRAPH. LAB   PR    50  20   36  P
 08. DIGITAL ELECTRONICS LABORATORY TW    25  10   22  P C    18. MICROPROCESSORS & INTERFACING LABTW    50  20   38  P
 09. DIGITAL ELECTRONICS LABORATORY PR    50  20   32  P C    19. MICROPROCESSORS & INTERFACING LABPR    50  20   25  P
 10. SOFT SKILLS                    TW    50  20   38  P C    20. DATA STRUCTURES LABORATORY       TW    50  20   41  P
                                                             21. DATA STRUCTURES LABORATORY       PR    50  20   35  P
GRAND TOTAL =  855/1500, RESULT: HIGHER SECOND CLASS
ORDN. 1 MARKS :
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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
        OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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 S80054268    KHANDAVE MAYUR NARAYANRAO              PUSHPA              , 71200923L    , S80054268 ,   PICT     , S80054268
 01. DISCRETE STRUCTURES           PP   100  40   58  P C    11. ENGINEERING MATHEMATICS III     PP    100  40   54  P
 02. PROGRAMMING & PROBLEM SOLVING  PP   100  40   57  P C    12. MICROPROC. & INTERFACING TECHNIQ.PP   100  40   69  P
 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP  100  40   62  P C    13. DATA STRUCTURES                 PP    100  40   56  P
 04. DATA STRUCTURES AND ALGORITHMS PP   100  40   51  P C    14. COMPUTER GRAPHICS               PP    100  40   56  P
 05. HUMANITIES AND SOCIAL SCIENCE  PP   100  40   66  P C    15. COMPUTER ORGANIZATION           PP    100  40   69  P
 06. PROGRAMMING LABORATORY         TW    25  10   16  P C    16. O. O. PROG. & COMP. GRAPH. LAB   TW     50  20   39  P
 07. PROGRAMMING LABORATORY         PR    50  20   41  P C    17. O. O. PROG. & COMP. GRAPH. LAB   PR     50  20   39  P
 08. DIGITAL ELECTRONICS LABORATORY TW    25  10   17  P C    18. MICROPROCESSORS & INTERFACING LABTW     50  20   39  P
 09. DIGITAL ELECTRONICS LABORATORY PR    50  20   44  P C    19. MICROPROCESSORS & INTERFACING LABPR     50  20   40  P
 10. SOFT SKILLS                    TW    50  20   33  P C    20. DATA STRUCTURES LABORATORY       TW     50  20   39  P
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GRAND TOTAL =  984/1500, RESULT: FIRST CLASS
ORDN. 1 MARKS :
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 S80054269    KOTHARI APARNA ASHOK                  ARUNA               , 71200931M    , S80054269 ,   PICT     , S80054269
 01. DISCRETE STRUCTURES           PP   100  40   73  P C    11. ENGINEERING MATHEMATICS III     PP    100  40   64  P
 02. PROGRAMMING & PROBLEM SOLVING  PP   100  40   57  P C    12. MICROPROC. & INTERFACING TECHNIQ.PP   100  40   66  P
 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP  100  40   60  P C    13. DATA STRUCTURES                 PP    100  40   62  P
 04. DATA STRUCTURES AND ALGORITHMS PP   100  40   52  P C    14. COMPUTER GRAPHICS               PP    100  40   62  P
 05. HUMANITIES AND SOCIAL SCIENCE  PP   100  40   53  P C    15. COMPUTER ORGANIZATION           PP    100  40   63  P
 06. PROGRAMMING LABORATORY         TW    25  10   21  P C    16. O. O. PROG. & COMP. GRAPH. LAB   TW     50  20   42  P
 07. PROGRAMMING LABORATORY         PR    50  20   37  P C    17. O. O. PROG. & COMP. GRAPH. LAB   PR     50  20   44  P
 08. DIGITAL ELECTRONICS LABORATORY TW    25  10   21  P C    18. MICROPROCESSORS & INTERFACING LABTW     50  20   40  P
 09. DIGITAL ELECTRONICS LABORATORY PR    50  20   46  P C    19. MICROPROCESSORS & INTERFACING LABPR     50  20   38  P
 10. SOFT SKILLS                    TW    50  20   36  P C    20. DATA STRUCTURES LABORATORY       TW     50  20   43  P
                                                              21. DATA STRUCTURES LABORATORY       PR     50  20   40  P
GRAND TOTAL = 1020/1500, RESULT: FIRST CLASS WITH DISTINCTION
ORDN. 1 MARKS :
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 S80054270    KULKARNI JANHAVI SUHASCHANDRA         SAVANI              , 71200934F    , S80054270 ,   PICT     , S80054270
 01. DISCRETE STRUCTURES           PP   100  40   74  P C    11. ENGINEERING MATHEMATICS III     PP    100  40   69  P
 02. PROGRAMMING & PROBLEM SOLVING  PP   100  40   69  P C    12. MICROPROC. & INTERFACING TECHNIQ.PP   100  40   75  P
 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP  100  40   63  P C    13. DATA STRUCTURES                 PP    100  40   58  P
 04. DATA STRUCTURES AND ALGORITHMS PP   100  40   49  P C    14. COMPUTER GRAPHICS               PP    100  40   62  P
 05. HUMANITIES AND SOCIAL SCIENCE  PP   100  40   62  P C    15. COMPUTER ORGANIZATION           PP    100  40   63  P
 06. PROGRAMMING LABORATORY         TW    25  10   18  P C    16. O. O. PROG. & COMP. GRAPH. LAB   TW     50  20   36  P
 07. PROGRAMMING LABORATORY         PR    50  20   40  P C    17. O. O. PROG. & COMP. GRAPH. LAB   PR     50  20   42  P
 08. DIGITAL ELECTRONICS LABORATORY TW    25  10   18  P C    18. MICROPROCESSORS & INTERFACING LABTW     50  20   38  P
 09. DIGITAL ELECTRONICS LABORATORY PR    50  20   41  P C    19. MICROPROCESSORS & INTERFACING LABPR     50  20   39  P
 10. SOFT SKILLS                    TW    50  20   38  P C    20. DATA STRUCTURES LABORATORY       TW     50  20   38  P
                                                              21. DATA STRUCTURES LABORATORY       PR     50  20   41  P
GRAND TOTAL = 1033/1500, RESULT: FIRST CLASS WITH DISTINCTION
ORDN. 1 MARKS :
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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.
         OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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S80054271 KULKARNI MALHAR SUNIL					VYJAYANTHI	, 71200935D , S80054271 , PICT , S80054271
01. DISCRETE STRUCTURES	PP	100	40	71	P C	11. ENGINEERING MATHEMATICS III PP 100 40 43 P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	52	P C	12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 57 P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	64	P C	13. DATA STRUCTURES PP 100 40 53 P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	56	P C	14. COMPUTER GRAPHICS PP 100 40 66 P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	65	P C	15. COMPUTER ORGANIZATION PP 100 40 63 P
06. PROGRAMMING LABORATORY	TW	25	10	22	P C	16. O. O. PROG. & COMP. GRAPH. LAB TW 50 20 45 P
07. PROGRAMMING LABORATORY	PR	50	20	42	P C	17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 46 P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	22	P C	18. MICROPROCESSORS & INTERFACING LABTW 50 20 43 P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	40	P C	19. MICROPROCESSORS & INTERFACING LABPR 50 20 45 P
10. SOFT SKILLS	TW	50	20	39	P C	20. DATA STRUCTURES LABORATORY TW 50 20 45 P
						21. DATA STRUCTURES LABORATORY PR 50 20 45 P

GRAND TOTAL = 1024/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :
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S80054272 LANKE PRASAD PRAVIN					SUNANDA	, 71200943E , S80054272 , PICT , S80054272
01. DISCRETE STRUCTURES	PP	100	40	84	P C	11. ENGINEERING MATHEMATICS III PP 100 40 93 P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	74	P C	12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 73 P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	87	P C	13. DATA STRUCTURES PP 100 40 66 P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	63	P C	14. COMPUTER GRAPHICS PP 100 40 81 P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	73	P C	15. COMPUTER ORGANIZATION PP 100 40 83 P
06. PROGRAMMING LABORATORY	TW	25	10	24	P C	16. O. O. PROG. & COMP. GRAPH. LAB TW 50 20 47 P
07. PROGRAMMING LABORATORY	PR	50	20	42	P C	17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 44 P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	24	P C	18. MICROPROCESSORS & INTERFACING LABTW 50 20 47 P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	48	P C	19. MICROPROCESSORS & INTERFACING LABPR 50 20 46 P
10. SOFT SKILLS	TW	50	20	46	P C	20. DATA STRUCTURES LABORATORY TW 50 20 47 P
						21. DATA STRUCTURES LABORATORY PR 50 20 43 P

GRAND TOTAL = 1235/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :
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S80054273 MAHAJAN ABOLI VIKRANT					ARCHANA	, 71200946K , S80054273 , PICT , S80054273
01. DISCRETE STRUCTURES	PP	100	40	49	P C	11. ENGINEERING MATHEMATICS III PP 100 40 81 P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	47	P C	12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40 60 P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	75	P C	13. DATA STRUCTURES PP 100 40 54 P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	48	P C	14. COMPUTER GRAPHICS PP 100 40 68 P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	61	P C	15. COMPUTER ORGANIZATION PP 100 40 57 P
06. PROGRAMMING LABORATORY	TW	25	10	23	P C	16. O. O. PROG. & COMP. GRAPH. LAB TW 50 20 37 P
07. PROGRAMMING LABORATORY	PR	50	20	43	P C	17. O. O. PROG. & COMP. GRAPH. LAB PR 50 20 45 P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	23	P C	18. MICROPROCESSORS & INTERFACING LABTW 50 20 37 P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	45	P C	19. MICROPROCESSORS & INTERFACING LABPR 50 20 38 P
10. SOFT SKILLS	TW	50	20	42	P C	20. DATA STRUCTURES LABORATORY TW 50 20 40 P
						21. DATA STRUCTURES LABORATORY PR 50 20 42 P

GRAND TOTAL = 1015/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :
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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
      OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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S80054274   MAHAJAN PRITESH JAYRAM   ANUSAYA   , 71200947H   , S80054274 ,   PICT   , S80054274
01. DISCRETE STRUCTURES   PP   100 40   40 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   AA F
02. PROGRAMMING & PROBLEM SOLVING   PP   100 40   AA F   12. MICROPROC. & INTERFACING TECHNIQ.PP   100 40   10 F
03. DIGIT. ELECTRONICS & LOGIC DESIGNPP   100 40   40 P C   13. DATA STRUCTURES   PP   100 40   40 P
04. DATA STRUCTURES AND ALGORITHMS PP   100 40   AA F   14. COMPUTER GRAPHICS   PP   100 40   40 P
05. HUMANITIES AND SOCIAL SCIENCE   PP   100 40   52 P C   15. COMPUTER ORGANIZATION   PP   100 40   24 F
06. PROGRAMMING LABORATORY   TW   25 10   22 P C   16. O. O. PROG. & COMP. GRAPH. LAB   TW   50 20   35 P
07. PROGRAMMING LABORATORY   PR   50 20   24 P C   17. O. O. PROG. & COMP. GRAPH. LAB   PR   50 20   22 P
08. DIGITAL ELECTRONICS LABORATORY TW   25 10   21 P C   18. MICROPROCESSORS & INTERFACING LABTW   50 20   32 P
09. DIGITAL ELECTRONICS LABORATORY PR   50 20   21 P   19. MICROPROCESSORS & INTERFACING LABPR   50 20   05 F
10. SOFT SKILLS   TW   50 20   38 P C   20. DATA STRUCTURES LABORATORY   TW   50 20   34 P
                                                              21. DATA STRUCTURES LABORATORY   PR   50 20   10 F
GRAND TOTAL = 510/1500, RESULT: FAILS
ORDN. 1 MARKS :
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S80054275   MAHALE DHANSHRI RAJENDRA   MANGALA   , 71200948F   , S80054275 ,   PICT   , S80054275
01. DISCRETE STRUCTURES   PP   100 40   60 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   58 P
02. PROGRAMMING & PROBLEM SOLVING   PP   100 40   45 P C   12. MICROPROC. & INTERFACING TECHNIQ.PP   100 40   60 P
03. DIGIT. ELECTRONICS & LOGIC DESIGNPP   100 40   46 P C   13. DATA STRUCTURES   PP   100 40   60 P
04. DATA STRUCTURES AND ALGORITHMS PP   100 40   40 P C   14. COMPUTER GRAPHICS   PP   100 40   58 P
05. HUMANITIES AND SOCIAL SCIENCE   PP   100 40   51 P C   15. COMPUTER ORGANIZATION   PP   100 40   67 P
06. PROGRAMMING LABORATORY   TW   25 10   18 P C   16. O. O. PROG. & COMP. GRAPH. LAB   TW   50 20   40 P
07. PROGRAMMING LABORATORY   PR   50 20   36 P C   17. O. O. PROG. & COMP. GRAPH. LAB   PR   50 20   41 P
08. DIGITAL ELECTRONICS LABORATORY TW   25 10   18 P C   18. MICROPROCESSORS & INTERFACING LABTW   50 20   42 P
09. DIGITAL ELECTRONICS LABORATORY PR   50 20   29 P C   19. MICROPROCESSORS & INTERFACING LABPR   50 20   28 P
10. SOFT SKILLS   TW   50 20   37 P C   20. DATA STRUCTURES LABORATORY   TW   50 20   40 P
                                                              21. DATA STRUCTURES LABORATORY   PR   50 20   38 P
GRAND TOTAL = 912/1500, RESULT: FIRST CLASS
ORDN. 1 MARKS :
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S80054276   MAITHILI TAKALE   MEENA   , 71200950H   , S80054276 ,   PICT   , S80054276
01. DISCRETE STRUCTURES   PP   100 40   48 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   17 F
02. PROGRAMMING & PROBLEM SOLVING   PP   100 40   50 P C   12. MICROPROC. & INTERFACING TECHNIQ.PP   100 40   49 P
03. DIGIT. ELECTRONICS & LOGIC DESIGNPP   100 40   51 P C   13. DATA STRUCTURES   PP   100 40   49 P
04. DATA STRUCTURES AND ALGORITHMS PP   100 40   40 P C   14. COMPUTER GRAPHICS   PP   100 40   59 P
05. HUMANITIES AND SOCIAL SCIENCE   PP   100 40   64 P C   15. COMPUTER ORGANIZATION   PP   100 40   55 P
06. PROGRAMMING LABORATORY   TW   25 10   21 P C   16. O. O. PROG. & COMP. GRAPH. LAB   TW   50 20   36 P
07. PROGRAMMING LABORATORY   PR   50 20   37 P C   17. O. O. PROG. & COMP. GRAPH. LAB   PR   50 20   34 P
08. DIGITAL ELECTRONICS LABORATORY TW   25 10   21 P C   18. MICROPROCESSORS & INTERFACING LABTW   50 20   35 P
09. DIGITAL ELECTRONICS LABORATORY PR   50 20   37 P C   19. MICROPROCESSORS & INTERFACING LABPR   50 20   07 F
10. SOFT SKILLS   TW   50 20   40 P C   20. DATA STRUCTURES LABORATORY   TW   50 20   38 P
                                                              21. DATA STRUCTURES LABORATORY   PR   50 20   32 P
GRAND TOTAL = 820/1500, RESULT: FAILS A.T.K.T.
ORDN. 1 MARKS :
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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.
        OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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  S80054277   MALI PARAG SHRIKRISHNA   SUNANDA   , 71200951F   , S80054277 , PICT   , S80054277
  01. DISCRETE STRUCTURES   PP   100 40   75 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   68 P
  02. PROGRAMMING & PROBLEM SOLVING PP   100 40   70 P C   12. MICROPROC. & INTERFACING TECHNIQ.PP   100 40   61 P
  03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 40   77 P C   13. DATA STRUCTURES   PP   100 40   68 P
  04. DATA STRUCTURES AND ALGORITHMS PP   100 40   55 P C   14. COMPUTER GRAPHICS   PP   100 40   74 P
  05. HUMANITIES AND SOCIAL SCIENCE PP   100 40   68 P C   15. COMPUTER ORGANIZATION   PP   100 40   82 P
  06. PROGRAMMING LABORATORY   TW   25 10   23 P C   16. O. O. PROG. & COMP. GRAPH. LAB TW   50 20   45 P
  07. PROGRAMMING LABORATORY   PR   50 20   42 P C   17. O. O. PROG. & COMP. GRAPH. LAB PR   50 20   40 P
  08. DIGITAL ELECTRONICS LABORATORY TW   25 10   23 P C   18. MICROPROCESSORS & INTERFACING LABTW   50 20   45 P
  09. DIGITAL ELECTRONICS LABORATORY PR   50 20   42 P C   19. MICROPROCESSORS & INTERFACING LABPR   50 20   43 P
  10. SOFT SKILLS   TW   50 20   45 P C   20. DATA STRUCTURES LABORATORY   TW   50 20   45 P
                                                              21. DATA STRUCTURES LABORATORY   PR   50 20   41 P
GRAND TOTAL = 1132/1500, RESULT: FIRST CLASS WITH DISTINCTION
ORDN. 1 MARKS :

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  S80054278   MANE SAGAR ASHOK   SANGITA   , 71200955J   , S80054278 , PICT   , S80054278
  01. DISCRETE STRUCTURES   PP   100 40   40 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   14 F
  02. PROGRAMMING & PROBLEM SOLVING PP   100 40   48 P C   12. MICROPROC. & INTERFACING TECHNIQ.PP   100 40   40 P
  03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 40   50 P C   13. DATA STRUCTURES   PP   100 40   47 P
  04. DATA STRUCTURES AND ALGORITHMS PP   100 40   42 P   14. COMPUTER GRAPHICS   PP   100 40   63 P
  05. HUMANITIES AND SOCIAL SCIENCE PP   100 40   62 P C   15. COMPUTER ORGANIZATION   PP   100 40   62 P
  06. PROGRAMMING LABORATORY   TW   25 10   21 P C   16. O. O. PROG. & COMP. GRAPH. LAB TW   50 20   37 P
  07. PROGRAMMING LABORATORY   PR   50 20   14 F   17. O. O. PROG. & COMP. GRAPH. LAB PR   50 20   30 P
  08. DIGITAL ELECTRONICS LABORATORY TW   25 10   21 P C   18. MICROPROCESSORS & INTERFACING LABTW   50 20   36 P
  09. DIGITAL ELECTRONICS LABORATORY PR   50 20   36 P C   19. MICROPROCESSORS & INTERFACING LABPR   50 20   07 F
  10. SOFT SKILLS   TW   50 20   35 P C   20. DATA STRUCTURES LABORATORY   TW   50 20   38 P
                                                              21. DATA STRUCTURES LABORATORY   PR   50 20   20 P
GRAND TOTAL = 763/1500, RESULT: FAILS A.T.K.T.
ORDN. 1 MARKS :

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  S80054279   MANTRI SHARAYU SHARAD   PRIYA   , 71200957E   , S80054279 , PICT   , S80054279
  01. DISCRETE STRUCTURES   PP   100 40   75 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   86 P
  02. PROGRAMMING & PROBLEM SOLVING PP   100 40   41 P C   12. MICROPROC. & INTERFACING TECHNIQ.PP   100 40   62 P
  03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 40   68 P C   13. DATA STRUCTURES   PP   100 40   66 P
  04. DATA STRUCTURES AND ALGORITHMS PP   100 40   61 P C   14. COMPUTER GRAPHICS   PP   100 40   67 P
  05. HUMANITIES AND SOCIAL SCIENCE PP   100 40   63 P C   15. COMPUTER ORGANIZATION   PP   100 40   69 P
  06. PROGRAMMING LABORATORY   TW   25 10   22 P C   16. O. O. PROG. & COMP. GRAPH. LAB TW   50 20   41 P
  07. PROGRAMMING LABORATORY   PR   50 20   39 P C   17. O. O. PROG. & COMP. GRAPH. LAB PR   50 20   43 P
  08. DIGITAL ELECTRONICS LABORATORY TW   25 10   21 P C   18. MICROPROCESSORS & INTERFACING LABTW   50 20   45 P
  09. DIGITAL ELECTRONICS LABORATORY PR   50 20   41 P C   19. MICROPROCESSORS & INTERFACING LABPR   50 20   30 P
  10. SOFT SKILLS   TW   50 20   42 P C   20. DATA STRUCTURES LABORATORY   TW   50 20   43 P
                                                              21. DATA STRUCTURES LABORATORY   PR   50 20   39 P
GRAND TOTAL = 1064/1500, RESULT: FIRST CLASS WITH DISTINCTION
ORDN. 1 MARKS :

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
         OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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 S80054280   MHASKE POOJA DAGDU   MANISHA   , 71347767K   , S80054280 , PICT   , S80054280
 01. DISCRETE STRUCTURES   PP   100 40   41 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   40 P
 02. PROGRAMMING & PROBLEM SOLVING PP 100 40   52 P C   12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40   61 P
 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 40 60 P C   13. DATA STRUCTURES   PP   100 40   64 P
 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 47 P C   14. COMPUTER GRAPHICS   PP   100 40   61 P
 05. HUMANITIES AND SOCIAL SCIENCE PP   100 40 55 P C   15. COMPUTER ORGANIZATION   PP   100 40   66 P
 06. PROGRAMMING LABORATORY   TW   25 10 20 P C   16. O. O. PROG. & COMP. GRAPH. LAB   TW   50 20   42 P
 07. PROGRAMMING LABORATORY   PR   50 20 29 P C   17. O. O. PROG. & COMP. GRAPH. LAB   PR   50 20   39 P
 08. DIGITAL ELECTRONICS LABORATORY TW   25 10 20 P C   18. MICROPROCESSORS & INTERFACING LABTW   50 20   43 P
 09. DIGITAL ELECTRONICS LABORATORY PR   50 20 30 P C   19. MICROPROCESSORS & INTERFACING LABPR   50 20   20 P
 10. SOFT SKILLS   TW   50 20 42 P C   20. DATA STRUCTURES LABORATORY   TW   50 20   44 P
                                                          21. DATA STRUCTURES LABORATORY   PR   50 20   39 P
GRAND TOTAL = 915/1500, RESULT: FIRST CLASS
ORDN. 1 MARKS :
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 S80054281   MHASKULE YOGESH DATTATRAYA   LATA   , 71347768H   , S80054281 , PICT   , S80054281
 01. DISCRETE STRUCTURES   PP   100 40   46 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   26 F
 02. PROGRAMMING & PROBLEM SOLVING PP 100 40   58 P C   12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40   63 P
 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 40 56 P C   13. DATA STRUCTURES   PP   100 40   58 P
 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 46 P C   14. COMPUTER GRAPHICS   PP   100 40   65 P
 05. HUMANITIES AND SOCIAL SCIENCE PP   100 40 64 P C   15. COMPUTER ORGANIZATION   PP   100 40   71 P
 06. PROGRAMMING LABORATORY   TW   25 10 21 P C   16. O. O. PROG. & COMP. GRAPH. LAB   TW   50 20   40 P
 07. PROGRAMMING LABORATORY   PR   50 20 24 P C   17. O. O. PROG. & COMP. GRAPH. LAB   PR   50 20   31 P
 08. DIGITAL ELECTRONICS LABORATORY TW   25 10 19 P C   18. MICROPROCESSORS & INTERFACING LABTW   50 20   42 P
 09. DIGITAL ELECTRONICS LABORATORY PR   50 20 28 P C   19. MICROPROCESSORS & INTERFACING LABPR   50 20   34 P
 10. SOFT SKILLS   TW   50 20 38 P C   20. DATA STRUCTURES LABORATORY   TW   50 20   39 P
                                                          21. DATA STRUCTURES LABORATORY   PR   50 20   39 P
GRAND TOTAL = 908/1500, RESULT: FAILS A.T.K.T.
ORDN. 1 MARKS :
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 S80054282   MISAL AKSHAYA ARJUN   SANGITA   , 71200962M   , S80054282 , PICT   , S80054282
 01. DISCRETE STRUCTURES   PP   100 40   61 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   45 P
 02. PROGRAMMING & PROBLEM SOLVING PP 100 40   56 P C   12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40   53 P
 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 40 58 P C   13. DATA STRUCTURES   PP   100 40   63 P
 04. DATA STRUCTURES AND ALGORITHMS PP 100 40 53 P C   14. COMPUTER GRAPHICS   PP   100 40   57 P
 05. HUMANITIES AND SOCIAL SCIENCE PP   100 40 59 P C   15. COMPUTER ORGANIZATION   PP   100 40   73 P
 06. PROGRAMMING LABORATORY   TW   25 10 22 P C   16. O. O. PROG. & COMP. GRAPH. LAB   TW   50 20   40 P
 07. PROGRAMMING LABORATORY   PR   50 20 26 P C   17. O. O. PROG. & COMP. GRAPH. LAB   PR   50 20   38 P
 08. DIGITAL ELECTRONICS LABORATORY TW   25 10 22 P C   18. MICROPROCESSORS & INTERFACING LABTW   50 20   40 P
 09. DIGITAL ELECTRONICS LABORATORY PR   50 20 29 P C   19. MICROPROCESSORS & INTERFACING LABPR   50 20   05 F
 10. SOFT SKILLS   TW   50 20 37 P C   20. DATA STRUCTURES LABORATORY   TW   50 20   42 P
                                                          21. DATA STRUCTURES LABORATORY   PR   50 20   40 P
GRAND TOTAL = 919/1500, RESULT: FAILS A.T.K.T.
ORDN. 1 MARKS :
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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
        OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S80054283   MORDANI NAVIN BHAGWAN   SUPARNA   , 71200965F   , S80054283 , PICT   , S80054283

01. DISCRETE STRUCTURES	PP	100	40	67	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	51 P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	63	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	49 P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	70	P C	13. DATA STRUCTURES	PP	100	40	62 P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	52	P C	14. COMPUTER GRAPHICS	PP	100	40	60 P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	58	P C	15. COMPUTER ORGANIZATION	PP	100	40	73 P
06. PROGRAMMING LABORATORY	TW	25	10	18	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	41 P
07. PROGRAMMING LABORATORY	PR	50	20	38	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	42 P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	20	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	44 P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	34	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	46 P
10. SOFT SKILLS	TW	50	20	38	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	41 P
						21. DATA STRUCTURES LABORATORY	PR	50	20	41 P

GRAND TOTAL = 1008/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

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S80054284   MORE SAYALI SHANKAR   MORE ANITA SHANKAR   , 71347769F   , S80054284 , PICT   , S80054284

01. DISCRETE STRUCTURES	PP	100	40	60	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	40 P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	67	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	45 P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	53	P C	13. DATA STRUCTURES	PP	100	40	56 P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	52	P C	14. COMPUTER GRAPHICS	PP	100	40	64 P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	69	P C	15. COMPUTER ORGANIZATION	PP	100	40	79 P
06. PROGRAMMING LABORATORY	TW	25	10	21	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	44 P
07. PROGRAMMING LABORATORY	PR	50	20	28	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	30 P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	20	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	44 P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	42	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	25 P
10. SOFT SKILLS	TW	50	20	43	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	42 P
						21. DATA STRUCTURES LABORATORY	PR	50	20	34 P

GRAND TOTAL = 958/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

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S80054285   MORE SOHAM SAMEER   HARSHA   , 71200966D   , S80054285 , PICT   , S80054285

01. DISCRETE STRUCTURES	PP	100	40	68	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	25 F
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	47	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	41 P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	61	P C	13. DATA STRUCTURES	PP	100	40	47 P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	51	P C	14. COMPUTER GRAPHICS	PP	100	40	53 P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	53	P C	15. COMPUTER ORGANIZATION	PP	100	40	55 P
06. PROGRAMMING LABORATORY	TW	25	10	16	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	30 P
07. PROGRAMMING LABORATORY	PR	50	20	41	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	38 P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	17	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	28 P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	40	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	35 P
10. SOFT SKILLS	TW	50	20	29	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	29 P
						21. DATA STRUCTURES LABORATORY	PR	50	20	35 P

GRAND TOTAL = 839/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
          OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S80054286   MUDASSIR KHAN MASOOD KHAN   AMENA KHANUM   , 71200967B   , S80054286 ,   PICT   , S80054286
01. DISCRETE STRUCTURES   PP   100 40   66 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   57 P
02. PROGRAMMING & PROBLEM SOLVING   PP   100 40   61 P C   12. MICROPROC. & INTERFACING TECHNIQ.PP   100 40   40 P
03. DIGIT. ELECTRONICS & LOGIC DESIGNPP   100 40   62 P C   13. DATA STRUCTURES   PP   100 40   55 P
04. DATA STRUCTURES AND ALGORITHMS   PP   100 40   57 P C   14. COMPUTER GRAPHICS   PP   100 40   54 P
05. HUMANITIES AND SOCIAL SCIENCE   PP   100 40   55 P C   15. COMPUTER ORGANIZATION   PP   100 40   59 P
06. PROGRAMMING LABORATORY   TW   25 10   17 P C   16. O. O. PROG. & COMP. GRAPH. LAB   TW   50 20   40 P
07. PROGRAMMING LABORATORY   PR   50 20   43 P C   17. O. O. PROG. & COMP. GRAPH. LAB   PR   50 20   38 P
08. DIGITAL ELECTRONICS LABORATORY   TW   25 10   17 P C   18. MICROPROCESSORS & INTERFACING LABTW   50 20   43 P
09. DIGITAL ELECTRONICS LABORATORY   PR   50 20   30 P C   19. MICROPROCESSORS & INTERFACING LABPR   50 20   30 P
10. SOFT SKILLS   TW   50 20   35 P C   20. DATA STRUCTURES LABORATORY   TW   50 20   40 P
                                        21. DATA STRUCTURES LABORATORY   PR   50 20   39 P
GRAND TOTAL = 938/1500, RESULT: FIRST CLASS
ORDN. 1 MARKS :

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S80054287   MUNOT BHAVESH RAJENDRA   MANORAMA   , 71200971L   , S80054287 ,   PICT   , S80054287
01. DISCRETE STRUCTURES   PP   100 40   82 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   46 P
02. PROGRAMMING & PROBLEM SOLVING   PP   100 40   68 P C   12. MICROPROC. & INTERFACING TECHNIQ.PP   100 40   47 P
03. DIGIT. ELECTRONICS & LOGIC DESIGNPP   100 40   53 P C   13. DATA STRUCTURES   PP   100 40   64 P
04. DATA STRUCTURES AND ALGORITHMS   PP   100 40   55 P C   14. COMPUTER GRAPHICS   PP   100 40   49 P
05. HUMANITIES AND SOCIAL SCIENCE   PP   100 40   52 P C   15. COMPUTER ORGANIZATION   PP   100 40   63 P
06. PROGRAMMING LABORATORY   TW   25 10   22 P C   16. O. O. PROG. & COMP. GRAPH. LAB   TW   50 20   44 P
07. PROGRAMMING LABORATORY   PR   50 20   40 P C   17. O. O. PROG. & COMP. GRAPH. LAB   PR   50 20   42 P
08. DIGITAL ELECTRONICS LABORATORY   TW   25 10   21 P C   18. MICROPROCESSORS & INTERFACING LABTW   50 20   40 P
09. DIGITAL ELECTRONICS LABORATORY   PR   50 20   40 P C   19. MICROPROCESSORS & INTERFACING LABPR   50 20   38 P
10. SOFT SKILLS   TW   50 20   41 P C   20. DATA STRUCTURES LABORATORY   TW   50 20   41 P
                                        21. DATA STRUCTURES LABORATORY   PR   50 20   43 P
GRAND TOTAL = 991/1500, RESULT: FIRST CLASS WITH DISTINCTION
ORDN. 1 MARKS :

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S80054288   MURARKA UTKARSH VINAY   HEMLATA   , 71200972J   , S80054288 ,   PICT   , S80054288
01. DISCRETE STRUCTURES   PP   100 40   68 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   40 P
02. PROGRAMMING & PROBLEM SOLVING   PP   100 40   56 P C   12. MICROPROC. & INTERFACING TECHNIQ.PP   100 40   41 P
03. DIGIT. ELECTRONICS & LOGIC DESIGNPP   100 40   55 P C   13. DATA STRUCTURES   PP   100 40   51 P
04. DATA STRUCTURES AND ALGORITHMS   PP   100 40   41 P C   14. COMPUTER GRAPHICS   PP   100 40   63 P
05. HUMANITIES AND SOCIAL SCIENCE   PP   100 40   60 P C   15. COMPUTER ORGANIZATION   PP   100 40   63 P
06. PROGRAMMING LABORATORY   TW   25 10   15 P C   16. O. O. PROG. & COMP. GRAPH. LAB   TW   50 20   20 P
07. PROGRAMMING LABORATORY   PR   50 20   22 P C   17. O. O. PROG. & COMP. GRAPH. LAB   PR   50 20   25 P
08. DIGITAL ELECTRONICS LABORATORY   TW   25 10   15 P C   18. MICROPROCESSORS & INTERFACING LABTW   50 20   20 P
09. DIGITAL ELECTRONICS LABORATORY   PR   50 20   33 P C   19. MICROPROCESSORS & INTERFACING LABPR   50 20   30 P
10. SOFT SKILLS   TW   50 20   33 P C   20. DATA STRUCTURES LABORATORY   TW   50 20   22 P
                                        21. DATA STRUCTURES LABORATORY   PR   50 20   38 P
GRAND TOTAL = 811/1500, RESULT: SECOND CLASS
ORDN. 1 MARKS :

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
        OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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  S80054289   MUZUMDAR VARSHA JAYANT   SUNITA   , 71200973G   , S80054289 ,   PICT   , S80054289
  01. DISCRETE STRUCTURES   PP   100 40   71 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   63 P
  02. PROGRAMMING & PROBLEM SOLVING PP   100 40   59 P C   12. MICROPROC. & INTERFACING TECHNIQ.PP   100 40   62 P
  03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 40   76 P C   13. DATA STRUCTURES   PP   100 40   67 P
  04. DATA STRUCTURES AND ALGORITHMS PP   100 40   47 P C   14. COMPUTER GRAPHICS   PP   100 40   68 P
  05. HUMANITIES AND SOCIAL SCIENCE PP   100 40   66 P C   15. COMPUTER ORGANIZATION   PP   100 40   73 P
  06. PROGRAMMING LABORATORY   TW   25 10   21 P C   16. O. O. PROG. & COMP. GRAPH. LAB   TW   50 20   45 P
  07. PROGRAMMING LABORATORY   PR   50 20   43 P C   17. O. O. PROG. & COMP. GRAPH. LAB   PR   50 20   40 P
  08. DIGITAL ELECTRONICS LABORATORY TW   25 10   19 P C   18. MICROPROCESSORS & INTERFACING LABTW   50 20   44 P
  09. DIGITAL ELECTRONICS LABORATORY PR   50 20   40 P C   19. MICROPROCESSORS & INTERFACING LABPR   50 20   38 P
  10. SOFT SKILLS   TW   50 20   38 P C   20. DATA STRUCTURES LABORATORY   TW   50 20   45 P
                                                             21. DATA STRUCTURES LABORATORY   PR   50 20   45 P
GRAND TOTAL = 1070/1500, RESULT: FIRST CLASS WITH DISTINCTION
ORDN. 1 MARKS :
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  S80054290   NADE APPURWA ANIL   ARUNA   , 71200974E   , S80054290 ,   PICT   , S80054290
  01. DISCRETE STRUCTURES   PP   100 40   55 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   52 P
  02. PROGRAMMING & PROBLEM SOLVING PP   100 40   40 P C   12. MICROPROC. & INTERFACING TECHNIQ.PP   100 40   40 P
  03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 40   52 P C   13. DATA STRUCTURES   PP   100 40   44 P
  04. DATA STRUCTURES AND ALGORITHMS PP   100 40   40 P C   14. COMPUTER GRAPHICS   PP   100 40   40 P
  05. HUMANITIES AND SOCIAL SCIENCE PP   100 40   41 P C   15. COMPUTER ORGANIZATION   PP   100 40   47 P
  06. PROGRAMMING LABORATORY   TW   25 10   17 P C   16. O. O. PROG. & COMP. GRAPH. LAB   TW   50 20   40 P
  07. PROGRAMMING LABORATORY   PR   50 20   27 P C   17. O. O. PROG. & COMP. GRAPH. LAB   PR   50 20   36 P
  08. DIGITAL ELECTRONICS LABORATORY TW   25 10   18 P C   18. MICROPROCESSORS & INTERFACING LABTW   50 20   40 P
  09. DIGITAL ELECTRONICS LABORATORY PR   50 20   30 P C   19. MICROPROCESSORS & INTERFACING LABPR   50 20   32 P
  10. SOFT SKILLS   TW   50 20   38 P C   20. DATA STRUCTURES LABORATORY   TW   50 20   40 P
                                                             21. DATA STRUCTURES LABORATORY   PR   50 20   38 P
GRAND TOTAL = 807/1500, RESULT: SECOND CLASS
ORDN. 1 MARKS :
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  S80054291   NAHAR AKSHAY VIJAY   MANGAL   , 71200976M   , S80054291 ,   PICT   , S80054291
  01. DISCRETE STRUCTURES   PP   100 40   64 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   82 P
  02. PROGRAMMING & PROBLEM SOLVING PP   100 40   67 P C   12. MICROPROC. & INTERFACING TECHNIQ.PP   100 40   47 P
  03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 40   75 P C   13. DATA STRUCTURES   PP   100 40   60 P
  04. DATA STRUCTURES AND ALGORITHMS PP   100 40   55 P C   14. COMPUTER GRAPHICS   PP   100 40   61 P
  05. HUMANITIES AND SOCIAL SCIENCE PP   100 40   69 P C   15. COMPUTER ORGANIZATION   PP   100 40   71 P
  06. PROGRAMMING LABORATORY   TW   25 10   21 P C   16. O. O. PROG. & COMP. GRAPH. LAB   TW   50 20   42 P
  07. PROGRAMMING LABORATORY   PR   50 20   36 P C   17. O. O. PROG. & COMP. GRAPH. LAB   PR   50 20   40 P
  08. DIGITAL ELECTRONICS LABORATORY TW   25 10   20 P C   18. MICROPROCESSORS & INTERFACING LABTW   50 20   41 P
  09. DIGITAL ELECTRONICS LABORATORY PR   50 20   32 P C   19. MICROPROCESSORS & INTERFACING LABPR   50 20   41 P
  10. SOFT SKILLS   TW   50 20   37 P C   20. DATA STRUCTURES LABORATORY   TW   50 20   43 P
                                                             21. DATA STRUCTURES LABORATORY   PR   50 20   41 P
GRAND TOTAL = 1045/1500, RESULT: FIRST CLASS WITH DISTINCTION
ORDN. 1 MARKS :
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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.
        OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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 S80054292   NAIK GANESH NILKANTH              SUREKHA           , 71347770K    , S80054292 ,  PICT     , S80054292
 01. DISCRETE STRUCTURES          PP   100 40   50  P C    11. ENGINEERING MATHEMATICS III     PP    100 40   49  P
 02. PROGRAMMING & PROBLEM SOLVING PP   100 40   55  P C    12. MICROPROC. & INTERFACING TECHNIQ.PP   100 40   67  P
 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 40   56  P C    13. DATA STRUCTURES                PP    100 40   58  P
 04. DATA STRUCTURES AND ALGORITHMS PP  100 40   43  P C    14. COMPUTER GRAPHICS              PP    100 40   63  P
 05. HUMANITIES AND SOCIAL SCIENCE PP   100 40   62  P C    15. COMPUTER ORGANIZATION          PP    100 40   52  P
 06. PROGRAMMING LABORATORY       TW    25 10   20  P C    16. O. O. PROG. & COMP. GRAPH. LAB  TW     50 20   39  P
 07. PROGRAMMING LABORATORY       PR    50 20   32  P C    17. O. O. PROG. & COMP. GRAPH. LAB  PR     50 20   36  P
 08. DIGITAL ELECTRONICS LABORATORY TW  25 10   22  P C    18. MICROPROCESSORS & INTERFACING LABTW    50 20   39  P
 09. DIGITAL ELECTRONICS LABORATORY PR  50 20   30  P C    19. MICROPROCESSORS & INTERFACING LABPR    50 20   40  P
 10. SOFT SKILLS                  TW    50 20   39  P C    20. DATA STRUCTURES LABORATORY      TW     50 20   42  P
                                                           21. DATA STRUCTURES LABORATORY      PR     50 20   39  P
GRAND TOTAL =  933/1500, RESULT: FIRST CLASS
ORDN. 1 MARKS :
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 S80054293   NAINA BALANA                     KUSUM             , 71200979F    , S80054293 ,  PICT     , S80054293
 01. DISCRETE STRUCTURES          PP   100 40   70  P C    11. ENGINEERING MATHEMATICS III     PP    100 40   40  P
 02. PROGRAMMING & PROBLEM SOLVING PP   100 40   43  P C    12. MICROPROC. & INTERFACING TECHNIQ.PP   100 40   61  P
 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 40   57  P C    13. DATA STRUCTURES                PP    100 40   56  P
 04. DATA STRUCTURES AND ALGORITHMS PP  100 40   48  P C    14. COMPUTER GRAPHICS              PP    100 40   63  P
 05. HUMANITIES AND SOCIAL SCIENCE PP   100 40   63  P C    15. COMPUTER ORGANIZATION          PP    100 40   65  P
 06. PROGRAMMING LABORATORY       TW    25 10   16  P C    16. O. O. PROG. & COMP. GRAPH. LAB  TW     50 20   29  P
 07. PROGRAMMING LABORATORY       PR    50 20   34  P C    17. O. O. PROG. & COMP. GRAPH. LAB  PR     50 20   32  P
 08. DIGITAL ELECTRONICS LABORATORY TW  25 10   17  P C    18. MICROPROCESSORS & INTERFACING LABTW    50 20   23  P
 09. DIGITAL ELECTRONICS LABORATORY PR  50 20   30  P      19. MICROPROCESSORS & INTERFACING LABPR    50 20   10  F
 10. SOFT SKILLS                  TW    50 20   38  P C    20. DATA STRUCTURES LABORATORY      TW     50 20   25  P
                                                           21. DATA STRUCTURES LABORATORY      PR     50 20   35  P
GRAND TOTAL =  855/1500, RESULT: FAILS A.T.K.T.
ORDN. 1 MARKS :
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 S80054294   NAZARKAR SHWETA SHRIPAD          SMITA             , 71200983D    , S80054294 ,  PICT     , S80054294
 01. DISCRETE STRUCTURES          PP   100 40   66  P C    11. ENGINEERING MATHEMATICS III     PP    100 40   77  P
 02. PROGRAMMING & PROBLEM SOLVING PP   100 40   60  P C    12. MICROPROC. & INTERFACING TECHNIQ.PP   100 40   64  P
 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 40   76  P C    13. DATA STRUCTURES                PP    100 40   55  P
 04. DATA STRUCTURES AND ALGORITHMS PP  100 40   56  P C    14. COMPUTER GRAPHICS              PP    100 40   61  P
 05. HUMANITIES AND SOCIAL SCIENCE PP   100 40   68  P C    15. COMPUTER ORGANIZATION          PP    100 40   70  P
 06. PROGRAMMING LABORATORY       TW    25 10   22  P C    16. O. O. PROG. & COMP. GRAPH. LAB  TW     50 20   43  P
 07. PROGRAMMING LABORATORY       PR    50 20   28  P C    17. O. O. PROG. & COMP. GRAPH. LAB  PR     50 20   38  P
 08. DIGITAL ELECTRONICS LABORATORY TW  25 10   21  P C    18. MICROPROCESSORS & INTERFACING LABTW    50 20   40  P
 09. DIGITAL ELECTRONICS LABORATORY PR  50 20   32  P C    19. MICROPROCESSORS & INTERFACING LABPR    50 20   38  P
 10. SOFT SKILLS                  TW    50 20   40  P C    20. DATA STRUCTURES LABORATORY      TW     50 20   42  P
                                                           21. DATA STRUCTURES LABORATORY      PR     50 20   40  P
GRAND TOTAL = 1037/1500, RESULT: FIRST CLASS WITH DISTINCTION
ORDN. 1 MARKS :
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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.
        OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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 S80054295   NIKHIL DWIVEDI   KUSUM   , 71200988E   , S80054295 ,   PICT   , S80054295
  01. DISCRETE STRUCTURES   PP   100 40   66 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   62 P
  02. PROGRAMMING & PROBLEM SOLVING PP   100 40   53 P C   12. MICROPROC. & INTERFACING TECHNIQ.PP   100 40   46 P
  03. DIGIT. ELECTRONICS & LOGIC DESIGNPP   100 40   72 P C   13. DATA STRUCTURES   PP   100 40   62 P
  04. DATA STRUCTURES AND ALGORITHMS PP   100 40   49 P C   14. COMPUTER GRAPHICS   PP   100 40   58 P
  05. HUMANITIES AND SOCIAL SCIENCE PP   100 40   61 P C   15. COMPUTER ORGANIZATION   PP   100 40   61 P
  06. PROGRAMMING LABORATORY   TW   25 10   19 P C   16. O. O. PROG. & COMP. GRAPH. LAB   TW   50 20   43 P
  07. PROGRAMMING LABORATORY   PR   50 20   40 P   17. O. O. PROG. & COMP. GRAPH. LAB   PR   50 20   40 P
  08. DIGITAL ELECTRONICS LABORATORY TW   25 10   21 P C   18. MICROPROCESSORS & INTERFACING LABTW   50 20   40 P
  09. DIGITAL ELECTRONICS LABORATORY PR   50 20   30 P C   19. MICROPROCESSORS & INTERFACING LABPR   50 20   35 P
  10. SOFT SKILLS   TW   50 20   42 P C   20. DATA STRUCTURES LABORATORY   TW   50 20   39 P
                                                                21. DATA STRUCTURES LABORATORY   PR   50 20   35 P
GRAND TOTAL = 974/1500, RESULT: FIRST CLASS
ORDN. 1 MARKS :
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 S80054296   NIMKAR HRUSHIKESH CHARUDATTA   SHARMILA   , 71200989C   , S80054296 ,   PICT   , S80054296
  01. DISCRETE STRUCTURES   PP   100 40   62 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   64 P
  02. PROGRAMMING & PROBLEM SOLVING PP   100 40   61 P C   12. MICROPROC. & INTERFACING TECHNIQ.PP   100 40   53 P
  03. DIGIT. ELECTRONICS & LOGIC DESIGNPP   100 40   71 P C   13. DATA STRUCTURES   PP   100 40   52 P
  04. DATA STRUCTURES AND ALGORITHMS PP   100 40   51 P C   14. COMPUTER GRAPHICS   PP   100 40   54 P
  05. HUMANITIES AND SOCIAL SCIENCE PP   100 40   60 P C   15. COMPUTER ORGANIZATION   PP   100 40   58 P
  06. PROGRAMMING LABORATORY   TW   25 10   21 P C   16. O. O. PROG. & COMP. GRAPH. LAB   TW   50 20   46 P
  07. PROGRAMMING LABORATORY   PR   50 20   30 P C   17. O. O. PROG. & COMP. GRAPH. LAB   PR   50 20   43 P
  08. DIGITAL ELECTRONICS LABORATORY TW   25 10   21 P C   18. MICROPROCESSORS & INTERFACING LABTW   50 20   43 P
  09. DIGITAL ELECTRONICS LABORATORY PR   50 20   41 P C   19. MICROPROCESSORS & INTERFACING LABPR   50 20   36 P
  10. SOFT SKILLS   TW   50 20   38 P C   20. DATA STRUCTURES LABORATORY   TW   50 20   44 P
                                                                21. DATA STRUCTURES LABORATORY   PR   50 20   42 P
GRAND TOTAL = 991/1500, RESULT: FIRST CLASS WITH DISTINCTION
ORDN. 1 MARKS :
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 S80054297   NISHTHA KALRA   SANGEETA   , 71200990G   , S80054297 ,   PICT   , S80054297
  01. DISCRETE STRUCTURES   PP   100 40   60 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   19 F
  02. PROGRAMMING & PROBLEM SOLVING PP   100 40   44 P C   12. MICROPROC. & INTERFACING TECHNIQ.PP   100 40   40 P
  03. DIGIT. ELECTRONICS & LOGIC DESIGNPP   100 40   55 P C   13. DATA STRUCTURES   PP   100 40   52 P
  04. DATA STRUCTURES AND ALGORITHMS PP   100 40   42 P C   14. COMPUTER GRAPHICS   PP   100 40   57 P
  05. HUMANITIES AND SOCIAL SCIENCE PP   100 40   47 P C   15. COMPUTER ORGANIZATION   PP   100 40   61 P
  06. PROGRAMMING LABORATORY   TW   25 10   15 P C   16. O. O. PROG. & COMP. GRAPH. LAB   TW   50 20   36 P
  07. PROGRAMMING LABORATORY   PR   50 20   37 P C   17. O. O. PROG. & COMP. GRAPH. LAB   PR   50 20   10 F
  08. DIGITAL ELECTRONICS LABORATORY TW   25 10   17 P C   18. MICROPROCESSORS & INTERFACING LABTW   50 20   36 P
  09. DIGITAL ELECTRONICS LABORATORY PR   50 20   33 P C   19. MICROPROCESSORS & INTERFACING LABPR   50 20   08 F
  10. SOFT SKILLS   TW   50 20   36 P C   20. DATA STRUCTURES LABORATORY   TW   50 20   35 P
                                                                21. DATA STRUCTURES LABORATORY   PR   50 20   33 P
GRAND TOTAL = 773/1500, RESULT: FAILS A.T.K.T.
ORDN. 1 MARKS :
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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.
        OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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 S80054298   PAGAR PRITESH DILIP                 PAGAR PALLAVI DILIP , 71347771H    , S80054298 ,  PICT     , S80054298
 01. DISCRETE STRUCTURES           PP   100  40   62  P C    11. ENGINEERING MATHEMATICS III     PP    100  40   41  P
 02. PROGRAMMING & PROBLEM SOLVING  PP   100  40   60  P C    12. MICROPROC. & INTERFACING TECHNIQ.PP  100  40   49  P
 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100  40   68  P C    13. DATA STRUCTURES                 PP    100  40   63  P
 04. DATA STRUCTURES AND ALGORITHMS PP   100  40   50  P C    14. COMPUTER GRAPHICS               PP    100  40   63  P
 05. HUMANITIES AND SOCIAL SCIENCE  PP   100  40   63  P C    15. COMPUTER ORGANIZATION           PP    100  40   70  P
 06. PROGRAMMING LABORATORY         TW    25  10   20  P C    16. O. O. PROG. & COMP. GRAPH. LAB  TW     50  20   42  P
 07. PROGRAMMING LABORATORY         PR    50  20   41  P C    17. O. O. PROG. & COMP. GRAPH. LAB  PR     50  20   37  P
 08. DIGITAL ELECTRONICS LABORATORY TW    25  10   20  P C    18. MICROPROCESSORS & INTERFACING LABTW    50  20   42  P
 09. DIGITAL ELECTRONICS LABORATORY PR    50  20   25  P C    19. MICROPROCESSORS & INTERFACING LABPR    50  20   38  P
 10. SOFT SKILLS                    TW    50  20   38  P C    20. DATA STRUCTURES LABORATORY      TW     50  20   41  P
                                                             21. DATA STRUCTURES LABORATORY      PR     50  20   38  P
GRAND TOTAL =  971/1500, RESULT: FIRST CLASS
ORDN. 1 MARKS :
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 S80054299   PAITHANKAR PRANETA VISHWANATH        UTKARSHA          , 71200995H    , S80054299 ,  PICT     , S80054299
 01. DISCRETE STRUCTURES           PP   100  40   71  P C    11. ENGINEERING MATHEMATICS III     PP    100  40   63  P
 02. PROGRAMMING & PROBLEM SOLVING  PP   100  40   57  P C    12. MICROPROC. & INTERFACING TECHNIQ.PP  100  40   62  P
 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100  40   61  P C    13. DATA STRUCTURES                 PP    100  40   62  P
 04. DATA STRUCTURES AND ALGORITHMS PP   100  40   58  P C    14. COMPUTER GRAPHICS               PP    100  40   68  P
 05. HUMANITIES AND SOCIAL SCIENCE  PP   100  40   62  P C    15. COMPUTER ORGANIZATION           PP    100  40   68  P
 06. PROGRAMMING LABORATORY         TW    25  10   22  P C    16. O. O. PROG. & COMP. GRAPH. LAB  TW     50  20   42  P
 07. PROGRAMMING LABORATORY         PR    50  20   36  P C    17. O. O. PROG. & COMP. GRAPH. LAB  PR     50  20   37  P
 08. DIGITAL ELECTRONICS LABORATORY TW    25  10   21  P C    18. MICROPROCESSORS & INTERFACING LABTW    50  20   38  P
 09. DIGITAL ELECTRONICS LABORATORY PR    50  20   25  P C    19. MICROPROCESSORS & INTERFACING LABPR    50  20   42  P
 10. SOFT SKILLS                    TW    50  20   40  P C    20. DATA STRUCTURES LABORATORY      TW     50  20   40  P
                                                             21. DATA STRUCTURES LABORATORY      PR     50  20   42  P
GRAND TOTAL = 1017/1500, RESULT: FIRST CLASS WITH DISTINCTION
ORDN. 1 MARKS :
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 S80054300   PANDHARE PRIYANKA RAVINDRA           RAJANI            , 71200998B    , S80054300 ,  PICT     , S80054300
 01. DISCRETE STRUCTURES           PP   100  40   49  P C    11. ENGINEERING MATHEMATICS III     PP    100  40   19  F
 02. PROGRAMMING & PROBLEM SOLVING  PP   100  40   55  P      12. MICROPROC. & INTERFACING TECHNIQ.PP  100  40   40  P
 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100  40   50  P C    13. DATA STRUCTURES                 PP    100  40   40  P
 04. DATA STRUCTURES AND ALGORITHMS PP   100  40   40  P C    14. COMPUTER GRAPHICS               PP    100  40   42  P
 05. HUMANITIES AND SOCIAL SCIENCE  PP   100  40   53  P C    15. COMPUTER ORGANIZATION           PP    100  40   44  P
 06. PROGRAMMING LABORATORY         TW    25  10   19  P C    16. O. O. PROG. & COMP. GRAPH. LAB  TW     50  20   38  P
 07. PROGRAMMING LABORATORY         PR    50  20   20  P      17. O. O. PROG. & COMP. GRAPH. LAB  PR     50  20   35  P
 08. DIGITAL ELECTRONICS LABORATORY TW    25  10   20  P C    18. MICROPROCESSORS & INTERFACING LABTW    50  20   35  P
 09. DIGITAL ELECTRONICS LABORATORY PR    50  20   30  P C    19. MICROPROCESSORS & INTERFACING LABPR    50  20   28  P
 10. SOFT SKILLS                    TW    50  20   40  P C    20. DATA STRUCTURES LABORATORY      TW     50  20   38  P
                                                             21. DATA STRUCTURES LABORATORY      PR     50  20   06  F
GRAND TOTAL =  741/1500, RESULT: FAILS A.T.K.T.
ORDN. 1 MARKS :
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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
      OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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  S80054301   PARAG MAHALA   PRAGYAN   , 71201000K   , S80054301 ,   PICT   , S80054301
  01. DISCRETE STRUCTURES   PP   100 40   64 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   40 P
  02. PROGRAMMING & PROBLEM SOLVING PP 100 40   40 P C   12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40   24 F
  03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 40 51 P C   13. DATA STRUCTURES   PP   100 40   40 P
  04. DATA STRUCTURES AND ALGORITHMS PP 100 40 44 P C   14. COMPUTER GRAPHICS   PP   100 40   44 P
  05. HUMANITIES AND SOCIAL SCIENCE PP 100 40   40 P C   15. COMPUTER ORGANIZATION   PP   100 40   41 P
  06. PROGRAMMING LABORATORY   TW   25 10   20 P C   16. O. O. PROG. & COMP. GRAPH. LAB TW   50 20   32 P
  07. PROGRAMMING LABORATORY   PR   50 20   32 P C   17. O. O. PROG. & COMP. GRAPH. LAB PR   50 20   34 P
  08. DIGITAL ELECTRONICS LABORATORY TW   25 10   20 P C   18. MICROPROCESSORS & INTERFACING LABTW   50 20   27 P
  09. DIGITAL ELECTRONICS LABORATORY PR   50 20   27 P C   19. MICROPROCESSORS & INTERFACING LABPR   50 20   33 P
  10. SOFT SKILLS   TW   50 20   35 P C   20. DATA STRUCTURES LABORATORY   TW   50 20   30 P
                                                           21. DATA STRUCTURES LABORATORY   PR   50 20   31 P
GRAND TOTAL = 749/1500, RESULT: FAILS A.T.K.T.
ORDN. 1 MARKS :
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  S80054302   PARAKH RUSHABH GIRISH   JYOTI   , 71201002F   , S80054302 ,   PICT   , S80054302
  01. DISCRETE STRUCTURES   PP   100 40   72 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   57 P
  02. PROGRAMMING & PROBLEM SOLVING PP 100 40   62 P C   12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40   52 P
  03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 40 66 P C   13. DATA STRUCTURES   PP   100 40   57 P
  04. DATA STRUCTURES AND ALGORITHMS PP 100 40 50 P C   14. COMPUTER GRAPHICS   PP   100 40   53 P
  05. HUMANITIES AND SOCIAL SCIENCE PP 100 40   57 P C   15. COMPUTER ORGANIZATION   PP   100 40   64 P
  06. PROGRAMMING LABORATORY   TW   25 10   23 P C   16. O. O. PROG. & COMP. GRAPH. LAB TW   50 20   39 P
  07. PROGRAMMING LABORATORY   PR   50 20   41 P C   17. O. O. PROG. & COMP. GRAPH. LAB PR   50 20   44 P
  08. DIGITAL ELECTRONICS LABORATORY TW   25 10   23 P C   18. MICROPROCESSORS & INTERFACING LABTW   50 20   40 P
  09. DIGITAL ELECTRONICS LABORATORY PR   50 20   40 P C   19. MICROPROCESSORS & INTERFACING LABPR   50 20   41 P
  10. SOFT SKILLS   TW   50 20   39 P C   20. DATA STRUCTURES LABORATORY   TW   50 20   40 P
                                                           21. DATA STRUCTURES LABORATORY   PR   50 20   39 P
GRAND TOTAL = 999/1500, RESULT: FIRST CLASS WITH DISTINCTION
ORDN. 1 MARKS :
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  S80054303   PATIL ABHILASHA VIJAY   SANDHYA   , 71201004B   , S80054303 ,   PICT   , S80054303
  01. DISCRETE STRUCTURES   PP   100 40   80 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   71 P
  02. PROGRAMMING & PROBLEM SOLVING PP 100 40   49 P C   12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40   47 P
  03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 40 70 P C   13. DATA STRUCTURES   PP   100 40   57 P
  04. DATA STRUCTURES AND ALGORITHMS PP 100 40 50 P C   14. COMPUTER GRAPHICS   PP   100 40   60 P
  05. HUMANITIES AND SOCIAL SCIENCE PP 100 40   67 P C   15. COMPUTER ORGANIZATION   PP   100 40   71 P
  06. PROGRAMMING LABORATORY   TW   25 10   20 P C   16. O. O. PROG. & COMP. GRAPH. LAB TW   50 20   39 P
  07. PROGRAMMING LABORATORY   PR   50 20   42 P C   17. O. O. PROG. & COMP. GRAPH. LAB PR   50 20   39 P
  08. DIGITAL ELECTRONICS LABORATORY TW   25 10   18 P C   18. MICROPROCESSORS & INTERFACING LABTW   50 20   39 P
  09. DIGITAL ELECTRONICS LABORATORY PR   50 20   32 P C   19. MICROPROCESSORS & INTERFACING LABPR   50 20   32 P
  10. SOFT SKILLS   TW   50 20   38 P C   20. DATA STRUCTURES LABORATORY   TW   50 20   42 P
                                                           21. DATA STRUCTURES LABORATORY   PR   50 20   39 P
GRAND TOTAL = 1002/1500, RESULT: FIRST CLASS WITH DISTINCTION
ORDN. 1 MARKS :
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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
        OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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  S80054305   PATIL PRATIK PRALHAD   PRATIBHA   , 71201008E   , S80054305 ,   PICT   , S80054305
  01. DISCRETE STRUCTURES   PP   100 40   80 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   51 P
  02. PROGRAMMING & PROBLEM SOLVING PP   100 40   57 P C   12. MICROPROC. & INTERFACING TECHNIQ.PP   100 40   52 P
  03. DIGIT. ELECTRONICS & LOGIC DESIGNPP   100 40   56 P C   13. DATA STRUCTURES   PP   100 40   58 P
  04. DATA STRUCTURES AND ALGORITHMS PP   100 40   49 P C   14. COMPUTER GRAPHICS   PP   100 40   56 P
  05. HUMANITIES AND SOCIAL SCIENCE PP   100 40   59 P C   15. COMPUTER ORGANIZATION   PP   100 40   72 P
  06. PROGRAMMING LABORATORY   TW   25 10   23 P C   16. O. O. PROG. & COMP. GRAPH. LAB   TW   50 20   40 P
  07. PROGRAMMING LABORATORY   PR   50 20   22 P C   17. O. O. PROG. & COMP. GRAPH. LAB   PR   50 20   38 P
  08. DIGITAL ELECTRONICS LABORATORY TW   25 10   22 P C   18. MICROPROCESSORS & INTERFACING LABTW   50 20   41 P
  09. DIGITAL ELECTRONICS LABORATORY PR   50 20   43 P C   19. MICROPROCESSORS & INTERFACING LABPR   50 20   34 P
  10. SOFT SKILLS   TW   50 20   42 P C   20. DATA STRUCTURES LABORATORY   TW   50 20   44 P
                                                            21. DATA STRUCTURES LABORATORY   PR   50 20   42 P
GRAND TOTAL = 981/1500, RESULT: FIRST CLASS
ORDN. 1 MARKS :
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  S80054306   PATTHE KAUSTUBH TEJRAM   SHOBHA   , 71347772F   , S80054306 ,   PICT   , S80054306
  01. DISCRETE STRUCTURES   PP   100 40   40 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   13 F
  02. PROGRAMMING & PROBLEM SOLVING PP   100 40   57 P C   12. MICROPROC. & INTERFACING TECHNIQ.PP   100 40   43 P
  03. DIGIT. ELECTRONICS & LOGIC DESIGNPP   100 40   58 P C   13. DATA STRUCTURES   PP   100 40   46 P
  04. DATA STRUCTURES AND ALGORITHMS PP   100 40   53 P C   14. COMPUTER GRAPHICS   PP   100 40   52 P
  05. HUMANITIES AND SOCIAL SCIENCE PP   100 40   55 P C   15. COMPUTER ORGANIZATION   PP   100 40   45 P
  06. PROGRAMMING LABORATORY   TW   25 10   20 P C   16. O. O. PROG. & COMP. GRAPH. LAB   TW   50 20   36 P
  07. PROGRAMMING LABORATORY   PR   50 20   35 P C   17. O. O. PROG. & COMP. GRAPH. LAB   PR   50 20   30 P
  08. DIGITAL ELECTRONICS LABORATORY TW   25 10   21 P C   18. MICROPROCESSORS & INTERFACING LABTW   50 20   36 P
  09. DIGITAL ELECTRONICS LABORATORY PR   50 20   30 P C   19. MICROPROCESSORS & INTERFACING LABPR   50 20   35 P
  10. SOFT SKILLS   TW   50 20   40 P C   20. DATA STRUCTURES LABORATORY   TW   50 20   35 P
                                                            21. DATA STRUCTURES LABORATORY   PR   50 20   36 P
GRAND TOTAL = 816/1500, RESULT: FAILS A.T.K.T.
ORDN. 1 MARKS :
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  S80054307   PHARANDE ABHISHEK VIJAY   RUKHMINEE   , 71201015H   , S80054307 ,   PICT   , S80054307
  01. DISCRETE STRUCTURES   PP   100 40   74 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   58 P
  02. PROGRAMMING & PROBLEM SOLVING PP   100 40   59 P C   12. MICROPROC. & INTERFACING TECHNIQ.PP   100 40   63 P
  03. DIGIT. ELECTRONICS & LOGIC DESIGNPP   100 40   72 P C   13. DATA STRUCTURES   PP   100 40   60 P
  04. DATA STRUCTURES AND ALGORITHMS PP   100 40   55 P C   14. COMPUTER GRAPHICS   PP   100 40   62 P
  05. HUMANITIES AND SOCIAL SCIENCE PP   100 40   72 P C   15. COMPUTER ORGANIZATION   PP   100 40   62 P
  06. PROGRAMMING LABORATORY   TW   25 10   23 P C   16. O. O. PROG. & COMP. GRAPH. LAB   TW   50 20   45 P
  07. PROGRAMMING LABORATORY   PR   50 20   42 P C   17. O. O. PROG. & COMP. GRAPH. LAB   PR   50 20   31 P
  08. DIGITAL ELECTRONICS LABORATORY TW   25 10   22 P C   18. MICROPROCESSORS & INTERFACING LABTW   50 20   44 P
  09. DIGITAL ELECTRONICS LABORATORY PR   50 20   31 P C   19. MICROPROCESSORS & INTERFACING LABPR   50 20   36 P
  10. SOFT SKILLS   TW   50 20   40 P C   20. DATA STRUCTURES LABORATORY   TW   50 20   44 P
                                                            21. DATA STRUCTURES LABORATORY   PR   50 20   42 P
GRAND TOTAL = 1037/1500, RESULT: FIRST CLASS WITH DISTINCTION
ORDN. 1 MARKS :
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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.
       OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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 S80054308   RAJEEV SEBASTIAN   THRESA   , 71201031K   , S80054308 , PICT   , S80054308
 01. DISCRETE STRUCTURES   PP   100 40   79 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   86 P
 02. PROGRAMMING & PROBLEM SOLVING PP   100 40   69 P C   12. MICROPROC. & INTERFACING TECHNIQ.PP   100 40   62 P
 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 40   76 P C   13. DATA STRUCTURES   PP   100 40   72 P
 04. DATA STRUCTURES AND ALGORITHMS PP   100 40   56 P C   14. COMPUTER GRAPHICS   PP   100 40   67 P
 05. HUMANITIES AND SOCIAL SCIENCE PP   100 40   67 P C   15. COMPUTER ORGANIZATION   PP   100 40   64 P
 06. PROGRAMMING LABORATORY   TW   25 10   23 P C   16. O. O. PROG. & COMP. GRAPH. LAB   TW   50 20   45 P
 07. PROGRAMMING LABORATORY   PR   50 20   41 P C   17. O. O. PROG. & COMP. GRAPH. LAB   PR   50 20   43 P
 08. DIGITAL ELECTRONICS LABORATORY TW   25 10   22 P C   18. MICROPROCESSORS & INTERFACING LABTW   50 20   47 P
 09. DIGITAL ELECTRONICS LABORATORY PR   50 20   40 P C   19. MICROPROCESSORS & INTERFACING LABPR   50 20   44 P
 10. SOFT SKILLS   TW   50 20   43 P C   20. DATA STRUCTURES LABORATORY   TW   50 20   46 P
                                                             21. DATA STRUCTURES LABORATORY   PR   50 20   40 P
GRAND TOTAL = 1132/1500, RESULT: FIRST CLASS WITH DISTINCTION
ORDN. 1 MARKS :
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 S80054309   RATHI ADITYA PRADIPKUMAR   SNEHAL   , 71201032H   , S80054309 , PICT   , S80054309
 01. DISCRETE STRUCTURES   PP   100 40   44 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   16 F
 02. PROGRAMMING & PROBLEM SOLVING PP   100 40   49 P C   12. MICROPROC. & INTERFACING TECHNIQ.PP   100 40   40 P
 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 40   47 P C   13. DATA STRUCTURES   PP   100 40   46 P
 04. DATA STRUCTURES AND ALGORITHMS PP   100 40   40 P C   14. COMPUTER GRAPHICS   PP   100 40   47 P
 05. HUMANITIES AND SOCIAL SCIENCE PP   100 40   55 P C   15. COMPUTER ORGANIZATION   PP   100 40   48 P
 06. PROGRAMMING LABORATORY   TW   25 10   20 P C   16. O. O. PROG. & COMP. GRAPH. LAB   TW   50 20   42 P
 07. PROGRAMMING LABORATORY   PR   50 20   40 P C   17. O. O. PROG. & COMP. GRAPH. LAB   PR   50 20   30 P
 08. DIGITAL ELECTRONICS LABORATORY TW   25 10   19 P C   18. MICROPROCESSORS & INTERFACING LABTW   50 20   37 P
 09. DIGITAL ELECTRONICS LABORATORY PR   50 20   35 P C   19. MICROPROCESSORS & INTERFACING LABPR   50 20   28 P
 10. SOFT SKILLS   TW   50 20   37 P C   20. DATA STRUCTURES LABORATORY   TW   50 20   40 P
                                                             21. DATA STRUCTURES LABORATORY   PR   50 20   05 F
GRAND TOTAL = 765/1500, RESULT: FAILS A.T.K.T.
ORDN. 1 MARKS :
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 S80054310   RATHORE DHRUVESH JAYESH   JYOTI   , 71201037J   , S80054310 , PICT   , S80054310
 01. DISCRETE STRUCTURES   PP   100 40   86 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   81 P
 02. PROGRAMMING & PROBLEM SOLVING PP   100 40   77 P C   12. MICROPROC. & INTERFACING TECHNIQ.PP   100 40   64 P
 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 40   71 P C   13. DATA STRUCTURES   PP   100 40   65 P
 04. DATA STRUCTURES AND ALGORITHMS PP   100 40   51 P C   14. COMPUTER GRAPHICS   PP   100 40   65 P
 05. HUMANITIES AND SOCIAL SCIENCE PP   100 40   70 P C   15. COMPUTER ORGANIZATION   PP   100 40   68 P
 06. PROGRAMMING LABORATORY   TW   25 10   24 P C   16. O. O. PROG. & COMP. GRAPH. LAB   TW   50 20   46 P
 07. PROGRAMMING LABORATORY   PR   50 20   39 P C   17. O. O. PROG. & COMP. GRAPH. LAB   PR   50 20   39 P
 08. DIGITAL ELECTRONICS LABORATORY TW   25 10   23 P C   18. MICROPROCESSORS & INTERFACING LABTW   50 20   45 P
 09. DIGITAL ELECTRONICS LABORATORY PR   50 20   44 P C   19. MICROPROCESSORS & INTERFACING LABPR   50 20   32 P
 10. SOFT SKILLS   TW   50 20   45 P C   20. DATA STRUCTURES LABORATORY   TW   50 20   45 P
                                                             21. DATA STRUCTURES LABORATORY   PR   50 20   38 P
GRAND TOTAL = 1118/1500, RESULT: FIRST CLASS WITH DISTINCTION
ORDN. 1 MARKS :
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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
       OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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S80054311   RATHORE VIPULKUMAR RAMESH   ANJANA   , 71201038G   , S80054311 , PICT   , S80054311
01. DISCRETE STRUCTURES   PP   100 40   83 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   73 P
02. PROGRAMMING & PROBLEM SOLVING PP   100 40   62 P C   12. MICROPROC. & INTERFACING TECHNIQ.PP   100 40   56 P
03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 40   65 P C   13. DATA STRUCTURES   PP   100 40   68 P
04. DATA STRUCTURES AND ALGORITHMS PP   100 40   55 P C   14. COMPUTER GRAPHICS   PP   100 40   76 P
05. HUMANITIES AND SOCIAL SCIENCE PP   100 40   59 P C   15. COMPUTER ORGANIZATION   PP   100 40   64 P
06. PROGRAMMING LABORATORY   TW   25 10   22 P C   16. O. O. PROG. & COMP. GRAPH. LAB   TW   50 20   43 P
07. PROGRAMMING LABORATORY   PR   50 20   42 P C   17. O. O. PROG. & COMP. GRAPH. LAB   PR   50 20   41 P
08. DIGITAL ELECTRONICS LABORATORY TW   25 10   21 P C   18. MICROPROCESSORS & INTERFACING LABTW   50 20   42 P
09. DIGITAL ELECTRONICS LABORATORY PR   50 20   35 P C   19. MICROPROCESSORS & INTERFACING LABPR   50 20   41 P
10. SOFT SKILLS   TW   50 20   40 P C   20. DATA STRUCTURES LABORATORY   TW   50 20   42 P
                                                            21. DATA STRUCTURES LABORATORY   PR   50 20   39 P
GRAND TOTAL = 1069/1500, RESULT: FIRST CLASS WITH DISTINCTION
ORDN. 1 MARKS :
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S80054312   RISHI AGRAWAL   MANJU   , 71201039E   , S80054312 , PICT   , S80054312
01. DISCRETE STRUCTURES   PP   100 40   65 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   45 P
02. PROGRAMMING & PROBLEM SOLVING PP   100 40   46 P C   12. MICROPROC. & INTERFACING TECHNIQ.PP   100 40   47 P
03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 40   60 P C   13. DATA STRUCTURES   PP   100 40   62 P
04. DATA STRUCTURES AND ALGORITHMS PP   100 40   52 P C   14. COMPUTER GRAPHICS   PP   100 40   58 P
05. HUMANITIES AND SOCIAL SCIENCE PP   100 40   60 P C   15. COMPUTER ORGANIZATION   PP   100 40   43 P
06. PROGRAMMING LABORATORY   TW   25 10   18 P C   16. O. O. PROG. & COMP. GRAPH. LAB   TW   50 20   39 P
07. PROGRAMMING LABORATORY   PR   50 20   26 P C   17. O. O. PROG. & COMP. GRAPH. LAB   PR   50 20   38 P
08. DIGITAL ELECTRONICS LABORATORY TW   25 10   17 P C   18. MICROPROCESSORS & INTERFACING LABTW   50 20   39 P
09. DIGITAL ELECTRONICS LABORATORY PR   50 20   32 P C   19. MICROPROCESSORS & INTERFACING LABPR   50 20   38 P
10. SOFT SKILLS   TW   50 20   37 P C   20. DATA STRUCTURES LABORATORY   TW   50 20   40 P
                                                            21. DATA STRUCTURES LABORATORY   PR   50 20   38 P
GRAND TOTAL = 900/1500, RESULT: FIRST CLASS
ORDN. 1 MARKS :
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S80054313   ROHERA SHWETA VINOD   VEENA   , 71201041G   , S80054313 , PICT   , S80054313
01. DISCRETE STRUCTURES   PP   100 40   46 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   21 F
02. PROGRAMMING & PROBLEM SOLVING PP   100 40   56 P   12. MICROPROC. & INTERFACING TECHNIQ.PP   100 40   40 P
03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 40   40 P   13. DATA STRUCTURES   PP   100 40   45 P
04. DATA STRUCTURES AND ALGORITHMS PP   100 40   40 P   14. COMPUTER GRAPHICS   PP   100 40   46 P
05. HUMANITIES AND SOCIAL SCIENCE PP   100 40   40 P C   15. COMPUTER ORGANIZATION   PP   100 40   49 P
06. PROGRAMMING LABORATORY   TW   25 10   18 P C   16. O. O. PROG. & COMP. GRAPH. LAB   TW   50 20   38 P
07. PROGRAMMING LABORATORY   PR   50 20   10 F   17. O. O. PROG. & COMP. GRAPH. LAB   PR   50 20   28 P
08. DIGITAL ELECTRONICS LABORATORY TW   25 10   17 P C   18. MICROPROCESSORS & INTERFACING LABTW   50 20   31 P
09. DIGITAL ELECTRONICS LABORATORY PR   50 20   30 P   19. MICROPROCESSORS & INTERFACING LABPR   50 20   20$ P
10. SOFT SKILLS   TW   50 20   30 P C   20. DATA STRUCTURES LABORATORY   TW   50 20   37 P
                                                            21. DATA STRUCTURES LABORATORY   PR   50 20   22 P
GRAND TOTAL = 704/1500, RESULT: FAILS A.T.K.T.   [$ O.1]
ORDN. 1 MARKS : (19)2,
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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.
          OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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  S80054314   RUIKAR AMEY SUHAS   ARATI   , 71201044M   , S80054314 ,   PICT   , S80054314
  01. DISCRETE STRUCTURES   PP   100 40   87 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   86 P
  02. PROGRAMMING & PROBLEM SOLVING PP   100 40   68 P C   12. MICROPROC. & INTERFACING TECHNIQ.PP   100 40   70 P
  03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 40   70 P C   13. DATA STRUCTURES   PP   100 40   75 P
  04. DATA STRUCTURES AND ALGORITHMS PP   100 40   56 P C   14. COMPUTER GRAPHICS   PP   100 40   73 P
  05. HUMANITIES AND SOCIAL SCIENCE PP   100 40   67 P C   15. COMPUTER ORGANIZATION   PP   100 40   71 P
  06. PROGRAMMING LABORATORY   TW   25 10   20 P C   16. O. O. PROG. & COMP. GRAPH. LAB TW   50 20   45 P
  07. PROGRAMMING LABORATORY   PR   50 20   38 P C   17. O. O. PROG. & COMP. GRAPH. LAB PR   50 20   40 P
  08. DIGITAL ELECTRONICS LABORATORY TW   25 10   19 P C   18. MICROPROCESSORS & INTERFACING LABTW   50 20   45 P
  09. DIGITAL ELECTRONICS LABORATORY PR   50 20   35 P C   19. MICROPROCESSORS & INTERFACING LABPR   50 20   40 P
  10. SOFT SKILLS   TW   50 20   39 P C   20. DATA STRUCTURES LABORATORY   TW   50 20   45 P
                                                               21. DATA STRUCTURES LABORATORY   PR   50 20   44 P
GRAND TOTAL = 1133/1500, RESULT: FIRST CLASS WITH DISTINCTION
ORDN. 1 MARKS :
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  S80054315   SAISH SURESH SALI   MEENA   , 71201047F   , S80054315 ,   PICT   , S80054315
  01. DISCRETE STRUCTURES   PP   100 40   64 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   53 P
  02. PROGRAMMING & PROBLEM SOLVING PP   100 40   71 P C   12. MICROPROC. & INTERFACING TECHNIQ.PP   100 40   69 P
  03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 40   68 P C   13. DATA STRUCTURES   PP   100 40   72 P
  04. DATA STRUCTURES AND ALGORITHMS PP   100 40   43 P C   14. COMPUTER GRAPHICS   PP   100 40   58 P
  05. HUMANITIES AND SOCIAL SCIENCE PP   100 40   56 P C   15. COMPUTER ORGANIZATION   PP   100 40   61 P
  06. PROGRAMMING LABORATORY   TW   25 10   21 P C   16. O. O. PROG. & COMP. GRAPH. LAB TW   50 20   43 P
  07. PROGRAMMING LABORATORY   PR   50 20   32 P C   17. O. O. PROG. & COMP. GRAPH. LAB PR   50 20   42 P
  08. DIGITAL ELECTRONICS LABORATORY TW   25 10   21 P C   18. MICROPROCESSORS & INTERFACING LABTW   50 20   40 P
  09. DIGITAL ELECTRONICS LABORATORY PR   50 20   42 P C   19. MICROPROCESSORS & INTERFACING LABPR   50 20   43 P
  10. SOFT SKILLS   TW   50 20   42 P C   20. DATA STRUCTURES LABORATORY   TW   50 20   41 P
                                                               21. DATA STRUCTURES LABORATORY   PR   50 20   43 P
GRAND TOTAL = 1025/1500, RESULT: FIRST CLASS WITH DISTINCTION
ORDN. 1 MARKS :
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  S80054316   SALVE HONEY SUNIL   SABINA   , 71201050F   , S80054316 ,   PICT   , S80054316
  01. DISCRETE STRUCTURES   PP   100 40   71 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   41 P
  02. PROGRAMMING & PROBLEM SOLVING PP   100 40   61 P C   12. MICROPROC. & INTERFACING TECHNIQ.PP   100 40   40 P
  03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 40   58 P C   13. DATA STRUCTURES   PP   100 40   57 P
  04. DATA STRUCTURES AND ALGORITHMS PP   100 40   49 P C   14. COMPUTER GRAPHICS   PP   100 40   47 P
  05. HUMANITIES AND SOCIAL SCIENCE PP   100 40   56 P C   15. COMPUTER ORGANIZATION   PP   100 40   57 P
  06. PROGRAMMING LABORATORY   TW   25 10   21 P C   16. O. O. PROG. & COMP. GRAPH. LAB TW   50 20   40 P
  07. PROGRAMMING LABORATORY   PR   50 20   28 P   17. O. O. PROG. & COMP. GRAPH. LAB PR   50 20   30 P
  08. DIGITAL ELECTRONICS LABORATORY TW   25 10   21 P C   18. MICROPROCESSORS & INTERFACING LABTW   50 20   40 P
  09. DIGITAL ELECTRONICS LABORATORY PR   50 20   30 P C   19. MICROPROCESSORS & INTERFACING LABPR   50 20   32 P
  10. SOFT SKILLS   TW   50 20   38 P C   20. DATA STRUCTURES LABORATORY   TW   50 20   41 P
                                                               21. DATA STRUCTURES LABORATORY   PR   50 20   39 P
GRAND TOTAL = 897+03/1500, RESULT: FIRST CLASS [O.2]
ORDN. 1 MARKS :
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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
       OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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  S80054317   SANAP VISHAL PRABHAKAR   SUMATI   , 71347773D   , S80054317 ,   PICT   , S80054317
  01. DISCRETE STRUCTURES   PP   100 40   64 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   27 F
  02. PROGRAMMING & PROBLEM SOLVING PP   100 40   55 P C   12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40   51 P
  03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 40   64 P C   13. DATA STRUCTURES   PP   100 40   71 P
  04. DATA STRUCTURES AND ALGORITHMS PP   100 40   50 P C   14. COMPUTER GRAPHICS   PP   100 40   63 P
  05. HUMANITIES AND SOCIAL SCIENCE PP   100 40   71 P C   15. COMPUTER ORGANIZATION   PP   100 40   53 P
  06. PROGRAMMING LABORATORY   TW   25 10   20 P C   16. O. O. PROG. & COMP. GRAPH. LAB   TW   50 20   38 P
  07. PROGRAMMING LABORATORY   PR   50 20   21 P C   17. O. O. PROG. & COMP. GRAPH. LAB   PR   50 20   30 P
  08. DIGITAL ELECTRONICS LABORATORY TW   25 10   21 P C   18. MICROPROCESSORS & INTERFACING LABTW   50 20   39 P
  09. DIGITAL ELECTRONICS LABORATORY PR   50 20   32 P C   19. MICROPROCESSORS & INTERFACING LABPR   50 20   35 P
  10. SOFT SKILLS   TW   50 20   38 P C   20. DATA STRUCTURES LABORATORY   TW   50 20   43 P
                                                                21. DATA STRUCTURES LABORATORY   PR   50 20   40 P
GRAND TOTAL = 926/1500, RESULT: FAILS A.T.K.T.
ORDN. 1 MARKS :
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  S80054318   SANDEEP JEEVRAJ PARMAR   PUSHPA PARMAR   , 71347774B   , S80054318 ,   PICT   , S80054318
  01. DISCRETE STRUCTURES   PP   100 40   46 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   40 P
  02. PROGRAMMING & PROBLEM SOLVING PP   100 40   50 P C   12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40   48 P
  03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 40   70 P C   13. DATA STRUCTURES   PP   100 40   57 P
  04. DATA STRUCTURES AND ALGORITHMS PP   100 40   52 P C   14. COMPUTER GRAPHICS   PP   100 40   49 P
  05. HUMANITIES AND SOCIAL SCIENCE PP   100 40   60 P C   15. COMPUTER ORGANIZATION   PP   100 40   56 P
  06. PROGRAMMING LABORATORY   TW   25 10   20 P C   16. O. O. PROG. & COMP. GRAPH. LAB   TW   50 20   44 P
  07. PROGRAMMING LABORATORY   PR   50 20   36 P C   17. O. O. PROG. & COMP. GRAPH. LAB   PR   50 20   37 P
  08. DIGITAL ELECTRONICS LABORATORY TW   25 10   21 P C   18. MICROPROCESSORS & INTERFACING LABTW   50 20   44 P
  09. DIGITAL ELECTRONICS LABORATORY PR   50 20   33 P C   19. MICROPROCESSORS & INTERFACING LABPR   50 20   42 P
  10. SOFT SKILLS   TW   50 20   39 P C   20. DATA STRUCTURES LABORATORY   TW   50 20   44 P
                                                                21. DATA STRUCTURES LABORATORY   PR   50 20   39 P
GRAND TOTAL = 927/1500, RESULT: FIRST CLASS
ORDN. 1 MARKS :
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  S80054319   SATHE VYANKTESHPRASAD GANESH   BHAGYASHRI   , 71201054J   , S80054319 ,   PICT   , S80054319
  01. DISCRETE STRUCTURES   PP   100 40   75 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   40 P
  02. PROGRAMMING & PROBLEM SOLVING PP   100 40   53 P C   12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40   44 P
  03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 40   60 P C   13. DATA STRUCTURES   PP   100 40   71 P
  04. DATA STRUCTURES AND ALGORITHMS PP   100 40   55 P C   14. COMPUTER GRAPHICS   PP   100 40   58 P
  05. HUMANITIES AND SOCIAL SCIENCE PP   100 40   60 P C   15. COMPUTER ORGANIZATION   PP   100 40   54 P
  06. PROGRAMMING LABORATORY   TW   25 10   17 P C   16. O. O. PROG. & COMP. GRAPH. LAB   TW   50 20   32 P
  07. PROGRAMMING LABORATORY   PR   50 20   38 P C   17. O. O. PROG. & COMP. GRAPH. LAB   PR   50 20   35 P
  08. DIGITAL ELECTRONICS LABORATORY TW   25 10   18 P C   18. MICROPROCESSORS & INTERFACING LABTW   50 20   28 P
  09. DIGITAL ELECTRONICS LABORATORY PR   50 20   30 P C   19. MICROPROCESSORS & INTERFACING LABPR   50 20   30 P
  10. SOFT SKILLS   TW   50 20   32 P C   20. DATA STRUCTURES LABORATORY   TW   50 20   32 P
                                                                21. DATA STRUCTURES LABORATORY   PR   50 20   36 P
GRAND TOTAL = 898+02/1500, RESULT: FIRST CLASS [O.2]
ORDN. 1 MARKS :
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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
       OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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S80054320   SHAHANE TANMAY SHIVKUMAR   VASUNDHARA   , 71201062K   , S80054320 , PICT   , S80054320
01. DISCRETE STRUCTURES   PP   100 40   40 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   22 F
02. PROGRAMMING & PROBLEM SOLVING PP   100 40   40 P C   12. MICROPROC. & INTERFACING TECHNIQ.PP   100 40   40 P
03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 40   40 P   13. DATA STRUCTURES   PP   100 40   41 P
04. DATA STRUCTURES AND ALGORITHMS PP   100 40   40 P C   14. COMPUTER GRAPHICS   PP   100 40   51 P
05. HUMANITIES AND SOCIAL SCIENCE PP   100 40   54 P C   15. COMPUTER ORGANIZATION   PP   100 40   54 P
06. PROGRAMMING LABORATORY   TW   25 10   18 P C   16. O. O. PROG. & COMP. GRAPH. LAB   TW   50 20   25 P
07. PROGRAMMING LABORATORY   PR   50 20   32 P C   17. O. O. PROG. & COMP. GRAPH. LAB   PR   50 20   36 P
08. DIGITAL ELECTRONICS LABORATORY TW   25 10   17 P C   18. MICROPROCESSORS & INTERFACING LABTW   50 20   23 P
09. DIGITAL ELECTRONICS LABORATORY PR   50 20   31 P C   19. MICROPROCESSORS & INTERFACING LABPR   50 20   20$ P
10. SOFT SKILLS   TW   50 20   37 P C   20. DATA STRUCTURES LABORATORY   TW   50 20   22 P
                                                            21. DATA STRUCTURES LABORATORY   PR   50 20   21 P
GRAND TOTAL = 704/1500, RESULT: FAILS A.T.K.T.   [$ O.1]
ORDN. 1 MARKS : (19)2,
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S80054321   SHENDKAR NISHIGANDHA NARSING   SANGEETA   , 71201067L   , S80054321 , PICT   , S80054321
01. DISCRETE STRUCTURES   PP   100 40   66 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   61 P
02. PROGRAMMING & PROBLEM SOLVING PP   100 40   55 P C   12. MICROPROC. & INTERFACING TECHNIQ.PP   100 40   43 P
03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 40   60 P C   13. DATA STRUCTURES   PP   100 40   51 P
04. DATA STRUCTURES AND ALGORITHMS PP   100 40   53 P C   14. COMPUTER GRAPHICS   PP   100 40   52 P
05. HUMANITIES AND SOCIAL SCIENCE PP   100 40   63 P C   15. COMPUTER ORGANIZATION   PP   100 40   63 P
06. PROGRAMMING LABORATORY   TW   25 10   21 P C   16. O. O. PROG. & COMP. GRAPH. LAB   TW   50 20   43 P
07. PROGRAMMING LABORATORY   PR   50 20   30 P C   17. O. O. PROG. & COMP. GRAPH. LAB   PR   50 20   34 P
08. DIGITAL ELECTRONICS LABORATORY TW   25 10   21 P C   18. MICROPROCESSORS & INTERFACING LABTW   50 20   42 P
09. DIGITAL ELECTRONICS LABORATORY PR   50 20   35 P C   19. MICROPROCESSORS & INTERFACING LABPR   50 20   39 P
10. SOFT SKILLS   TW   50 20   41 P C   20. DATA STRUCTURES LABORATORY   TW   50 20   42 P
                                                            21. DATA STRUCTURES LABORATORY   PR   50 20   44 P
GRAND TOTAL = 959/1500, RESULT: FIRST CLASS
ORDN. 1 MARKS :
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S80054322   SHETE PRASHANT SAYAJI   MEERA   , 71201069G   , S80054322 , PICT   , S80054322
01. DISCRETE STRUCTURES   PP   100 40   66 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   63 P
02. PROGRAMMING & PROBLEM SOLVING PP   100 40   56 P C   12. MICROPROC. & INTERFACING TECHNIQ.PP   100 40   55 P
03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 40   66 P C   13. DATA STRUCTURES   PP   100 40   66 P
04. DATA STRUCTURES AND ALGORITHMS PP   100 40   58 P C   14. COMPUTER GRAPHICS   PP   100 40   59 P
05. HUMANITIES AND SOCIAL SCIENCE PP   100 40   62 P C   15. COMPUTER ORGANIZATION   PP   100 40   68 P
06. PROGRAMMING LABORATORY   TW   25 10   19 P C   16. O. O. PROG. & COMP. GRAPH. LAB   TW   50 20   33 P
07. PROGRAMMING LABORATORY   PR   50 20   20 P C   17. O. O. PROG. & COMP. GRAPH. LAB   PR   50 20   32 P
08. DIGITAL ELECTRONICS LABORATORY TW   25 10   19 P C   18. MICROPROCESSORS & INTERFACING LABTW   50 20   30 P
09. DIGITAL ELECTRONICS LABORATORY PR   50 20   27 P   19. MICROPROCESSORS & INTERFACING LABPR   50 20   30 P
10. SOFT SKILLS   TW   50 20   38 P C   20. DATA STRUCTURES LABORATORY   TW   50 20   35 P
                                                            21. DATA STRUCTURES LABORATORY   PR   50 20   35 P
GRAND TOTAL = 937/1500, RESULT: FIRST CLASS
ORDN. 1 MARKS :
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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S80054323   SHETTY VIPLUV GANESH   PUSHPA   , 71201070L   , S80054323 ,   PICT   , S80054323

01. DISCRETE STRUCTURES	PP	100	40	74	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	40	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	51	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	40	P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	60	P C	13. DATA STRUCTURES	PP	100	40	64	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	47	P C	14. COMPUTER GRAPHICS	PP	100	40	48	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	60	P C	15. COMPUTER ORGANIZATION	PP	100	40	57	P
06. PROGRAMMING LABORATORY	TW	25	10	22	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	43	P
07. PROGRAMMING LABORATORY	PR	50	20	34	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	40	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	21	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	42	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	39	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	44	P
10. SOFT SKILLS	TW	50	20	40	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	40	P
						21. DATA STRUCTURES LABORATORY	PR	50	20	40	P

GRAND TOTAL = 946/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

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S80054324   SHEWALE NEHA SHAMRAO   SHEWALE VAISHALI   , 71347775L   , S80054324 ,   PICT   , S80054324

01. DISCRETE STRUCTURES	PP	100	40	74	P	11. ENGINEERING MATHEMATICS III	PP	100	40	15	F
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	49	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	42	P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	53	P C	13. DATA STRUCTURES	PP	100	40	56	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	43	P C	14. COMPUTER GRAPHICS	PP	100	40	56	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	58	P C	15. COMPUTER ORGANIZATION	PP	100	40	49	P
06. PROGRAMMING LABORATORY	TW	25	10	22	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	40	P
07. PROGRAMMING LABORATORY	PR	50	20	28	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	28	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	21	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	40	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	32	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	35	P
10. SOFT SKILLS	TW	50	20	41	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	40	P
						21. DATA STRUCTURES LABORATORY	PR	50	20	40	P

GRAND TOTAL = 862/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

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S80054325   SIDDHANT SURENDRA GADRE   VIDULA   , 71201074C   , S80054325 ,   PICT   , S80054325

01. DISCRETE STRUCTURES	PP	100	40	68	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	40	P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	53	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	53	P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	42	P C	13. DATA STRUCTURES	PP	100	40	63	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	58	P C	14. COMPUTER GRAPHICS	PP	100	40	52	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	69	P C	15. COMPUTER ORGANIZATION	PP	100	40	57	P
06. PROGRAMMING LABORATORY	TW	25	10	18	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	39	P
07. PROGRAMMING LABORATORY	PR	50	20	40	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	35	P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	17	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	39	P
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	32	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	20	P
10. SOFT SKILLS	TW	50	20	36	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	39	P
						21. DATA STRUCTURES LABORATORY	PR	50	20	40	P

GRAND TOTAL = 910/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.
       OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S80054326   SIDHANT AGGARWAL   ALPANA   , 71201076K   , S80054326 ,   PICT   , S80054326
01. DISCRETE STRUCTURES   PP   100 40   47 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   46 P
02. PROGRAMMING & PROBLEM SOLVING   PP   100 40   52 P C   12. MICROPROC. & INTERFACING TECHNIQ.PP   100 40   51 P
03. DIGIT. ELECTRONICS & LOGIC DESIGNPP   100 40   58 P C   13. DATA STRUCTURES   PP   100 40   54 P
04. DATA STRUCTURES AND ALGORITHMS   PP   100 40   48 P C   14. COMPUTER GRAPHICS   PP   100 40   47 P
05. HUMANITIES AND SOCIAL SCIENCE   PP   100 40   57 P C   15. COMPUTER ORGANIZATION   PP   100 40   44 P
06. PROGRAMMING LABORATORY   TW   25 10   20 P C   16. O. O. PROG. & COMP. GRAPH. LAB   TW   50 20   43 P
07. PROGRAMMING LABORATORY   PR   50 20   42 P C   17. O. O. PROG. & COMP. GRAPH. LAB   PR   50 20   42 P
08. DIGITAL ELECTRONICS LABORATORY   TW   25 10   18 P C   18. MICROPROCESSORS & INTERFACING LABTW   50 20   40 P
09. DIGITAL ELECTRONICS LABORATORY   PR   50 20   38 P C   19. MICROPROCESSORS & INTERFACING LABPR   50 20   42 P
10. SOFT SKILLS   TW   50 20   35 P C   20. DATA STRUCTURES LABORATORY   TW   50 20   41 P
                                                             21. DATA STRUCTURES LABORATORY   PR   50 20   39 P
GRAND TOTAL = 904/1500, RESULT: FIRST CLASS
ORDN. 1 MARKS :

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S80054327   SOMDEEP DEY   JHUMA   , 71201078F   , S80054327 ,   PICT   , S80054327
01. DISCRETE STRUCTURES   PP   100 40   67 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   40 P
02. PROGRAMMING & PROBLEM SOLVING   PP   100 40   63 P C   12. MICROPROC. & INTERFACING TECHNIQ.PP   100 40   63 P
03. DIGIT. ELECTRONICS & LOGIC DESIGNPP   100 40   61 P C   13. DATA STRUCTURES   PP   100 40   54 P
04. DATA STRUCTURES AND ALGORITHMS   PP   100 40   45 P C   14. COMPUTER GRAPHICS   PP   100 40   58 P
05. HUMANITIES AND SOCIAL SCIENCE   PP   100 40   63 P C   15. COMPUTER ORGANIZATION   PP   100 40   58 P
06. PROGRAMMING LABORATORY   TW   25 10   22 P C   16. O. O. PROG. & COMP. GRAPH. LAB   TW   50 20   44 P
07. PROGRAMMING LABORATORY   PR   50 20   40 P C   17. O. O. PROG. & COMP. GRAPH. LAB   PR   50 20   41 P
08. DIGITAL ELECTRONICS LABORATORY   TW   25 10   20 P C   18. MICROPROCESSORS & INTERFACING LABTW   50 20   42 P
09. DIGITAL ELECTRONICS LABORATORY   PR   50 20   35 P C   19. MICROPROCESSORS & INTERFACING LABPR   50 20   43 P
10. SOFT SKILLS   TW   50 20   43 P C   20. DATA STRUCTURES LABORATORY   TW   50 20   44 P
                                                             21. DATA STRUCTURES LABORATORY   PR   50 20   39 P
GRAND TOTAL = 985/1500, RESULT: FIRST CLASS
ORDN. 1 MARKS :

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S80054328   SUKET SHARMA   SEEMA   , 71201081F   , S80054328 ,   PICT   , S80054328
01. DISCRETE STRUCTURES   PP   100 40   76 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   75 P
02. PROGRAMMING & PROBLEM SOLVING   PP   100 40   68 P C   12. MICROPROC. & INTERFACING TECHNIQ.PP   100 40   70 P
03. DIGIT. ELECTRONICS & LOGIC DESIGNPP   100 40   70 P C   13. DATA STRUCTURES   PP   100 40   68 P
04. DATA STRUCTURES AND ALGORITHMS   PP   100 40   57 P C   14. COMPUTER GRAPHICS   PP   100 40   66 P
05. HUMANITIES AND SOCIAL SCIENCE   PP   100 40   68 P C   15. COMPUTER ORGANIZATION   PP   100 40   59 P
06. PROGRAMMING LABORATORY   TW   25 10   23 P C   16. O. O. PROG. & COMP. GRAPH. LAB   TW   50 20   45 P
07. PROGRAMMING LABORATORY   PR   50 20   43 P C   17. O. O. PROG. & COMP. GRAPH. LAB   PR   50 20   42 P
08. DIGITAL ELECTRONICS LABORATORY   TW   25 10   23 P C   18. MICROPROCESSORS & INTERFACING LABTW   50 20   45 P
09. DIGITAL ELECTRONICS LABORATORY   PR   50 20   40 P C   19. MICROPROCESSORS & INTERFACING LABPR   50 20   45 P
10. SOFT SKILLS   TW   50 20   42 P C   20. DATA STRUCTURES LABORATORY   TW   50 20   44 P
                                                             21. DATA STRUCTURES LABORATORY   PR   50 20   46 P
GRAND TOTAL = 1115/1500, RESULT: FIRST CLASS WITH DISTINCTION
ORDN. 1 MARKS :

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.
         OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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S80054329   SURADKAR VIVEK PRAKASH                 DEEPALI           , 71201083B    , S80054329 ,   PICT     , S80054329
 01. DISCRETE STRUCTURES           PP   100  40   80  P C    11. ENGINEERING MATHEMATICS III      PP   100  40   25  F
 02. PROGRAMMING & PROBLEM SOLVING  PP   100  40   53  P C    12. MICROPROC. & INTERFACING TECHNIQ.PP   100  40   47  P
 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100  40   58  P C    13. DATA STRUCTURES                  PP   100  40   55  P
 04. DATA STRUCTURES AND ALGORITHMS PP   100  40   57  P C    14. COMPUTER GRAPHICS                PP   100  40   54  P
 05. HUMANITIES AND SOCIAL SCIENCE  PP   100  40   58  P C    15. COMPUTER ORGANIZATION            PP   100  40   62  P
 06. PROGRAMMING LABORATORY         TW    25  10   20  P C    16. O. O. PROG. & COMP. GRAPH. LAB   TW    50  20   27  P
 07. PROGRAMMING LABORATORY         PR    50  20   41  P C    17. O. O. PROG. & COMP. GRAPH. LAB   PR    50  20   28  P
 08. DIGITAL ELECTRONICS LABORATORY TW    25  10   20  P C    18. MICROPROCESSORS & INTERFACING LABTW    50  20   27  P
 09. DIGITAL ELECTRONICS LABORATORY PR    50  20   45  P C    19. MICROPROCESSORS & INTERFACING LABPR    50  20   35  P
 10. SOFT SKILLS                    TW    50  20   38  P C    20. DATA STRUCTURES LABORATORY       TW    50  20   29  P
                                                             21. DATA STRUCTURES LABORATORY       PR    50  20   24  P
GRAND TOTAL =  883/1500, RESULT: FAILS A.T.K.T.
ORDN. 1 MARKS :
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S80054330   SURUCHI SINGH                          RENU              , 71201084L    , S80054330 ,   PICT     , S80054330
 01. DISCRETE STRUCTURES           PP   100  40   64  P C    11. ENGINEERING MATHEMATICS III      PP   100  40   25  F
 02. PROGRAMMING & PROBLEM SOLVING  PP   100  40   49  P C    12. MICROPROC. & INTERFACING TECHNIQ.PP   100  40   41  P
 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100  40   51  P      13. DATA STRUCTURES                  PP   100  40   57  P
 04. DATA STRUCTURES AND ALGORITHMS PP   100  40   52  P      14. COMPUTER GRAPHICS                PP   100  40   47  P
 05. HUMANITIES AND SOCIAL SCIENCE  PP   100  40   54  P C    15. COMPUTER ORGANIZATION            PP   100  40   60  P
 06. PROGRAMMING LABORATORY         TW    25  10   22  P C    16. O. O. PROG. & COMP. GRAPH. LAB   TW    50  20   44  P
 07. PROGRAMMING LABORATORY         PR    50  20   36  P C    17. O. O. PROG. & COMP. GRAPH. LAB   PR    50  20   16  F
 08. DIGITAL ELECTRONICS LABORATORY TW    25  10   21  P C    18. MICROPROCESSORS & INTERFACING LABTW    50  20   39  P
 09. DIGITAL ELECTRONICS LABORATORY PR    50  20   28  P      19. MICROPROCESSORS & INTERFACING LABPR    50  20   30  P
 10. SOFT SKILLS                    TW    50  20   37  P C    20. DATA STRUCTURES LABORATORY       TW    50  20   40  P
                                                             21. DATA STRUCTURES LABORATORY       PR    50  20   29  P
GRAND TOTAL =  842/1500, RESULT: FAILS A.T.K.T.
ORDN. 1 MARKS :
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S80054331   SUTAR ASHUTOSH SANJAY               SUTAR MANISHA SANJAY , 71347776J    , S80054331 ,   PICT     , S80054331
 01. DISCRETE STRUCTURES           PP   100  40   61  P C    11. ENGINEERING MATHEMATICS III      PP   100  40   19  F
 02. PROGRAMMING & PROBLEM SOLVING  PP   100  40   53  P C    12. MICROPROC. & INTERFACING TECHNIQ.PP   100  40   51  P
 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100  40   51  P C    13. DATA STRUCTURES                  PP   100  40   56  P
 04. DATA STRUCTURES AND ALGORITHMS PP   100  40   46  P C    14. COMPUTER GRAPHICS                PP   100  40   47  P
 05. HUMANITIES AND SOCIAL SCIENCE  PP   100  40   61  P C    15. COMPUTER ORGANIZATION            PP   100  40   57  P
 06. PROGRAMMING LABORATORY         TW    25  10   20  P C    16. O. O. PROG. & COMP. GRAPH. LAB   TW    50  20   39  P
 07. PROGRAMMING LABORATORY         PR    50  20   29  P C    17. O. O. PROG. & COMP. GRAPH. LAB   PR    50  20   37  P
 08. DIGITAL ELECTRONICS LABORATORY TW    25  10   21  P C    18. MICROPROCESSORS & INTERFACING LABTW    50  20   39  P
 09. DIGITAL ELECTRONICS LABORATORY PR    50  20   34  P C    19. MICROPROCESSORS & INTERFACING LABPR    50  20   26  P
 10. SOFT SKILLS                    TW    50  20   36  P C    20. DATA STRUCTURES LABORATORY       TW    50  20   41  P
                                                             21. DATA STRUCTURES LABORATORY       PR    50  20   37  P
GRAND TOTAL =  861/1500, RESULT: FAILS A.T.K.T.
ORDN. 1 MARKS :
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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
      OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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  S80054332   SUYASH PRAMOD MULBAGAL   SHAILAJA   , 71201086G   , S80054332 ,   PICT   , S80054332
  01. DISCRETE STRUCTURES   PP   100 40   51 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   40 P
  02. PROGRAMMING & PROBLEM SOLVING PP   100 40   54 P C   12. MICROPROC. & INTERFACING TECHNIQ.PP   100 40   29 F
  03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 40   56 P C   13. DATA STRUCTURES   PP   100 40   59 P
  04. DATA STRUCTURES AND ALGORITHMS PP   100 40   43 P C   14. COMPUTER GRAPHICS   PP   100 40   49 P
  05. HUMANITIES AND SOCIAL SCIENCE PP   100 40   51 P C   15. COMPUTER ORGANIZATION   PP   100 40   53 P
  06. PROGRAMMING LABORATORY   TW   25 10   20 P C   16. O. O. PROG. & COMP. GRAPH. LAB   TW   50 20   39 P
  07. PROGRAMMING LABORATORY   PR   50 20   36 P C   17. O. O. PROG. & COMP. GRAPH. LAB   PR   50 20   37 P
  08. DIGITAL ELECTRONICS LABORATORY TW   25 10   18 P C   18. MICROPROCESSORS & INTERFACING LABTW   50 20   40 P
  09. DIGITAL ELECTRONICS LABORATORY PR   50 20   30 P C   19. MICROPROCESSORS & INTERFACING LABPR   50 20   34 P
  10. SOFT SKILLS   TW   50 20   35 P C   20. DATA STRUCTURES LABORATORY   TW   50 20   41 P
                                                               21. DATA STRUCTURES LABORATORY   PR   50 20   38 P
GRAND TOTAL = 853/1500, RESULT: FAILS A.T.K.T.
ORDN. 1 MARKS :
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  S80054333   SWAPNIL KUMAR LANKE   SUNITA   , 71201087E   , S80054333 ,   PICT   , S80054333
  01. DISCRETE STRUCTURES   PP   100 40   63 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   40 P
  02. PROGRAMMING & PROBLEM SOLVING PP   100 40   57 P C   12. MICROPROC. & INTERFACING TECHNIQ.PP   100 40   41 P
  03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 40   54 P C   13. DATA STRUCTURES   PP   100 40   55 P
  04. DATA STRUCTURES AND ALGORITHMS PP   100 40   47 P C   14. COMPUTER GRAPHICS   PP   100 40   54 P
  05. HUMANITIES AND SOCIAL SCIENCE PP   100 40   58 P C   15. COMPUTER ORGANIZATION   PP   100 40   56 P
  06. PROGRAMMING LABORATORY   TW   25 10   18 P C   16. O. O. PROG. & COMP. GRAPH. LAB   TW   50 20   35 P
  07. PROGRAMMING LABORATORY   PR   50 20   25 P C   17. O. O. PROG. & COMP. GRAPH. LAB   PR   50 20   31 P
  08. DIGITAL ELECTRONICS LABORATORY TW   25 10   18 P C   18. MICROPROCESSORS & INTERFACING LABTW   50 20   30 P
  09. DIGITAL ELECTRONICS LABORATORY PR   50 20   33 P C   19. MICROPROCESSORS & INTERFACING LABPR   50 20   22 P
  10. SOFT SKILLS   TW   50 20   35 P C   20. DATA STRUCTURES LABORATORY   TW   50 20   34 P
                                                               21. DATA STRUCTURES LABORATORY   PR   50 20   32 P
GRAND TOTAL = 838/1500, RESULT: HIGHER SECOND CLASS
ORDN. 1 MARKS :
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  S80054334   TALPE SHITAL LAXMAN   HEMLATA   , 71347777G   , S80054334 ,   PICT   , S80054334
  01. DISCRETE STRUCTURES   PP   100 40   40 P   11. ENGINEERING MATHEMATICS III   PP   100 40   01 F
  02. PROGRAMMING & PROBLEM SOLVING PP   100 40   40 P C   12. MICROPROC. & INTERFACING TECHNIQ.PP   100 40   20 F
  03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 40   40 P   13. DATA STRUCTURES   PP   100 40   40 P
  04. DATA STRUCTURES AND ALGORITHMS PP   100 40   29 F   14. COMPUTER GRAPHICS   PP   100 40   40 P
  05. HUMANITIES AND SOCIAL SCIENCE PP   100 40   40 P C   15. COMPUTER ORGANIZATION   PP   100 40   40 P
  06. PROGRAMMING LABORATORY   TW   25 10   18 P C   16. O. O. PROG. & COMP. GRAPH. LAB   TW   50 20   35 P
  07. PROGRAMMING LABORATORY   PR   50 20   11 F   17. O. O. PROG. & COMP. GRAPH. LAB   PR   50 20   07 F
  08. DIGITAL ELECTRONICS LABORATORY TW   25 10   17 P C   18. MICROPROCESSORS & INTERFACING LABTW   50 20   34 P
  09. DIGITAL ELECTRONICS LABORATORY PR   50 20   25 P   19. MICROPROCESSORS & INTERFACING LABPR   50 20   21 P
  10. SOFT SKILLS   TW   50 20   37 P C   20. DATA STRUCTURES LABORATORY   TW   50 20   34 P
                                                               21. DATA STRUCTURES LABORATORY   PR   50 20   05 F
GRAND TOTAL = 574/1500, RESULT: FAILS
ORDN. 1 MARKS :
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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
          OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S80054335   TAMHANE NEIL AJIT   VAIBHAVI   , 71201091C   , S80054335 , PICT   , S80054335
01. DISCRETE STRUCTURES   PP   100 40   48 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   40 P
02. PROGRAMMING & PROBLEM SOLVING PP   100 40   50 P C   12. MICROPROC. & INTERFACING TECHNIQ.PP   100 40   25 F
03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 40   56 P C   13. DATA STRUCTURES   PP   100 40   47 P
04. DATA STRUCTURES AND ALGORITHMS PP   100 40   40 P C   14. COMPUTER GRAPHICS   PP   100 40   41 P
05. HUMANITIES AND SOCIAL SCIENCE   PP   100 40   61 P C   15. COMPUTER ORGANIZATION   PP   100 40   50 P
06. PROGRAMMING LABORATORY   TW   25 10   20 P C   16. O. O. PROG. & COMP. GRAPH. LAB   TW   50 20   39 P
07. PROGRAMMING LABORATORY   PR   50 20   37 P C   17. O. O. PROG. & COMP. GRAPH. LAB   PR   50 20   30 P
08. DIGITAL ELECTRONICS LABORATORY TW   25 10   18 P C   18. MICROPROCESSORS & INTERFACING LABTW   50 20   27 P
09. DIGITAL ELECTRONICS LABORATORY PR   50 20   25 P C   19. MICROPROCESSORS & INTERFACING LABPR   50 20   08 F
10. SOFT SKILLS   TW   50 20   34 P C   20. DATA STRUCTURES LABORATORY   TW   50 20   28 P
                                                             21. DATA STRUCTURES LABORATORY   PR   50 20   29 P
GRAND TOTAL = 753/1500, RESULT: FAILS A.T.K.T.
ORDN. 1 MARKS :

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S80054336   TAYADE SUSHANT RAMESH   NANDINI   , 71201094H   , S80054336 , PICT   , S80054336
01. DISCRETE STRUCTURES   PP   100 40   62 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   AA F
02. PROGRAMMING & PROBLEM SOLVING PP   100 40   63 P   12. MICROPROC. & INTERFACING TECHNIQ.PP   100 40   AA F
03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 40   40 P C   13. DATA STRUCTURES   PP   100 40   40 P
04. DATA STRUCTURES AND ALGORITHMS PP   100 40   50 P   14. COMPUTER GRAPHICS   PP   100 40   40 P
05. HUMANITIES AND SOCIAL SCIENCE   PP   100 40   53 P   15. COMPUTER ORGANIZATION   PP   100 40   AA F
06. PROGRAMMING LABORATORY   TW   25 10   19 P C   16. O. O. PROG. & COMP. GRAPH. LAB   TW   50 20   30 P
07. PROGRAMMING LABORATORY   PR   50 20   20 P C   17. O. O. PROG. & COMP. GRAPH. LAB   PR   50 20   33 P
08. DIGITAL ELECTRONICS LABORATORY TW   25 10   17 P C   18. MICROPROCESSORS & INTERFACING LABTW   50 20   26 P
09. DIGITAL ELECTRONICS LABORATORY PR   50 20   32 P C   19. MICROPROCESSORS & INTERFACING LABPR   50 20   23 P
10. SOFT SKILLS   TW   50 20   33 P C   20. DATA STRUCTURES LABORATORY   TW   50 20   30 P
                                                             21. DATA STRUCTURES LABORATORY   PR   50 20   28 P
GRAND TOTAL = 639/1500, RESULT: FAILS A.T.K.T.
ORDN. 1 MARKS :

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S80054337   TIPARE ROHAN RAVINDRA   KALPANA   , 71201097B   , S80054337 , PICT   , S80054337
01. DISCRETE STRUCTURES   PP   100 40   68 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   40 P
02. PROGRAMMING & PROBLEM SOLVING PP   100 40   57 P C   12. MICROPROC. & INTERFACING TECHNIQ.PP   100 40   52 P
03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 40   58 P C   13. DATA STRUCTURES   PP   100 40   59 P
04. DATA STRUCTURES AND ALGORITHMS PP   100 40   51 P C   14. COMPUTER GRAPHICS   PP   100 40   58 P
05. HUMANITIES AND SOCIAL SCIENCE   PP   100 40   47 P C   15. COMPUTER ORGANIZATION   PP   100 40   52 P
06. PROGRAMMING LABORATORY   TW   25 10   22 P C   16. O. O. PROG. & COMP. GRAPH. LAB   TW   50 20   40 P
07. PROGRAMMING LABORATORY   PR   50 20   32 P C   17. O. O. PROG. & COMP. GRAPH. LAB   PR   50 20   32 P
08. DIGITAL ELECTRONICS LABORATORY TW   25 10   21 P C   18. MICROPROCESSORS & INTERFACING LABTW   50 20   44 P
09. DIGITAL ELECTRONICS LABORATORY PR   50 20   30 P C   19. MICROPROCESSORS & INTERFACING LABPR   50 20   38 P
10. SOFT SKILLS   TW   50 20   39 P C   20. DATA STRUCTURES LABORATORY   TW   50 20   40 P
                                                             21. DATA STRUCTURES LABORATORY   PR   50 20   34 P
GRAND TOTAL = 914/1500, RESULT: FIRST CLASS
ORDN. 1 MARKS :

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
          OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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S80054338   UMATE PIYUSH NAGANATH   NAINA   , 71201099J   , S80054338 ,   PICT   , S80054338
  01. DISCRETE STRUCTURES   PP   100 40   61 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   62 P
  02. PROGRAMMING & PROBLEM SOLVING   PP   100 40   69 P C   12. MICROPROC. & INTERFACING TECHNIQ.PP   100 40   50 P
  03. DIGIT. ELECTRONICS & LOGIC DESIGNPP   100 40   71 P C   13. DATA STRUCTURES   PP   100 40   61 P
  04. DATA STRUCTURES AND ALGORITHMS PP   100 40   44 P C   14. COMPUTER GRAPHICS   PP   100 40   59 P
  05. HUMANITIES AND SOCIAL SCIENCE   PP   100 40   65 P C   15. COMPUTER ORGANIZATION   PP   100 40   61 P
  06. PROGRAMMING LABORATORY   TW   25 10   21 P C   16. O. O. PROG. & COMP. GRAPH. LAB   TW   50 20   44 P
  07. PROGRAMMING LABORATORY   PR   50 20   31 P C   17. O. O. PROG. & COMP. GRAPH. LAB   PR   50 20   38 P
  08. DIGITAL ELECTRONICS LABORATORY TW   25 10   21 P C   18. MICROPROCESSORS & INTERFACING LABTW   50 20   43 P
  09. DIGITAL ELECTRONICS LABORATORY PR   50 20   40 P C   19. MICROPROCESSORS & INTERFACING LABPR   50 20   40 P
  10. SOFT SKILLS   TW   50 20   40 P C   20. DATA STRUCTURES LABORATORY   TW   50 20   43 P
                                                               21. DATA STRUCTURES LABORATORY   PR   50 20   39 P
GRAND TOTAL = 1003/1500, RESULT: FIRST CLASS WITH DISTINCTION
ORDN. 1 MARKS :
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S80054339   UNAVANE RUTUJA RAJESH   ANJALI   , 71201100F   , S80054339 ,   PICT   , S80054339
  01. DISCRETE STRUCTURES   PP   100 40   80 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   70 P
  02. PROGRAMMING & PROBLEM SOLVING   PP   100 40   69 P C   12. MICROPROC. & INTERFACING TECHNIQ.PP   100 40   64 P
  03. DIGIT. ELECTRONICS & LOGIC DESIGNPP   100 40   66 P C   13. DATA STRUCTURES   PP   100 40   54 P
  04. DATA STRUCTURES AND ALGORITHMS PP   100 40   59 P C   14. COMPUTER GRAPHICS   PP   100 40   58 P
  05. HUMANITIES AND SOCIAL SCIENCE   PP   100 40   70 P C   15. COMPUTER ORGANIZATION   PP   100 40   71 P
  06. PROGRAMMING LABORATORY   TW   25 10   21 P C   16. O. O. PROG. & COMP. GRAPH. LAB   TW   50 20   42 P
  07. PROGRAMMING LABORATORY   PR   50 20   38 P C   17. O. O. PROG. & COMP. GRAPH. LAB   PR   50 20   35 P
  08. DIGITAL ELECTRONICS LABORATORY TW   25 10   22 P C   18. MICROPROCESSORS & INTERFACING LABTW   50 20   40 P
  09. DIGITAL ELECTRONICS LABORATORY PR   50 20   32 P C   19. MICROPROCESSORS & INTERFACING LABPR   50 20   38 P
  10. SOFT SKILLS   TW   50 20   43 P C   20. DATA STRUCTURES LABORATORY   TW   50 20   41 P
                                                               21. DATA STRUCTURES LABORATORY   PR   50 20   39 P
GRAND TOTAL = 1052/1500, RESULT: FIRST CLASS WITH DISTINCTION
ORDN. 1 MARKS :
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S80054340   VALLARI ANAND   RACHANA   , 71201104J   , S80054340 ,   PICT   , S80054340
  01. DISCRETE STRUCTURES   PP   100 40   54 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   17 F
  02. PROGRAMMING & PROBLEM SOLVING   PP   100 40   47 P C   12. MICROPROC. & INTERFACING TECHNIQ.PP   100 40   45 P
  03. DIGIT. ELECTRONICS & LOGIC DESIGNPP   100 40   49 P C   13. DATA STRUCTURES   PP   100 40   51 P
  04. DATA STRUCTURES AND ALGORITHMS PP   100 40   40 P   14. COMPUTER GRAPHICS   PP   100 40   40 P
  05. HUMANITIES AND SOCIAL SCIENCE   PP   100 40   53 P C   15. COMPUTER ORGANIZATION   PP   100 40   40 P
  06. PROGRAMMING LABORATORY   TW   25 10   20 P C   16. O. O. PROG. & COMP. GRAPH. LAB   TW   50 20   41 P
  07. PROGRAMMING LABORATORY   PR   50 20   35 P C   17. O. O. PROG. & COMP. GRAPH. LAB   PR   50 20   33 P
  08. DIGITAL ELECTRONICS LABORATORY TW   25 10   21 P C   18. MICROPROCESSORS & INTERFACING LABTW   50 20   39 P
  09. DIGITAL ELECTRONICS LABORATORY PR   50 20   34 P   19. MICROPROCESSORS & INTERFACING LABPR   50 20   23 P
  10. SOFT SKILLS   TW   50 20   35 P C   20. DATA STRUCTURES LABORATORY   TW   50 20   40 P
                                                               21. DATA STRUCTURES LABORATORY   PR   50 20   14 F
GRAND TOTAL = 771/1500, RESULT: FAILS A.T.K.T.
ORDN. 1 MARKS :
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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
       OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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S80054341   VARADE POOJA VIJAY              CHANDA            , 71201105G   , S80054341 ,  PICT    , S80054341
 01. DISCRETE STRUCTURES          PP   100  40   84  P C    11. ENGINEERING MATHEMATICS III     PP   100  40   65  P
 02. PROGRAMMING & PROBLEM SOLVING PP   100  40   67  P C    12. MICROPROC. & INTERFACING TECHNIQ.PP   100  40   57  P
 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100  40   69  P C    13. DATA STRUCTURES                 PP   100  40   65  P
 04. DATA STRUCTURES AND ALGORITHMS PP  100  40   50  P C    14. COMPUTER GRAPHICS               PP   100  40   67  P
 05. HUMANITIES AND SOCIAL SCIENCE PP   100  40   66  P C    15. COMPUTER ORGANIZATION           PP   100  40   70  P
 06. PROGRAMMING LABORATORY        TW    25  10   21  P C    16. O. O. PROG. & COMP. GRAPH. LAB  TW    50  20   40  P
 07. PROGRAMMING LABORATORY        PR    50  20   42  P C    17. O. O. PROG. & COMP. GRAPH. LAB  PR    50  20   35  P
 08. DIGITAL ELECTRONICS LABORATORY TW   25  10   19  P C    18. MICROPROCESSORS & INTERFACING LABTW   50  20   39  P
 09. DIGITAL ELECTRONICS LABORATORY PR   50  20   39  P C    19. MICROPROCESSORS & INTERFACING LABPR   50  20   30  P
 10. SOFT SKILLS                   TW    50  20   39  P C    20. DATA STRUCTURES LABORATORY      TW    50  20   38  P
                                                             21. DATA STRUCTURES LABORATORY      PR    50  20   40  P
GRAND TOTAL = 1042/1500, RESULT: FIRST CLASS WITH DISTINCTION
ORDN. 1 MARKS :
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S80054342   VARMA RASHMI RAVI              LEELA            , 71201106E   , S80054342 ,  PICT    , S80054342
 01. DISCRETE STRUCTURES          PP   100  40   66  P C    11. ENGINEERING MATHEMATICS III     PP   100  40   24  F
 02. PROGRAMMING & PROBLEM SOLVING PP   100  40   46  P C    12. MICROPROC. & INTERFACING TECHNIQ.PP   100  40   44  P
 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100  40   60  P C    13. DATA STRUCTURES                 PP   100  40   58  P
 04. DATA STRUCTURES AND ALGORITHMS PP  100  40   54  P C    14. COMPUTER GRAPHICS               PP   100  40   56  P
 05. HUMANITIES AND SOCIAL SCIENCE PP   100  40   60  P C    15. COMPUTER ORGANIZATION           PP   100  40   66  P
 06. PROGRAMMING LABORATORY        TW    25  10   18  P C    16. O. O. PROG. & COMP. GRAPH. LAB  TW    50  20   32  P
 07. PROGRAMMING LABORATORY        PR    50  20   33  P C    17. O. O. PROG. & COMP. GRAPH. LAB  PR    50  20   35  P
 08. DIGITAL ELECTRONICS LABORATORY TW   25  10   16  P C    18. MICROPROCESSORS & INTERFACING LABTW   50  20   28  P
 09. DIGITAL ELECTRONICS LABORATORY PR   50  20   25  P C    19. MICROPROCESSORS & INTERFACING LABPR   50  20   41  P
 10. SOFT SKILLS                   TW    50  20   33  P C    20. DATA STRUCTURES LABORATORY      TW    50  20   29  P
                                                             21. DATA STRUCTURES LABORATORY      PR    50  20   15  F
GRAND TOTAL =  839/1500, RESULT: FAILS A.T.K.T.
ORDN. 1 MARKS :
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S80054343   VASAVE SUSMITA MANUVEL          INDU             , 71201107C   , S80054343 ,  PICT    , S80054343
 01. DISCRETE STRUCTURES          PP   100  40   66  P C    11. ENGINEERING MATHEMATICS III     PP   100  40   41  P
 02. PROGRAMMING & PROBLEM SOLVING PP   100  40   40  P C    12. MICROPROC. & INTERFACING TECHNIQ.PP   100  40   47  P
 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100  40   58  P C    13. DATA STRUCTURES                 PP   100  40   63  P
 04. DATA STRUCTURES AND ALGORITHMS PP  100  40   40  P C    14. COMPUTER GRAPHICS               PP   100  40   59  P
 05. HUMANITIES AND SOCIAL SCIENCE PP   100  40   46  P C    15. COMPUTER ORGANIZATION           PP   100  40   56  P
 06. PROGRAMMING LABORATORY        TW    25  10   20  P C    16. O. O. PROG. & COMP. GRAPH. LAB  TW    50  20   41  P
 07. PROGRAMMING LABORATORY        PR    50  20   30  P C    17. O. O. PROG. & COMP. GRAPH. LAB  PR    50  20   21  P
 08. DIGITAL ELECTRONICS LABORATORY TW   25  10   22  P C    18. MICROPROCESSORS & INTERFACING LABTW   50  20   40  P
 09. DIGITAL ELECTRONICS LABORATORY PR   50  20   29  P C    19. MICROPROCESSORS & INTERFACING LABPR   50  20   25  P
 10. SOFT SKILLS                   TW    50  20   40  P C    20. DATA STRUCTURES LABORATORY      TW    50  20   40  P
                                                             21. DATA STRUCTURES LABORATORY      PR    50  20   33  P
GRAND TOTAL =  857/1500, RESULT: HIGHER SECOND CLASS
ORDN. 1 MARKS :
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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
      OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S80054344   VAZIRABADKAR KETAN MADHAVRAO          ANITA              , 71201108M    , S80054344 ,  PICT     , S80054344
 01. DISCRETE STRUCTURES           PP   100  40   49  P C   11. ENGINEERING MATHEMATICS III      PP   100  40   45  P
 02. PROGRAMMING & PROBLEM SOLVING  PP   100  40   53  P C   12. MICROPROC. & INTERFACING TECHNIQ.PP   100  40   57  P
 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100  40   60  P C   13. DATA STRUCTURES                  PP   100  40   69  P
 04. DATA STRUCTURES AND ALGORITHMS PP   100  40   44  P C   14. COMPUTER GRAPHICS                PP   100  40   63  P
 05. HUMANITIES AND SOCIAL SCIENCE  PP   100  40   63  P C   15. COMPUTER ORGANIZATION            PP   100  40   66  P
 06. PROGRAMMING LABORATORY         TW    25  10   19  P C   16. O. O. PROG. & COMP. GRAPH. LAB   TW    50  20   39  P
 07. PROGRAMMING LABORATORY         PR    50  20   35  P C   17. O. O. PROG. & COMP. GRAPH. LAB   PR    50  20   35  P
 08. DIGITAL ELECTRONICS LABORATORY TW    25  10   16  P C   18. MICROPROCESSORS & INTERFACING LABTW    50  20   38  P
 09. DIGITAL ELECTRONICS LABORATORY PR    50  20   34  P C   19. MICROPROCESSORS & INTERFACING LABPR    50  20   40  P
 10. SOFT SKILLS                    TW    50  20   28  P C   20. DATA STRUCTURES LABORATORY       TW    50  20   37  P
                                                             21. DATA STRUCTURES LABORATORY       PR    50  20   32  P
GRAND TOTAL =  922/1500, RESULT: FIRST CLASS
ORDN. 1 MARKS :
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S80054345   VIKRAM SUNIL PATIL               JAYMALA             , 71201109K    , S80054345 ,  PICT     , S80054345
 01. DISCRETE STRUCTURES           PP   100  40   77  P C   11. ENGINEERING MATHEMATICS III      PP   100  40   57  P
 02. PROGRAMMING & PROBLEM SOLVING  PP   100  40   54  P C   12. MICROPROC. & INTERFACING TECHNIQ.PP   100  40   56  P
 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100  40   64  P C   13. DATA STRUCTURES                  PP   100  40   54  P
 04. DATA STRUCTURES AND ALGORITHMS PP   100  40   56  P C   14. COMPUTER GRAPHICS                PP   100  40   64  P
 05. HUMANITIES AND SOCIAL SCIENCE  PP   100  40   63  P C   15. COMPUTER ORGANIZATION            PP   100  40   58  P
 06. PROGRAMMING LABORATORY         TW    25  10   23  P C   16. O. O. PROG. & COMP. GRAPH. LAB   TW    50  20   44  P
 07. PROGRAMMING LABORATORY         PR    50  20   43  P C   17. O. O. PROG. & COMP. GRAPH. LAB   PR    50  20   38  P
 08. DIGITAL ELECTRONICS LABORATORY TW    25  10   22  P C   18. MICROPROCESSORS & INTERFACING LABTW    50  20   42  P
 09. DIGITAL ELECTRONICS LABORATORY PR    50  20   38  P C   19. MICROPROCESSORS & INTERFACING LABPR    50  20   34  P
 10. SOFT SKILLS                    TW    50  20   40  P C   20. DATA STRUCTURES LABORATORY       TW    50  20   42  P
                                                             21. DATA STRUCTURES LABORATORY       PR    50  20   39  P
GRAND TOTAL = 1008/1500, RESULT: FIRST CLASS WITH DISTINCTION
ORDN. 1 MARKS :
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S80054346   VISPUTE PRIYANKA SUNIL           ANITA              , 71201112K    , S80054346 ,  PICT     , S80054346
 01. DISCRETE STRUCTURES           PP   100  40   52  P C   11. ENGINEERING MATHEMATICS III      PP   100  40   24  F
 02. PROGRAMMING & PROBLEM SOLVING  PP   100  40   40  P C   12. MICROPROC. & INTERFACING TECHNIQ.PP   100  40   50  P
 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100  40   59  P C   13. DATA STRUCTURES                  PP   100  40   50  P
 04. DATA STRUCTURES AND ALGORITHMS PP   100  40   40  P C   14. COMPUTER GRAPHICS                PP   100  40   49  P
 05. HUMANITIES AND SOCIAL SCIENCE  PP   100  40   60  P C   15. COMPUTER ORGANIZATION            PP   100  40   58  P
 06. PROGRAMMING LABORATORY         TW    25  10   21  P C   16. O. O. PROG. & COMP. GRAPH. LAB   TW    50  20   36  P
 07. PROGRAMMING LABORATORY         PR    50  20   29  P C   17. O. O. PROG. & COMP. GRAPH. LAB   PR    50  20   37  P
 08. DIGITAL ELECTRONICS LABORATORY TW    25  10   19  P C   18. MICROPROCESSORS & INTERFACING LABTW    50  20   28  P
 09. DIGITAL ELECTRONICS LABORATORY PR    50  20   33  P C   19. MICROPROCESSORS & INTERFACING LABPR    50  20   32  P
 10. SOFT SKILLS                    TW    50  20   40  P C   20. DATA STRUCTURES LABORATORY       TW    50  20   32  P
                                                             21. DATA STRUCTURES LABORATORY       PR    50  20   26  P
GRAND TOTAL =  815/1500, RESULT: FAILS A.T.K.T.
ORDN. 1 MARKS :
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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.
      OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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 S80054347   WAGH SHARAD DADASAHEB   DAYABAI   , 71347778E   , S80054347 ,   PICT   , S80054347
 01. DISCRETE STRUCTURES   PP   100 40   50 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   40 P
 02. PROGRAMMING & PROBLEM SOLVING PP   100 40   43 P C   12. MICROPROC. & INTERFACING TECHNIQ.PP   100 40   44 P
 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP   100 40   56 P C   13. DATA STRUCTURES   PP   100 40   52 P
 04. DATA STRUCTURES AND ALGORITHMS PP   100 40   48 P C   14. COMPUTER GRAPHICS   PP   100 40   60 P
 05. HUMANITIES AND SOCIAL SCIENCE PP   100 40   54 P C   15. COMPUTER ORGANIZATION   PP   100 40   65 P
 06. PROGRAMMING LABORATORY   TW   25 10   18 P C   16. O. O. PROG. & COMP. GRAPH. LAB   TW   50 20   35 P
 07. PROGRAMMING LABORATORY   PR   50 20   27 P   17. O. O. PROG. & COMP. GRAPH. LAB   PR   50 20   10 F
 08. DIGITAL ELECTRONICS LABORATORY TW   25 10   18 P C   18. MICROPROCESSORS & INTERFACING LABTW   50 20   35 P
 09. DIGITAL ELECTRONICS LABORATORY PR   50 20   29 P C   19. MICROPROCESSORS & INTERFACING LABPR   50 20   23 P
 10. SOFT SKILLS   TW   50 20   32 P C   20. DATA STRUCTURES LABORATORY   TW   50 20   32 P
                                                            21. DATA STRUCTURES LABORATORY   PR   50 20   30 P
GRAND TOTAL = 801/1500, RESULT: FAILS A.T.K.T.
ORDN. 1 MARKS :
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 S80054348   WALVE SUWAS NANDAKUMAR   SAMPADA   , 71201116B   , S80054348 ,   PICT   , S80054348
 01. DISCRETE STRUCTURES   PP   100 40   51 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   44 P
 02. PROGRAMMING & PROBLEM SOLVING PP   100 40   40 P C   12. MICROPROC. & INTERFACING TECHNIQ.PP   100 40   43 P
 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP   100 40   49 P C   13. DATA STRUCTURES   PP   100 40   58 P
 04. DATA STRUCTURES AND ALGORITHMS PP   100 40   40 P C   14. COMPUTER GRAPHICS   PP   100 40   65 P
 05. HUMANITIES AND SOCIAL SCIENCE PP   100 40   51 P C   15. COMPUTER ORGANIZATION   PP   100 40   56 P
 06. PROGRAMMING LABORATORY   TW   25 10   21 P C   16. O. O. PROG. & COMP. GRAPH. LAB   TW   50 20   38 P
 07. PROGRAMMING LABORATORY   PR   50 20   35 P   17. O. O. PROG. & COMP. GRAPH. LAB   PR   50 20   39 P
 08. DIGITAL ELECTRONICS LABORATORY TW   25 10   21 P C   18. MICROPROCESSORS & INTERFACING LABTW   50 20   38 P
 09. DIGITAL ELECTRONICS LABORATORY PR   50 20   30 P C   19. MICROPROCESSORS & INTERFACING LABPR   50 20   32 P
 10. SOFT SKILLS   TW   50 20   38 P C   20. DATA STRUCTURES LABORATORY   TW   50 20   39 P
                                                            21. DATA STRUCTURES LABORATORY   PR   50 20   31 P
GRAND TOTAL = 859/1500, RESULT: HIGHER SECOND CLASS
ORDN. 1 MARKS :
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 S80054349   WALVEKAR PARITOSH MANDAR   APARNA   , 71201117L   , S80054349 ,   PICT   , S80054349
 01. DISCRETE STRUCTURES   PP   100 40   63 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   54 P
 02. PROGRAMMING & PROBLEM SOLVING PP   100 40   42 P C   12. MICROPROC. & INTERFACING TECHNIQ.PP   100 40   47 P
 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP   100 40   64 P C   13. DATA STRUCTURES   PP   100 40   53 P
 04. DATA STRUCTURES AND ALGORITHMS PP   100 40   42 P C   14. COMPUTER GRAPHICS   PP   100 40   60 P
 05. HUMANITIES AND SOCIAL SCIENCE PP   100 40   60 P C   15. COMPUTER ORGANIZATION   PP   100 40   61 P
 06. PROGRAMMING LABORATORY   TW   25 10   16 P C   16. O. O. PROG. & COMP. GRAPH. LAB   TW   50 20   40 P
 07. PROGRAMMING LABORATORY   PR   50 20   35 P C   17. O. O. PROG. & COMP. GRAPH. LAB   PR   50 20   38 P
 08. DIGITAL ELECTRONICS LABORATORY TW   25 10   16 P C   18. MICROPROCESSORS & INTERFACING LABTW   50 20   42 P
 09. DIGITAL ELECTRONICS LABORATORY PR   50 20   38 P C   19. MICROPROCESSORS & INTERFACING LABPR   50 20   39 P
 10. SOFT SKILLS   TW   50 20   30 P C   20. DATA STRUCTURES LABORATORY   TW   50 20   39 P
                                                            21. DATA STRUCTURES LABORATORY   PR   50 20   33 P
GRAND TOTAL = 912/1500, RESULT: FIRST CLASS
ORDN. 1 MARKS :
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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.
        OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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  S80054350   YAWALKAR VIBHAV VIRENDRA   SHARAYU   , 71201122G   , S80054350 ,   PICT   , S80054350
  01. DISCRETE STRUCTURES   PP   100 40   58 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   70 P
  02. PROGRAMMING & PROBLEM SOLVING PP   100 40   40 P C   12. MICROPROC. & INTERFACING TECHNIQ.PP   100 40   57 P
  03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 40   58 P C   13. DATA STRUCTURES   PP   100 40   40 P
  04. DATA STRUCTURES AND ALGORITHMS PP   100 40   52 P C   14. COMPUTER GRAPHICS   PP   100 40   62 P
  05. HUMANITIES AND SOCIAL SCIENCE PP   100 40   62 P C   15. COMPUTER ORGANIZATION   PP   100 40   62 P
  06. PROGRAMMING LABORATORY   TW   25 10   21 P C   16. O. O. PROG. & COMP. GRAPH. LAB   TW   50 20   45 P
  07. PROGRAMMING LABORATORY   PR   50 20   40 P C   17. O. O. PROG. & COMP. GRAPH. LAB   PR   50 20   38 P
  08. DIGITAL ELECTRONICS LABORATORY TW   25 10   22 P C   18. MICROPROCESSORS & INTERFACING LABTW   50 20   45 P
  09. DIGITAL ELECTRONICS LABORATORY PR   50 20   45 P C   19. MICROPROCESSORS & INTERFACING LABPR   50 20   44 P
  10. SOFT SKILLS   TW   50 20   42 P C   20. DATA STRUCTURES LABORATORY   TW   50 20   44 P
                                                              21. DATA STRUCTURES LABORATORY   PR   50 20   39 P
GRAND TOTAL = 986/1500, RESULT: FIRST CLASS
ORDN. 1 MARKS :
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  S80054351   ZAGADE VISHAL RAJU   SARALA   , 71201124C   , S80054351 ,   PICT   , S80054351
  01. DISCRETE STRUCTURES   PP   100 40   56 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   67 P
  02. PROGRAMMING & PROBLEM SOLVING PP   100 40   40 P C   12. MICROPROC. & INTERFACING TECHNIQ.PP   100 40   53 P
  03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 40   54 P C   13. DATA STRUCTURES   PP   100 40   50 P
  04. DATA STRUCTURES AND ALGORITHMS PP   100 40   44 P C   14. COMPUTER GRAPHICS   PP   100 40   66 P
  05. HUMANITIES AND SOCIAL SCIENCE PP   100 40   52 P C   15. COMPUTER ORGANIZATION   PP   100 40   62 P
  06. PROGRAMMING LABORATORY   TW   25 10   19 P C   16. O. O. PROG. & COMP. GRAPH. LAB   TW   50 20   35 P
  07. PROGRAMMING LABORATORY   PR   50 20   25 P C   17. O. O. PROG. & COMP. GRAPH. LAB   PR   50 20   38 P
  08. DIGITAL ELECTRONICS LABORATORY TW   25 10   19 P C   18. MICROPROCESSORS & INTERFACING LABTW   50 20   37 P
  09. DIGITAL ELECTRONICS LABORATORY PR   50 20   34 P C   19. MICROPROCESSORS & INTERFACING LABPR   50 20   38 P
  10. SOFT SKILLS   TW   50 20   36 P C   20. DATA STRUCTURES LABORATORY   TW   50 20   35 P
                                                              21. DATA STRUCTURES LABORATORY   PR   50 20   36 P
GRAND TOTAL = 896+04/1500, RESULT: FIRST CLASS   [O.2]
ORDN. 1 MARKS :
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  S80054352   ABNANE OMKAR RAJENDRA   SHOBHA   , 71045358C   , S80054352 ,   PICT   , S80054352
  01. DISCRETE STRUCTURES   PP   100 40   40 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   42 P C
  02. PROGRAMMING & PROBLEM SOLVING PP   100 40   40 P C   12. MICROPROC. & INTERFACING TECHNIQ.PP   100 40   43 P C
  03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 40   40 P C   13. DATA STRUCTURES   PP   100 40   40 P C
  04. DATA STRUCTURES AND ALGORITHMS PP   100 40   40 P C   14. COMPUTER GRAPHICS   PP   100 40   57 P C
  05. HUMANITIES AND SOCIAL SCIENCE PP   100 40   41 P C   15. COMPUTER ORGANIZATION   PP   100 40   45 P C
  06. PROGRAMMING LABORATORY   TW   25 10   13 P C   16. O. O. PROG. & COMP. GRAPH. LAB   TW   50 20   23 P C
  07. PROGRAMMING LABORATORY   PR   50 20   AA F   17. O. O. PROG. & COMP. GRAPH. LAB   PR   50 20   AA F
  08. DIGITAL ELECTRONICS LABORATORY TW   25 10   13 P C   18. MICROPROCESSORS & INTERFACING LABTW   50 20   21 P C
  09. DIGITAL ELECTRONICS LABORATORY PR   50 20   23 P C   19. MICROPROCESSORS & INTERFACING LABPR   50 20   20 P C
  10. SOFT SKILLS   TW   50 20   22 P C   20. DATA STRUCTURES LABORATORY   TW   50 20   22 P C
                                                              21. DATA STRUCTURES LABORATORY   PR   50 20   AA F
GRAND TOTAL = 585/1500, RESULT: FAILS A.T.K.T.
ORDN. 1 MARKS :
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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
        OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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  S80054353   AMAN TEWARY   SUNANDA   , 71100725L   , S80054353 ,   PICT   , S80054353
  01. DISCRETE STRUCTURES   PP   100 40   43 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   AA F
  02. PROGRAMMING & PROBLEM SOLVING PP   100 40   AA F   12. MICROPROC. & INTERFACING TECHNIQ.PP   100 40   AA F
  03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 40   AA F   13. DATA STRUCTURES   PP   100 40   AA F
  04. DATA STRUCTURES AND ALGORITHMS PP   100 40   AA F   14. COMPUTER GRAPHICS   PP   100 40   AA F
  05. HUMANITIES AND SOCIAL SCIENCE PP   100 40   40 P C   15. COMPUTER ORGANIZATION   PP   100 40   AA F
  06. PROGRAMMING LABORATORY   TW   25 10   17 P C   16. O. O. PROG. & COMP. GRAPH. LAB   TW   50 20   30 P C
  07. PROGRAMMING LABORATORY   PR   50 20   AA F   17. O. O. PROG. & COMP. GRAPH. LAB   PR   50 20   25 P C
  08. DIGITAL ELECTRONICS LABORATORY TW   25 10   17 P C   18. MICROPROCESSORS & INTERFACING LABTW   50 20   21 P C
  09. DIGITAL ELECTRONICS LABORATORY PR   50 20   34 P C   19. MICROPROCESSORS & INTERFACING LABPR   50 20   21 P C
  10. SOFT SKILLS   TW   50 20   33 P C   20. DATA STRUCTURES LABORATORY   TW   50 20   22 P C
                                                              21. DATA STRUCTURES LABORATORY   PR   50 20   AA F
GRAND TOTAL = 303/1500, RESULT: FAILS
ORDN. 1 MARKS :
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  S80054356   BAPAT RUTUGANDHA SUNIL   ANAGHA   , 71239286G   , S80054356 ,   PICT   , S80054356
  01. DISCRETE STRUCTURES   PP   100 40   45 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   40 P
  02. PROGRAMMING & PROBLEM SOLVING PP   100 40   54 P C   12. MICROPROC. & INTERFACING TECHNIQ.PP   100 40   46 P C
  03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 40   50 P C   13. DATA STRUCTURES   PP   100 40   71 P C
  04. DATA STRUCTURES AND ALGORITHMS PP   100 40   54 P C   14. COMPUTER GRAPHICS   PP   100 40   59 P C
  05. HUMANITIES AND SOCIAL SCIENCE PP   100 40   61 P C   15. COMPUTER ORGANIZATION   PP   100 40   61 P C
  06. PROGRAMMING LABORATORY   TW   25 10   19 P C   16. O. O. PROG. & COMP. GRAPH. LAB   TW   50 20   38 P C
  07. PROGRAMMING LABORATORY   PR   50 20   30 P C   17. O. O. PROG. & COMP. GRAPH. LAB   PR   50 20   36 P C
  08. DIGITAL ELECTRONICS LABORATORY TW   25 10   22 P C   18. MICROPROCESSORS & INTERFACING LABTW   50 20   44 P C
  09. DIGITAL ELECTRONICS LABORATORY PR   50 20   32 P C   19. MICROPROCESSORS & INTERFACING LABPR   50 20   39 P C
  10. SOFT SKILLS   TW   50 20   40 P C   20. DATA STRUCTURES LABORATORY   TW   50 20   37 P C
                                                              21. DATA STRUCTURES LABORATORY   PR   50 20   24 P C
GRAND TOTAL = 902/1500, RESULT: FIRST CLASS
ORDN. 1 MARKS :
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  S80054357   BASAPURE SHRAVYA RAMESH   VIJAYA   , 71100743J   , S80054357 ,   PICT   , S80054357
  01. DISCRETE STRUCTURES   PP   100 40   AA F   11. ENGINEERING MATHEMATICS III   PP   100 40   16 F
  02. PROGRAMMING & PROBLEM SOLVING PP   100 40   13 F   12. MICROPROC. & INTERFACING TECHNIQ.PP   100 40   AA F
  03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 40   18 F   13. DATA STRUCTURES   PP   100 40   AA F
  04. DATA STRUCTURES AND ALGORITHMS PP   100 40   18 F   14. COMPUTER GRAPHICS   PP   100 40   AA F
  05. HUMANITIES AND SOCIAL SCIENCE PP   100 40   27 F   15. COMPUTER ORGANIZATION   PP   100 40   AA F
  06. PROGRAMMING LABORATORY   TW   25 10   21 P C   16. O. O. PROG. & COMP. GRAPH. LAB   TW   50 20   32 P C
  07. PROGRAMMING LABORATORY   PR   50 20   AA F   17. O. O. PROG. & COMP. GRAPH. LAB   PR   50 20   AA F
  08. DIGITAL ELECTRONICS LABORATORY TW   25 10   18 P C   18. MICROPROCESSORS & INTERFACING LABTW   50 20   21 P C
  09. DIGITAL ELECTRONICS LABORATORY PR   50 20   25 P C   19. MICROPROCESSORS & INTERFACING LABPR   50 20   AA F
  10. SOFT SKILLS   TW   50 20   36 P C   20. DATA STRUCTURES LABORATORY   TW   50 20   26 P C
                                                              21. DATA STRUCTURES LABORATORY   PR   50 20   AA F
GRAND TOTAL = 271/1500, RESULT: FAILS   RESULT RESERVED FOR BKLG
ORDN. 1 MARKS :
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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
      OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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 S80054358   BHANDARI RUHI BABU   SMITA   , 71239290E   , S80054358 , PICT   , S80054358
 01. DISCRETE STRUCTURES   PP   100 40   41 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   49 P
 02. PROGRAMMING & PROBLEM SOLVING PP   100 40   57 P C   12. MICROPROC. & INTERFACING TECHNIQ.PP   100 40   49 P C
 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 40   40 P C   13. DATA STRUCTURES   PP   100 40   72 P C
 04. DATA STRUCTURES AND ALGORITHMS PP   100 40   49 P C   14. COMPUTER GRAPHICS   PP   100 40   57 P C
 05. HUMANITIES AND SOCIAL SCIENCE   PP   100 40   58 P C   15. COMPUTER ORGANIZATION   PP   100 40   54 P C
 06. PROGRAMMING LABORATORY   TW   25 10   17 P C   16. O. O. PROG. & COMP. GRAPH. LAB   TW   50 20   39 P C
 07. PROGRAMMING LABORATORY   PR   50 20   20 P C   17. O. O. PROG. & COMP. GRAPH. LAB   PR   50 20   42 P C
 08. DIGITAL ELECTRONICS LABORATORY TW   25 10   18 P C   18. MICROPROCESSORS & INTERFACING LABTW   50 20   41 P C
 09. DIGITAL ELECTRONICS LABORATORY PR   50 20   33 P C   19. MICROPROCESSORS & INTERFACING LABPR   50 20   36 P C
 10. SOFT SKILLS   TW   50 20   31 P C   20. DATA STRUCTURES LABORATORY   TW   50 20   33 P C
                                                             21. DATA STRUCTURES LABORATORY   PR   50 20   30 P C
GRAND TOTAL = 866/1500, RESULT: HIGHER SECOND CLASS
ORDN. 1 MARKS :
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 S80054359   BHOIR RUPALI LAXMAN   VANITA   , 71045390G   , S80054359 , PICT   , S80054359
 01. DISCRETE STRUCTURES   PP   100 40   42 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   40 P C
 02. PROGRAMMING & PROBLEM SOLVING PP   100 40   40 P C   12. MICROPROC. & INTERFACING TECHNIQ.PP   100 40   40 P C
 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 40   46 P   13. DATA STRUCTURES   PP   100 40   49 P C
 04. DATA STRUCTURES AND ALGORITHMS PP   100 40   41 P C   14. COMPUTER GRAPHICS   PP   100 40   42 P C
 05. HUMANITIES AND SOCIAL SCIENCE   PP   100 40   40 P C   15. COMPUTER ORGANIZATION   PP   100 40   40 P
 06. PROGRAMMING LABORATORY   TW   25 10   19 P C   16. O. O. PROG. & COMP. GRAPH. LAB   TW   50 20   43 P C
 07. PROGRAMMING LABORATORY   PR   50 20   24 P C   17. O. O. PROG. & COMP. GRAPH. LAB   PR   50 20   23 P C
 08. DIGITAL ELECTRONICS LABORATORY TW   25 10   18 P C   18. MICROPROCESSORS & INTERFACING LABTW   50 20   39 P C
 09. DIGITAL ELECTRONICS LABORATORY PR   50 20   27 P C   19. MICROPROCESSORS & INTERFACING LABPR   50 20   21 P C
 10. SOFT SKILLS   TW   50 20   34 P C   20. DATA STRUCTURES LABORATORY   TW   50 20   38 P C
                                                             21. DATA STRUCTURES LABORATORY   PR   50 20   30 P C
GRAND TOTAL = 736/1500, RESULT: PASS CLASS
ORDN. 1 MARKS :
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 S80054360   BHOSALE SHITAL MANOHAR   JAYA   , 71132426D   , S80054360 , PICT   , S80054360
 01. DISCRETE STRUCTURES   PP   100 40   40 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   21 F
 02. PROGRAMMING & PROBLEM SOLVING PP   100 40   44 P C   12. MICROPROC. & INTERFACING TECHNIQ.PP   100 40   40 P C
 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 40   41 P C   13. DATA STRUCTURES   PP   100 40   40 P C
 04. DATA STRUCTURES AND ALGORITHMS PP   100 40   47 P C   14. COMPUTER GRAPHICS   PP   100 40   46 P C
 05. HUMANITIES AND SOCIAL SCIENCE   PP   100 40   40 P C   15. COMPUTER ORGANIZATION   PP   100 40   40 P C
 06. PROGRAMMING LABORATORY   TW   25 10   17 P C   16. O. O. PROG. & COMP. GRAPH. LAB   TW   50 20   39 P C
 07. PROGRAMMING LABORATORY   PR   50 20   25 P C   17. O. O. PROG. & COMP. GRAPH. LAB   PR   50 20   20 P C
 08. DIGITAL ELECTRONICS LABORATORY TW   25 10   19 P C   18. MICROPROCESSORS & INTERFACING LABTW   50 20   34 P C
 09. DIGITAL ELECTRONICS LABORATORY PR   50 20   20 P C   19. MICROPROCESSORS & INTERFACING LABPR   50 20   28 P C
 10. SOFT SKILLS   TW   50 20   34 P C   20. DATA STRUCTURES LABORATORY   TW   50 20   32 P C
                                                             21. DATA STRUCTURES LABORATORY   PR   50 20   20 P C
GRAND TOTAL = 687/1500, RESULT: FAILS A.T.K.T.
ORDN. 1 MARKS :
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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.
       OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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  S80054362   DAMSE SAYALI LAXMAN   LATA   , 71100774J   , S80054362 ,   PICT   , S80054362
  01. DISCRETE STRUCTURES   PP   100 40   40 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   40 P
  02. PROGRAMMING & PROBLEM SOLVING PP   100 40   40 P C   12. MICROPROC. & INTERFACING TECHNIQ.PP   100 40   40 P C
  03. DIGIT. ELECTRONICS & LOGIC DESIGNPP   100 40   40 P C   13. DATA STRUCTURES   PP   100 40   58 P C
  04. DATA STRUCTURES AND ALGORITHMS PP   100 40   40 P C   14. COMPUTER GRAPHICS   PP   100 40   41 P C
  05. HUMANITIES AND SOCIAL SCIENCE PP   100 40   40 P C   15. COMPUTER ORGANIZATION   PP   100 40   40 P C
  06. PROGRAMMING LABORATORY   TW   25 10   18 P C   16. O. O. PROG. & COMP. GRAPH. LAB   TW   50 20   34 P C
  07. PROGRAMMING LABORATORY   PR   50 20   21 P C   17. O. O. PROG. & COMP. GRAPH. LAB   PR   50 20   35 P C
  08. DIGITAL ELECTRONICS LABORATORY TW   25 10   17 P C   18. MICROPROCESSORS & INTERFACING LABTW   50 20   36 P C
  09. DIGITAL ELECTRONICS LABORATORY PR   50 20   21 P C   19. MICROPROCESSORS & INTERFACING LABPR   50 20   27 P C
  10. SOFT SKILLS   TW   50 20   34 P C   20. DATA STRUCTURES LABORATORY   TW   50 20   34 P C
                                                               21. DATA STRUCTURES LABORATORY   PR   50 20   28 P
GRAND TOTAL = 724/1500, RESULT: PASS CLASS
ORDN. 1 MARKS :
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  S80054366   GABHALE VARSHA LIMBAJI   JANABAI   , 71045426M   , S80054366 ,   PICT   , S80054366
  01. DISCRETE STRUCTURES   PP   100 40   52 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   40 P C
  02. PROGRAMMING & PROBLEM SOLVING PP   100 40   44 P C   12. MICROPROC. & INTERFACING TECHNIQ.PP   100 40   40 P C
  03. DIGIT. ELECTRONICS & LOGIC DESIGNPP   100 40   40 P C   13. DATA STRUCTURES   PP   100 40   53 P C
  04. DATA STRUCTURES AND ALGORITHMS PP   100 40   48 P C   14. COMPUTER GRAPHICS   PP   100 40   40 P C
  05. HUMANITIES AND SOCIAL SCIENCE PP   100 40   40 P C   15. COMPUTER ORGANIZATION   PP   100 40   51 P
  06. PROGRAMMING LABORATORY   TW   25 10   21 P C   16. O. O. PROG. & COMP. GRAPH. LAB   TW   50 20   39 P C
  07. PROGRAMMING LABORATORY   PR   50 20   30 P C   17. O. O. PROG. & COMP. GRAPH. LAB   PR   50 20   34 P C
  08. DIGITAL ELECTRONICS LABORATORY TW   25 10   20 P C   18. MICROPROCESSORS & INTERFACING LABTW   50 20   39 P C
  09. DIGITAL ELECTRONICS LABORATORY PR   50 20   27 P C   19. MICROPROCESSORS & INTERFACING LABPR   50 20   36 P C
  10. SOFT SKILLS   TW   50 20   35 P C   20. DATA STRUCTURES LABORATORY   TW   50 20   36 P C
                                                               21. DATA STRUCTURES LABORATORY   PR   50 20   22 P C
GRAND TOTAL = 787/1500, RESULT: SECOND CLASS
ORDN. 1 MARKS :
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  S80054368   GANVIR AJAY SUDHAKAR   SUREKHA   , 71100797H   , S80054368 ,   PICT   , S80054368
  01. DISCRETE STRUCTURES   PP   100 40   58 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   41 P C
  02. PROGRAMMING & PROBLEM SOLVING PP   100 40   50 P C   12. MICROPROC. & INTERFACING TECHNIQ.PP   100 40   40 P C
  03. DIGIT. ELECTRONICS & LOGIC DESIGNPP   100 40   66 P C   13. DATA STRUCTURES   PP   100 40   71 P C
  04. DATA STRUCTURES AND ALGORITHMS PP   100 40   63 P C   14. COMPUTER GRAPHICS   PP   100 40   40 P C
  05. HUMANITIES AND SOCIAL SCIENCE PP   100 40   50 P C   15. COMPUTER ORGANIZATION   PP   100 40   41 P C
  06. PROGRAMMING LABORATORY   TW   25 10   18 P C   16. O. O. PROG. & COMP. GRAPH. LAB   TW   50 20   26 P C
  07. PROGRAMMING LABORATORY   PR   50 20   30 P C   17. O. O. PROG. & COMP. GRAPH. LAB   PR   50 20   28 P C
  08. DIGITAL ELECTRONICS LABORATORY TW   25 10   14 P C   18. MICROPROCESSORS & INTERFACING LABTW   50 20   28 P C
  09. DIGITAL ELECTRONICS LABORATORY PR   50 20   23 P C   19. MICROPROCESSORS & INTERFACING LABPR   50 20   24 P
  10. SOFT SKILLS   TW   50 20   32 P C   20. DATA STRUCTURES LABORATORY   TW   50 20   20 P C
                                                               21. DATA STRUCTURES LABORATORY   PR   50 20   20 P C
GRAND TOTAL = 783/1500, RESULT: SECOND CLASS
ORDN. 1 MARKS :
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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
        OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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 S80054369   GAVALI TANUJA KASHINATH              PARVATA          , 71100799D    , S80054369 ,  PICT    , S80054369
 01. DISCRETE STRUCTURES            PP   100  40   46  P C    11. ENGINEERING MATHEMATICS III     PP   100  40   40  P C
 02. PROGRAMMING & PROBLEM SOLVING  PP   100  40   41  P C    12. MICROPROC. & INTERFACING TECHNIQ.PP   100  40   40  P C
 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100  40   44  P C    13. DATA STRUCTURES                 PP   100  40   61  P C
 04. DATA STRUCTURES AND ALGORITHMS PP   100  40   40  P C    14. COMPUTER GRAPHICS               PP   100  40   40  P C
 05. HUMANITIES AND SOCIAL SCIENCE  PP   100  40   46  P C    15. COMPUTER ORGANIZATION           PP   100  40   40  P C
 06. PROGRAMMING LABORATORY         TW    25  10   21  P C    16. O. O. PROG. & COMP. GRAPH. LAB  TW    50  20   35  P C
 07. PROGRAMMING LABORATORY         PR    50  20   25  P C    17. O. O. PROG. & COMP. GRAPH. LAB  PR    50  20   29  P C
 08. DIGITAL ELECTRONICS LABORATORY TW    25  10   17  P C    18. MICROPROCESSORS & INTERFACING LABTW    50  20   31  P C
 09. DIGITAL ELECTRONICS LABORATORY PR    50  20   21  P C    19. MICROPROCESSORS & INTERFACING LABPR    50  20   21  P C
 10. SOFT SKILLS                    TW    50  20   31  P C    20. DATA STRUCTURES LABORATORY      TW    50  20   37  P C
                                                             21. DATA STRUCTURES LABORATORY      PR    50  20   25  P
GRAND TOTAL =  731/1500, RESULT: PASS CLASS
ORDN. 1 MARKS :
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 S80054370   GUNJAL DIPALI BHANUDAS               SUREKHA          , 71100812E    , S80054370 ,  PICT    , S80054370
 01. DISCRETE STRUCTURES            PP   100  40   41  P C    11. ENGINEERING MATHEMATICS III     PP   100  40   40  P
 02. PROGRAMMING & PROBLEM SOLVING  PP   100  40   40  P C    12. MICROPROC. & INTERFACING TECHNIQ.PP   100  40   48  P
 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100  40   44  P C    13. DATA STRUCTURES                 PP   100  40   51  P
 04. DATA STRUCTURES AND ALGORITHMS PP   100  40   50  P C    14. COMPUTER GRAPHICS               PP   100  40   40  P
 05. HUMANITIES AND SOCIAL SCIENCE  PP   100  40   52  P C    15. COMPUTER ORGANIZATION           PP   100  40   57  P
 06. PROGRAMMING LABORATORY         TW    25  10   15  P C    16. O. O. PROG. & COMP. GRAPH. LAB  TW    50  20   35  P
 07. PROGRAMMING LABORATORY         PR    50  20   20  P C    17. O. O. PROG. & COMP. GRAPH. LAB  PR    50  20   25  P
 08. DIGITAL ELECTRONICS LABORATORY TW    25  10   14  P C    18. MICROPROCESSORS & INTERFACING LABTW    50  20   30  P
 09. DIGITAL ELECTRONICS LABORATORY PR    50  20   24  P C    19. MICROPROCESSORS & INTERFACING LABPR    50  20   21  P
 10. SOFT SKILLS                    TW    50  20   27  P C    20. DATA STRUCTURES LABORATORY      TW    50  20   33  P
                                                             21. DATA STRUCTURES LABORATORY      PR    50  20   15# P
GRAND TOTAL =  722/1500, RESULT: PASS CLASS   # [O.4]
ORDN. 1 MARKS :
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 S80054375   KEDARE SWAPNIL PANDIT                NIRMALA          , 71045479B    , S80054375 ,  PICT    , S80054375
 01. DISCRETE STRUCTURES            PP   100  40   40  P C    11. ENGINEERING MATHEMATICS III     PP   100  40   06  F
 02. PROGRAMMING & PROBLEM SOLVING  PP   100  40   40  P C    12. MICROPROC. & INTERFACING TECHNIQ.PP   100  40   40  P C
 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100  40   40  P C    13. DATA STRUCTURES                 PP   100  40   40  P C
 04. DATA STRUCTURES AND ALGORITHMS PP   100  40   43  P C    14. COMPUTER GRAPHICS               PP   100  40   43  P C
 05. HUMANITIES AND SOCIAL SCIENCE  PP   100  40   40  P C    15. COMPUTER ORGANIZATION           PP   100  40   47  P
 06. PROGRAMMING LABORATORY         TW    25  10   10  P C    16. O. O. PROG. & COMP. GRAPH. LAB  TW    50  20   20  P C
 07. PROGRAMMING LABORATORY         PR    50  20   20  P      17. O. O. PROG. & COMP. GRAPH. LAB  PR    50  20   20  P
 08. DIGITAL ELECTRONICS LABORATORY TW    25  10   10  P C    18. MICROPROCESSORS & INTERFACING LABTW    50  20   20  P C
 09. DIGITAL ELECTRONICS LABORATORY PR    50  20   21  P      19. MICROPROCESSORS & INTERFACING LABPR    50  20   AA  F
 10. SOFT SKILLS                    TW    50  20   20  P C    20. DATA STRUCTURES LABORATORY      TW    50  20   20  P C
                                                             21. DATA STRUCTURES LABORATORY      PR    50  20   AA  F
GRAND TOTAL =  540/1500, RESULT: FAILS A.T.K.T.
ORDN. 1 MARKS :
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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
        OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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  S80054376   KHALID RAZA KHAN   AYESHA   , 70925473D   , S80054376 ,   PICT   , S80054376
  01. DISCRETE STRUCTURES   PP   100 40   40 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   00 F
  02. PROGRAMMING & PROBLEM SOLVING PP   100 40   40 P C   12. MICROPROC. & INTERFACING TECHNIQ.PP   100 40   AA F
  03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 40   40 P C   13. DATA STRUCTURES   PP   100 40   24 F
  04. DATA STRUCTURES AND ALGORITHMS PP   100 40   40 P C   14. COMPUTER GRAPHICS   PP   100 40   43 P C
  05. HUMANITIES AND SOCIAL SCIENCE PP   100 40   42 P C   15. COMPUTER ORGANIZATION   PP   100 40   40 P C
  06. PROGRAMMING LABORATORY   TW   25 10   10 P C   16. O. O. PROG. & COMP. GRAPH. LAB   TW   50 20   20 P C
  07. PROGRAMMING LABORATORY   PR   50 20   25 P C   17. O. O. PROG. & COMP. GRAPH. LAB   PR   50 20   20 P C
  08. DIGITAL ELECTRONICS LABORATORY TW   25 10   10 P C   18. MICROPROCESSORS & INTERFACING LABTW   50 20   20 P C
  09. DIGITAL ELECTRONICS LABORATORY PR   50 20   23 P C   19. MICROPROCESSORS & INTERFACING LABPR   50 20   AA F
  10. SOFT SKILLS   TW   50 20   20 P C   20. DATA STRUCTURES LABORATORY   TW   50 20   20 P C
                                                              21. DATA STRUCTURES LABORATORY   PR   50 20   22 P
GRAND TOTAL = 499/1500, RESULT: FAILS A.T.K.T.
ORDN. 1 MARKS :
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  S80054383   MOHIT RAJVARDHAN   MEENA KUMARI   , 71045671K   , S80054383 ,   PICT   , S80054383
  01. DISCRETE STRUCTURES   PP   100 40   43 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   40 P C
  02. PROGRAMMING & PROBLEM SOLVING PP   100 40   40 P C   12. MICROPROC. & INTERFACING TECHNIQ.PP   100 40   46 P C
  03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 40   47 P C   13. DATA STRUCTURES   PP   100 40   51 P C
  04. DATA STRUCTURES AND ALGORITHMS PP   100 40   51 P C   14. COMPUTER GRAPHICS   PP   100 40   40 P
  05. HUMANITIES AND SOCIAL SCIENCE PP   100 40   40 P C   15. COMPUTER ORGANIZATION   PP   100 40   43 P C
  06. PROGRAMMING LABORATORY   TW   25 10   23 P C   16. O. O. PROG. & COMP. GRAPH. LAB   TW   50 20   34 P C
  07. PROGRAMMING LABORATORY   PR   50 20   38 P C   17. O. O. PROG. & COMP. GRAPH. LAB   PR   50 20   36 P C
  08. DIGITAL ELECTRONICS LABORATORY TW   25 10   18 P C   18. MICROPROCESSORS & INTERFACING LABTW   50 20   34 P C
  09. DIGITAL ELECTRONICS LABORATORY PR   50 20   32 P C   19. MICROPROCESSORS & INTERFACING LABPR   50 20   30 P C
  10. SOFT SKILLS   TW   50 20   31 P C   20. DATA STRUCTURES LABORATORY   TW   50 20   34 P C
                                                              21. DATA STRUCTURES LABORATORY   PR   50 20   34 P C
GRAND TOTAL = 785/1500, RESULT: SECOND CLASS
ORDN. 1 MARKS :
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  S80054384   MUNMOON GHOSH   REKHA   , 71100902D   , S80054384 ,   PICT   , S80054384
  01. DISCRETE STRUCTURES   PP   100 40   40 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   48 P C
  02. PROGRAMMING & PROBLEM SOLVING PP   100 40   54 P C   12. MICROPROC. & INTERFACING TECHNIQ.PP   100 40   40 P C
  03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 40   40 P C   13. DATA STRUCTURES   PP   100 40   55 P C
  04. DATA STRUCTURES AND ALGORITHMS PP   100 40   44 P C   14. COMPUTER GRAPHICS   PP   100 40   51 P C
  05. HUMANITIES AND SOCIAL SCIENCE PP   100 40   41 P C   15. COMPUTER ORGANIZATION   PP   100 40   50 P C
  06. PROGRAMMING LABORATORY   TW   25 10   18 P C   16. O. O. PROG. & COMP. GRAPH. LAB   TW   50 20   28 P C
  07. PROGRAMMING LABORATORY   PR   50 20   22 P C   17. O. O. PROG. & COMP. GRAPH. LAB   PR   50 20   23 P C
  08. DIGITAL ELECTRONICS LABORATORY TW   25 10   12 P C   18. MICROPROCESSORS & INTERFACING LABTW   50 20   30 P C
  09. DIGITAL ELECTRONICS LABORATORY PR   50 20   23 P   19. MICROPROCESSORS & INTERFACING LABPR   50 20   22 P C
  10. SOFT SKILLS   TW   50 20   32 P C   20. DATA STRUCTURES LABORATORY   TW   50 20   34 P C
                                                              21. DATA STRUCTURES LABORATORY   PR   50 20   30 P C
GRAND TOTAL = 737/1500, RESULT: PASS CLASS
ORDN. 1 MARKS :
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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.
          OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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  S80054385   NAVEEN KUMAR GUPTA   ANITA   , 71100915F   , S80054385 ,   PICT   , S80054385
  01. DISCRETE STRUCTURES   PP   100 40   40 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   53 P C
  02. PROGRAMMING & PROBLEM SOLVING PP   100 40   55 P C   12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40   45 P C
  03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 40   58 P C   13. DATA STRUCTURES   PP   100 40   62 P C
  04. DATA STRUCTURES AND ALGORITHMS PP   100 40   42 P C   14. COMPUTER GRAPHICS   PP   100 40   55 P C
  05. HUMANITIES AND SOCIAL SCIENCE PP   100 40   52 P C   15. COMPUTER ORGANIZATION   PP   100 40   60 P C
  06. PROGRAMMING LABORATORY   TW   25 10   17 P C   16. O. O. PROG. & COMP. GRAPH. LAB   TW   50 20   21 P C
  07. PROGRAMMING LABORATORY   PR   50 20   30 P C   17. O. O. PROG. & COMP. GRAPH. LAB   PR   50 20   39 P C
  08. DIGITAL ELECTRONICS LABORATORY TW   25 10   15 P C   18. MICROPROCESSORS & INTERFACING LABTW   50 20   25 P C
  09. DIGITAL ELECTRONICS LABORATORY PR   50 20   35 P C   19. MICROPROCESSORS & INTERFACING LABPR   50 20   30 P
  10. SOFT SKILLS   TW   50 20   29 P C   20. DATA STRUCTURES LABORATORY   TW   50 20   23 P C
                                                                21. DATA STRUCTURES LABORATORY   PR   50 20   32 P C
GRAND TOTAL = 818+07/1500, RESULT: HIGHER SECOND CLASS [O.2]
ORDN. 1 MARKS :
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  S80054390   RAMTIRTH IRA NITIN   ANJALI   , 71100965B   , S80054390 ,   PICT   , S80054390
  01. DISCRETE STRUCTURES   PP   100 40   69 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   47 P
  02. PROGRAMMING & PROBLEM SOLVING PP   100 40   64 P C   12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40   47 P C
  03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 40   60 P C   13. DATA STRUCTURES   PP   100 40   70 P C
  04. DATA STRUCTURES AND ALGORITHMS PP   100 40   58 P C   14. COMPUTER GRAPHICS   PP   100 40   68 P C
  05. HUMANITIES AND SOCIAL SCIENCE PP   100 40   55 P C   15. COMPUTER ORGANIZATION   PP   100 40   53 P C
  06. PROGRAMMING LABORATORY   TW   25 10   20 P C   16. O. O. PROG. & COMP. GRAPH. LAB   TW   50 20   39 P C
  07. PROGRAMMING LABORATORY   PR   50 20   41 P C   17. O. O. PROG. & COMP. GRAPH. LAB   PR   50 20   35 P C
  08. DIGITAL ELECTRONICS LABORATORY TW   25 10   18 P C   18. MICROPROCESSORS & INTERFACING LABTW   50 20   40 P C
  09. DIGITAL ELECTRONICS LABORATORY PR   50 20   39 P C   19. MICROPROCESSORS & INTERFACING LABPR   50 20   38 P C
  10. SOFT SKILLS   TW   50 20   36 P C   20. DATA STRUCTURES LABORATORY   TW   50 20   40 P C
                                                                21. DATA STRUCTURES LABORATORY   PR   50 20   37 P C
GRAND TOTAL = 974/1500, RESULT: FIRST CLASS
ORDN. 1 MARKS :
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  S80054394   SAMRIDDH HADA MAHESH   RUBY   , 71100985G   , S80054394 ,   PICT   , S80054394
  01. DISCRETE STRUCTURES   PP   100 40   40 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   40 P C
  02. PROGRAMMING & PROBLEM SOLVING PP   100 40   44 P C   12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40   42 P C
  03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 40   43 P C   13. DATA STRUCTURES   PP   100 40   42 P C
  04. DATA STRUCTURES AND ALGORITHMS PP   100 40   44 P C   14. COMPUTER GRAPHICS   PP   100 40   50 P C
  05. HUMANITIES AND SOCIAL SCIENCE PP   100 40   47 P C   15. COMPUTER ORGANIZATION   PP   100 40   40 P C
  06. PROGRAMMING LABORATORY   TW   25 10   18 P C   16. O. O. PROG. & COMP. GRAPH. LAB   TW   50 20   21 P C
  07. PROGRAMMING LABORATORY   PR   50 20   20 P C   17. O. O. PROG. & COMP. GRAPH. LAB   PR   50 20   22 P C
  08. DIGITAL ELECTRONICS LABORATORY TW   25 10   15 P C   18. MICROPROCESSORS & INTERFACING LABTW   50 20   28 P C
  09. DIGITAL ELECTRONICS LABORATORY PR   50 20   30 P C   19. MICROPROCESSORS & INTERFACING LABPR   50 20   20 P
  10. SOFT SKILLS   TW   50 20   28 P C   20. DATA STRUCTURES LABORATORY   TW   50 20   25 P C
                                                                21. DATA STRUCTURES LABORATORY   PR   50 20   28 P
GRAND TOTAL = 687/1500, RESULT: PASS CLASS
ORDN. 1 MARKS :
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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.

          OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S80054395   SANDEEP AGARWAL   RITA   , 70925587L   , S80054395 ,   PICT   , S80054395

01. DISCRETE STRUCTURES	PP	100	40	59	P C		11. ENGINEERING MATHEMATICS III	PP	100	40	53	P C
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	40	P C		12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	42	P
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	47	P C		13. DATA STRUCTURES	PP	100	40	43	P C
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	40	P C		14. COMPUTER GRAPHICS	PP	100	40	40	P C
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	52	P C		15. COMPUTER ORGANIZATION	PP	100	40	43	P C
06. PROGRAMMING LABORATORY	TW	25	10	20	P C		16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	30	P C
07. PROGRAMMING LABORATORY	PR	50	20	30	P C		17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	24	P C
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	17	P C		18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	32	P C
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	25	P C		19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	20	P C
10. SOFT SKILLS	TW	50	20	40	P C		20. DATA STRUCTURES LABORATORY	TW	50	20	22	P C
							21. DATA STRUCTURES LABORATORY	PR	50	20	38	P C

GRAND TOTAL = 757/1500, RESULT: SECOND CLASS

ORDN. 1 MARKS :

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S80054397   SAURAV JAUHARI   POONAM   , 70925593E   , S80054397 ,   PICT   , S80054397

01. DISCRETE STRUCTURES	PP	100	40	19	F		11. ENGINEERING MATHEMATICS III	PP	100	40	00	F
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	40	P C		12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	AA	F
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	40	P C		13. DATA STRUCTURES	PP	100	40	40	P
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	25	F		14. COMPUTER GRAPHICS	PP	100	40	40	P
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	47	P C		15. COMPUTER ORGANIZATION	PP	100	40	41	P
06. PROGRAMMING LABORATORY	TW	25	10	13	P C		16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	20	P C
07. PROGRAMMING LABORATORY	PR	50	20	20	P C		17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	20	P C
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	10	P C		18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	20	P C
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	22	P C		19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	20	P C
10. SOFT SKILLS	TW	50	20	26	P C		20. DATA STRUCTURES LABORATORY	TW	50	20	20	P C
							21. DATA STRUCTURES LABORATORY	PR	50	20	20	P

GRAND TOTAL = 503/1500, RESULT: FAILS

ORDN. 1 MARKS :

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S80054398   SAWANT TEJASWINI NANASAHEB   SUNITA   , 71100992K   , S80054398 ,   PICT   , S80054398

01. DISCRETE STRUCTURES	PP	100	40	48	P C		11. ENGINEERING MATHEMATICS III	PP	100	40	40	P C
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	47	P C		12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	52	P C
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	40	P C		13. DATA STRUCTURES	PP	100	40	57	P C
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	40	P C		14. COMPUTER GRAPHICS	PP	100	40	42	P C
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	58	P C		15. COMPUTER ORGANIZATION	PP	100	40	43	P C
06. PROGRAMMING LABORATORY	TW	25	10	19	P C		16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	38	P C
07. PROGRAMMING LABORATORY	PR	50	20	21	P C		17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	34	P C
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	16	P C		18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	38	P C
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	29	P C		19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	29	P C
10. SOFT SKILLS	TW	50	20	29	P C		20. DATA STRUCTURES LABORATORY	TW	50	20	37	P C
							21. DATA STRUCTURES LABORATORY	PR	50	20	26	P

GRAND TOTAL = 783/1500, RESULT: SECOND CLASS

ORDN. 1 MARKS :

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO.,   PREVIOUS SEAT NO.,   COLLEGE,   SEAT NO.
          OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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S80054399   SHELKANDE AKASH DAGADU   SEETA   , 71101000F   , S80054399 ,   PICT   , S80054399
01. DISCRETE STRUCTURES   PP   100 40   41 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   10 F
02. PROGRAMMING & PROBLEM SOLVING   PP   100 40   40 P C   12. MICROPROC. & INTERFACING TECHNIQ.PP   100 40   40 P C
03. DIGIT. ELECTRONICS & LOGIC DESIGNPP   100 40   54 P C   13. DATA STRUCTURES   PP   100 40   AA F
04. DATA STRUCTURES AND ALGORITHMS   PP   100 40   43 P C   14. COMPUTER GRAPHICS   PP   100 40   40 P C
05. HUMANITIES AND SOCIAL SCIENCE   PP   100 40   40 P C   15. COMPUTER ORGANIZATION   PP   100 40   40 P
06. PROGRAMMING LABORATORY   TW   25 10   17 P C   16. O. O. PROG. & COMP. GRAPH. LAB   TW   50 20   26 P C
07. PROGRAMMING LABORATORY   PR   50 20   21 P C   17. O. O. PROG. & COMP. GRAPH. LAB   PR   50 20   28 P C
08. DIGITAL ELECTRONICS LABORATORY   TW   25 10   17 P C   18. MICROPROCESSORS & INTERFACING LABTW   50 20   33 P C
09. DIGITAL ELECTRONICS LABORATORY   PR   50 20   20 P C   19. MICROPROCESSORS & INTERFACING LABPR   50 20   23 P
10. SOFT SKILLS   TW   50 20   33 P C   20. DATA STRUCTURES LABORATORY   TW   50 20   32 P C
                                                               21. DATA STRUCTURES LABORATORY   PR   50 20   24 P
GRAND TOTAL = 622/1500, RESULT: FAILS A.T.K.T.
ORDN. 1 MARKS :
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S80054401   SHINDE AMIT KEDARNATH   SANGEETA   , 71045614L   , S80054401 ,   PICT   , S80054401
01. DISCRETE STRUCTURES   PP   100 40   53 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   40 P C
02. PROGRAMMING & PROBLEM SOLVING   PP   100 40   40 P C   12. MICROPROC. & INTERFACING TECHNIQ.PP   100 40   49 P
03. DIGIT. ELECTRONICS & LOGIC DESIGNPP   100 40   49 P C   13. DATA STRUCTURES   PP   100 40   54 P C
04. DATA STRUCTURES AND ALGORITHMS   PP   100 40   40 P C   14. COMPUTER GRAPHICS   PP   100 40   46 P C
05. HUMANITIES AND SOCIAL SCIENCE   PP   100 40   40 P C   15. COMPUTER ORGANIZATION   PP   100 40   54 P C
06. PROGRAMMING LABORATORY   TW   25 10   17 P C   16. O. O. PROG. & COMP. GRAPH. LAB   TW   50 20   32 P C
07. PROGRAMMING LABORATORY   PR   50 20   35 P C   17. O. O. PROG. & COMP. GRAPH. LAB   PR   50 20   32 P C
08. DIGITAL ELECTRONICS LABORATORY   TW   25 10   16 P C   18. MICROPROCESSORS & INTERFACING LABTW   50 20   36 P C
09. DIGITAL ELECTRONICS LABORATORY   PR   50 20   21 P C   19. MICROPROCESSORS & INTERFACING LABPR   50 20   38 P C
10. SOFT SKILLS   TW   50 20   34 P C   20. DATA STRUCTURES LABORATORY   TW   50 20   30 P C
                                                               21. DATA STRUCTURES LABORATORY   PR   50 20   21 P C
GRAND TOTAL = 777/1500, RESULT: SECOND CLASS
ORDN. 1 MARKS :
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S80054403   SIDDHARTH BATRA   SANGEETA   , 71045624H   , S80054403 ,   PICT   , S80054403
01. DISCRETE STRUCTURES   PP   100 40   71 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   59 P
02. PROGRAMMING & PROBLEM SOLVING   PP   100 40   40 P C   12. MICROPROC. & INTERFACING TECHNIQ.PP   100 40   55 P C
03. DIGIT. ELECTRONICS & LOGIC DESIGNPP   100 40   40 P C   13. DATA STRUCTURES   PP   100 40   40 P C
04. DATA STRUCTURES AND ALGORITHMS   PP   100 40   48 P C   14. COMPUTER GRAPHICS   PP   100 40   43 P C
05. HUMANITIES AND SOCIAL SCIENCE   PP   100 40   43 P C   15. COMPUTER ORGANIZATION   PP   100 40   61 P C
06. PROGRAMMING LABORATORY   TW   25 10   14 P C   16. O. O. PROG. & COMP. GRAPH. LAB   TW   50 20   21 P C
07. PROGRAMMING LABORATORY   PR   50 20   24 P C   17. O. O. PROG. & COMP. GRAPH. LAB   PR   50 20   30 P C
08. DIGITAL ELECTRONICS LABORATORY   TW   25 10   10 P C   18. MICROPROCESSORS & INTERFACING LABTW   50 20   24 P C
09. DIGITAL ELECTRONICS LABORATORY   PR   50 20   20 P C   19. MICROPROCESSORS & INTERFACING LABPR   50 20   20 P C
10. SOFT SKILLS   TW   50 20   23 P C   20. DATA STRUCTURES LABORATORY   TW   50 20   26 P C
                                                               21. DATA STRUCTURES LABORATORY   PR   50 20   30 P
GRAND TOTAL = 742+08/1500, RESULT: SECOND CLASS [O.2]
ORDN. 1 MARKS :
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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.
       OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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 S80054409   YADAV PRAKHAR SUNILKUMAR   CHHAYA   , 71101059F   , S80054409 ,   PICT   , S80054409
 01. DISCRETE STRUCTURES   PP   100 40   40 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   22 F
 02. PROGRAMMING & PROBLEM SOLVING PP   100 40   41 P C   12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40   45 P C
 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 40   40 P C   13. DATA STRUCTURES   PP   100 40   47 P C
 04. DATA STRUCTURES AND ALGORITHMS PP   100 40   49 P C   14. COMPUTER GRAPHICS   PP   100 40   42 P C
 05. HUMANITIES AND SOCIAL SCIENCE PP   100 40   55 P C   15. COMPUTER ORGANIZATION   PP   100 40   53 P
 06. PROGRAMMING LABORATORY   TW   25 10   15 P C   16. O. O. PROG. & COMP. GRAPH. LAB TW   50 20   20 P C
 07. PROGRAMMING LABORATORY   PR   50 20   20 P C   17. O. O. PROG. & COMP. GRAPH. LAB PR   50 20   20 P C
 08. DIGITAL ELECTRONICS LABORATORY TW   25 10   13 P C   18. MICROPROCESSORS & INTERFACING LABTW   50 20   21 P C
 09. DIGITAL ELECTRONICS LABORATORY PR   50 20   35 P C   19. MICROPROCESSORS & INTERFACING LABPR   50 20   33 P
 10. SOFT SKILLS   TW   50 20   24 P C   20. DATA STRUCTURES LABORATORY   TW   50 20   20 P C
                                                             21. DATA STRUCTURES LABORATORY   PR   50 20   25 P
GRAND TOTAL = 680/1500, RESULT: FAILS A.T.K.T.
ORDN. 1 MARKS :
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 S80054410   YEREKAR KARISHMA TUKARAM   RANJANA   , 71101064B   , S80054410 ,   PICT   , S80054410
 01. DISCRETE STRUCTURES   PP   100 40   60 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   63 P C
 02. PROGRAMMING & PROBLEM SOLVING PP   100 40   47 P C   12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40   54 P C
 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 40   59 P C   13. DATA STRUCTURES   PP   100 40   55 P C
 04. DATA STRUCTURES AND ALGORITHMS PP   100 40   61 P C   14. COMPUTER GRAPHICS   PP   100 40   60 P C
 05. HUMANITIES AND SOCIAL SCIENCE PP   100 40   55 P C   15. COMPUTER ORGANIZATION   PP   100 40   63 P C
 06. PROGRAMMING LABORATORY   TW   25 10   17 P C   16. O. O. PROG. & COMP. GRAPH. LAB TW   50 20   38 P C
 07. PROGRAMMING LABORATORY   PR   50 20   39 P C   17. O. O. PROG. & COMP. GRAPH. LAB PR   50 20   30 P C
 08. DIGITAL ELECTRONICS LABORATORY TW   25 10   17 P C   18. MICROPROCESSORS & INTERFACING LABTW   50 20   33 P C
 09. DIGITAL ELECTRONICS LABORATORY PR   50 20   25 P C   19. MICROPROCESSORS & INTERFACING LABPR   50 20   24 P
 10. SOFT SKILLS   TW   50 20   46 P C   20. DATA STRUCTURES LABORATORY   TW   50 20   33 P C
                                                             21. DATA STRUCTURES LABORATORY   PR   50 20   31 P C
GRAND TOTAL = 910/1500, RESULT: FIRST CLASS
ORDN. 1 MARKS :
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 S80054411   YEWALE SHUBHANGI KAILAS   MANISHA   , 71101065L   , S80054411 ,   PICT   , S80054411
 01. DISCRETE STRUCTURES   PP   100 40   40 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   27 F
 02. PROGRAMMING & PROBLEM SOLVING PP   100 40   41 P C   12. MICROPROC. & INTERFACING TECHNIQ.PP 100 40   50 P C
 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 40   40 P C   13. DATA STRUCTURES   PP   100 40   40 P C
 04. DATA STRUCTURES AND ALGORITHMS PP   100 40   40 P C   14. COMPUTER GRAPHICS   PP   100 40   40 P C
 05. HUMANITIES AND SOCIAL SCIENCE PP   100 40   49 P C   15. COMPUTER ORGANIZATION   PP   100 40   40 P C
 06. PROGRAMMING LABORATORY   TW   25 10   22 P C   16. O. O. PROG. & COMP. GRAPH. LAB TW   50 20   33 P C
 07. PROGRAMMING LABORATORY   PR   50 20   20 P C   17. O. O. PROG. & COMP. GRAPH. LAB PR   50 20   29 P C
 08. DIGITAL ELECTRONICS LABORATORY TW   25 10   21 P C   18. MICROPROCESSORS & INTERFACING LABTW   50 20   39 P C
 09. DIGITAL ELECTRONICS LABORATORY PR   50 20   30 P C   19. MICROPROCESSORS & INTERFACING LABPR   50 20   23 P
 10. SOFT SKILLS   TW   50 20   37 P C   20. DATA STRUCTURES LABORATORY   TW   50 20   36 P C
                                                             21. DATA STRUCTURES LABORATORY   PR   50 20   21 P
GRAND TOTAL = 718/1500, RESULT: FAILS A.T.K.T.
ORDN. 1 MARKS :
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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.
      OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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 S80054412   ABHISHEK KUMAR SAURAV   SUMAN   , 71045357E   ,   , PICT   , S80054412
 01. DISCRETE STRUCTURES   PP   100 40   40 P C   11. ENGINEERING MATHEMATICS III   PP   100 40   40 P C
 02. PROGRAMMING & PROBLEM SOLVING PP   100 40   AA F   12. MICROPROC. & INTERFACING TECHNIQ.PP   100 40   AA F
 03. DIGIT. ELECTRONICS & LOGIC DESIGNPP 100 40   43 P C   13. DATA STRUCTURES   PP   100 40   43 P C
 04. DATA STRUCTURES AND ALGORITHMS PP   100 40   41 P C   14. COMPUTER GRAPHICS   PP   100 40   40 P
 05. HUMANITIES AND SOCIAL SCIENCE PP   100 40   41 P C   15. COMPUTER ORGANIZATION   PP   100 40   25 F
 06. PROGRAMMING LABORATORY   TW   25 10   10 P C   16. O. O. PROG. & COMP. GRAPH. LAB   TW   50 20   38 P C
 07. PROGRAMMING LABORATORY   PR   50 20   AA F   17. O. O. PROG. & COMP. GRAPH. LAB   PR   50 20   AA F
 08. DIGITAL ELECTRONICS LABORATORY TW   25 10   10 P C   18. MICROPROCESSORS & INTERFACING LABTW   50 20   22 P C
 09. DIGITAL ELECTRONICS LABORATORY PR   50 20   25 P   19. MICROPROCESSORS & INTERFACING LABPR   50 20   AA F
 10. SOFT SKILLS   TW   50 20   20 P C   20. DATA STRUCTURES LABORATORY   TW   50 20   20 P C
                                                               21. DATA STRUCTURES LABORATORY   PR   50 20   05 F
GRAND TOTAL = 463/1500, RESULT: FAILS   RESULT RESERVED FOR BKLG
ORDN. 1 MARKS :

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    NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.
          OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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    S80058501   ABHALE SAURABH BHANUDAS   SUNITA   , 71200746G   , S80058501 ,   PICT   , S80058501
    01. DISCRETE STRUCTURES   PP   100 40   72 P C   11. ENG MATHS III   PP   100 40   61 P
    02. COMPUTER ORGANIZATION   PP   100 40   70 P C   12. COMPUTER GRAPHICS   PP   100 40   49 P
    03. DIGITAL ELECTRONICS & LOGIC DESIGPP   100 40   75 P C   13. PROCESSOR ARCHITECTURE & INTER. PP   100 40   41 P
    04. FUNDAMENTAL OF DATA STRUCTURES PP   100 40   63 P C   14. DATA STRUCTURES AND FILES   PP   100 40   57 P
    05. HUMANITIES AND SOCIAL SCIENCES PP   100 40   55 P C   15. DATA COMMUNICATION   PP   100 40   65 P
    06. DIGITAL LABORATORY   TW   50 20   46 P C   16. PROCESSOR INTERFACING LABORATORY TW   25 10   19 P
    07. DIGITAL LABORATORY   PR   50 20   43 P C   17. PROCESSOR INTERFACING LABORATORY OR   50 20   42 P
    08. PROGRAMMING LABORATORY   TW   50 20   48 P C   18. DATA STRUCTURES AND FILES LAB   TW   25 10   22 P
    09. PROGRAMMING LABORATORY   PR   50 20   42 P C   19. DATA STRUCTURES AND FILES LAB   PR   50 20   38 P
    10. COMMUNICATION AND LANGUAGE LAB. TW   50 20   46 P C   20. OBJECT ORIENTED PROGRAMMING LAB TW   50 20   43 P
                                                                21. OBJECT ORIENTED PROGRAMMING LAB PR   50 20   40 P
GRAND TOTAL = 1037/1500, RESULT: FIRST CLASS WITH DISTINCTION
ORDN. 1 MARKS :
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    S80058502   ADARSH KUMAR   KIRAN   , 71200750E   , S80058502 ,   PICT   , S80058502
    01. DISCRETE STRUCTURES   PP   100 40   57 P C   11. ENG MATHS III   PP   100 40   40 P
    02. COMPUTER ORGANIZATION   PP   100 40   55 P   12. COMPUTER GRAPHICS   PP   100 40   40 P
    03. DIGITAL ELECTRONICS & LOGIC DESIGPP   100 40   40 P C   13. PROCESSOR ARCHITECTURE & INTER. PP   100 40   40 P
    04. FUNDAMENTAL OF DATA STRUCTURES PP   100 40   43 P C   14. DATA STRUCTURES AND FILES   PP   100 40   40 P
    05. HUMANITIES AND SOCIAL SCIENCES PP   100 40   50 P C   15. DATA COMMUNICATION   PP   100 40   49 P
    06. DIGITAL LABORATORY   TW   50 20   28 P C   16. PROCESSOR INTERFACING LABORATORY TW   25 10   10 P
    07. DIGITAL LABORATORY   PR   50 20   32 P   17. PROCESSOR INTERFACING LABORATORY OR   50 20   AA F
    08. PROGRAMMING LABORATORY   TW   50 20   22 P C   18. DATA STRUCTURES AND FILES LAB   TW   25 10   10 P
    09. PROGRAMMING LABORATORY   PR   50 20   00 F   19. DATA STRUCTURES AND FILES LAB   PR   50 20   38 P
    10. COMMUNICATION AND LANGUAGE LAB. TW   50 20   31 P C   20. OBJECT ORIENTED PROGRAMMING LAB TW   50 20   20 P
                                                                21. OBJECT ORIENTED PROGRAMMING LAB PR   50 20   AA F
GRAND TOTAL = 645/1500, RESULT: FAILS A.T.K.T.
ORDN. 1 MARKS :
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    S80058503   AHIRE AKSHATA RAJENDRA   MEENAKSHI   , 71200752M   , S80058503 ,   PICT   , S80058503
    01. DISCRETE STRUCTURES   PP   100 40   75 P C   11. ENG MATHS III   PP   100 40   40 P
    02. COMPUTER ORGANIZATION   PP   100 40   53 P C   12. COMPUTER GRAPHICS   PP   100 40   40 P
    03. DIGITAL ELECTRONICS & LOGIC DESIGPP   100 40   59 P C   13. PROCESSOR ARCHITECTURE & INTER. PP   100 40   40 P
    04. FUNDAMENTAL OF DATA STRUCTURES PP   100 40   52 P C   14. DATA STRUCTURES AND FILES   PP   100 40   58 P
    05. HUMANITIES AND SOCIAL SCIENCES PP   100 40   50 P C   15. DATA COMMUNICATION   PP   100 40   60 P
    06. DIGITAL LABORATORY   TW   50 20   43 P C   16. PROCESSOR INTERFACING LABORATORY TW   25 10   19 P
    07. DIGITAL LABORATORY   PR   50 20   40 P C   17. PROCESSOR INTERFACING LABORATORY OR   50 20   30 P
    08. PROGRAMMING LABORATORY   TW   50 20   44 P C   18. DATA STRUCTURES AND FILES LAB   TW   25 10   22 P
    09. PROGRAMMING LABORATORY   PR   50 20   30 P C   19. DATA STRUCTURES AND FILES LAB   PR   50 20   45 P
    10. COMMUNICATION AND LANGUAGE LAB. TW   50 20   47 P C   20. OBJECT ORIENTED PROGRAMMING LAB TW   50 20   43 P
                                                                21. OBJECT ORIENTED PROGRAMMING LAB PR   50 20   36 P
GRAND TOTAL = 926/1500, RESULT: FIRST CLASS
ORDN. 1 MARKS :
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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
      OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S80058504   AKSHAY A ARLIKATTI   SHANTHI   , 71200755F   , S80058504 ,   PICT   , S80058504

01. DISCRETE STRUCTURES	PP	100	40	80	P C	11. ENG MATHS III	PP	100	40	66	P
02. COMPUTER ORGANIZATION	PP	100	40	66	P C	12. COMPUTER GRAPHICS	PP	100	40	47	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	64	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	47	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	50	P	14. DATA STRUCTURES AND FILES	PP	100	40	57	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	44	P C	15. DATA COMMUNICATION	PP	100	40	62	P
06. DIGITAL LABORATORY	TW	50	20	39	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	18	P
07. DIGITAL LABORATORY	PR	50	20	37	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	38	P
08. PROGRAMMING LABORATORY	TW	50	20	40	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	24	P
09. PROGRAMMING LABORATORY	PR	50	20	48	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	46	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	39	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	42	P
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	43	P

GRAND TOTAL = 997/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

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S80058506   AKSHAY SHATRUGHAN DHOTRE   RAJISHREE   , 71350914H   , S80058506 ,   PICT   , S80058506

01. DISCRETE STRUCTURES	PP	100	40	48	P C	11. ENG MATHS III	PP	100	40	17	F
02. COMPUTER ORGANIZATION	PP	100	40	48	P C	12. COMPUTER GRAPHICS	PP	100	40	52	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	51	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	28	F
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	47	P C	14. DATA STRUCTURES AND FILES	PP	100	40	40	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	47	P C	15. DATA COMMUNICATION	PP	100	40	57	P
06. DIGITAL LABORATORY	TW	50	20	27	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	10	P
07. DIGITAL LABORATORY	PR	50	20	25	P	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	AA	F
08. PROGRAMMING LABORATORY	TW	50	20	33	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	12	P
09. PROGRAMMING LABORATORY	PR	50	20	20	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	38	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	33	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	23	P
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	AA	F

GRAND TOTAL = 656/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

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S80058507   AMAN KUMAR NIGAM   ROOPALI NIGAM   , 71200758L   , S80058507 ,   PICT   , S80058507

01. DISCRETE STRUCTURES	PP	100	40	56	P C	11. ENG MATHS III	PP	100	40	59	P
02. COMPUTER ORGANIZATION	PP	100	40	57	P C	12. COMPUTER GRAPHICS	PP	100	40	44	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	57	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	44	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	53	P C	14. DATA STRUCTURES AND FILES	PP	100	40	44	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	46	P C	15. DATA COMMUNICATION	PP	100	40	51	P
06. DIGITAL LABORATORY	TW	50	20	44	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	20	P
07. DIGITAL LABORATORY	PR	50	20	40	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	39	P
08. PROGRAMMING LABORATORY	TW	50	20	43	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	22	P
09. PROGRAMMING LABORATORY	PR	50	20	40	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	42	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	43	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	39	P
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	36	P

GRAND TOTAL = 919/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.

          OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S80058508   ARIJIT PANDE                     ANNAPURNA          , 71200764E    , S80058508 ,  PICT     , S80058508
01. DISCRETE STRUCTURES           PP   100  40   62  P C    11. ENG MATHS III              PP   100  40   79  P
02. COMPUTER ORGANIZATION         PP   100  40   69  P C    12. COMPUTER GRAPHICS          PP   100  40   49  P
03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100  40  68  P C    13. PROCESSOR ARCHITECTURE & INTER. PP 100 40  54  P
04. FUNDAMENTAL OF DATA STRUCTURES PP  100  40   47  P C    14. DATA STRUCTURES AND FILES   PP   100  40   51  P
05. HUMANITIES AND SOCIAL SCIENCES PP  100  40   56  P C    15. DATA COMMUNICATION         PP   100  40   69  P
06. DIGITAL LABORATORY            TW    50  20   43  P C    16. PROCESSOR INTERFACING LABORATORY TW  25  10   21  P
07. DIGITAL LABORATORY            PR    50  20   42  P C    17. PROCESSOR INTERFACING LABORATORY OR  50  20   40  P
08. PROGRAMMING LABORATORY        TW    50  20   48  P C    18. DATA STRUCTURES AND FILES LAB  TW   25  10   23  P
09. PROGRAMMING LABORATORY        PR    50  20   37  P C    19. DATA STRUCTURES AND FILES LAB  PR   50  20   43  P
10. COMMUNICATION AND LANGUAGE LAB. TW  50  20   47  P C    20. OBJECT ORIENTED PROGRAMMING LAB  TW  50  20   46  P
                                                           21. OBJECT ORIENTED PROGRAMMING LAB  PR  50  20   37  P
GRAND TOTAL = 1031/1500, RESULT: FIRST CLASS WITH DISTINCTION
ORDN. 1 MARKS :
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S80058509   ASHISH GUPTA                     MEENA              , 71200765C    , S80058509 ,  PICT     , S80058509
01. DISCRETE STRUCTURES           PP   100  40   75  P C    11. ENG MATHS III              PP   100  40   46  P
02. COMPUTER ORGANIZATION         PP   100  40   50  P C    12. COMPUTER GRAPHICS          PP   100  40   45  P
03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100  40  56  P C    13. PROCESSOR ARCHITECTURE & INTER. PP 100 40  46  P
04. FUNDAMENTAL OF DATA STRUCTURES PP  100  40   54  P C    14. DATA STRUCTURES AND FILES   PP   100  40   48  P
05. HUMANITIES AND SOCIAL SCIENCES PP  100  40   45  P C    15. DATA COMMUNICATION         PP   100  40   54  P
06. DIGITAL LABORATORY            TW    50  20   36  P C    16. PROCESSOR INTERFACING LABORATORY TW  25  10   20  P
07. DIGITAL LABORATORY            PR    50  20   35  P C    17. PROCESSOR INTERFACING LABORATORY OR  50  20   33  P
08. PROGRAMMING LABORATORY        TW    50  20   37  P C    18. DATA STRUCTURES AND FILES LAB  TW   25  10   23  P
09. PROGRAMMING LABORATORY        PR    50  20   45  P C    19. DATA STRUCTURES AND FILES LAB  PR   50  20   43  P
10. COMMUNICATION AND LANGUAGE LAB. TW  50  20   37  P C    20. OBJECT ORIENTED PROGRAMMING LAB  TW  50  20   40  P
                                                           21. OBJECT ORIENTED PROGRAMMING LAB  PR  50  20   41  P
GRAND TOTAL =  909/1500, RESULT: FIRST CLASS
ORDN. 1 MARKS :
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S80058510   BAGMAR ADESH SANTOSH             ASHA               , 71350915F    , S80058510 ,  PICT     , S80058510
01. DISCRETE STRUCTURES           PP   100  40   47  P C    11. ENG MATHS III              PP   100  40   05  F
02. COMPUTER ORGANIZATION         PP   100  40   63  P C    12. COMPUTER GRAPHICS          PP   100  40   43  P
03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100  40  41  P C    13. PROCESSOR ARCHITECTURE & INTER. PP 100 40  28  F
04. FUNDAMENTAL OF DATA STRUCTURES PP  100  40   51  P C    14. DATA STRUCTURES AND FILES   PP   100  40   27  F
05. HUMANITIES AND SOCIAL SCIENCES PP  100  40   57  P C    15. DATA COMMUNICATION         PP   100  40   68  P
06. DIGITAL LABORATORY            TW    50  20   42  P C    16. PROCESSOR INTERFACING LABORATORY TW  25  10   15  P
07. DIGITAL LABORATORY            PR    50  20   26  P C    17. PROCESSOR INTERFACING LABORATORY OR  50  20   20  P
08. PROGRAMMING LABORATORY        TW    50  20   40  P C    18. DATA STRUCTURES AND FILES LAB  TW   25  10   20  P
09. PROGRAMMING LABORATORY        PR    50  20   23  P C    19. DATA STRUCTURES AND FILES LAB  PR   50  20   25  P
10. COMMUNICATION AND LANGUAGE LAB. TW  50  20   43  P C    20. OBJECT ORIENTED PROGRAMMING LAB  TW  50  20   33  P
                                                           21. OBJECT ORIENTED PROGRAMMING LAB  PR  50  20   20$ P
GRAND TOTAL =  737/1500, RESULT: FAILS A.T.K.T.   [$ O.1]
ORDN. 1 MARKS : (21)2,
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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S80058511   BALDAWA KOMAL DWARKADAS   USHA   , 71200778E   , S80058511 ,   PICT   , S80058511

01. DISCRETE STRUCTURES	PP	100	40	73	P C		11. ENG MATHS III	PP	100	40	58	P		
02. COMPUTER ORGANIZATION	PP	100	40	57	P C		12. COMPUTER GRAPHICS	PP	100	40	52	P		
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	65	P C		13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	46	P		
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	49	P C		14. DATA STRUCTURES AND FILES	PP	100	40	55	P		
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	44	P C		15. DATA COMMUNICATION	PP	100	40	53	P		
06. DIGITAL LABORATORY	TW	50	20	32	P C		16. PROCESSOR INTERFACING LABORATORY	TW	25	10	14	P		
07. DIGITAL LABORATORY	PR	50	20	27	P C		17. PROCESSOR INTERFACING LABORATORY	OR	50	20	07	F		
08. PROGRAMMING LABORATORY	TW	50	20	31	P C		18. DATA STRUCTURES AND FILES LAB	TW	25	10	18	P		
09. PROGRAMMING LABORATORY	PR	50	20	35	P C		19. DATA STRUCTURES AND FILES LAB	PR	50	20	38	P		
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	36	P C		20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	33	P		
							21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	32	P		

GRAND TOTAL = 855/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

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S80058512   BANSODE ASHLESHA ANKUSH   SUREKHA   , 71200782C   , S80058512 ,   PICT   , S80058512

01. DISCRETE STRUCTURES	PP	100	40	70	P C		11. ENG MATHS III	PP	100	40	41	P		
02. COMPUTER ORGANIZATION	PP	100	40	70	P C		12. COMPUTER GRAPHICS	PP	100	40	53	P		
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	67	P C		13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	48	P		
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	61	P C		14. DATA STRUCTURES AND FILES	PP	100	40	60	P		
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	52	P C		15. DATA COMMUNICATION	PP	100	40	63	P		
06. DIGITAL LABORATORY	TW	50	20	41	P C		16. PROCESSOR INTERFACING LABORATORY	TW	25	10	17	P		
07. DIGITAL LABORATORY	PR	50	20	38	P C		17. PROCESSOR INTERFACING LABORATORY	OR	50	20	28	P		
08. PROGRAMMING LABORATORY	TW	50	20	37	P C		18. DATA STRUCTURES AND FILES LAB	TW	25	10	20	P		
09. PROGRAMMING LABORATORY	PR	50	20	34	P C		19. DATA STRUCTURES AND FILES LAB	PR	50	20	33	P		
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	44	P C		20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	42	P		
							21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	39	P		

GRAND TOTAL = 958/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

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S80058513   BARASKAR ADITI NARENDRA   LEENA   , 71200783M   , S80058513 ,   PICT   , S80058513

01. DISCRETE STRUCTURES	PP	100	40	66	P C		11. ENG MATHS III	PP	100	40	58	P		
02. COMPUTER ORGANIZATION	PP	100	40	69	P C		12. COMPUTER GRAPHICS	PP	100	40	49	P		
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	67	P C		13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	53	P		
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	53	P C		14. DATA STRUCTURES AND FILES	PP	100	40	65	P		
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	56	P C		15. DATA COMMUNICATION	PP	100	40	62	P		
06. DIGITAL LABORATORY	TW	50	20	40	P C		16. PROCESSOR INTERFACING LABORATORY	TW	25	10	17	P		
07. DIGITAL LABORATORY	PR	50	20	38	P C		17. PROCESSOR INTERFACING LABORATORY	OR	50	20	28	P		
08. PROGRAMMING LABORATORY	TW	50	20	38	P C		18. DATA STRUCTURES AND FILES LAB	TW	25	10	20	P		
09. PROGRAMMING LABORATORY	PR	50	20	41	P C		19. DATA STRUCTURES AND FILES LAB	PR	50	20	36	P		
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	41	P C		20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	40	P		
							21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	40	P		

GRAND TOTAL = 977/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.
       OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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S80058514   BARDE REEMA ANIL   JYOTI   , 71200784K   , S80058514 ,   PICT   , S80058514
01. DISCRETE STRUCTURES   PP   100 40   59 P C   11. ENG MATHS III   PP   100 40   25 F
02. COMPUTER ORGANIZATION   PP   100 40   55 P C   12. COMPUTER GRAPHICS   PP   100 40   40 P
03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 40   67 P C   13. PROCESSOR ARCHITECTURE & INTER. PP   100 40   40 P
04. FUNDAMENTAL OF DATA STRUCTURES PP   100 40   43 P C   14. DATA STRUCTURES AND FILES   PP   100 40   51 P
05. HUMANITIES AND SOCIAL SCIENCES PP   100 40   53 P C   15. DATA COMMUNICATION   PP   100 40   57 P
06. DIGITAL LABORATORY   TW   50 20   44 P C   16. PROCESSOR INTERFACING LABORATORY TW   25 10   17 P
07. DIGITAL LABORATORY   PR   50 20   36 P C   17. PROCESSOR INTERFACING LABORATORY OR   50 20   22 P
08. PROGRAMMING LABORATORY   TW   50 20   40 P C   18. DATA STRUCTURES AND FILES LAB   TW   25 10   21 P
09. PROGRAMMING LABORATORY   PR   50 20   40 P C   19. DATA STRUCTURES AND FILES LAB   PR   50 20   43 P
10. COMMUNICATION AND LANGUAGE LAB. TW   50 20   47 P C   20. OBJECT ORIENTED PROGRAMMING LAB TW   50 20   43 P
                                                              21. OBJECT ORIENTED PROGRAMMING LAB PR   50 20   37 P
GRAND TOTAL = 880/1500, RESULT: FAILS A.T.K.T.
ORDN. 1 MARKS :
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S80058515   BENDHALE AJAY MARUTI   KALPANA   , 71350916D   , S80058515 ,   PICT   , S80058515
01. DISCRETE STRUCTURES   PP   100 40   64 P   11. ENG MATHS III   PP   100 40   40 P
02. COMPUTER ORGANIZATION   PP   100 40   77 P C   12. COMPUTER GRAPHICS   PP   100 40   48 P
03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 40   70 P C   13. PROCESSOR ARCHITECTURE & INTER. PP   100 40   55 P
04. FUNDAMENTAL OF DATA STRUCTURES PP   100 40   56 P C   14. DATA STRUCTURES AND FILES   PP   100 40   70 P
05. HUMANITIES AND SOCIAL SCIENCES PP   100 40   76 P C   15. DATA COMMUNICATION   PP   100 40   64 P
06. DIGITAL LABORATORY   TW   50 20   34 P C   16. PROCESSOR INTERFACING LABORATORY TW   25 10   20 P
07. DIGITAL LABORATORY   PR   50 20   34 P C   17. PROCESSOR INTERFACING LABORATORY OR   50 20   22 P
08. PROGRAMMING LABORATORY   TW   50 20   34 P C   18. DATA STRUCTURES AND FILES LAB   TW   25 10   23 P
09. PROGRAMMING LABORATORY   PR   50 20   44 P C   19. DATA STRUCTURES AND FILES LAB   PR   50 20   40 P
10. COMMUNICATION AND LANGUAGE LAB. TW   50 20   34 P C   20. OBJECT ORIENTED PROGRAMMING LAB TW   50 20   47 P
                                                              21. OBJECT ORIENTED PROGRAMMING LAB PR   50 20   43 P
GRAND TOTAL = 995/1500, RESULT: FIRST CLASS WITH DISTINCTION
ORDN. 1 MARKS :
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S80058516   BHALCHIM NILESH LAKSHMAN   JIJABAI   , 71350917B   , S80058516 ,   PICT   , S80058516
01. DISCRETE STRUCTURES   PP   100 40   AA F   11. ENG MATHS III   PP   100 40   01 F
02. COMPUTER ORGANIZATION   PP   100 40   AA F   12. COMPUTER GRAPHICS   PP   100 40   AA F
03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 40   AA F   13. PROCESSOR ARCHITECTURE & INTER. PP   100 40   AA F
04. FUNDAMENTAL OF DATA STRUCTURES PP   100 40   AA F   14. DATA STRUCTURES AND FILES   PP   100 40   AA F
05. HUMANITIES AND SOCIAL SCIENCES PP   100 40   45 P C   15. DATA COMMUNICATION   PP   100 40   AA F
06. DIGITAL LABORATORY   TW   50 20   30 P C   16. PROCESSOR INTERFACING LABORATORY TW   25 10   14 P
07. DIGITAL LABORATORY   PR   50 20   20 P C   17. PROCESSOR INTERFACING LABORATORY OR   50 20   25 P
08. PROGRAMMING LABORATORY   TW   50 20   30 P C   18. DATA STRUCTURES AND FILES LAB   TW   25 10   14 P
09. PROGRAMMING LABORATORY   PR   50 20   20 P   19. DATA STRUCTURES AND FILES LAB   PR   50 20   37 P
10. COMMUNICATION AND LANGUAGE LAB. TW   50 20   32 P C   20. OBJECT ORIENTED PROGRAMMING LAB TW   50 20   29 P
                                                              21. OBJECT ORIENTED PROGRAMMING LAB PR   50 20   AA F
GRAND TOTAL = 297/1500, RESULT: FAILS
ORDN. 1 MARKS :
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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.
      OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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S80058517   BHALERAO DHAIRYASHEEL RATNAKAR   RENUKA   , 71200790D   , S80058517 ,   PICT   , S80058517
01. DISCRETE STRUCTURES   PP   100 40   45 P C   11. ENG MATHS III   PP   100 40   19 F
02. COMPUTER ORGANIZATION   PP   100 40   64 P C   12. COMPUTER GRAPHICS   PP   100 40   45 P
03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 40   40 P C   13. PROCESSOR ARCHITECTURE & INTER. PP   100 40   40 P
04. FUNDAMENTAL OF DATA STRUCTURES PP   100 40   40 P C   14. DATA STRUCTURES AND FILES   PP   100 40   40 P
05. HUMANITIES AND SOCIAL SCIENCES PP   100 40   44 P C   15. DATA COMMUNICATION   PP   100 40   56 P
06. DIGITAL LABORATORY   TW   50 20   41 P C   16. PROCESSOR INTERFACING LABORATORY TW   25 10   19 P
07. DIGITAL LABORATORY   PR   50 20   27 P C   17. PROCESSOR INTERFACING LABORATORY OR   50 20   25 P
08. PROGRAMMING LABORATORY   TW   50 20   37 P C   18. DATA STRUCTURES AND FILES LAB   TW   25 10   18 P
09. PROGRAMMING LABORATORY   PR   50 20   25 P C   19. DATA STRUCTURES AND FILES LAB   PR   50 20   17 F
10. COMMUNICATION AND LANGUAGE LAB. TW   50 20   43 P C   20. OBJECT ORIENTED PROGRAMMING LAB TW   50 20   36 P
                                                          21. OBJECT ORIENTED PROGRAMMING LAB PR   50 20   12 F
GRAND TOTAL = 733/1500, RESULT: FAILS A.T.K.T.
ORDN. 1 MARKS :
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S80058518   BHANDARKUMTHE MADHUR MADHAVRAO   UMA   , 71200792L   , S80058518 ,   PICT   , S80058518
01. DISCRETE STRUCTURES   PP   100 40   44 P C   11. ENG MATHS III   PP   100 40   55 P
02. COMPUTER ORGANIZATION   PP   100 40   66 P C   12. COMPUTER GRAPHICS   PP   100 40   47 P
03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 40   57 P C   13. PROCESSOR ARCHITECTURE & INTER. PP   100 40   46 P
04. FUNDAMENTAL OF DATA STRUCTURES PP   100 40   48 P C   14. DATA STRUCTURES AND FILES   PP   100 40   55 P
05. HUMANITIES AND SOCIAL SCIENCES PP   100 40   49 P C   15. DATA COMMUNICATION   PP   100 40   69 P
06. DIGITAL LABORATORY   TW   50 20   43 P C   16. PROCESSOR INTERFACING LABORATORY TW   25 10   23 P
07. DIGITAL LABORATORY   PR   50 20   25 P C   17. PROCESSOR INTERFACING LABORATORY OR   50 20   32 P
08. PROGRAMMING LABORATORY   TW   50 20   46 P C   18. DATA STRUCTURES AND FILES LAB   TW   25 10   23 P
09. PROGRAMMING LABORATORY   PR   50 20   39 P C   19. DATA STRUCTURES AND FILES LAB   PR   50 20   39 P
10. COMMUNICATION AND LANGUAGE LAB. TW   50 20   47 P C   20. OBJECT ORIENTED PROGRAMMING LAB TW   50 20   47 P
                                                          21. OBJECT ORIENTED PROGRAMMING LAB PR   50 20   40 P
GRAND TOTAL = 940/1500, RESULT: FIRST CLASS
ORDN. 1 MARKS :
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S80058519   BHANDE KRISHNA SHRIDHAR   SHILABAI   , 71200793J   , S80058519 ,   PICT   , S80058519
01. DISCRETE STRUCTURES   PP   100 40   60 P C   11. ENG MATHS III   PP   100 40   40 P
02. COMPUTER ORGANIZATION   PP   100 40   52 P C   12. COMPUTER GRAPHICS   PP   100 40   48 P
03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 40   54 P C   13. PROCESSOR ARCHITECTURE & INTER. PP   100 40   47 P
04. FUNDAMENTAL OF DATA STRUCTURES PP   100 40   52 P C   14. DATA STRUCTURES AND FILES   PP   100 40   61 P
05. HUMANITIES AND SOCIAL SCIENCES PP   100 40   56 P C   15. DATA COMMUNICATION   PP   100 40   53 P
06. DIGITAL LABORATORY   TW   50 20   36 P C   16. PROCESSOR INTERFACING LABORATORY TW   25 10   19 P
07. DIGITAL LABORATORY   PR   50 20   32 P C   17. PROCESSOR INTERFACING LABORATORY OR   50 20   20 P
08. PROGRAMMING LABORATORY   TW   50 20   38 P C   18. DATA STRUCTURES AND FILES LAB   TW   25 10   20 P
09. PROGRAMMING LABORATORY   PR   50 20   32 P C   19. DATA STRUCTURES AND FILES LAB   PR   50 20   39 P
10. COMMUNICATION AND LANGUAGE LAB. TW   50 20   41 P C   20. OBJECT ORIENTED PROGRAMMING LAB TW   50 20   36 P
                                                          21. OBJECT ORIENTED PROGRAMMING LAB PR   50 20   37 P
GRAND TOTAL = 873/1500, RESULT: HIGHER SECOND CLASS
ORDN. 1 MARKS :
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    NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.
          OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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    S80058520   BHOIR KALPESH SHIVRAM   SOMABAI   , 71200796C   , S80058520 , PICT   , S80058520
    01. DISCRETE STRUCTURES   PP   100 40   40 P C   11. ENG MATHS III   PP   100 40   13 F
    02. COMPUTER ORGANIZATION   PP   100 40   45 P C   12. COMPUTER GRAPHICS   PP   100 40   29 F
    03. DIGITAL ELECTRONICS & LOGIC DESIGPP   100 40   49 P C   13. PROCESSOR ARCHITECTURE & INTER. PP   100 40   14 F
    04. FUNDAMENTAL OF DATA STRUCTURES PP   100 40   44 P C   14. DATA STRUCTURES AND FILES   PP   100 40   40 P
    05. HUMANITIES AND SOCIAL SCIENCES PP   100 40   42 P C   15. DATA COMMUNICATION   PP   100 40   45 P
    06. DIGITAL LABORATORY   TW   50 20   37 P C   16. PROCESSOR INTERFACING LABORATORY TW   25 10   12 P
    07. DIGITAL LABORATORY   PR   50 20   27 P C   17. PROCESSOR INTERFACING LABORATORY OR   50 20   21 P
    08. PROGRAMMING LABORATORY   TW   50 20   35 P C   18. DATA STRUCTURES AND FILES LAB   TW   25 10   15 P
    09. PROGRAMMING LABORATORY   PR   50 20   37 P C   19. DATA STRUCTURES AND FILES LAB   PR   50 20   30 P
    10. COMMUNICATION AND LANGUAGE LAB. TW   50 20   43 P C   20. OBJECT ORIENTED PROGRAMMING LAB TW   50 20   29 P
                                                                 21. OBJECT ORIENTED PROGRAMMING LAB PR   50 20   22 P
  GRAND TOTAL = 669/1500, RESULT: FAILS A.T.K.T.
  ORDN. 1 MARKS :
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    S80058521   BHOSURE YOGESH KASHINATH   SHEVANTA   , 71350918L   , S80058521 , PICT   , S80058521
    01. DISCRETE STRUCTURES   PP   100 40   59 P C   11. ENG MATHS III   PP   100 40   23 F
    02. COMPUTER ORGANIZATION   PP   100 40   57 P C   12. COMPUTER GRAPHICS   PP   100 40   51 P
    03. DIGITAL ELECTRONICS & LOGIC DESIGPP   100 40   59 P C   13. PROCESSOR ARCHITECTURE & INTER. PP   100 40   51 P
    04. FUNDAMENTAL OF DATA STRUCTURES PP   100 40   56 P C   14. DATA STRUCTURES AND FILES   PP   100 40   49 P
    05. HUMANITIES AND SOCIAL SCIENCES PP   100 40   58 P C   15. DATA COMMUNICATION   PP   100 40   57 P
    06. DIGITAL LABORATORY   TW   50 20   37 P C   16. PROCESSOR INTERFACING LABORATORY TW   25 10   22 P
    07. DIGITAL LABORATORY   PR   50 20   31 P C   17. PROCESSOR INTERFACING LABORATORY OR   50 20   40 P
    08. PROGRAMMING LABORATORY   TW   50 20   36 P C   18. DATA STRUCTURES AND FILES LAB   TW   25 10   22 P
    09. PROGRAMMING LABORATORY   PR   50 20   25 P C   19. DATA STRUCTURES AND FILES LAB   PR   50 20   40 P
    10. COMMUNICATION AND LANGUAGE LAB. TW   50 20   39 P C   20. OBJECT ORIENTED PROGRAMMING LAB TW   50 20   40 P
                                                                 21. OBJECT ORIENTED PROGRAMMING LAB PR   50 20   35 P
  GRAND TOTAL = 887/1500, RESULT: FAILS A.T.K.T.
  ORDN. 1 MARKS :
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    S80058522   BIRHADE NIKETAN SHASHIKANT   VANDANA   , 71200801C   , S80058522 , PICT   , S80058522
    01. DISCRETE STRUCTURES   PP   100 40   52 P C   11. ENG MATHS III   PP   100 40   40 P
    02. COMPUTER ORGANIZATION   PP   100 40   45 P C   12. COMPUTER GRAPHICS   PP   100 40   45 P
    03. DIGITAL ELECTRONICS & LOGIC DESIGPP   100 40   63 P C   13. PROCESSOR ARCHITECTURE & INTER. PP   100 40   40 P
    04. FUNDAMENTAL OF DATA STRUCTURES PP   100 40   46 P C   14. DATA STRUCTURES AND FILES   PP   100 40   45 P
    05. HUMANITIES AND SOCIAL SCIENCES PP   100 40   52 P C   15. DATA COMMUNICATION   PP   100 40   49 P
    06. DIGITAL LABORATORY   TW   50 20   41 P C   16. PROCESSOR INTERFACING LABORATORY TW   25 10   16 P
    07. DIGITAL LABORATORY   PR   50 20   25 P C   17. PROCESSOR INTERFACING LABORATORY OR   50 20   28 P
    08. PROGRAMMING LABORATORY   TW   50 20   38 P C   18. DATA STRUCTURES AND FILES LAB   TW   25 10   19 P
    09. PROGRAMMING LABORATORY   PR   50 20   22 P C   19. DATA STRUCTURES AND FILES LAB   PR   50 20   22 P
    10. COMMUNICATION AND LANGUAGE LAB. TW   50 20   43 P C   20. OBJECT ORIENTED PROGRAMMING LAB TW   50 20   41 P
                                                                 21. OBJECT ORIENTED PROGRAMMING LAB PR   50 20   44 P
  GRAND TOTAL = 816+09/1500, RESULT: HIGHER SECOND CLASS [O.2]
  ORDN. 1 MARKS :
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   NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.
           OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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   S80058523   BORATE SHRADDHA RAJARAM   KAMAL   , 71350919J   , S80058523 ,   PICT   , S80058523
   01. DISCRETE STRUCTURES   PP   100 40   40 P C   11. ENG MATHS III   PP   100 40   11 F
   02. COMPUTER ORGANIZATION   PP   100 40   45 P C   12. COMPUTER GRAPHICS   PP   100 40   47 P
   03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 40   44 P C   13. PROCESSOR ARCHITECTURE & INTER. PP 100 40   56 P
   04. FUNDAMENTAL OF DATA STRUCTURES PP   100 40   40 P C   14. DATA STRUCTURES AND FILES   PP   100 40   53 P
   05. HUMANITIES AND SOCIAL SCIENCES PP   100 40   56 P C   15. DATA COMMUNICATION   PP   100 40   54 P
   06. DIGITAL LABORATORY   TW   50 20   43 P C   16. PROCESSOR INTERFACING LABORATORY TW   25 10   22 P
   07. DIGITAL LABORATORY   PR   50 20   20 P C   17. PROCESSOR INTERFACING LABORATORY OR   50 20   25 P
   08. PROGRAMMING LABORATORY   TW   50 20   44 P C   18. DATA STRUCTURES AND FILES LAB TW   25 10   23 P
   09. PROGRAMMING LABORATORY   PR   50 20   20 P C   19. DATA STRUCTURES AND FILES LAB PR   50 20   40 P
   10. COMMUNICATION AND LANGUAGE LAB. TW   50 20   43 P C   20. OBJECT ORIENTED PROGRAMMING LAB TW   50 20   43 P
                                                                21. OBJECT ORIENTED PROGRAMMING LAB PR   50 20   35 P
GRAND TOTAL = 804/1500, RESULT: FAILS A.T.K.T.
ORDN. 1 MARKS :
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   S80058524   CHANDRATRE ABHISHEK SURENDRA   ARCHANA   , 71200807B   , S80058524 ,   PICT   , S80058524
   01. DISCRETE STRUCTURES   PP   100 40   66 P C   11. ENG MATHS III   PP   100 40   69 P
   02. COMPUTER ORGANIZATION   PP   100 40   62 P C   12. COMPUTER GRAPHICS   PP   100 40   46 P
   03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 40   61 P C   13. PROCESSOR ARCHITECTURE & INTER. PP 100 40   40 P
   04. FUNDAMENTAL OF DATA STRUCTURES PP   100 40   50 P C   14. DATA STRUCTURES AND FILES   PP   100 40   40 P
   05. HUMANITIES AND SOCIAL SCIENCES PP   100 40   46 P C   15. DATA COMMUNICATION   PP   100 40   66 P
   06. DIGITAL LABORATORY   TW   50 20   36 P C   16. PROCESSOR INTERFACING LABORATORY TW   25 10   19 P
   07. DIGITAL LABORATORY   PR   50 20   45 P C   17. PROCESSOR INTERFACING LABORATORY OR   50 20   40 P
   08. PROGRAMMING LABORATORY   TW   50 20   38 P C   18. DATA STRUCTURES AND FILES LAB TW   25 10   20 P
   09. PROGRAMMING LABORATORY   PR   50 20   42 P C   19. DATA STRUCTURES AND FILES LAB PR   50 20   39 P
   10. COMMUNICATION AND LANGUAGE LAB. TW   50 20   36 P C   20. OBJECT ORIENTED PROGRAMMING LAB TW   50 20   43 P
                                                                21. OBJECT ORIENTED PROGRAMMING LAB PR   50 20   41 P
GRAND TOTAL = 945/1500, RESULT: FIRST CLASS
ORDN. 1 MARKS :
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   S80058525   CHANDRIKA PARIMOO   ANITA   , 71200808L   , S80058525 ,   PICT   , S80058525
   01. DISCRETE STRUCTURES   PP   100 40   88 P C   11. ENG MATHS III   PP   100 40   84 P
   02. COMPUTER ORGANIZATION   PP   100 40   79 P C   12. COMPUTER GRAPHICS   PP   100 40   58 P
   03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 40   73 P C   13. PROCESSOR ARCHITECTURE & INTER. PP 100 40   55 P
   04. FUNDAMENTAL OF DATA STRUCTURES PP   100 40   60 P C   14. DATA STRUCTURES AND FILES   PP   100 40   63 P
   05. HUMANITIES AND SOCIAL SCIENCES PP   100 40   59 P C   15. DATA COMMUNICATION   PP   100 40   75 P
   06. DIGITAL LABORATORY   TW   50 20   40 P C   16. PROCESSOR INTERFACING LABORATORY TW   25 10   22 P
   07. DIGITAL LABORATORY   PR   50 20   41 P C   17. PROCESSOR INTERFACING LABORATORY OR   50 20   42 P
   08. PROGRAMMING LABORATORY   TW   50 20   45 P C   18. DATA STRUCTURES AND FILES LAB TW   25 10   24 P
   09. PROGRAMMING LABORATORY   PR   50 20   46 P C   19. DATA STRUCTURES AND FILES LAB PR   50 20   44 P
   10. COMMUNICATION AND LANGUAGE LAB. TW   50 20   41 P C   20. OBJECT ORIENTED PROGRAMMING LAB TW   50 20   45 P
                                                                21. OBJECT ORIENTED PROGRAMMING LAB PR   50 20   45 P
GRAND TOTAL = 1129/1500, RESULT: FIRST CLASS WITH DISTINCTION
ORDN. 1 MARKS :
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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.
        OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S80058526   CHARKHA BHUSHAN HEMANT   SEEMA   , 71200809J   , S80058526 ,   PICT   , S80058526
01. DISCRETE STRUCTURES   PP   100 40   53 P C   11. ENG MATHS III   PP   100 40   54 P
02. COMPUTER ORGANIZATION   PP   100 40   53 P C   12. COMPUTER GRAPHICS   PP   100 40   45 P
03. DIGITAL ELECTRONICS & LOGIC DESIGPP   100 40   52 P C   13. PROCESSOR ARCHITECTURE & INTER. PP   100 40   47 P
04. FUNDAMENTAL OF DATA STRUCTURES   PP   100 40   46 P C   14. DATA STRUCTURES AND FILES   PP   100 40   46 P
05. HUMANITIES AND SOCIAL SCIENCES   PP   100 40   49 P C   15. DATA COMMUNICATION   PP   100 40   46 P
06. DIGITAL LABORATORY   TW   50 20   34 P C   16. PROCESSOR INTERFACING LABORATORY TW   25 10   16 P
07. DIGITAL LABORATORY   PR   50 20   30 P C   17. PROCESSOR INTERFACING LABORATORY OR   50 20   09 F
08. PROGRAMMING LABORATORY   TW   50 20   29 P C   18. DATA STRUCTURES AND FILES LAB   TW   25 10   17 P
09. PROGRAMMING LABORATORY   PR   50 20   40 P C   19. DATA STRUCTURES AND FILES LAB   PR   50 20   32 P
10. COMMUNICATION AND LANGUAGE LAB. TW   50 20   37 P C   20. OBJECT ORIENTED PROGRAMMING LAB TW   50 20   33 P
                                                               21. OBJECT ORIENTED PROGRAMMING LAB PR   50 20   40 P
GRAND TOTAL = 808/1500, RESULT: FAILS A.T.K.T.
ORDN. 1 MARKS :
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S80058527   CHASKAR VIPUL VINAYAK   POOJA   , 71200811L   , S80058527 ,   PICT   , S80058527
01. DISCRETE STRUCTURES   PP   100 40   77 P C   11. ENG MATHS III   PP   100 40   60 P
02. COMPUTER ORGANIZATION   PP   100 40   63 P C   12. COMPUTER GRAPHICS   PP   100 40   51 P
03. DIGITAL ELECTRONICS & LOGIC DESIGPP   100 40   64 P C   13. PROCESSOR ARCHITECTURE & INTER. PP   100 40   54 P
04. FUNDAMENTAL OF DATA STRUCTURES   PP   100 40   49 P C   14. DATA STRUCTURES AND FILES   PP   100 40   59 P
05. HUMANITIES AND SOCIAL SCIENCES   PP   100 40   53 P C   15. DATA COMMUNICATION   PP   100 40   73 P
06. DIGITAL LABORATORY   TW   50 20   44 P C   16. PROCESSOR INTERFACING LABORATORY TW   25 10   24 P
07. DIGITAL LABORATORY   PR   50 20   42 P C   17. PROCESSOR INTERFACING LABORATORY OR   50 20   40 P
08. PROGRAMMING LABORATORY   TW   50 20   46 P C   18. DATA STRUCTURES AND FILES LAB   TW   25 10   23 P
09. PROGRAMMING LABORATORY   PR   50 20   42 P C   19. DATA STRUCTURES AND FILES LAB   PR   50 20   39 P
10. COMMUNICATION AND LANGUAGE LAB. TW   50 20   44 P C   20. OBJECT ORIENTED PROGRAMMING LAB TW   50 20   46 P
                                                               21. OBJECT ORIENTED PROGRAMMING LAB PR   50 20   38 P
GRAND TOTAL = 1031/1500, RESULT: FIRST CLASS WITH DISTINCTION
ORDN. 1 MARKS :
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S80058528   CHATTERJEE SUDIPTO ANJAN   MADHUCHHANDA   , 71200812J   , S80058528 ,   PICT   , S80058528
01. DISCRETE STRUCTURES   PP   100 40   58 P C   11. ENG MATHS III   PP   100 40   57 P
02. COMPUTER ORGANIZATION   PP   100 40   78 P C   12. COMPUTER GRAPHICS   PP   100 40   48 P
03. DIGITAL ELECTRONICS & LOGIC DESIGPP   100 40   63 P C   13. PROCESSOR ARCHITECTURE & INTER. PP   100 40   44 P
04. FUNDAMENTAL OF DATA STRUCTURES   PP   100 40   50 P C   14. DATA STRUCTURES AND FILES   PP   100 40   50 P
05. HUMANITIES AND SOCIAL SCIENCES   PP   100 40   49 P C   15. DATA COMMUNICATION   PP   100 40   63 P
06. DIGITAL LABORATORY   TW   50 20   41 P C   16. PROCESSOR INTERFACING LABORATORY TW   25 10   20 P
07. DIGITAL LABORATORY   PR   50 20   39 P C   17. PROCESSOR INTERFACING LABORATORY OR   50 20   42 P
08. PROGRAMMING LABORATORY   TW   50 20   42 P C   18. DATA STRUCTURES AND FILES LAB   TW   25 10   23 P
09. PROGRAMMING LABORATORY   PR   50 20   45 P C   19. DATA STRUCTURES AND FILES LAB   PR   50 20   40 P
10. COMMUNICATION AND LANGUAGE LAB. TW   50 20   42 P C   20. OBJECT ORIENTED PROGRAMMING LAB TW   50 20   45 P
                                                               21. OBJECT ORIENTED PROGRAMMING LAB PR   50 20   44 P
GRAND TOTAL = 983/1500, RESULT: FIRST CLASS
ORDN. 1 MARKS :
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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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S80058530   CHAVAN SONAL NILKANTH   SAROJ   , 71200817K   , S80058530 ,   PICT   , S80058530

01. DISCRETE STRUCTURES	PP	100	40	51	P C	11. ENG MATHS III	PP	100	40	49	P
02. COMPUTER ORGANIZATION	PP	100	40	57	P C	12. COMPUTER GRAPHICS	PP	100	40	48	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	61	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	70	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	53	P C	14. DATA STRUCTURES AND FILES	PP	100	40	46	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	47	P C	15. DATA COMMUNICATION	PP	100	40	63	P
06. DIGITAL LABORATORY	TW	50	20	31	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	16	P
07. DIGITAL LABORATORY	PR	50	20	32	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	28	P
08. PROGRAMMING LABORATORY	TW	50	20	34	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	17	P
09. PROGRAMMING LABORATORY	PR	50	20	44	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	40	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	39	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	35	P
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	41	P

GRAND TOTAL = 902/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :
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S80058531   CHAWARE PURUSHOTTAM GAJANAN   MANJUSHA   , 71200820K   , S80058531 ,   PICT   , S80058531

01. DISCRETE STRUCTURES	PP	100	40	86	P C	11. ENG MATHS III	PP	100	40	94	P
02. COMPUTER ORGANIZATION	PP	100	40	74	P C	12. COMPUTER GRAPHICS	PP	100	40	53	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	72	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	53	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	58	P C	14. DATA STRUCTURES AND FILES	PP	100	40	65	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	54	P C	15. DATA COMMUNICATION	PP	100	40	61	P
06. DIGITAL LABORATORY	TW	50	20	45	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	23	P
07. DIGITAL LABORATORY	PR	50	20	40	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	44	P
08. PROGRAMMING LABORATORY	TW	50	20	48	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	22	P
09. PROGRAMMING LABORATORY	PR	50	20	44	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	42	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	48	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	44	P
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	35	P

GRAND TOTAL = 1105/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :
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S80058532   CHOUDHARI PRIYANKA SANJAY   POONAM   , 71200822F   , S80058532 ,   PICT   , S80058532

01. DISCRETE STRUCTURES	PP	100	40	63	P C	11. ENG MATHS III	PP	100	40	40	P
02. COMPUTER ORGANIZATION	PP	100	40	40	P C	12. COMPUTER GRAPHICS	PP	100	40	40	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	55	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	28	F
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	53	P C	14. DATA STRUCTURES AND FILES	PP	100	40	40	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	43	P C	15. DATA COMMUNICATION	PP	100	40	24	F
06. DIGITAL LABORATORY	TW	50	20	40	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	16	P
07. DIGITAL LABORATORY	PR	50	20	30	P	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	25	P
08. PROGRAMMING LABORATORY	TW	50	20	37	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	16	P
09. PROGRAMMING LABORATORY	PR	50	20	36	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	41	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	42	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	35	P
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	31	P

GRAND TOTAL = 775/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :
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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
          OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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 S80058533   D CUNHA JOANNE JOHN              ANNIE              , 71200824B    , S80058533 ,   PICT     , S80058533
 01. DISCRETE STRUCTURES          PP   100  40   69  P C    11. ENG MATHS III              PP   100  40   40  P
 02. COMPUTER ORGANIZATION         PP   100  40   45  P C    12. COMPUTER GRAPHICS          PP   100  40   18  F
 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100  40   50  P     13. PROCESSOR ARCHITECTURE & INTER. PP 100  40   20  F
 04. FUNDAMENTAL OF DATA STRUCTURES PP  100  40   59  P      14. DATA STRUCTURES AND FILES    PP   100  40   40  P
 05. HUMANITIES AND SOCIAL SCIENCES PP  100  40   64  P      15. DATA COMMUNICATION          PP   100  40   40  P
 06. DIGITAL LABORATORY            TW    50  20   45  P C    16. PROCESSOR INTERFACING LABORATORY TW  25  10   20  P
 07. DIGITAL LABORATORY            PR    50  20   27  P C    17. PROCESSOR INTERFACING LABORATORY OR  50  20   38  P
 08. PROGRAMMING LABORATORY        TW    50  20   46  P C    18. DATA STRUCTURES AND FILES LAB  TW   25  10   20  P
 09. PROGRAMMING LABORATORY        PR    50  20   38  P C    19. DATA STRUCTURES AND FILES LAB  PR   50  20   40  P
 10. COMMUNICATION AND LANGUAGE LAB. TW  50  20   47  P C    20. OBJECT ORIENTED PROGRAMMING LAB TW  50  20   42  P
                                                            21. OBJECT ORIENTED PROGRAMMING LAB PR  50  20   38  P
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GRAND TOTAL = 846/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

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 S80058534   DABARE DEEP RAJESH              RASHMI             , 71200825L    , S80058534 ,   PICT     , S80058534
 01. DISCRETE STRUCTURES          PP   100  40   75  P C    11. ENG MATHS III              PP   100  40   40  P
 02. COMPUTER ORGANIZATION         PP   100  40   50  P C    12. COMPUTER GRAPHICS          PP   100  40   40  P
 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100  40   42  P C   13. PROCESSOR ARCHITECTURE & INTER. PP 100  40   48  P
 04. FUNDAMENTAL OF DATA STRUCTURES PP  100  40   53  P C    14. DATA STRUCTURES AND FILES    PP   100  40   51  P
 05. HUMANITIES AND SOCIAL SCIENCES PP  100  40   52  P C    15. DATA COMMUNICATION          PP   100  40   64  P
 06. DIGITAL LABORATORY            TW    50  20   35  P C    16. PROCESSOR INTERFACING LABORATORY TW  25  10   15  P
 07. DIGITAL LABORATORY            PR    50  20   37  P C    17. PROCESSOR INTERFACING LABORATORY OR  50  20   27  P
 08. PROGRAMMING LABORATORY        TW    50  20   32  P C    18. DATA STRUCTURES AND FILES LAB  TW   25  10   16  P
 09. PROGRAMMING LABORATORY        PR    50  20   32  P C    19. DATA STRUCTURES AND FILES LAB  PR   50  20   30  P
 10. COMMUNICATION AND LANGUAGE LAB. TW  50  20   40  P C    20. OBJECT ORIENTED PROGRAMMING LAB TW  50  20   27  P
                                                            21. OBJECT ORIENTED PROGRAMMING LAB PR  50  20   37  P
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GRAND TOTAL = 843/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

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 S80058535   DARAK SHREYA TEJRAJ             JYOTI              , 71200827G    , S80058535 ,   PICT     , S80058535
 01. DISCRETE STRUCTURES          PP   100  40   71  P C    11. ENG MATHS III              PP   100  40   40  P
 02. COMPUTER ORGANIZATION         PP   100  40   67  P C    12. COMPUTER GRAPHICS          PP   100  40   55  P
 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100  40   67  P C   13. PROCESSOR ARCHITECTURE & INTER. PP 100  40   56  P
 04. FUNDAMENTAL OF DATA STRUCTURES PP  100  40   60  P C    14. DATA STRUCTURES AND FILES    PP   100  40   46  P
 05. HUMANITIES AND SOCIAL SCIENCES PP  100  40   51  P C    15. DATA COMMUNICATION          PP   100  40   55  P
 06. DIGITAL LABORATORY            TW    50  20   38  P C    16. PROCESSOR INTERFACING LABORATORY TW  25  10   20  P
 07. DIGITAL LABORATORY            PR    50  20   30  P C    17. PROCESSOR INTERFACING LABORATORY OR  50  20   32  P
 08. PROGRAMMING LABORATORY        TW    50  20   32  P C    18. DATA STRUCTURES AND FILES LAB  TW   25  10   20  P
 09. PROGRAMMING LABORATORY        PR    50  20   35  P C    19. DATA STRUCTURES AND FILES LAB  PR   50  20   38  P
 10. COMMUNICATION AND LANGUAGE LAB. TW  50  20   39  P C    20. OBJECT ORIENTED PROGRAMMING LAB TW  50  20   45  P
                                                            21. OBJECT ORIENTED PROGRAMMING LAB PR  50  20   36  P
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GRAND TOTAL = 933/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.
       OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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S80058536   DEOGHATKAR BRAMHESH NAMDEO   MAYALI   , 71200829C   , S80058536 ,   PICT   , S80058536
01. DISCRETE STRUCTURES   PP   100 40   64 P C   11. ENG MATHS III   PP   100 40   40 P
02. COMPUTER ORGANIZATION   PP   100 40   57 P C   12. COMPUTER GRAPHICS   PP   100 40   44 P
03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 40   49 P C   13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 40 P
04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40   51 P C   14. DATA STRUCTURES AND FILES   PP   100 40   54 P
05. HUMANITIES AND SOCIAL SCIENCES PP 100 40   53 P C   15. DATA COMMUNICATION   PP   100 40   51 P
06. DIGITAL LABORATORY   TW   50 20   34 P C   16. PROCESSOR INTERFACING LABORATORY TW 25 10   14 P
07. DIGITAL LABORATORY   PR   50 20   33 P C   17. PROCESSOR INTERFACING LABORATORY OR 50 20   09 F
08. PROGRAMMING LABORATORY   TW   50 20   29 P C   18. DATA STRUCTURES AND FILES LAB   TW   25 10   15 P
09. PROGRAMMING LABORATORY   PR   50 20   27 P C   19. DATA STRUCTURES AND FILES LAB   PR   50 20   42 P
10. COMMUNICATION AND LANGUAGE LAB. TW 50 20   34 P C   20. OBJECT ORIENTED PROGRAMMING LAB TW   50 20   30 P
                                                           21. OBJECT ORIENTED PROGRAMMING LAB PR   50 20   43 P
GRAND TOTAL = 813/1500, RESULT: FAILS A.T.K.T.
ORDN. 1 MARKS :
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S80058538   DESHMUKH MONIKA JAYANT   JYOTI   , 71200831E   , S80058538 ,   PICT   , S80058538
01. DISCRETE STRUCTURES   PP   100 40   79 P C   11. ENG MATHS III   PP   100 40   59 P
02. COMPUTER ORGANIZATION   PP   100 40   69 P C   12. COMPUTER GRAPHICS   PP   100 40   59 P
03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 40   71 P C   13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 64 P
04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40   60 P C   14. DATA STRUCTURES AND FILES   PP   100 40   55 P
05. HUMANITIES AND SOCIAL SCIENCES PP 100 40   68 P C   15. DATA COMMUNICATION   PP   100 40   66 P
06. DIGITAL LABORATORY   TW   50 20   43 P C   16. PROCESSOR INTERFACING LABORATORY TW 25 10   20 P
07. DIGITAL LABORATORY   PR   50 20   34 P C   17. PROCESSOR INTERFACING LABORATORY OR 50 20   40 P
08. PROGRAMMING LABORATORY   TW   50 20   42 P C   18. DATA STRUCTURES AND FILES LAB   TW   25 10   23 P
09. PROGRAMMING LABORATORY   PR   50 20   40 P C   19. DATA STRUCTURES AND FILES LAB   PR   50 20   43 P
10. COMMUNICATION AND LANGUAGE LAB. TW 50 20   43 P C   20. OBJECT ORIENTED PROGRAMMING LAB TW   50 20   44 P
                                                           21. OBJECT ORIENTED PROGRAMMING LAB PR   50 20   37 P
GRAND TOTAL = 1059/1500, RESULT: FIRST CLASS WITH DISTINCTION
ORDN. 1 MARKS :
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S80058539   DEVASHISH SINGH   SUSHMA   , 71200836F   , S80058539 ,   PICT   , S80058539
01. DISCRETE STRUCTURES   PP   100 40   77 P C   11. ENG MATHS III   PP   100 40   61 P
02. COMPUTER ORGANIZATION   PP   100 40   65 P C   12. COMPUTER GRAPHICS   PP   100 40   54 P
03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 40   56 P C   13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 65 P
04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40   55 P C   14. DATA STRUCTURES AND FILES   PP   100 40   59 P
05. HUMANITIES AND SOCIAL SCIENCES PP 100 40   59 P C   15. DATA COMMUNICATION   PP   100 40   64 P
06. DIGITAL LABORATORY   TW   50 20   31 P C   16. PROCESSOR INTERFACING LABORATORY TW 25 10   15 P
07. DIGITAL LABORATORY   PR   50 20   38 P C   17. PROCESSOR INTERFACING LABORATORY OR 50 20   37 P
08. PROGRAMMING LABORATORY   TW   50 20   28 P C   18. DATA STRUCTURES AND FILES LAB   TW   25 10   15 P
09. PROGRAMMING LABORATORY   PR   50 20   40 P C   19. DATA STRUCTURES AND FILES LAB   PR   50 20   42 P
10. COMMUNICATION AND LANGUAGE LAB. TW 50 20   36 P C   20. OBJECT ORIENTED PROGRAMMING LAB TW   50 20   33 P
                                                           21. OBJECT ORIENTED PROGRAMMING LAB PR   50 20   32 P
GRAND TOTAL = 962/1500, RESULT: FIRST CLASS
ORDN. 1 MARKS :
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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.

      OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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S80058540   DHAMANE AKANKSHA ARUN   KANCHAN   , 71200838B   , S80058540 ,   PICT   , S80058540

01. DISCRETE STRUCTURES	PP	100	40	77	P C	11. ENG MATHS III	PP	100	40	62	P
02. COMPUTER ORGANIZATION	PP	100	40	48	P C	12. COMPUTER GRAPHICS	PP	100	40	52	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	68	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	62	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	60	P C	14. DATA STRUCTURES AND FILES	PP	100	40	59	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	54	P C	15. DATA COMMUNICATION	PP	100	40	60	P
06. DIGITAL LABORATORY	TW	50	20	43	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	17	P
07. DIGITAL LABORATORY	PR	50	20	32	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	30	P
08. PROGRAMMING LABORATORY	TW	50	20	40	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	22	P
09. PROGRAMMING LABORATORY	PR	50	20	42	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	41	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	45	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	40	P
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	40	P

GRAND TOTAL = 994/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :
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S80058541   DISALE GITA BIBHISHAN   RADHA   , 71200845E   , S80058541 ,   PICT   , S80058541

01. DISCRETE STRUCTURES	PP	100	40	63	P C	11. ENG MATHS III	PP	100	40	43	P
02. COMPUTER ORGANIZATION	PP	100	40	57	P C	12. COMPUTER GRAPHICS	PP	100	40	43	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	51	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	40	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	48	P C	14. DATA STRUCTURES AND FILES	PP	100	40	46	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	58	P C	15. DATA COMMUNICATION	PP	100	40	52	P
06. DIGITAL LABORATORY	TW	50	20	40	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	14	P
07. DIGITAL LABORATORY	PR	50	20	36	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	07	F
08. PROGRAMMING LABORATORY	TW	50	20	37	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	17	P
09. PROGRAMMING LABORATORY	PR	50	20	39	P	19. DATA STRUCTURES AND FILES LAB	PR	50	20	35	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	44	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	36	P
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	32	P

GRAND TOTAL = 838/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :
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S80058542   DIXIT PRANAV SUDHIR   SHUBHANGI   , 71200846C   , S80058542 ,   PICT   , S80058542

01. DISCRETE STRUCTURES	PP	100	40	63	P C	11. ENG MATHS III	PP	100	40	65	P
02. COMPUTER ORGANIZATION	PP	100	40	60	P C	12. COMPUTER GRAPHICS	PP	100	40	58	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	68	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	48	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	60	P C	14. DATA STRUCTURES AND FILES	PP	100	40	62	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	51	P C	15. DATA COMMUNICATION	PP	100	40	61	P
06. DIGITAL LABORATORY	TW	50	20	40	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	17	P
07. DIGITAL LABORATORY	PR	50	20	45	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	28	P
08. PROGRAMMING LABORATORY	TW	50	20	42	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	21	P
09. PROGRAMMING LABORATORY	PR	50	20	40	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	43	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	44	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	42	P
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	44	P

GRAND TOTAL = 1002/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :
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      NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.
            OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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    S80058543   DURGE PRAPTI ANIL   BABITA   , 71200850M   , S80058543 ,   PICT   , S80058543
    01. DISCRETE STRUCTURES   PP   100 40   47 P C   11. ENG MATHS III   PP   100 40   50 P
    02. COMPUTER ORGANIZATION   PP   100 40   43 P C   12. COMPUTER GRAPHICS   PP   100 40   47 P
    03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 40   44 P C   13. PROCESSOR ARCHITECTURE & INTER. PP 100 40   40 P
    04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40   65 P C   14. DATA STRUCTURES AND FILES   PP   100 40   43 P
    05. HUMANITIES AND SOCIAL SCIENCES PP 100 40   59 P C   15. DATA COMMUNICATION   PP   100 40   43 P
    06. DIGITAL LABORATORY   TW   50 20   40 P C   16. PROCESSOR INTERFACING LABORATORY TW 25 10   18 P
    07. DIGITAL LABORATORY   PR   50 20   20 P C   17. PROCESSOR INTERFACING LABORATORY OR 50 20   22 P
    08. PROGRAMMING LABORATORY   TW   50 20   38 P C   18. DATA STRUCTURES AND FILES LAB TW   25 10   22 P
    09. PROGRAMMING LABORATORY   PR   50 20   35 P C   19. DATA STRUCTURES AND FILES LAB PR   50 20   32 P
    10. COMMUNICATION AND LANGUAGE LAB. TW 50 20   44 P C   20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20   41 P
                                                                 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20   35 P
   GRAND TOTAL = 828/1500, RESULT: HIGHER SECOND CLASS
   ORDN. 1 MARKS :
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    S80058544   GAIKWAD PRINCE VIJAYKUMAR   PRABHA   , 71200853F   , S80058544 ,   PICT   , S80058544
    01. DISCRETE STRUCTURES   PP   100 40   53 P C   11. ENG MATHS III   PP   100 40   40 P
    02. COMPUTER ORGANIZATION   PP   100 40   42 P C   12. COMPUTER GRAPHICS   PP   100 40   44 P
    03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 40   52 P C   13. PROCESSOR ARCHITECTURE & INTER. PP 100 40   27 F
    04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40   49 P C   14. DATA STRUCTURES AND FILES   PP   100 40   46 P
    05. HUMANITIES AND SOCIAL SCIENCES PP 100 40   43 P C   15. DATA COMMUNICATION   PP   100 40   56 P
    06. DIGITAL LABORATORY   TW   50 20   35 P C   16. PROCESSOR INTERFACING LABORATORY TW 25 10   15 P
    07. DIGITAL LABORATORY   PR   50 20   35 P C   17. PROCESSOR INTERFACING LABORATORY OR 50 20   08 F
    08. PROGRAMMING LABORATORY   TW   50 20   35 P C   18. DATA STRUCTURES AND FILES LAB TW   25 10   13 P
    09. PROGRAMMING LABORATORY   PR   50 20   38 P C   19. DATA STRUCTURES AND FILES LAB PR   50 20   35 P
    10. COMMUNICATION AND LANGUAGE LAB. TW 50 20   38 P C   20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20   29 P
                                                                 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20   37 P
   GRAND TOTAL = 770/1500, RESULT: FAILS A.T.K.T.
   ORDN. 1 MARKS :
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    S80058545   GAIKWAD ROHAN KIRAN   LATA   , 71200854D   , S80058545 ,   PICT   , S80058545
    01. DISCRETE STRUCTURES   PP   100 40   40 P C   11. ENG MATHS III   PP   100 40   40 P
    02. COMPUTER ORGANIZATION   PP   100 40   44 P C   12. COMPUTER GRAPHICS   PP   100 40   44 P
    03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 40   45 P   13. PROCESSOR ARCHITECTURE & INTER. PP 100 40   19 F
    04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40   40 P C   14. DATA STRUCTURES AND FILES   PP   100 40   40 P
    05. HUMANITIES AND SOCIAL SCIENCES PP 100 40   50 P C   15. DATA COMMUNICATION   PP   100 40   51 P
    06. DIGITAL LABORATORY   TW   50 20   21 P C   16. PROCESSOR INTERFACING LABORATORY TW 25 10   10 P
    07. DIGITAL LABORATORY   PR   50 20   21 P C   17. PROCESSOR INTERFACING LABORATORY OR 50 20   AA F
    08. PROGRAMMING LABORATORY   TW   50 20   22 P C   18. DATA STRUCTURES AND FILES LAB TW   25 10   10 P
    09. PROGRAMMING LABORATORY   PR   50 20   AA F   19. DATA STRUCTURES AND FILES LAB PR   50 20   07 F
    10. COMMUNICATION AND LANGUAGE LAB. TW 50 20   20 P C   20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20   22 P
                                                                 21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20   AA F
   GRAND TOTAL = 546/1500, RESULT: FAILS
   ORDN. 1 MARKS :
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    NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.
          OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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    S80058546   GAME AJAY BALASAHEB   ANITA   , 71200855B   , S80058546 ,   PICT   , S80058546
    01. DISCRETE STRUCTURES   PP   100 40   80 P C   11. ENG MATHS III   PP   100 40   82 P
    02. COMPUTER ORGANIZATION   PP   100 40   76 P C   12. COMPUTER GRAPHICS   PP   100 40   65 P
    03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 40   68 P C   13. PROCESSOR ARCHITECTURE & INTER. PP   100 40   67 P
    04. FUNDAMENTAL OF DATA STRUCTURES PP   100 40   61 P C   14. DATA STRUCTURES AND FILES   PP   100 40   63 P
    05. HUMANITIES AND SOCIAL SCIENCES PP   100 40   64 P C   15. DATA COMMUNICATION   PP   100 40   73 P
    06. DIGITAL LABORATORY   TW   50 20   44 P C   16. PROCESSOR INTERFACING LABORATORY TW   25 10   19 P
    07. DIGITAL LABORATORY   PR   50 20   27 P C   17. PROCESSOR INTERFACING LABORATORY OR   50 20   27 P
    08. PROGRAMMING LABORATORY   TW   50 20   45 P C   18. DATA STRUCTURES AND FILES LAB   TW   25 10   23 P
    09. PROGRAMMING LABORATORY   PR   50 20   32 P C   19. DATA STRUCTURES AND FILES LAB   PR   50 20   39 P
    10. COMMUNICATION AND LANGUAGE LAB. TW   50 20   46 P C   20. OBJECT ORIENTED PROGRAMMING LAB TW   50 20   45 P
                                                                21. OBJECT ORIENTED PROGRAMMING LAB PR   50 20   43 P
    GRAND TOTAL = 1089/1500, RESULT: FIRST CLASS WITH DISTINCTION
    ORDN. 1 MARKS :
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    S80058547   GANDHI KAMLESH ISHWARLAL   SUVARNA   , 71200856L   , S80058547 ,   PICT   , S80058547
    01. DISCRETE STRUCTURES   PP   100 40   48 P C   11. ENG MATHS III   PP   100 40   64 P
    02. COMPUTER ORGANIZATION   PP   100 40   67 P C   12. COMPUTER GRAPHICS   PP   100 40   47 P
    03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 40   59 P C   13. PROCESSOR ARCHITECTURE & INTER. PP   100 40   60 P
    04. FUNDAMENTAL OF DATA STRUCTURES PP   100 40   53 P C   14. DATA STRUCTURES AND FILES   PP   100 40   45 P
    05. HUMANITIES AND SOCIAL SCIENCES PP   100 40   46 P C   15. DATA COMMUNICATION   PP   100 40   64 P
    06. DIGITAL LABORATORY   TW   50 20   43 P C   16. PROCESSOR INTERFACING LABORATORY TW   25 10   21 P
    07. DIGITAL LABORATORY   PR   50 20   40 P C   17. PROCESSOR INTERFACING LABORATORY OR   50 20   30 P
    08. PROGRAMMING LABORATORY   TW   50 20   44 P C   18. DATA STRUCTURES AND FILES LAB   TW   25 10   22 P
    09. PROGRAMMING LABORATORY   PR   50 20   36 P C   19. DATA STRUCTURES AND FILES LAB   PR   50 20   37 P
    10. COMMUNICATION AND LANGUAGE LAB. TW   50 20   42 P C   20. OBJECT ORIENTED PROGRAMMING LAB TW   50 20   45 P
                                                                21. OBJECT ORIENTED PROGRAMMING LAB PR   50 20   41 P
    GRAND TOTAL = 954/1500, RESULT: FIRST CLASS
    ORDN. 1 MARKS :
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    S80058548   GAVALI ANKITA SUNIL   SUDHA   , 71200860J   , S80058548 ,   PICT   , S80058548
    01. DISCRETE STRUCTURES   PP   100 40   49 P C   11. ENG MATHS III   PP   100 40   40 P
    02. COMPUTER ORGANIZATION   PP   100 40   60 P C   12. COMPUTER GRAPHICS   PP   100 40   52 P
    03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 40   54 P   13. PROCESSOR ARCHITECTURE & INTER. PP   100 40   40 P
    04. FUNDAMENTAL OF DATA STRUCTURES PP   100 40   44 P C   14. DATA STRUCTURES AND FILES   PP   100 40   54 P
    05. HUMANITIES AND SOCIAL SCIENCES PP   100 40   52 P C   15. DATA COMMUNICATION   PP   100 40   52 P
    06. DIGITAL LABORATORY   TW   50 20   39 P C   16. PROCESSOR INTERFACING LABORATORY TW   25 10   17 P
    07. DIGITAL LABORATORY   PR   50 20   39 P   17. PROCESSOR INTERFACING LABORATORY OR   50 20   08 F
    08. PROGRAMMING LABORATORY   TW   50 20   37 P C   18. DATA STRUCTURES AND FILES LAB   TW   25 10   17 P
    09. PROGRAMMING LABORATORY   PR   50 20   40 P C   19. DATA STRUCTURES AND FILES LAB   PR   50 20   38 P
    10. COMMUNICATION AND LANGUAGE LAB. TW   50 20   38 P C   20. OBJECT ORIENTED PROGRAMMING LAB TW   50 20   30 P
                                                                21. OBJECT ORIENTED PROGRAMMING LAB PR   50 20   35 P
    GRAND TOTAL = 835/1500, RESULT: FAILS A.T.K.T.
    ORDN. 1 MARKS :
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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.
      OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S80058549   GHATE ABHIJIT SURESHRAO              KUNDA              , 71200864M    , S80058549 ,  PICT     , S80058549
01. DISCRETE STRUCTURES           PP   100  40   53  P C    11. ENG MATHS III                   PP   100  40   44  P
02. COMPUTER ORGANIZATION         PP   100  40   50  P C    12. COMPUTER GRAPHICS               PP   100  40   46  P
03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100  40   40  P C    13. PROCESSOR ARCHITECTURE & INTER.  PP   100  40   40  P
04. FUNDAMENTAL OF DATA STRUCTURES PP  100  40   46  P C    14. DATA STRUCTURES AND FILES       PP   100  40   51  P
05. HUMANITIES AND SOCIAL SCIENCES PP  100  40   55  P C    15. DATA COMMUNICATION              PP   100  40   40  P
06. DIGITAL LABORATORY            TW    50  20   32  P C    16. PROCESSOR INTERFACING LABORATORY TW   25  10   15  P
07. DIGITAL LABORATORY            PR    50  20   37  P C    17. PROCESSOR INTERFACING LABORATORY OR   50  20   20  P
08. PROGRAMMING LABORATORY        TW    50  20   34  P C    18. DATA STRUCTURES AND FILES LAB   TW    25  10   15  P
09. PROGRAMMING LABORATORY        PR    50  20   34  P C    19. DATA STRUCTURES AND FILES LAB   PR    50  20   32  P
10. COMMUNICATION AND LANGUAGE LAB. TW  50  20   36  P C    20. OBJECT ORIENTED PROGRAMMING LAB TW    50  20   28  P
                                                           21. OBJECT ORIENTED PROGRAMMING LAB PR    50  20   38  P
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GRAND TOTAL = 786/1500, RESULT: SECOND CLASS

ORDN. 1 MARKS :

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S80058550   GHODKE NIRANJAN DATTATRAYA           PRADNYA            , 71200865K    , S80058550 ,  PICT     , S80058550
01. DISCRETE STRUCTURES           PP   100  40   82  P C    11. ENG MATHS III                   PP   100  40   65  P
02. COMPUTER ORGANIZATION         PP   100  40   78  P C    12. COMPUTER GRAPHICS               PP   100  40   63  P
03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100  40   65  P C    13. PROCESSOR ARCHITECTURE & INTER.  PP   100  40   62  P
04. FUNDAMENTAL OF DATA STRUCTURES PP  100  40   66  P C    14. DATA STRUCTURES AND FILES       PP   100  40   57  P
05. HUMANITIES AND SOCIAL SCIENCES PP  100  40   61  P C    15. DATA COMMUNICATION              PP   100  40   67  P
06. DIGITAL LABORATORY            TW    50  20   43  P C    16. PROCESSOR INTERFACING LABORATORY TW   25  10   20  P
07. DIGITAL LABORATORY            PR    50  20   41  P C    17. PROCESSOR INTERFACING LABORATORY OR   50  20   32  P
08. PROGRAMMING LABORATORY        TW    50  20   44  P C    18. DATA STRUCTURES AND FILES LAB   TW    25  10   23  P
09. PROGRAMMING LABORATORY        PR    50  20   43  P C    19. DATA STRUCTURES AND FILES LAB   PR    50  20   40  P
10. COMMUNICATION AND LANGUAGE LAB. TW  50  20   44  P C    20. OBJECT ORIENTED PROGRAMMING LAB TW    50  20   44  P
                                                           21. OBJECT ORIENTED PROGRAMMING LAB PR    50  20   32  P
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GRAND TOTAL = 1072/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

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S80058551   GOHOKAR KIRAN DADAJI                 REKHA              , 71350921L    , S80058551 ,  PICT     , S80058551
01. DISCRETE STRUCTURES           PP   100  40   42  P C    11. ENG MATHS III                   PP   100  40   25  F
02. COMPUTER ORGANIZATION         PP   100  40   54  P C    12. COMPUTER GRAPHICS               PP   100  40   40  P
03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100  40   54  P C    13. PROCESSOR ARCHITECTURE & INTER.  PP   100  40   41  P
04. FUNDAMENTAL OF DATA STRUCTURES PP  100  40   44  P C    14. DATA STRUCTURES AND FILES       PP   100  40   50  P
05. HUMANITIES AND SOCIAL SCIENCES PP  100  40   61  P C    15. DATA COMMUNICATION              PP   100  40   59  P
06. DIGITAL LABORATORY            TW    50  20   33  P C    16. PROCESSOR INTERFACING LABORATORY TW   25  10   16  P
07. DIGITAL LABORATORY            PR    50  20   23  P C    17. PROCESSOR INTERFACING LABORATORY OR   50  20   09  F
08. PROGRAMMING LABORATORY        TW    50  20   35  P C    18. DATA STRUCTURES AND FILES LAB   TW    25  10   18  P
09. PROGRAMMING LABORATORY        PR    50  20   35  P      19. DATA STRUCTURES AND FILES LAB   PR    50  20   22  P
10. COMMUNICATION AND LANGUAGE LAB. TW  50  20   37  P C    20. OBJECT ORIENTED PROGRAMMING LAB TW    50  20   38  P
                                                           21. OBJECT ORIENTED PROGRAMMING LAB PR    50  20   25  P
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GRAND TOTAL = 761/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

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   NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.
         OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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   S80058552   GOKHALE VALLARI SHRIKANT   PADMAJA   , 71200867F   , S80058552 ,   PICT   , S80058552
   01. DISCRETE STRUCTURES   PP   100 40   65 P C   11. ENG MATHS III   PP   100 40   62 P
   02. COMPUTER ORGANIZATION   PP   100 40   72 P C   12. COMPUTER GRAPHICS   PP   100 40   53 P
   03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 40   70 P C   13. PROCESSOR ARCHITECTURE & INTER. PP   100 40   63 P
   04. FUNDAMENTAL OF DATA STRUCTURES PP   100 40   54 P C   14. DATA STRUCTURES AND FILES   PP   100 40   65 P
   05. HUMANITIES AND SOCIAL SCIENCES PP   100 40   56 P C   15. DATA COMMUNICATION   PP   100 40   63 P
   06. DIGITAL LABORATORY   TW   50 20   43 P C   16. PROCESSOR INTERFACING LABORATORY TW   25 10   20 P
   07. DIGITAL LABORATORY   PR   50 20   34 P C   17. PROCESSOR INTERFACING LABORATORY OR   50 20   38 P
   08. PROGRAMMING LABORATORY   TW   50 20   42 P C   18. DATA STRUCTURES AND FILES LAB   TW   25 10   21 P
   09. PROGRAMMING LABORATORY   PR   50 20   36 P C   19. DATA STRUCTURES AND FILES LAB   PR   50 20   40 P
   10. COMMUNICATION AND LANGUAGE LAB. TW   50 20   45 P C   20. OBJECT ORIENTED PROGRAMMING LAB TW   50 20   44 P
                                                                 21. OBJECT ORIENTED PROGRAMMING LAB PR   50 20   38 P
   GRAND TOTAL = 1024/1500, RESULT: FIRST CLASS WITH DISTINCTION
   ORDN. 1 MARKS :
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   S80058553   GOLAPKAR PIYUSH ANIL   APARNA   , 71200868D   , S80058553 ,   PICT   , S80058553
   01. DISCRETE STRUCTURES   PP   100 40   68 P C   11. ENG MATHS III   PP   100 40   40 P
   02. COMPUTER ORGANIZATION   PP   100 40   51 P C   12. COMPUTER GRAPHICS   PP   100 40   55 P
   03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 40   53 P C   13. PROCESSOR ARCHITECTURE & INTER. PP   100 40   58 P
   04. FUNDAMENTAL OF DATA STRUCTURES PP   100 40   49 P C   14. DATA STRUCTURES AND FILES   PP   100 40   44 P
   05. HUMANITIES AND SOCIAL SCIENCES PP   100 40   56 P C   15. DATA COMMUNICATION   PP   100 40   60 P
   06. DIGITAL LABORATORY   TW   50 20   38 P C   16. PROCESSOR INTERFACING LABORATORY TW   25 10   16 P
   07. DIGITAL LABORATORY   PR   50 20   34 P C   17. PROCESSOR INTERFACING LABORATORY OR   50 20   22 P
   08. PROGRAMMING LABORATORY   TW   50 20   41 P C   18. DATA STRUCTURES AND FILES LAB   TW   25 10   20 P
   09. PROGRAMMING LABORATORY   PR   50 20   35 P C   19. DATA STRUCTURES AND FILES LAB   PR   50 20   38 P
   10. COMMUNICATION AND LANGUAGE LAB. TW   50 20   42 P C   20. OBJECT ORIENTED PROGRAMMING LAB TW   50 20   42 P
                                                                 21. OBJECT ORIENTED PROGRAMMING LAB PR   50 20   41 P
   GRAND TOTAL = 903/1500, RESULT: FIRST CLASS
   ORDN. 1 MARKS :
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   S80058554   GOSAVI KALYANI RAJGIR   KAMINI   , 71200869B   , S80058554 ,   PICT   , S80058554
   01. DISCRETE STRUCTURES   PP   100 40   60 P C   11. ENG MATHS III   PP   100 40   40 P
   02. COMPUTER ORGANIZATION   PP   100 40   58 P C   12. COMPUTER GRAPHICS   PP   100 40   60 P
   03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 40   52 P C   13. PROCESSOR ARCHITECTURE & INTER. PP   100 40   62 P
   04. FUNDAMENTAL OF DATA STRUCTURES PP   100 40   48 P C   14. DATA STRUCTURES AND FILES   PP   100 40   63 P
   05. HUMANITIES AND SOCIAL SCIENCES PP   100 40   69 P C   15. DATA COMMUNICATION   PP   100 40   68 P
   06. DIGITAL LABORATORY   TW   50 20   42 P C   16. PROCESSOR INTERFACING LABORATORY TW   25 10   19 P
   07. DIGITAL LABORATORY   PR   50 20   27 P C   17. PROCESSOR INTERFACING LABORATORY OR   50 20   30 P
   08. PROGRAMMING LABORATORY   TW   50 20   38 P C   18. DATA STRUCTURES AND FILES LAB   TW   25 10   22 P
   09. PROGRAMMING LABORATORY   PR   50 20   27 P C   19. DATA STRUCTURES AND FILES LAB   PR   50 20   42 P
   10. COMMUNICATION AND LANGUAGE LAB. TW   50 20   43 P C   20. OBJECT ORIENTED PROGRAMMING LAB TW   50 20   40 P
                                                                 21. OBJECT ORIENTED PROGRAMMING LAB PR   50 20   28 P
   GRAND TOTAL = 938/1500, RESULT: FIRST CLASS
   ORDN. 1 MARKS :
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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.
       OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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 S80058555   GUPTA SHRADHA RAVINDRA   AMITA   , 71200873L   , S80058555 ,   PICT   , S80058555
 01. DISCRETE STRUCTURES   PP   100 40   73 P C   11. ENG MATHS III   PP   100 40   74 P
 02. COMPUTER ORGANIZATION   PP   100 40   60 P C   12. COMPUTER GRAPHICS   PP   100 40   50 P
 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 40   77 P C   13. PROCESSOR ARCHITECTURE & INTER. PP 100 40   61 P
 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40   56 P C   14. DATA STRUCTURES AND FILES   PP   100 40   61 P
 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40   68 P C   15. DATA COMMUNICATION   PP   100 40   69 P
 06. DIGITAL LABORATORY   TW   50 20   45 P C   16. PROCESSOR INTERFACING LABORATORY TW 25 10   24 P
 07. DIGITAL LABORATORY   PR   50 20   39 P C   17. PROCESSOR INTERFACING LABORATORY OR   50 20   40 P
 08. PROGRAMMING LABORATORY   TW   50 20   47 P C   18. DATA STRUCTURES AND FILES LAB   TW   25 10   23 P
 09. PROGRAMMING LABORATORY   PR   50 20   40 P C   19. DATA STRUCTURES AND FILES LAB   PR   50 20   44 P
 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20   47 P C   20. OBJECT ORIENTED PROGRAMMING LAB TW   50 20   46 P
                                                             21. OBJECT ORIENTED PROGRAMMING LAB PR   50 20   42 P
GRAND TOTAL = 1086/1500, RESULT: FIRST CLASS WITH DISTINCTION
ORDN. 1 MARKS :
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 S80058556   HADKE ANUP BANDA   MANIK   , 71200875G   , S80058556 ,   PICT   , S80058556
 01. DISCRETE STRUCTURES   PP   100 40   42 P C   11. ENG MATHS III   PP   100 40   61 P
 02. COMPUTER ORGANIZATION   PP   100 40   50 P C   12. COMPUTER GRAPHICS   PP   100 40   41 P
 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 40   45 P C   13. PROCESSOR ARCHITECTURE & INTER. PP 100 40   40 P
 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40   46 P C   14. DATA STRUCTURES AND FILES   PP   100 40   45 P
 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40   58 P C   15. DATA COMMUNICATION   PP   100 40   59 P
 06. DIGITAL LABORATORY   TW   50 20   38 P C   16. PROCESSOR INTERFACING LABORATORY TW 25 10   17 P
 07. DIGITAL LABORATORY   PR   50 20   37 P   17. PROCESSOR INTERFACING LABORATORY OR   50 20   20 P
 08. PROGRAMMING LABORATORY   TW   50 20   36 P C   18. DATA STRUCTURES AND FILES LAB   TW   25 10   18 P
 09. PROGRAMMING LABORATORY   PR   50 20   27 P C   19. DATA STRUCTURES AND FILES LAB   PR   50 20   37 P
 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20   37 P C   20. OBJECT ORIENTED PROGRAMMING LAB TW   50 20   25 P
                                                             21. OBJECT ORIENTED PROGRAMMING LAB PR   50 20   15# P
GRAND TOTAL = 794/1500, RESULT: SECOND CLASS   # [O.4]
ORDN. 1 MARKS :
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 S80058557   HARSH BAHETI   ANJANA   , 71200877C   , S80058557 ,   PICT   , S80058557
 01. DISCRETE STRUCTURES   PP   100 40   55 P C   11. ENG MATHS III   PP   100 40   24 F
 02. COMPUTER ORGANIZATION   PP   100 40   51 P C   12. COMPUTER GRAPHICS   PP   100 40   45 P
 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 40   40 P C   13. PROCESSOR ARCHITECTURE & INTER. PP 100 40   29 F
 04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40   53 P C   14. DATA STRUCTURES AND FILES   PP   100 40   43 P
 05. HUMANITIES AND SOCIAL SCIENCES PP 100 40   55 P C   15. DATA COMMUNICATION   PP   100 40   59 P
 06. DIGITAL LABORATORY   TW   50 20   30 P C   16. PROCESSOR INTERFACING LABORATORY TW 25 10   10 P
 07. DIGITAL LABORATORY   PR   50 20   20 P C   17. PROCESSOR INTERFACING LABORATORY OR   50 20   30 P
 08. PROGRAMMING LABORATORY   TW   50 20   31 P C   18. DATA STRUCTURES AND FILES LAB   TW   25 10   15 P
 09. PROGRAMMING LABORATORY   PR   50 20   26 P C   19. DATA STRUCTURES AND FILES LAB   PR   50 20   10 F
 10. COMMUNICATION AND LANGUAGE LAB. TW 50 20   37 P C   20. OBJECT ORIENTED PROGRAMMING LAB TW   50 20   28 P
                                                             21. OBJECT ORIENTED PROGRAMMING LAB PR   50 20   33 P
GRAND TOTAL = 724/1500, RESULT: FAILS A.T.K.T.
ORDN. 1 MARKS :
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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.
           OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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  S80058558   HATOLKAR ABHIRAM PRASANNA   MANJUSHA   , 71200878M   , S80058558 ,   PICT   , S80058558
  01. DISCRETE STRUCTURES   PP   100 40   52 P C   11. ENG MATHS III   PP   100 40   46 P
  02. COMPUTER ORGANIZATION   PP   100 40   57 P C   12. COMPUTER GRAPHICS   PP   100 40   54 P
  03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 40   64 P C   13. PROCESSOR ARCHITECTURE & INTER. PP   100 40   53 P
  04. FUNDAMENTAL OF DATA STRUCTURES PP   100 40   53 P C   14. DATA STRUCTURES AND FILES   PP   100 40   56 P
  05. HUMANITIES AND SOCIAL SCIENCES PP   100 40   63 P C   15. DATA COMMUNICATION   PP   100 40   68 P
  06. DIGITAL LABORATORY   TW   50 20   39 P C   16. PROCESSOR INTERFACING LABORATORY TW   25 10   18 P
  07. DIGITAL LABORATORY   PR   50 20   32 P C   17. PROCESSOR INTERFACING LABORATORY OR   50 20   32 P
  08. PROGRAMMING LABORATORY   TW   50 20   42 P C   18. DATA STRUCTURES AND FILES LAB   TW   25 10   23 P
  09. PROGRAMMING LABORATORY   PR   50 20   45 P C   19. DATA STRUCTURES AND FILES LAB   PR   50 20   43 P
  10. COMMUNICATION AND LANGUAGE LAB. TW   50 20   42 P C   20. OBJECT ORIENTED PROGRAMMING LAB TW   50 20   44 P
                                                               21. OBJECT ORIENTED PROGRAMMING LAB PR   50 20   38 P
GRAND TOTAL = 964/1500, RESULT: FIRST CLASS
ORDN. 1 MARKS :

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  S80058559   IRNAK SWAPNIL ASHOK   MEENA   , 71350922J   , S80058559 ,   PICT   , S80058559
  01. DISCRETE STRUCTURES   PP   100 40   20 F   11. ENG MATHS III   PP   100 40   13 F
  02. COMPUTER ORGANIZATION   PP   100 40   48 P C   12. COMPUTER GRAPHICS   PP   100 40   29 F
  03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 40   43 P C   13. PROCESSOR ARCHITECTURE & INTER. PP   100 40   24 F
  04. FUNDAMENTAL OF DATA STRUCTURES PP   100 40   40 P C   14. DATA STRUCTURES AND FILES   PP   100 40   40 P
  05. HUMANITIES AND SOCIAL SCIENCES PP   100 40   49 P C   15. DATA COMMUNICATION   PP   100 40   48 P
  06. DIGITAL LABORATORY   TW   50 20   25 P C   16. PROCESSOR INTERFACING LABORATORY TW   25 10   14 P
  07. DIGITAL LABORATORY   PR   50 20   22 P   17. PROCESSOR INTERFACING LABORATORY OR   50 20   07 F
  08. PROGRAMMING LABORATORY   TW   50 20   32 P C   18. DATA STRUCTURES AND FILES LAB   TW   25 10   16 P
  09. PROGRAMMING LABORATORY   PR   50 20   22 P C   19. DATA STRUCTURES AND FILES LAB   PR   50 20   25 P
  10. COMMUNICATION AND LANGUAGE LAB. TW   50 20   33 P C   20. OBJECT ORIENTED PROGRAMMING LAB TW   50 20   39 P
                                                               21. OBJECT ORIENTED PROGRAMMING LAB PR   50 20   28 P
GRAND TOTAL = 617/1500, RESULT: FAILS
ORDN. 1 MARKS :

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  S80058560   IYER PRIYANKA SUBRAMANIAN   USHA   , 71200884F   , S80058560 ,   PICT   , S80058560
  01. DISCRETE STRUCTURES   PP   100 40   50 P C   11. ENG MATHS III   PP   100 40   62 P
  02. COMPUTER ORGANIZATION   PP   100 40   61 P C   12. COMPUTER GRAPHICS   PP   100 40   61 P
  03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 40   62 P C   13. PROCESSOR ARCHITECTURE & INTER. PP   100 40   48 P
  04. FUNDAMENTAL OF DATA STRUCTURES PP   100 40   69 P C   14. DATA STRUCTURES AND FILES   PP   100 40   65 P
  05. HUMANITIES AND SOCIAL SCIENCES PP   100 40   60 P C   15. DATA COMMUNICATION   PP   100 40   64 P
  06. DIGITAL LABORATORY   TW   50 20   42 P C   16. PROCESSOR INTERFACING LABORATORY TW   25 10   16 P
  07. DIGITAL LABORATORY   PR   50 20   35 P C   17. PROCESSOR INTERFACING LABORATORY OR   50 20   40 P
  08. PROGRAMMING LABORATORY   TW   50 20   41 P C   18. DATA STRUCTURES AND FILES LAB   TW   25 10   19 P
  09. PROGRAMMING LABORATORY   PR   50 20   32 P C   19. DATA STRUCTURES AND FILES LAB   PR   50 20   35 P
  10. COMMUNICATION AND LANGUAGE LAB. TW   50 20   43 P C   20. OBJECT ORIENTED PROGRAMMING LAB TW   50 20   42 P
                                                               21. OBJECT ORIENTED PROGRAMMING LAB PR   50 20   41 P
GRAND TOTAL = 988/1500, RESULT: FIRST CLASS
ORDN. 1 MARKS :

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
          OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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S80058562   JAGTAP MANISH KISHOR   BHARTI   , 71350923G   , S80058562 ,   PICT   , S80058562
01. DISCRETE STRUCTURES   PP   100 40   48 P C   11. ENG MATHS III   PP   100 40   40 P
02. COMPUTER ORGANIZATION   PP   100 40   58 P C   12. COMPUTER GRAPHICS   PP   100 40   54 P
03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 40   52 P C   13. PROCESSOR ARCHITECTURE & INTER. PP   100 40   62 P
04. FUNDAMENTAL OF DATA STRUCTURES PP   100 40   41 P C   14. DATA STRUCTURES AND FILES   PP   100 40   56 P
05. HUMANITIES AND SOCIAL SCIENCES PP   100 40   63 P C   15. DATA COMMUNICATION   PP   100 40   67 P
06. DIGITAL LABORATORY   TW   50 20   42 P C   16. PROCESSOR INTERFACING LABORATORY TW   25 10   20 P
07. DIGITAL LABORATORY   PR   50 20   26 P C   17. PROCESSOR INTERFACING LABORATORY OR   50 20   35 P
08. PROGRAMMING LABORATORY   TW   50 20   42 P C   18. DATA STRUCTURES AND FILES LAB   TW   25 10   24 P
09. PROGRAMMING LABORATORY   PR   50 20   38 P C   19. DATA STRUCTURES AND FILES LAB   PR   50 20   45 P
10. COMMUNICATION AND LANGUAGE LAB. TW   50 20   43 P C   20. OBJECT ORIENTED PROGRAMMING LAB TW   50 20   42 P
                                                           21. OBJECT ORIENTED PROGRAMMING LAB PR   50 20   36 P
GRAND TOTAL = 934/1500, RESULT: FIRST CLASS
ORDN. 1 MARKS :
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S80058563   JALAMKAR ABHILASH RAJENDRA   SUNITA   , 71200889G   , S80058563 ,   PICT   , S80058563
01. DISCRETE STRUCTURES   PP   100 40   75 P C   11. ENG MATHS III   PP   100 40   40 P
02. COMPUTER ORGANIZATION   PP   100 40   59 P C   12. COMPUTER GRAPHICS   PP   100 40   40 P
03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 40   70 P C   13. PROCESSOR ARCHITECTURE & INTER. PP   100 40   AA F
04. FUNDAMENTAL OF DATA STRUCTURES PP   100 40   51 P C   14. DATA STRUCTURES AND FILES   PP   100 40   AA F
05. HUMANITIES AND SOCIAL SCIENCES PP   100 40   49 P C   15. DATA COMMUNICATION   PP   100 40   AA F
06. DIGITAL LABORATORY   TW   50 20   38 P C   16. PROCESSOR INTERFACING LABORATORY TW   25 10   16 P
07. DIGITAL LABORATORY   PR   50 20   37 P C   17. PROCESSOR INTERFACING LABORATORY OR   50 20   38 P
08. PROGRAMMING LABORATORY   TW   50 20   42 P C   18. DATA STRUCTURES AND FILES LAB   TW   25 10   21 P
09. PROGRAMMING LABORATORY   PR   50 20   40 P C   19. DATA STRUCTURES AND FILES LAB   PR   50 20   39 P
10. COMMUNICATION AND LANGUAGE LAB. TW   50 20   41 P C   20. OBJECT ORIENTED PROGRAMMING LAB TW   50 20   44 P
                                                           21. OBJECT ORIENTED PROGRAMMING LAB PR   50 20   40 P
GRAND TOTAL = 780/1500, RESULT: FAILS A.T.K.T.
ORDN. 1 MARKS :
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S80058564   JARHAD PRATIBHA BHAGAWAN   MANDA   , 71200890L   , S80058564 ,   PICT   , S80058564
01. DISCRETE STRUCTURES   PP   100 40   58 P C   11. ENG MATHS III   PP   100 40   49 P
02. COMPUTER ORGANIZATION   PP   100 40   40 P C   12. COMPUTER GRAPHICS   PP   100 40   44 P
03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 40   52 P C   13. PROCESSOR ARCHITECTURE & INTER. PP   100 40   46 P
04. FUNDAMENTAL OF DATA STRUCTURES PP   100 40   47 P C   14. DATA STRUCTURES AND FILES   PP   100 40   45 P
05. HUMANITIES AND SOCIAL SCIENCES PP   100 40   51 P C   15. DATA COMMUNICATION   PP   100 40   48 P
06. DIGITAL LABORATORY   TW   50 20   40 P C   16. PROCESSOR INTERFACING LABORATORY TW   25 10   17 P
07. DIGITAL LABORATORY   PR   50 20   23 P C   17. PROCESSOR INTERFACING LABORATORY OR   50 20   35 P
08. PROGRAMMING LABORATORY   TW   50 20   42 P C   18. DATA STRUCTURES AND FILES LAB   TW   25 10   20 P
09. PROGRAMMING LABORATORY   PR   50 20   40 P C   19. DATA STRUCTURES AND FILES LAB   PR   50 20   44 P
10. COMMUNICATION AND LANGUAGE LAB. TW   50 20   43 P C   20. OBJECT ORIENTED PROGRAMMING LAB TW   50 20   34 P
                                                           21. OBJECT ORIENTED PROGRAMMING LAB PR   50 20   30 P
GRAND TOTAL = 848/1500, RESULT: HIGHER SECOND CLASS
ORDN. 1 MARKS :
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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
      OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S80058565   JATAB NIKHIL HARIDWARILAL   YASHODA   , 71350924E   , S80058565 , PICT   , S80058565

01. DISCRETE STRUCTURES	PP	100	40	47	P C		11. ENG MATHS III	PP	100	40	40	P
02. COMPUTER ORGANIZATION	PP	100	40	86	P C		12. COMPUTER GRAPHICS	PP	100	40	61	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	53	P C		13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	63	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	59	P C		14. DATA STRUCTURES AND FILES	PP	100	40	60	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	60	P C		15. DATA COMMUNICATION	PP	100	40	71	P
06. DIGITAL LABORATORY	TW	50	20	35	P C		16. PROCESSOR INTERFACING LABORATORY	TW	25	10	22	P
07. DIGITAL LABORATORY	PR	50	20	34	P		17. PROCESSOR INTERFACING LABORATORY	OR	50	20	40	P
08. PROGRAMMING LABORATORY	TW	50	20	34	P C		18. DATA STRUCTURES AND FILES LAB	TW	25	10	24	P
09. PROGRAMMING LABORATORY	PR	50	20	35	P C		19. DATA STRUCTURES AND FILES LAB	PR	50	20	32	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	35	P C		20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	38	P
							21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	35	P

GRAND TOTAL = 964/1500, RESULT: FIRST CLASS
ORDN. 1 MARKS :

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S80058566   JEEVJYOT SUKHDEV SINGH CHHABDA   SANGEET KAUR CHHABDA , 71350925C   , S80058566 , PICT   , S80058566

01. DISCRETE STRUCTURES	PP	100	40	40	P C		11. ENG MATHS III	PP	100	40	40	P
02. COMPUTER ORGANIZATION	PP	100	40	59	P C		12. COMPUTER GRAPHICS	PP	100	40	52	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	53	P C		13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	53	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	58	P C		14. DATA STRUCTURES AND FILES	PP	100	40	54	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	57	P C		15. DATA COMMUNICATION	PP	100	40	63	P
06. DIGITAL LABORATORY	TW	50	20	39	P C		16. PROCESSOR INTERFACING LABORATORY	TW	25	10	21	P
07. DIGITAL LABORATORY	PR	50	20	46	P C		17. PROCESSOR INTERFACING LABORATORY	OR	50	20	43	P
08. PROGRAMMING LABORATORY	TW	50	20	43	P C		18. DATA STRUCTURES AND FILES LAB	TW	25	10	23	P
09. PROGRAMMING LABORATORY	PR	50	20	22	P C		19. DATA STRUCTURES AND FILES LAB	PR	50	20	32	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	45	P C		20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	44	P
							21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	34	P

GRAND TOTAL = 921/1500, RESULT: FIRST CLASS
ORDN. 1 MARKS :

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S80058567   JOSHI SWANAND ARVIND   ARUNA   , 71200896K   , S80058567 , PICT   , S80058567

01. DISCRETE STRUCTURES	PP	100	40	74	P C		11. ENG MATHS III	PP	100	40	70	P
02. COMPUTER ORGANIZATION	PP	100	40	69	P C		12. COMPUTER GRAPHICS	PP	100	40	48	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	72	P C		13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	46	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	63	P C		14. DATA STRUCTURES AND FILES	PP	100	40	53	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	49	P C		15. DATA COMMUNICATION	PP	100	40	67	P
06. DIGITAL LABORATORY	TW	50	20	37	P C		16. PROCESSOR INTERFACING LABORATORY	TW	25	10	17	P
07. DIGITAL LABORATORY	PR	50	20	34	P C		17. PROCESSOR INTERFACING LABORATORY	OR	50	20	40	P
08. PROGRAMMING LABORATORY	TW	50	20	42	P C		18. DATA STRUCTURES AND FILES LAB	TW	25	10	23	P
09. PROGRAMMING LABORATORY	PR	50	20	42	P C		19. DATA STRUCTURES AND FILES LAB	PR	50	20	44	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	41	P C		20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	40	P
							21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	39	P

GRAND TOTAL = 1010/1500, RESULT: FIRST CLASS WITH DISTINCTION
ORDN. 1 MARKS :

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.
       OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S80058568   KADAM YASHASWINI VISHNU   ALKA   , 71200899D   , S80058568 ,   PICT   , S80058568

No	Subject	Type	Max	Min	Obt	P	C	No	Subject	Type	Max	Min	Obt	P
01.	DISCRETE STRUCTURES	PP	100	40	75	P	C	11.	ENG MATHS III	PP	100	40	40	P
02.	COMPUTER ORGANIZATION	PP	100	40	64	P	C	12.	COMPUTER GRAPHICS	PP	100	40	56	P
03.	DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	67	P	C	13.	PROCESSOR ARCHITECTURE & INTER.	PP	100	40	58	P
04.	FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	61	P	C	14.	DATA STRUCTURES AND FILES	PP	100	40	50	P
05.	HUMANITIES AND SOCIAL SCIENCES	PP	100	40	65	P	C	15.	DATA COMMUNICATION	PP	100	40	65	P
06.	DIGITAL LABORATORY	TW	50	20	33	P	C	16.	PROCESSOR INTERFACING LABORATORY	TW	25	10	12	P
07.	DIGITAL LABORATORY	PR	50	20	31	P	C	17.	PROCESSOR INTERFACING LABORATORY	OR	50	20	30	P
08.	PROGRAMMING LABORATORY	TW	50	20	35	P	C	18.	DATA STRUCTURES AND FILES LAB	TW	25	10	20	P
09.	PROGRAMMING LABORATORY	PR	50	20	39	P	C	19.	DATA STRUCTURES AND FILES LAB	PR	50	20	44	P
10.	COMMUNICATION AND LANGUAGE LAB.	TW	50	20	37	P	C	20.	OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	32	P
								21.	OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	32	P

GRAND TOTAL = 946/1500, RESULT: FIRST CLASS
ORDN. 1 MARKS :

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S80058569   KADU SHRADDHA AJIT   MANJIRI   , 71200900M   , S80058569 ,   PICT   , S80058569

No	Subject	Type	Max	Min	Obt	P	C	No	Subject	Type	Max	Min	Obt	P
01.	DISCRETE STRUCTURES	PP	100	40	84	P	C	11.	ENG MATHS III	PP	100	40	67	P
02.	COMPUTER ORGANIZATION	PP	100	40	74	P	C	12.	COMPUTER GRAPHICS	PP	100	40	62	P
03.	DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	79	P	C	13.	PROCESSOR ARCHITECTURE & INTER.	PP	100	40	68	P
04.	FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	61	P	C	14.	DATA STRUCTURES AND FILES	PP	100	40	65	P
05.	HUMANITIES AND SOCIAL SCIENCES	PP	100	40	69	P	C	15.	DATA COMMUNICATION	PP	100	40	69	P
06.	DIGITAL LABORATORY	TW	50	20	47	P	C	16.	PROCESSOR INTERFACING LABORATORY	TW	25	10	21	P
07.	DIGITAL LABORATORY	PR	50	20	37	P	C	17.	PROCESSOR INTERFACING LABORATORY	OR	50	20	42	P
08.	PROGRAMMING LABORATORY	TW	50	20	47	P	C	18.	DATA STRUCTURES AND FILES LAB	TW	25	10	24	P
09.	PROGRAMMING LABORATORY	PR	50	20	43	P	C	19.	DATA STRUCTURES AND FILES LAB	PR	50	20	41	P
10.	COMMUNICATION AND LANGUAGE LAB.	TW	50	20	47	P	C	20.	OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	45	P
								21.	OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	40	P

GRAND TOTAL = 1132/1500, RESULT: FIRST CLASS WITH DISTINCTION
ORDN. 1 MARKS :

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S80058570   KAMBLE NIKITA MAHADEV   VANDANA   , 71200907J   , S80058570 ,   PICT   , S80058570

No	Subject	Type	Max	Min	Obt	P	C	No	Subject	Type	Max	Min	Obt	P
01.	DISCRETE STRUCTURES	PP	100	40	46	P	C	11.	ENG MATHS III	PP	100	40	58	P
02.	COMPUTER ORGANIZATION	PP	100	40	65	P	C	12.	COMPUTER GRAPHICS	PP	100	40	51	P
03.	DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	61	P	C	13.	PROCESSOR ARCHITECTURE & INTER.	PP	100	40	54	P
04.	FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	57	P	C	14.	DATA STRUCTURES AND FILES	PP	100	40	47	P
05.	HUMANITIES AND SOCIAL SCIENCES	PP	100	40	67	P	C	15.	DATA COMMUNICATION	PP	100	40	66	P
06.	DIGITAL LABORATORY	TW	50	20	44	P	C	16.	PROCESSOR INTERFACING LABORATORY	TW	25	10	21	P
07.	DIGITAL LABORATORY	PR	50	20	25	P	C	17.	PROCESSOR INTERFACING LABORATORY	OR	50	20	33	P
08.	PROGRAMMING LABORATORY	TW	50	20	45	P	C	18.	DATA STRUCTURES AND FILES LAB	TW	25	10	23	P
09.	PROGRAMMING LABORATORY	PR	50	20	35	P	C	19.	DATA STRUCTURES AND FILES LAB	PR	50	20	32	P
10.	COMMUNICATION AND LANGUAGE LAB.	TW	50	20	46	P	C	20.	OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	42	P
								21.	OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	37	P

GRAND TOTAL = 955/1500, RESULT: FIRST CLASS
ORDN. 1 MARKS :

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   NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.
           OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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   S80058571   KATKE AMRUTA SURESH   MANISHA   , 71200916H   , S80058571 ,   PICT   , S80058571
   01. DISCRETE STRUCTURES   PP   100 40   65 P C   11. ENG MATHS III   PP   100 40   70 P
   02. COMPUTER ORGANIZATION   PP   100 40   72 P C   12. COMPUTER GRAPHICS   PP   100 40   40 P
   03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 40   61 P C   13. PROCESSOR ARCHITECTURE & INTER. PP   100 40   40 P
   04. FUNDAMENTAL OF DATA STRUCTURES PP   100 40   62 P C   14. DATA STRUCTURES AND FILES   PP   100 40   47 P
   05. HUMANITIES AND SOCIAL SCIENCES PP   100 40   58 P C   15. DATA COMMUNICATION   PP   100 40   55 P
   06. DIGITAL LABORATORY   TW   50 20   42 P C   16. PROCESSOR INTERFACING LABORATORY TW   25 10   19 P
   07. DIGITAL LABORATORY   PR   50 20   35 P C   17. PROCESSOR INTERFACING LABORATORY OR   50 20   20 P
   08. PROGRAMMING LABORATORY   TW   50 20   44 P C   18. DATA STRUCTURES AND FILES LAB   TW   25 10   21 P
   09. PROGRAMMING LABORATORY   PR   50 20   40 P C   19. DATA STRUCTURES AND FILES LAB   PR   50 20   39 P
   10. COMMUNICATION AND LANGUAGE LAB. TW   50 20   45 P C   20. OBJECT ORIENTED PROGRAMMING LAB TW   50 20   42 P
                                                                   21. OBJECT ORIENTED PROGRAMMING LAB PR   50 20   32 P
   GRAND TOTAL = 949/1500, RESULT: FIRST CLASS
   ORDN. 1 MARKS :
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   S80058572   KAVADE PRIYANKA DHANANJAY   RAJASHREE   , 71200917F   , S80058572 ,   PICT   , S80058572
   01. DISCRETE STRUCTURES   PP   100 40   68 P C   11. ENG MATHS III   PP   100 40   69 P
   02. COMPUTER ORGANIZATION   PP   100 40   64 P C   12. COMPUTER GRAPHICS   PP   100 40   50 P
   03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 40   74 P C   13. PROCESSOR ARCHITECTURE & INTER. PP   100 40   45 P
   04. FUNDAMENTAL OF DATA STRUCTURES PP   100 40   53 P C   14. DATA STRUCTURES AND FILES   PP   100 40   63 P
   05. HUMANITIES AND SOCIAL SCIENCES PP   100 40   57 P C   15. DATA COMMUNICATION   PP   100 40   59 P
   06. DIGITAL LABORATORY   TW   50 20   43 P C   16. PROCESSOR INTERFACING LABORATORY TW   25 10   20 P
   07. DIGITAL LABORATORY   PR   50 20   35 P C   17. PROCESSOR INTERFACING LABORATORY OR   50 20   34 P
   08. PROGRAMMING LABORATORY   TW   50 20   44 P C   18. DATA STRUCTURES AND FILES LAB   TW   25 10   22 P
   09. PROGRAMMING LABORATORY   PR   50 20   35 P C   19. DATA STRUCTURES AND FILES LAB   PR   50 20   42 P
   10. COMMUNICATION AND LANGUAGE LAB. TW   50 20   44 P C   20. OBJECT ORIENTED PROGRAMMING LAB TW   50 20   43 P
                                                                   21. OBJECT ORIENTED PROGRAMMING LAB PR   50 20   33 P
   GRAND TOTAL = 997/1500, RESULT: FIRST CLASS WITH DISTINCTION
   ORDN. 1 MARKS :
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   S80058573   KAVITKAR JAYASHREE GOPAL   ANITA   , 71200918D   , S80058573 ,   PICT   , S80058573
   01. DISCRETE STRUCTURES   PP   100 40   77 P C   11. ENG MATHS III   PP   100 40   72 P
   02. COMPUTER ORGANIZATION   PP   100 40   63 P C   12. COMPUTER GRAPHICS   PP   100 40   47 P
   03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 40   66 P C   13. PROCESSOR ARCHITECTURE & INTER. PP   100 40   54 P
   04. FUNDAMENTAL OF DATA STRUCTURES PP   100 40   61 P C   14. DATA STRUCTURES AND FILES   PP   100 40   52 P
   05. HUMANITIES AND SOCIAL SCIENCES PP   100 40   59 P C   15. DATA COMMUNICATION   PP   100 40   54 P
   06. DIGITAL LABORATORY   TW   50 20   40 P C   16. PROCESSOR INTERFACING LABORATORY TW   25 10   18 P
   07. DIGITAL LABORATORY   PR   50 20   33 P C   17. PROCESSOR INTERFACING LABORATORY OR   50 20   34 P
   08. PROGRAMMING LABORATORY   TW   50 20   43 P C   18. DATA STRUCTURES AND FILES LAB   TW   25 10   23 P
   09. PROGRAMMING LABORATORY   PR   50 20   44 P C   19. DATA STRUCTURES AND FILES LAB   PR   50 20   43 P
   10. COMMUNICATION AND LANGUAGE LAB. TW   50 20   43 P C   20. OBJECT ORIENTED PROGRAMMING LAB TW   50 20   44 P
                                                                   21. OBJECT ORIENTED PROGRAMMING LAB PR   50 20   41 P
   GRAND TOTAL = 1011/1500, RESULT: FIRST CLASS WITH DISTINCTION
   ORDN. 1 MARKS :
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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
       OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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S80058574   KHABIYA ASHWINI ASHOK   SUREKHA   , 71350926M   , S80058574 ,   PICT   , S80058574

01. DISCRETE STRUCTURES	PP	100	40	61	P C	11. ENG MATHS III	PP	100	40	40	P	
02. COMPUTER ORGANIZATION	PP	100	40	81	P C	12. COMPUTER GRAPHICS	PP	100	40	63	P	
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	77	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	66	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	62	P C	14. DATA STRUCTURES AND FILES	PP	100	40	53	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	62	P C	15. DATA COMMUNICATION	PP	100	40	63	P
06. DIGITAL LABORATORY	TW	50	20	37	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	21	P
07. DIGITAL LABORATORY	PR	50	20	30	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	32	P
08. PROGRAMMING LABORATORY	TW	50	20	41	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	23	P
09. PROGRAMMING LABORATORY	PR	50	20	40	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	35	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	42	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	42	P
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	34	P

GRAND TOTAL = 1005/1500, RESULT: FIRST CLASS WITH DISTINCTION
ORDN. 1 MARKS :
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S80058575   KHAN SHAHBAZ AHMAD   FATIMA   , 71200922B   , S80058575 ,   PICT   , S80058575

01. DISCRETE STRUCTURES	PP	100	40	57	P C	11. ENG MATHS III	PP	100	40	40	P	
02. COMPUTER ORGANIZATION	PP	100	40	53	P	12. COMPUTER GRAPHICS	PP	100	40	47	P	
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	51	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	56	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	54	P C	14. DATA STRUCTURES AND FILES	PP	100	40	52	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	40	P C	15. DATA COMMUNICATION	PP	100	40	50	P
06. DIGITAL LABORATORY	TW	50	20	39	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	17	P
07. DIGITAL LABORATORY	PR	50	20	38	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	33	P
08. PROGRAMMING LABORATORY	TW	50	20	41	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	19	P
09. PROGRAMMING LABORATORY	PR	50	20	30	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	37	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	41	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	39	P
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	37	P

GRAND TOTAL = 871/1500, RESULT: HIGHER SECOND CLASS
ORDN. 1 MARKS :
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S80058576   KHANDELWAL ABHISHEK GOURISHANKAR   SANTOSHI   , 71200924J   , S80058576 ,   PICT   , S80058576

01. DISCRETE STRUCTURES	PP	100	40	77	P C	11. ENG MATHS III	PP	100	40	74	P	
02. COMPUTER ORGANIZATION	PP	100	40	75	P C	12. COMPUTER GRAPHICS	PP	100	40	61	P	
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	80	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	64	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	67	P C	14. DATA STRUCTURES AND FILES	PP	100	40	61	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	69	P C	15. DATA COMMUNICATION	PP	100	40	65	P
06. DIGITAL LABORATORY	TW	50	20	46	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	20	P
07. DIGITAL LABORATORY	PR	50	20	38	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	40	P
08. PROGRAMMING LABORATORY	TW	50	20	45	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	24	P
09. PROGRAMMING LABORATORY	PR	50	20	47	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	47	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	44	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	44	P
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	36	P

GRAND TOTAL = 1124/1500, RESULT: FIRST CLASS WITH DISTINCTION
ORDN. 1 MARKS :
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    NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.
          OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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    S80058577   KHANDELWAL ARPIT NARENDRA   MAMTA   , 71200925G   , S80058577 ,   PICT   , S80058577
    01. DISCRETE STRUCTURES   PP   100 40   71 P C   11. ENG MATHS III   PP   100 40   40 P
    02. COMPUTER ORGANIZATION   PP   100 40   55 P C   12. COMPUTER GRAPHICS   PP   100 40   40 P
    03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 40   58 P C   13. PROCESSOR ARCHITECTURE & INTER. PP 100 40   40 P
    04. FUNDAMENTAL OF DATA STRUCTURES PP   100 40   50 P C   14. DATA STRUCTURES AND FILES   PP   100 40   40 P
    05. HUMANITIES AND SOCIAL SCIENCES PP   100 40   40 P C   15. DATA COMMUNICATION   PP   100 40   40 P
    06. DIGITAL LABORATORY   TW   50 20   36 P C   16. PROCESSOR INTERFACING LABORATORY TW   25 10   12 P
    07. DIGITAL LABORATORY   PR   50 20   36 P C   17. PROCESSOR INTERFACING LABORATORY OR   50 20   22 P
    08. PROGRAMMING LABORATORY   TW   50 20   37 P C   18. DATA STRUCTURES AND FILES LAB TW   25 10   15 P
    09. PROGRAMMING LABORATORY   PR   50 20   39 P C   19. DATA STRUCTURES AND FILES LAB PR   50 20   42 P
    10. COMMUNICATION AND LANGUAGE LAB. TW   50 20   41 P C   20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20   30 P
                                                                     21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20   38 P
    GRAND TOTAL = 822+03/1500, RESULT: HIGHER SECOND CLASS [O.2]
    ORDN. 1 MARKS :
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    S80058578   KHAPLI TEJAS RAVINDRA   RANJANA   , 71200926E   , S80058578 ,   PICT   , S80058578
    01. DISCRETE STRUCTURES   PP   100 40   74 P C   11. ENG MATHS III   PP   100 40   45 P
    02. COMPUTER ORGANIZATION   PP   100 40   55 P C   12. COMPUTER GRAPHICS   PP   100 40   49 P
    03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 40   74 P C   13. PROCESSOR ARCHITECTURE & INTER. PP 100 40   52 P
    04. FUNDAMENTAL OF DATA STRUCTURES PP   100 40   57 P C   14. DATA STRUCTURES AND FILES   PP   100 40   50 P
    05. HUMANITIES AND SOCIAL SCIENCES PP   100 40   60 P C   15. DATA COMMUNICATION   PP   100 40   60 P
    06. DIGITAL LABORATORY   TW   50 20   42 P C   16. PROCESSOR INTERFACING LABORATORY TW   25 10   20 P
    07. DIGITAL LABORATORY   PR   50 20   38 P C   17. PROCESSOR INTERFACING LABORATORY OR   50 20   20 P
    08. PROGRAMMING LABORATORY   TW   50 20   45 P C   18. DATA STRUCTURES AND FILES LAB TW   25 10   23 P
    09. PROGRAMMING LABORATORY   PR   50 20   45 P C   19. DATA STRUCTURES AND FILES LAB PR   50 20   40 P
    10. COMMUNICATION AND LANGUAGE LAB. TW   50 20   45 P C   20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20   44 P
                                                                     21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20   37 P
    GRAND TOTAL = 975/1500, RESULT: FIRST CLASS
    ORDN. 1 MARKS :
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    S80058579   KHARAT GANESH MAHADEO   SHARDHA   , 71045482B   , S80058579 ,   PICT   , S80058579
    01. DISCRETE STRUCTURES   PP   100 40   40 P C   11. ENG MATHS III   PP   100 40   40 P
    02. COMPUTER ORGANIZATION   PP   100 40   52 P C   12. COMPUTER GRAPHICS   PP   100 40   40 P
    03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 40   50 P C   13. PROCESSOR ARCHITECTURE & INTER. PP 100 40   44 P
    04. FUNDAMENTAL OF DATA STRUCTURES PP   100 40   46 P C   14. DATA STRUCTURES AND FILES   PP   100 40   40 P
    05. HUMANITIES AND SOCIAL SCIENCES PP   100 40   51 P C   15. DATA COMMUNICATION   PP   100 40   44 P
    06. DIGITAL LABORATORY   TW   50 20   39 P C   16. PROCESSOR INTERFACING LABORATORY TW   25 10   17 P
    07. DIGITAL LABORATORY   PR   50 20   22 P C   17. PROCESSOR INTERFACING LABORATORY OR   50 20   07 F
    08. PROGRAMMING LABORATORY   TW   50 20   41 P C   18. DATA STRUCTURES AND FILES LAB TW   25 10   21 P
    09. PROGRAMMING LABORATORY   PR   50 20   32 P C   19. DATA STRUCTURES AND FILES LAB PR   50 20   38 P
    10. COMMUNICATION AND LANGUAGE LAB. TW   50 20   44 P C   20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20   41 P
                                                                     21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20   33 P
    GRAND TOTAL = 782/1500, RESULT: FAILS A.T.K.T.
    ORDN. 1 MARKS :
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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.
      OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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S80058580   KHARCHE KAUSTUBH PRAMOD   NEHA   , 71200927C   , S80058580 ,   PICT   , S80058580
01. DISCRETE STRUCTURES   PP   100 40   73 P C   11. ENG MATHS III   PP   100 40   41 P
02. COMPUTER ORGANIZATION   PP   100 40   64 P C   12. COMPUTER GRAPHICS   PP   100 40   55 P
03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 40   65 P C   13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 65 P
04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40   58 P C   14. DATA STRUCTURES AND FILES   PP   100 40   50 P
05. HUMANITIES AND SOCIAL SCIENCES PP 100 40   62 P C   15. DATA COMMUNICATION   PP   100 40   68 P
06. DIGITAL LABORATORY   TW   50 20   41 P C   16. PROCESSOR INTERFACING LABORATORY TW 25 10   21 P
07. DIGITAL LABORATORY   PR   50 20   41 P C   17. PROCESSOR INTERFACING LABORATORY OR   50 20   39 P
08. PROGRAMMING LABORATORY   TW   50 20   44 P C   18. DATA STRUCTURES AND FILES LAB TW   25 10   22 P
09. PROGRAMMING LABORATORY   PR   50 20   42 P C   19. DATA STRUCTURES AND FILES LAB PR   50 20   44 P
10. COMMUNICATION AND LANGUAGE LAB. TW 50 20   45 P C   20. OBJECT ORIENTED PROGRAMMING LAB TW   50 20   44 P
                                                            21. OBJECT ORIENTED PROGRAMMING LAB PR   50 20   45 P
GRAND TOTAL = 1029/1500, RESULT: FIRST CLASS WITH DISTINCTION
ORDN. 1 MARKS :
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S80058581   KHEDKAR NITIN AJINATH   SUNITA   , 71200928M   , S80058581 ,   PICT   , S80058581
01. DISCRETE STRUCTURES   PP   100 40   48 P C   11. ENG MATHS III   PP   100 40   26 F
02. COMPUTER ORGANIZATION   PP   100 40   40 P C   12. COMPUTER GRAPHICS   PP   100 40   40 P
03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 40   53 P C   13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 41 P
04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40   43 P C   14. DATA STRUCTURES AND FILES   PP   100 40   43 P
05. HUMANITIES AND SOCIAL SCIENCES PP 100 40   57 P C   15. DATA COMMUNICATION   PP   100 40   60 P
06. DIGITAL LABORATORY   TW   50 20   37 P C   16. PROCESSOR INTERFACING LABORATORY TW 25 10   17 P
07. DIGITAL LABORATORY   PR   50 20   21 P C   17. PROCESSOR INTERFACING LABORATORY OR   50 20   20 P
08. PROGRAMMING LABORATORY   TW   50 20   36 P C   18. DATA STRUCTURES AND FILES LAB TW   25 10   20 P
09. PROGRAMMING LABORATORY   PR   50 20   32 P C   19. DATA STRUCTURES AND FILES LAB PR   50 20   33 P
10. COMMUNICATION AND LANGUAGE LAB. TW 50 20   40 P C   20. OBJECT ORIENTED PROGRAMMING LAB TW   50 20   37 P
                                                            21. OBJECT ORIENTED PROGRAMMING LAB PR   50 20   34 P
GRAND TOTAL = 778/1500, RESULT: FAILS A.T.K.T.
ORDN. 1 MARKS :
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S80058582   KOTHADIA RAMANI BHUPENDRA   ANAGHA   , 71200930C   , S80058582 ,   PICT   , S80058582
01. DISCRETE STRUCTURES   PP   100 40   55 P C   11. ENG MATHS III   PP   100 40   40 P
02. COMPUTER ORGANIZATION   PP   100 40   49 P C   12. COMPUTER GRAPHICS   PP   100 40   48 P
03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 40   56 P C   13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 42 P
04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40   60 P C   14. DATA STRUCTURES AND FILES   PP   100 40   43 P
05. HUMANITIES AND SOCIAL SCIENCES PP 100 40   65 P C   15. DATA COMMUNICATION   PP   100 40   43 P
06. DIGITAL LABORATORY   TW   50 20   40 P C   16. PROCESSOR INTERFACING LABORATORY TW 25 10   16 P
07. DIGITAL LABORATORY   PR   50 20   38 P C   17. PROCESSOR INTERFACING LABORATORY OR   50 20   20 P
08. PROGRAMMING LABORATORY   TW   50 20   41 P C   18. DATA STRUCTURES AND FILES LAB TW   25 10   21 P
09. PROGRAMMING LABORATORY   PR   50 20   34 P C   19. DATA STRUCTURES AND FILES LAB PR   50 20   41 P
10. COMMUNICATION AND LANGUAGE LAB. TW 50 20   42 P C   20. OBJECT ORIENTED PROGRAMMING LAB TW   50 20   35 P
                                                            21. OBJECT ORIENTED PROGRAMMING LAB PR   50 20   37 P
GRAND TOTAL = 866/1500, RESULT: HIGHER SECOND CLASS
ORDN. 1 MARKS :
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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
      OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S80058583   KULKARNI PALLAVI RAJENDRA   REKHA   , 71350927K   , S80058583 , PICT   , S80058583

01. DISCRETE STRUCTURES	PP	100	40	50	P C	11. ENG MATHS III		PP	100	40	40	P
02. COMPUTER ORGANIZATION	PP	100	40	67	P C	12. COMPUTER GRAPHICS		PP	100	40	54	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	62	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	55	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	59	P C	14. DATA STRUCTURES AND FILES		PP	100	40	43	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	68	P C	15. DATA COMMUNICATION		PP	100	40	63	P
06. DIGITAL LABORATORY	TW	50	20	36	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	19	P
07. DIGITAL LABORATORY	PR	50	20	32	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	27	P
08. PROGRAMMING LABORATORY	TW	50	20	37	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	20	P
09. PROGRAMMING LABORATORY	PR	50	20	36	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	40	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	39	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	43	P
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	36	P

GRAND TOTAL = 926/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

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S80058584   KULKARNI PRANIT KRISHNA   LATA   , 71200937L   , S80058584 , PICT   , S80058584

01. DISCRETE STRUCTURES	PP	100	40	51	P C	11. ENG MATHS III		PP	100	40	40	P
02. COMPUTER ORGANIZATION	PP	100	40	52	P C	12. COMPUTER GRAPHICS		PP	100	40	40	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	59	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	44	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	60	P C	14. DATA STRUCTURES AND FILES		PP	100	40	40	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	48	P C	15. DATA COMMUNICATION		PP	100	40	50	P
06. DIGITAL LABORATORY	TW	50	20	38	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	14	P
07. DIGITAL LABORATORY	PR	50	20	34	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	24	P
08. PROGRAMMING LABORATORY	TW	50	20	36	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	20	P
09. PROGRAMMING LABORATORY	PR	50	20	36	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	38	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	39	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	33	P
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	40	P

GRAND TOTAL = 836/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

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S80058585   KULKARNI TANMAY VIJAY   ALKA   , 71200938J   , S80058585 , PICT   , S80058585

01. DISCRETE STRUCTURES	PP	100	40	61	P C	11. ENG MATHS III		PP	100	40	62	P
02. COMPUTER ORGANIZATION	PP	100	40	54	P C	12. COMPUTER GRAPHICS		PP	100	40	51	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	59	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	63	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	49	P C	14. DATA STRUCTURES AND FILES		PP	100	40	59	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	59	P C	15. DATA COMMUNICATION		PP	100	40	64	P
06. DIGITAL LABORATORY	TW	50	20	30	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	14	P
07. DIGITAL LABORATORY	PR	50	20	37	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	28	P
08. PROGRAMMING LABORATORY	TW	50	20	31	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	18	P
09. PROGRAMMING LABORATORY	PR	50	20	30	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	42	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	36	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	32	P
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	31	P

GRAND TOTAL = 910/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.
      OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S80058586   KUMARGAURAV SINGH                  MAMATA              , 71200940L    , S80058586 ,   PICT     , S80058586
01. DISCRETE STRUCTURES            PP   100  40   69  P C    11. ENG MATHS III                   PP   100  40   41  P
02. COMPUTER ORGANIZATION          PP   100  40   57  P C    12. COMPUTER GRAPHICS               PP   100  40   40  P
03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100  40   63  P C    13. PROCESSOR ARCHITECTURE & INTER. PP   100  40   40  P
04. FUNDAMENTAL OF DATA STRUCTURES PP   100  40   52  P C    14. DATA STRUCTURES AND FILES       PP   100  40   53  P
05. HUMANITIES AND SOCIAL SCIENCES PP   100  40   56  P C    15. DATA COMMUNICATION              PP   100  40   47  P
06. DIGITAL LABORATORY             TW    50  20   46  P C    16. PROCESSOR INTERFACING LABORATORY TW   25  10   22  P
07. DIGITAL LABORATORY             PR    50  20   34  P C    17. PROCESSOR INTERFACING LABORATORY OR   50  20   35  P
08. PROGRAMMING LABORATORY         TW    50  20   43  P C    18. DATA STRUCTURES AND FILES LAB    TW   25  10   22  P
09. PROGRAMMING LABORATORY         PR    50  20   42  P C    19. DATA STRUCTURES AND FILES LAB    PR   50  20   42  P
10. COMMUNICATION AND LANGUAGE LAB. TW   50  20   48  P C    20. OBJECT ORIENTED PROGRAMMING LAB  TW   50  20   47  P
                                                            21. OBJECT ORIENTED PROGRAMMING LAB  PR   50  20   35  P
GRAND TOTAL =  934/1500, RESULT: FIRST CLASS
ORDN. 1 MARKS :
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S80058587   KUNJI RAHUL SRINIVASAN             USHA                , 71200941J    , S80058587 ,   PICT     , S80058587
01. DISCRETE STRUCTURES            PP   100  40   83  P C    11. ENG MATHS III                   PP   100  40   72  P
02. COMPUTER ORGANIZATION          PP   100  40   77  P C    12. COMPUTER GRAPHICS               PP   100  40   70  P
03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100  40   81  P C    13. PROCESSOR ARCHITECTURE & INTER. PP   100  40   76  P
04. FUNDAMENTAL OF DATA STRUCTURES PP   100  40   70  P C    14. DATA STRUCTURES AND FILES       PP   100  40   60  P
05. HUMANITIES AND SOCIAL SCIENCES PP   100  40   66  P C    15. DATA COMMUNICATION              PP   100  40   73  P
06. DIGITAL LABORATORY             TW    50  20   45  P C    16. PROCESSOR INTERFACING LABORATORY TW   25  10   22  P
07. DIGITAL LABORATORY             PR    50  20   42  P C    17. PROCESSOR INTERFACING LABORATORY OR   50  20   39  P
08. PROGRAMMING LABORATORY         TW    50  20   45  P C    18. DATA STRUCTURES AND FILES LAB    TW   25  10   24  P
09. PROGRAMMING LABORATORY         PR    50  20   45  P C    19. DATA STRUCTURES AND FILES LAB    PR   50  20   48  P
10. COMMUNICATION AND LANGUAGE LAB. TW   50  20   45  P C    20. OBJECT ORIENTED PROGRAMMING LAB  TW   50  20   45  P
                                                            21. OBJECT ORIENTED PROGRAMMING LAB  PR   50  20   43  P
GRAND TOTAL = 1171/1500, RESULT: FIRST CLASS WITH DISTINCTION
ORDN. 1 MARKS :
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S80058588   LUNKAD JAYESH PRAKASH              UJJWALA             , 71350928H    , S80058588 ,   PICT     , S80058588
01. DISCRETE STRUCTURES            PP   100  40   71  P C    11. ENG MATHS III                   PP   100  40   56  P
02. COMPUTER ORGANIZATION          PP   100  40   83  P C    12. COMPUTER GRAPHICS               PP   100  40   51  P
03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100  40   74  P C    13. PROCESSOR ARCHITECTURE & INTER. PP   100  40   73  P
04. FUNDAMENTAL OF DATA STRUCTURES PP   100  40   66  P C    14. DATA STRUCTURES AND FILES       PP   100  40   63  P
05. HUMANITIES AND SOCIAL SCIENCES PP   100  40   66  P C    15. DATA COMMUNICATION              PP   100  40   75  P
06. DIGITAL LABORATORY             TW    50  20   43  P C    16. PROCESSOR INTERFACING LABORATORY TW   25  10   22  P
07. DIGITAL LABORATORY             PR    50  20   45  P C    17. PROCESSOR INTERFACING LABORATORY OR   50  20   36  P
08. PROGRAMMING LABORATORY         TW    50  20   44  P C    18. DATA STRUCTURES AND FILES LAB    TW   25  10   24  P
09. PROGRAMMING LABORATORY         PR    50  20   43  P C    19. DATA STRUCTURES AND FILES LAB    PR   50  20   48  P
10. COMMUNICATION AND LANGUAGE LAB. TW   50  20   44  P C    20. OBJECT ORIENTED PROGRAMMING LAB  TW   50  20   48  P
                                                            21. OBJECT ORIENTED PROGRAMMING LAB  PR   50  20   44  P
GRAND TOTAL = 1119/1500, RESULT: FIRST CLASS WITH DISTINCTION
ORDN. 1 MARKS :
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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

          OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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 S80058589   MAHENDRA TANVEERSINGH TEJENDRASINGH      SURINDERKAUR          , 71200949D    , S80058589 ,   PICT     , S80058589
 01. DISCRETE STRUCTURES              PP   100  40   56  P C    11. ENG MATHS III                  PP   100  40   40  P
 02. COMPUTER ORGANIZATION            PP   100  40   50  P C    12. COMPUTER GRAPHICS              PP   100  40   53  P
 03. DIGITAL ELECTRONICS & LOGIC DESIGPP  100  40   57  P C    13. PROCESSOR ARCHITECTURE & INTER. PP   100  40   43  P
 04. FUNDAMENTAL OF DATA STRUCTURES   PP   100  40   49  P C    14. DATA STRUCTURES AND FILES      PP   100  40   40  P
 05. HUMANITIES AND SOCIAL SCIENCES   PP   100  40   52  P C    15. DATA COMMUNICATION             PP   100  40   53  P
 06. DIGITAL LABORATORY               TW    50  20   34  P C    16. PROCESSOR INTERFACING LABORATORY TW  25  10   16  P
 07. DIGITAL LABORATORY               PR    50  20   33  P C    17. PROCESSOR INTERFACING LABORATORY OR  50  20   36  P
 08. PROGRAMMING LABORATORY           TW    50  20   34  P C    18. DATA STRUCTURES AND FILES LAB   TW    25  10   18  P
 09. PROGRAMMING LABORATORY           PR    50  20   38  P C    19. DATA STRUCTURES AND FILES LAB   PR    50  20   42  P
 10. COMMUNICATION AND LANGUAGE LAB.  TW    50  20   38  P C    20. OBJECT ORIENTED PROGRAMMING LAB TW    50  20   35  P
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GRAND TOTAL = 859/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

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 S80058590   MANALI DESAI                            MONA                 , 71200952D    , S80058590 ,   PICT     , S80058590
 01. DISCRETE STRUCTURES              PP   100  40   63  P C    11. ENG MATHS III                  PP   100  40   40  P
 02. COMPUTER ORGANIZATION            PP   100  40   53  P C    12. COMPUTER GRAPHICS              PP   100  40   51  P
 03. DIGITAL ELECTRONICS & LOGIC DESIGPP  100  40   55  P C    13. PROCESSOR ARCHITECTURE & INTER. PP   100  40   44  P
 04. FUNDAMENTAL OF DATA STRUCTURES   PP   100  40   43  P C    14. DATA STRUCTURES AND FILES      PP   100  40   46  P
 05. HUMANITIES AND SOCIAL SCIENCES   PP   100  40   41  P C    15. DATA COMMUNICATION             PP   100  40   40  P
 06. DIGITAL LABORATORY               TW    50  20   33  P C    16. PROCESSOR INTERFACING LABORATORY TW  25  10   15  P
 07. DIGITAL LABORATORY               PR    50  20   31  P C    17. PROCESSOR INTERFACING LABORATORY OR  50  20   20  P
 08. PROGRAMMING LABORATORY           TW    50  20   36  P C    18. DATA STRUCTURES AND FILES LAB   TW    25  10   17  P
 09. PROGRAMMING LABORATORY           PR    50  20   45  P C    19. DATA STRUCTURES AND FILES LAB   PR    50  20   46  P
 10. COMMUNICATION AND LANGUAGE LAB.  TW    50  20   37  P C    20. OBJECT ORIENTED PROGRAMMING LAB TW    50  20   29  P
                                                                21. OBJECT ORIENTED PROGRAMMING LAB PR    50  20   46  P
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GRAND TOTAL = 831/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

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 S80058591   MANDGE SHIVSHANKAR SHIVKUMAR            USHA                 , 71200953B    , S80058591 ,   PICT     , S80058591
 01. DISCRETE STRUCTURES              PP   100  40   40  P C    11. ENG MATHS III                  PP   100  40   40  P
 02. COMPUTER ORGANIZATION            PP   100  40   47  P C    12. COMPUTER GRAPHICS              PP   100  40   44  P
 03. DIGITAL ELECTRONICS & LOGIC DESIGPP  100  40   48  P C    13. PROCESSOR ARCHITECTURE & INTER. PP   100  40   48  P
 04. FUNDAMENTAL OF DATA STRUCTURES   PP   100  40   40  P C    14. DATA STRUCTURES AND FILES      PP   100  40   43  P
 05. HUMANITIES AND SOCIAL SCIENCES   PP   100  40   49  P C    15. DATA COMMUNICATION             PP   100  40   56  P
 06. DIGITAL LABORATORY               TW    50  20   39  P C    16. PROCESSOR INTERFACING LABORATORY TW  25  10   20  P
 07. DIGITAL LABORATORY               PR    50  20   25  P C    17. PROCESSOR INTERFACING LABORATORY OR  50  20   37  P
 08. PROGRAMMING LABORATORY           TW    50  20   40  P C    18. DATA STRUCTURES AND FILES LAB   TW    25  10   20  P
 09. PROGRAMMING LABORATORY           PR    50  20   33  P C    19. DATA STRUCTURES AND FILES LAB   PR    50  20   40  P
 10. COMMUNICATION AND LANGUAGE LAB.  TW    50  20   42  P C    20. OBJECT ORIENTED PROGRAMMING LAB TW    50  20   38  P
                                                                21. OBJECT ORIENTED PROGRAMMING LAB PR    50  20   37  P
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GRAND TOTAL = 826/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.
          OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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S80058592   MANIK AGARWAL   REENA   , 71200956G   , S80058592 ,   PICT   , S80058592

01. DISCRETE STRUCTURES	PP	100	40	58	P C	11. ENG MATHS III	PP	100	40	40	P
02. COMPUTER ORGANIZATION	PP	100	40	62	P C	12. COMPUTER GRAPHICS	PP	100	40	52	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	61	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	43	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	42	P C	14. DATA STRUCTURES AND FILES	PP	100	40	42	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	62	P C	15. DATA COMMUNICATION	PP	100	40	55	P
06. DIGITAL LABORATORY	TW	50	20	33	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	14	P
07. DIGITAL LABORATORY	PR	50	20	24	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	08	F
08. PROGRAMMING LABORATORY	TW	50	20	28	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	17	P
09. PROGRAMMING LABORATORY	PR	50	20	34	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	36	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	35	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	33	P
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	35	P

GRAND TOTAL = 814/1500, RESULT: FAILS A.T.K.T.
ORDN. 1 MARKS :
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S80058593   MARADKAR VISHAL PRABHAKAR   SUMATI   , 71200959M   , S80058593 ,   PICT   , S80058593

01. DISCRETE STRUCTURES	PP	100	40	76	P C	11. ENG MATHS III	PP	100	40	46	P
02. COMPUTER ORGANIZATION	PP	100	40	66	P C	12. COMPUTER GRAPHICS	PP	100	40	57	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	68	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	64	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	65	P C	14. DATA STRUCTURES AND FILES	PP	100	40	48	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	63	P C	15. DATA COMMUNICATION	PP	100	40	53	P
06. DIGITAL LABORATORY	TW	50	20	40	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	20	P
07. DIGITAL LABORATORY	PR	50	20	44	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	42	P
08. PROGRAMMING LABORATORY	TW	50	20	41	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	19	P
09. PROGRAMMING LABORATORY	PR	50	20	42	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	41	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	43	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	43	P
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	43	P

GRAND TOTAL = 1024/1500, RESULT: FIRST CLASS WITH DISTINCTION
ORDN. 1 MARKS :
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S80058594   MITRA ANSHUMAN SUDEEP   MALINI   , 71100897D   , S80058594 ,   PICT   , S80058594

01. DISCRETE STRUCTURES	PP	100	40	54	P C	11. ENG MATHS III	PP	100	40	08	F
02. COMPUTER ORGANIZATION	PP	100	40	40	P	12. COMPUTER GRAPHICS	PP	100	40	50	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	40	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	28	F
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	40	P	14. DATA STRUCTURES AND FILES	PP	100	40	40	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	56	P C	15. DATA COMMUNICATION	PP	100	40	28	F
06. DIGITAL LABORATORY	TW	50	20	22	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	10	P
07. DIGITAL LABORATORY	PR	50	20	20	P	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	05	F
08. PROGRAMMING LABORATORY	TW	50	20	21	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	10	P
09. PROGRAMMING LABORATORY	PR	50	20	22	P	19. DATA STRUCTURES AND FILES LAB	PR	50	20	AA	F
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	30	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	20	P
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	AA	F

GRAND TOTAL = 544/1500, RESULT: FAILS
ORDN. 1 MARKS :
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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
         OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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S80058595   MOGRA ISHITA HEMANT   SUNITA   , 71200963K   , S80058595 ,   PICT   , S80058595
01. DISCRETE STRUCTURES   PP   100 40   79 P C   11. ENG MATHS III   PP   100 40   55 P
02. COMPUTER ORGANIZATION   PP   100 40   65 P C   12. COMPUTER GRAPHICS   PP   100 40   57 P
03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 40 66 P C   13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 44 P
04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40   57 P C   14. DATA STRUCTURES AND FILES   PP   100 40   40 P
05. HUMANITIES AND SOCIAL SCIENCES PP 100 40   51 P C   15. DATA COMMUNICATION   PP   100 40   52 P
06. DIGITAL LABORATORY   TW   50 20   43 P C   16. PROCESSOR INTERFACING LABORATORY TW   25 10   14 P
07. DIGITAL LABORATORY   PR   50 20   36 P C   17. PROCESSOR INTERFACING LABORATORY OR   50 20   38 P
08. PROGRAMMING LABORATORY   TW   50 20   42 P C   18. DATA STRUCTURES AND FILES LAB   TW   25 10   20 P
09. PROGRAMMING LABORATORY   PR   50 20   44 P C   19. DATA STRUCTURES AND FILES LAB   PR   50 20   43 P
10. COMMUNICATION AND LANGUAGE LAB. TW 50 20   43 P C   20. OBJECT ORIENTED PROGRAMMING LAB TW   50 20   40 P
                                                            21. OBJECT ORIENTED PROGRAMMING LAB PR   50 20   46 P
GRAND TOTAL = 975/1500, RESULT: FIRST CLASS
ORDN. 1 MARKS :
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S80058596   MOKASHI SUPRIYA MANIK   SHARADA   , 71200964H   , S80058596 ,   PICT   , S80058596
01. DISCRETE STRUCTURES   PP   100 40   51 P C   11. ENG MATHS III   PP   100 40   40 P
02. COMPUTER ORGANIZATION   PP   100 40   48 P C   12. COMPUTER GRAPHICS   PP   100 40   51 P
03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 40 63 P C   13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 42 P
04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40   59 P C   14. DATA STRUCTURES AND FILES   PP   100 40   48 P
05. HUMANITIES AND SOCIAL SCIENCES PP 100 40   68 P C   15. DATA COMMUNICATION   PP   100 40   52 P
06. DIGITAL LABORATORY   TW   50 20   35 P C   16. PROCESSOR INTERFACING LABORATORY TW   25 10   10 P
07. DIGITAL LABORATORY   PR   50 20   22 P C   17. PROCESSOR INTERFACING LABORATORY OR   50 20   20 P
08. PROGRAMMING LABORATORY   TW   50 20   32 P C   18. DATA STRUCTURES AND FILES LAB   TW   25 10   12 P
09. PROGRAMMING LABORATORY   PR   50 20   35 P C   19. DATA STRUCTURES AND FILES LAB   PR   50 20   15 F
10. COMMUNICATION AND LANGUAGE LAB. TW 50 20   39 P C   20. OBJECT ORIENTED PROGRAMMING LAB TW   50 20   31 P
                                                            21. OBJECT ORIENTED PROGRAMMING LAB PR   50 20   16 F
GRAND TOTAL = 789/1500, RESULT: FAILS A.T.K.T.
ORDN. 1 MARKS :
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S80058597   MULE MANJUSHA ASHOK   SUVARNA   , 71200968L   , S80058597 ,   PICT   , S80058597
01. DISCRETE STRUCTURES   PP   100 40   62 P C   11. ENG MATHS III   PP   100 40   44 P
02. COMPUTER ORGANIZATION   PP   100 40   59 P C   12. COMPUTER GRAPHICS   PP   100 40   62 P
03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 40 69 P C   13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 63 P
04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40   68 P C   14. DATA STRUCTURES AND FILES   PP   100 40   58 P
05. HUMANITIES AND SOCIAL SCIENCES PP 100 40   62 P C   15. DATA COMMUNICATION   PP   100 40   69 P
06. DIGITAL LABORATORY   TW   50 20   43 P C   16. PROCESSOR INTERFACING LABORATORY TW   25 10   22 P
07. DIGITAL LABORATORY   PR   50 20   42 P C   17. PROCESSOR INTERFACING LABORATORY OR   50 20   38 P
08. PROGRAMMING LABORATORY   TW   50 20   42 P C   18. DATA STRUCTURES AND FILES LAB   TW   25 10   22 P
09. PROGRAMMING LABORATORY   PR   50 20   38 P C   19. DATA STRUCTURES AND FILES LAB   PR   50 20   45 P
10. COMMUNICATION AND LANGUAGE LAB. TW 50 20   44 P C   20. OBJECT ORIENTED PROGRAMMING LAB TW   50 20   44 P
                                                            21. OBJECT ORIENTED PROGRAMMING LAB PR   50 20   39 P
GRAND TOTAL = 1035/1500, RESULT: FIRST CLASS WITH DISTINCTION
ORDN. 1 MARKS :
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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S80058598   NAIK PRIYANKA MANOJ   KAVITA   , 71200977K   , S80058598 ,   PICT   , S80058598

01. DISCRETE STRUCTURES	PP	100	40	69	P C	11. ENG MATHS III	PP	100	40	65	P
02. COMPUTER ORGANIZATION	PP	100	40	71	P C	12. COMPUTER GRAPHICS	PP	100	40	51	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	76	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	61	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	60	P C	14. DATA STRUCTURES AND FILES	PP	100	40	51	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	55	P C	15. DATA COMMUNICATION	PP	100	40	63	P
06. DIGITAL LABORATORY	TW	50	20	45	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	24	P
07. DIGITAL LABORATORY	PR	50	20	34	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	39	P
08. PROGRAMMING LABORATORY	TW	50	20	47	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	22	P
09. PROGRAMMING LABORATORY	PR	50	20	46	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	44	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	47	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	46	P
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	40	P

GRAND TOTAL = 1056/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

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S80058599   NAIK SHIVANI SHRIKANT   RADHIKA   , 71200978H   , S80058599 ,   PICT   , S80058599

01. DISCRETE STRUCTURES	PP	100	40	62	P C	11. ENG MATHS III	PP	100	40	80	P
02. COMPUTER ORGANIZATION	PP	100	40	71	P C	12. COMPUTER GRAPHICS	PP	100	40	61	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	70	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	82	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	55	P C	14. DATA STRUCTURES AND FILES	PP	100	40	57	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	54	P C	15. DATA COMMUNICATION	PP	100	40	60	P
06. DIGITAL LABORATORY	TW	50	20	39	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	20	P
07. DIGITAL LABORATORY	PR	50	20	41	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	41	P
08. PROGRAMMING LABORATORY	TW	50	20	37	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	19	P
09. PROGRAMMING LABORATORY	PR	50	20	44	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	47	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	38	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	38	P
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	43	P

GRAND TOTAL = 1059/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

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S80058600   NATU SWAPNIL RAVINDRA   ASHA   , 71200980K   , S80058600 ,   PICT   , S80058600

01. DISCRETE STRUCTURES	PP	100	40	74	P C	11. ENG MATHS III	PP	100	40	73	P
02. COMPUTER ORGANIZATION	PP	100	40	69	P C	12. COMPUTER GRAPHICS	PP	100	40	56	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	64	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	65	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	57	P C	14. DATA STRUCTURES AND FILES	PP	100	40	55	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	55	P C	15. DATA COMMUNICATION	PP	100	40	71	P
06. DIGITAL LABORATORY	TW	50	20	38	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	22	P
07. DIGITAL LABORATORY	PR	50	20	41	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	42	P
08. PROGRAMMING LABORATORY	TW	50	20	47	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	24	P
09. PROGRAMMING LABORATORY	PR	50	20	46	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	48	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	41	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	48	P
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	48	P

GRAND TOTAL = 1084/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S80058601   NAVGIRE SAGAR PRADEEP   SUNETRA   , 71200981H   , S80058601 , PICT   , S80058601

01. DISCRETE STRUCTURES	PP	100	40	50	P C	11. ENG MATHS III	PP	100	40	51	P
02. COMPUTER ORGANIZATION	PP	100	40	72	P C	12. COMPUTER GRAPHICS	PP	100	40	61	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	77	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	59	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	59	P C	14. DATA STRUCTURES AND FILES	PP	100	40	58	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	71	P C	15. DATA COMMUNICATION	PP	100	40	64	P
06. DIGITAL LABORATORY	TW	50	20	39	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	15	P
07. DIGITAL LABORATORY	PR	50	20	42	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	28	P
08. PROGRAMMING LABORATORY	TW	50	20	42	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	21	P
09. PROGRAMMING LABORATORY	PR	50	20	39	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	48	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	43	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	44	P
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	36	P

GRAND TOTAL = 1019/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

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S80058602   NEHE ABHISHEK ASHOK   SUNITA   , 71350929F   , S80058602 , PICT   , S80058602

01. DISCRETE STRUCTURES	PP	100	40	56	P C	11. ENG MATHS III	PP	100	40	40	P
02. COMPUTER ORGANIZATION	PP	100	40	44	P C	12. COMPUTER GRAPHICS	PP	100	40	57	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	70	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	57	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	60	P C	14. DATA STRUCTURES AND FILES	PP	100	40	59	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	54	P C	15. DATA COMMUNICATION	PP	100	40	67	P
06. DIGITAL LABORATORY	TW	50	20	38	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	19	P
07. DIGITAL LABORATORY	PR	50	20	30	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	09	F
08. PROGRAMMING LABORATORY	TW	50	20	43	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	20	P
09. PROGRAMMING LABORATORY	PR	50	20	35	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	40	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	44	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	43	P
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	33	P

GRAND TOTAL = 918/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

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S80058603   NEWALKAR REVA VIVEK   SMITA   , 71200986J   , S80058603 , PICT   , S80058603

01. DISCRETE STRUCTURES	PP	100	40	69	P C	11. ENG MATHS III	PP	100	40	69	P
02. COMPUTER ORGANIZATION	PP	100	40	75	P C	12. COMPUTER GRAPHICS	PP	100	40	61	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	73	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	50	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	60	P C	14. DATA STRUCTURES AND FILES	PP	100	40	63	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	66	P C	15. DATA COMMUNICATION	PP	100	40	60	P
06. DIGITAL LABORATORY	TW	50	20	41	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	19	P
07. DIGITAL LABORATORY	PR	50	20	38	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	38	P
08. PROGRAMMING LABORATORY	TW	50	20	44	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	21	P
09. PROGRAMMING LABORATORY	PR	50	20	36	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	43	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	46	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	43	P
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	35	P

GRAND TOTAL = 1050/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.

        OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S80058604   OSWAL SAHIT SURESH                  SUMITRA            , 71200992C    , S80058604 ,   PICT     , S80058604
01. DISCRETE STRUCTURES           PP   100  40   52  P C    11. ENG MATHS III               PP   100  40   40  P
02. COMPUTER ORGANIZATION         PP   100  40   55  P C    12. COMPUTER GRAPHICS           PP   100  40   49  P
03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100  40   66  P C    13. PROCESSOR ARCHITECTURE & INTER. PP 100  40   53  P
04. FUNDAMENTAL OF DATA STRUCTURES PP   100  40   56  P C    14. DATA STRUCTURES AND FILES     PP   100  40   56  P
05. HUMANITIES AND SOCIAL SCIENCES PP   100  40   64  P C    15. DATA COMMUNICATION           PP   100  40   66  P
06. DIGITAL LABORATORY            TW    50  20   39  P C    16. PROCESSOR INTERFACING LABORATORY TW  25  10   17  P
07. DIGITAL LABORATORY            PR    50  20   23  P C    17. PROCESSOR INTERFACING LABORATORY OR  50  20   36  P
08. PROGRAMMING LABORATORY        TW    50  20   43  P C    18. DATA STRUCTURES AND FILES LAB   TW   25  10   19  P
09. PROGRAMMING LABORATORY        PR    50  20   38  P C    19. DATA STRUCTURES AND FILES LAB   PR   50  20   44  P
10. COMMUNICATION AND LANGUAGE LAB. TW  50  20   43  P C    20. OBJECT ORIENTED PROGRAMMING LAB TW   50  20   39  P
                                                            21. OBJECT ORIENTED PROGRAMMING LAB PR   50  20   36  P
GRAND TOTAL =  934/1500, RESULT: FIRST CLASS
ORDN. 1 MARKS :
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S80058605   PABALKAR SARANG SHIVNATH            VIDYA              , 71200993M    , S80058605 ,   PICT     , S80058605
01. DISCRETE STRUCTURES           PP   100  40   58  P C    11. ENG MATHS III               PP   100  40   40  P
02. COMPUTER ORGANIZATION         PP   100  40   47  P C    12. COMPUTER GRAPHICS           PP   100  40   59  P
03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100  40   65  P C    13. PROCESSOR ARCHITECTURE & INTER. PP 100  40   47  P
04. FUNDAMENTAL OF DATA STRUCTURES PP   100  40   54  P C    14. DATA STRUCTURES AND FILES     PP   100  40   59  P
05. HUMANITIES AND SOCIAL SCIENCES PP   100  40   49  P C    15. DATA COMMUNICATION           PP   100  40   51  P
06. DIGITAL LABORATORY            TW    50  20   37  P C    16. PROCESSOR INTERFACING LABORATORY TW  25  10   15  P
07. DIGITAL LABORATORY            PR    50  20   33  P C    17. PROCESSOR INTERFACING LABORATORY OR  50  20   32  P
08. PROGRAMMING LABORATORY        TW    50  20   40  P C    18. DATA STRUCTURES AND FILES LAB   TW   25  10   17  P
09. PROGRAMMING LABORATORY        PR    50  20   33  P C    19. DATA STRUCTURES AND FILES LAB   PR   50  20   43  P
10. COMMUNICATION AND LANGUAGE LAB. TW  50  20   41  P C    20. OBJECT ORIENTED PROGRAMMING LAB TW   50  20   36  P
                                                            21. OBJECT ORIENTED PROGRAMMING LAB PR   50  20   46  P
GRAND TOTAL =  902/1500, RESULT: FIRST CLASS
ORDN. 1 MARKS :
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S80058606   PADIA SARVESH LALIT                 SEEMA              , 71200994K    , S80058606 ,   PICT     , S80058606
01. DISCRETE STRUCTURES           PP   100  40   72  P C    11. ENG MATHS III               PP   100  40   57  P
02. COMPUTER ORGANIZATION         PP   100  40   72  P C    12. COMPUTER GRAPHICS           PP   100  40   51  P
03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100  40   71  P C    13. PROCESSOR ARCHITECTURE & INTER. PP 100  40   61  P
04. FUNDAMENTAL OF DATA STRUCTURES PP   100  40   51  P C    14. DATA STRUCTURES AND FILES     PP   100  40   50  P
05. HUMANITIES AND SOCIAL SCIENCES PP   100  40   50  P C    15. DATA COMMUNICATION           PP   100  40   57  P
06. DIGITAL LABORATORY            TW    50  20   41  P C    16. PROCESSOR INTERFACING LABORATORY TW  25  10   20  P
07. DIGITAL LABORATORY            PR    50  20   39  P C    17. PROCESSOR INTERFACING LABORATORY OR  50  20   42  P
08. PROGRAMMING LABORATORY        TW    50  20   44  P C    18. DATA STRUCTURES AND FILES LAB   TW   25  10   21  P
09. PROGRAMMING LABORATORY        PR    50  20   40  P C    19. DATA STRUCTURES AND FILES LAB   PR   50  20   48  P
10. COMMUNICATION AND LANGUAGE LAB. TW  50  20   43  P C    20. OBJECT ORIENTED PROGRAMMING LAB TW   50  20   45  P
                                                            21. OBJECT ORIENTED PROGRAMMING LAB PR   50  20   45  P
GRAND TOTAL = 1020/1500, RESULT: FIRST CLASS WITH DISTINCTION
ORDN. 1 MARKS :
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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S80058607   PALLOD HRISHIKESH RAMNARAYAN   VANDANA   , 71200996F   , S80058607 ,   PICT   , S80058607

01. DISCRETE STRUCTURES	PP	100	40	77	P C	11. ENG MATHS III	PP	100	40	75	P
02. COMPUTER ORGANIZATION	PP	100	40	72	P C	12. COMPUTER GRAPHICS	PP	100	40	61	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	62	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	59	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	63	P C	14. DATA STRUCTURES AND FILES	PP	100	40	58	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	63	P C	15. DATA COMMUNICATION	PP	100	40	63	P
06. DIGITAL LABORATORY	TW	50	20	41	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	23	P
07. DIGITAL LABORATORY	PR	50	20	40	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	45	P
08. PROGRAMMING LABORATORY	TW	50	20	46	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	23	P
09. PROGRAMMING LABORATORY	PR	50	20	46	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	45	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	42	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	43	P
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	40	P

GRAND TOTAL = 1087/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

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S80058608   PANCHARIYA ANIKET SANJAY   LEENA   , 71200997D   , S80058608 ,   PICT   , S80058608

01. DISCRETE STRUCTURES	PP	100	40	60	P C	11. ENG MATHS III	PP	100	40	66	P
02. COMPUTER ORGANIZATION	PP	100	40	71	P C	12. COMPUTER GRAPHICS	PP	100	40	58	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	74	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	58	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	55	P C	14. DATA STRUCTURES AND FILES	PP	100	40	61	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	51	P C	15. DATA COMMUNICATION	PP	100	40	54	P
06. DIGITAL LABORATORY	TW	50	20	40	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	15	P
07. DIGITAL LABORATORY	PR	50	20	37	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	30	P
08. PROGRAMMING LABORATORY	TW	50	20	40	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	19	P
09. PROGRAMMING LABORATORY	PR	50	20	41	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	47	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	40	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	43	P
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	38	P

GRAND TOTAL = 998/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

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S80058609   PARAKH NAMAN RAJENDRA   SANGEETA   , 71201001H   , S80058609 ,   PICT   , S80058609

01. DISCRETE STRUCTURES	PP	100	40	71	P C	11. ENG MATHS III	PP	100	40	64	P
02. COMPUTER ORGANIZATION	PP	100	40	58	P C	12. COMPUTER GRAPHICS	PP	100	40	43	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	67	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	42	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	58	P C	14. DATA STRUCTURES AND FILES	PP	100	40	54	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	46	P C	15. DATA COMMUNICATION	PP	100	40	60	P
06. DIGITAL LABORATORY	TW	50	20	34	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	14	P
07. DIGITAL LABORATORY	PR	50	20	38	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	39	P
08. PROGRAMMING LABORATORY	TW	50	20	35	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	20	P
09. PROGRAMMING LABORATORY	PR	50	20	36	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	43	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	36	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	35	P
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	42	P

GRAND TOTAL = 935/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.

        OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S80058610   PARINITA MATHARU   RENU   , 71201003D   , S80058610 ,   PICT   , S80058610

01. DISCRETE STRUCTURES	PP	100	40	70	P C		11. ENG MATHS III	PP	100	40	47	P	
02. COMPUTER ORGANIZATION	PP	100	40	68	P C		12. COMPUTER GRAPHICS	PP	100	40	55	P	
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	64	P C		13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	42	P	
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	51	P C		14. DATA STRUCTURES AND FILES	PP	100	40	56	P	
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	56	P C		15. DATA COMMUNICATION	PP	100	40	54	P	
06. DIGITAL LABORATORY	TW	50	20	36	P C		16. PROCESSOR INTERFACING LABORATORY	TW	25	10	14	P	
07. DIGITAL LABORATORY	PR	50	20	40	P C		17. PROCESSOR INTERFACING LABORATORY	OR	50	20	23	P	
08. PROGRAMMING LABORATORY	TW	50	20	39	P C		18. DATA STRUCTURES AND FILES LAB	TW	25	10	16	P	
09. PROGRAMMING LABORATORY	PR	50	20	39	P C		19. DATA STRUCTURES AND FILES LAB	PR	50	20	44	P	
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	41	P C		20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	37	P	
							21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	36	P	

GRAND TOTAL = 928/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

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S80058611   PATADE PRATIK RAMDAS   SEEMA   , 71350930K   , S80058611 ,   PICT   , S80058611

01. DISCRETE STRUCTURES	PP	100	40	60	P C		11. ENG MATHS III	PP	100	40	42	P	
02. COMPUTER ORGANIZATION	PP	100	40	67	P C		12. COMPUTER GRAPHICS	PP	100	40	54	P	
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	61	P C		13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	52	P	
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	65	P C		14. DATA STRUCTURES AND FILES	PP	100	40	56	P	
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	51	P C		15. DATA COMMUNICATION	PP	100	40	57	P	
06. DIGITAL LABORATORY	TW	50	20	42	P C		16. PROCESSOR INTERFACING LABORATORY	TW	25	10	24	P	
07. DIGITAL LABORATORY	PR	50	20	28	P C		17. PROCESSOR INTERFACING LABORATORY	OR	50	20	40	P	
08. PROGRAMMING LABORATORY	TW	50	20	40	P C		18. DATA STRUCTURES AND FILES LAB	TW	25	10	23	P	
09. PROGRAMMING LABORATORY	PR	50	20	44	P C		19. DATA STRUCTURES AND FILES LAB	PR	50	20	45	P	
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	42	P C		20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	43	P	
							21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	38	P	

GRAND TOTAL = 974/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

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S80058612   PATIL DEEPAK RAMESH   MINAKSHEE   , 71201005L   , S80058612 ,   PICT   , S80058612

01. DISCRETE STRUCTURES	PP	100	40	67	P C		11. ENG MATHS III	PP	100	40	59	P	
02. COMPUTER ORGANIZATION	PP	100	40	63	P C		12. COMPUTER GRAPHICS	PP	100	40	59	P	
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	78	P C		13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	50	P	
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	68	P C		14. DATA STRUCTURES AND FILES	PP	100	40	57	P	
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	67	P C		15. DATA COMMUNICATION	PP	100	40	64	P	
06. DIGITAL LABORATORY	TW	50	20	41	P C		16. PROCESSOR INTERFACING LABORATORY	TW	25	10	22	P	
07. DIGITAL LABORATORY	PR	50	20	37	P C		17. PROCESSOR INTERFACING LABORATORY	OR	50	20	40	P	
08. PROGRAMMING LABORATORY	TW	50	20	41	P C		18. DATA STRUCTURES AND FILES LAB	TW	25	10	23	P	
09. PROGRAMMING LABORATORY	PR	50	20	41	P C		19. DATA STRUCTURES AND FILES LAB	PR	50	20	40	P	
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	44	P C		20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	43	P	
							21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	33	P	

GRAND TOTAL = 1037/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S80058613   PATIL INDRAJEET ANIL                    PRIYA              , 71201006J    , S80058613 ,  PICT     , S80058613
01. DISCRETE STRUCTURES           PP   100  40   40  P C    11. ENG MATHS III                 PP   100  40   40  P
02. COMPUTER ORGANIZATION         PP   100  40   45  P C    12. COMPUTER GRAPHICS             PP   100  40   47  P
03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100  40   56  P C    13. PROCESSOR ARCHITECTURE & INTER. PP  100  40   18  F
04. FUNDAMENTAL OF DATA STRUCTURES PP  100  40   44  P C    14. DATA STRUCTURES AND FILES      PP   100  40   40  P
05. HUMANITIES AND SOCIAL SCIENCES PP  100  40   55  P C    15. DATA COMMUNICATION            PP   100  40   44  P
06. DIGITAL LABORATORY            TW    50  20   35  P C    16. PROCESSOR INTERFACING LABORATORY TW  25  10   15  P
07. DIGITAL LABORATORY            PR    50  20   32  P C    17. PROCESSOR INTERFACING LABORATORY OR  50  20   20  P
08. PROGRAMMING LABORATORY        TW    50  20   34  P C    18. DATA STRUCTURES AND FILES LAB   TW   25  10   15  P
09. PROGRAMMING LABORATORY        PR    50  20   34  P      19. DATA STRUCTURES AND FILES LAB   PR   50  20   29  P
10. COMMUNICATION AND LANGUAGE LAB. TW  50  20   39  P C    20. OBJECT ORIENTED PROGRAMMING LAB  TW  50  20   35  P
                                                           21. OBJECT ORIENTED PROGRAMMING LAB  PR  50  20   30  P
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GRAND TOTAL = 747/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

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S80058614   PATIL MAYUR PAVANKUMAR                  MANDAKINI          , 71350931H    , S80058614 ,  PICT     , S80058614
01. DISCRETE STRUCTURES           PP   100  40   56  P C    11. ENG MATHS III                 PP   100  40   40  P
02. COMPUTER ORGANIZATION         PP   100  40   64  P C    12. COMPUTER GRAPHICS             PP   100  40   53  P
03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100  40   57  P C    13. PROCESSOR ARCHITECTURE & INTER. PP  100  40   47  P
04. FUNDAMENTAL OF DATA STRUCTURES PP  100  40   52  P C    14. DATA STRUCTURES AND FILES      PP   100  40   61  P
05. HUMANITIES AND SOCIAL SCIENCES PP  100  40   64  P C    15. DATA COMMUNICATION            PP   100  40   62  P
06. DIGITAL LABORATORY            TW    50  20   39  P C    16. PROCESSOR INTERFACING LABORATORY TW  25  10   19  P
07. DIGITAL LABORATORY            PR    50  20   30  P C    17. PROCESSOR INTERFACING LABORATORY OR  50  20   40  P
08. PROGRAMMING LABORATORY        TW    50  20   41  P C    18. DATA STRUCTURES AND FILES LAB   TW   25  10   23  P
09. PROGRAMMING LABORATORY        PR    50  20   35  P C    19. DATA STRUCTURES AND FILES LAB   PR   50  20   44  P
10. COMMUNICATION AND LANGUAGE LAB. TW  50  20   40  P C    20. OBJECT ORIENTED PROGRAMMING LAB  TW  50  20   40  P
                                                           21. OBJECT ORIENTED PROGRAMMING LAB  PR  50  20   43  P
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GRAND TOTAL = 950/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

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S80058615   PATKE SWATI SANJAY                      SUNITA             , 71201011E    , S80058615 ,  PICT     , S80058615
01. DISCRETE STRUCTURES           PP   100  40   61  P C    11. ENG MATHS III                 PP   100  40   27  F
02. COMPUTER ORGANIZATION         PP   100  40   40  P C    12. COMPUTER GRAPHICS             PP   100  40   40  P
03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100  40   63  P C    13. PROCESSOR ARCHITECTURE & INTER. PP  100  40   43  P
04. FUNDAMENTAL OF DATA STRUCTURES PP  100  40   60  P C    14. DATA STRUCTURES AND FILES      PP   100  40   52  P
05. HUMANITIES AND SOCIAL SCIENCES PP  100  40   41  P C    15. DATA COMMUNICATION            PP   100  40   40  P
06. DIGITAL LABORATORY            TW    50  20   39  P C    16. PROCESSOR INTERFACING LABORATORY TW  25  10   18  P
07. DIGITAL LABORATORY            PR    50  20   35  P C    17. PROCESSOR INTERFACING LABORATORY OR  50  20   33  P
08. PROGRAMMING LABORATORY        TW    50  20   42  P C    18. DATA STRUCTURES AND FILES LAB   TW   25  10   18  P
09. PROGRAMMING LABORATORY        PR    50  20   42  P C    19. DATA STRUCTURES AND FILES LAB   PR   50  20   45  P
10. COMMUNICATION AND LANGUAGE LAB. TW  50  20   40  P C    20. OBJECT ORIENTED PROGRAMMING LAB  TW  50  20   35  P
                                                           21. OBJECT ORIENTED PROGRAMMING LAB  PR  50  20   38  P
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GRAND TOTAL = 852/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

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   NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.
         OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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   S80058616   PAWAR KAVITA SANDIPAN   SHEVANTA   , 71350932F   , S80058616 ,   PICT   , S80058616
   01. DISCRETE STRUCTURES   PP   100 40   40 P   11. ENG MATHS III   PP   100 40   03 F
   02. COMPUTER ORGANIZATION   PP   100 40   47 P C   12. COMPUTER GRAPHICS   PP   100 40   40 P
   03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 40   44 P   13. PROCESSOR ARCHITECTURE & INTER. PP   100 40   13 F
   04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40   25 F   14. DATA STRUCTURES AND FILES   PP   100 40   40 P
   05. HUMANITIES AND SOCIAL SCIENCES PP 100 40   64 P C   15. DATA COMMUNICATION   PP   100 40   56 P
   06. DIGITAL LABORATORY   TW   50 20   32 P C   16. PROCESSOR INTERFACING LABORATORY TW   25 10   16 P
   07. DIGITAL LABORATORY   PR   50 20   22 P C   17. PROCESSOR INTERFACING LABORATORY OR   50 20   06 F
   08. PROGRAMMING LABORATORY   TW   50 20   30 P C   18. DATA STRUCTURES AND FILES LAB   TW   25 10   17 P
   09. PROGRAMMING LABORATORY   PR   50 20   22 P   19. DATA STRUCTURES AND FILES LAB   PR   50 20   22 P
   10. COMMUNICATION AND LANGUAGE LAB. TW 50 20   32 P C   20. OBJECT ORIENTED PROGRAMMING LAB TW   50 20   30 P
                                                             21. OBJECT ORIENTED PROGRAMMING LAB PR   50 20   17 F
GRAND TOTAL = 618/1500, RESULT: FAILS A.T.K.T.
ORDN. 1 MARKS :
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   S80058617   PHADNIS SHARWARI SADANAND   SUPRIYA   , 71201014K   , S80058617 ,   PICT   , S80058617
   01. DISCRETE STRUCTURES   PP   100 40   54 P C   11. ENG MATHS III   PP   100 40   40 P
   02. COMPUTER ORGANIZATION   PP   100 40   53 P C   12. COMPUTER GRAPHICS   PP   100 40   51 P
   03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 40   63 P C 13. PROCESSOR ARCHITECTURE & INTER. PP   100 40   52 P
   04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40   53 P C   14. DATA STRUCTURES AND FILES   PP   100 40   44 P
   05. HUMANITIES AND SOCIAL SCIENCES PP 100 40   52 P C   15. DATA COMMUNICATION   PP   100 40   48 P
   06. DIGITAL LABORATORY   TW   50 20   42 P C   16. PROCESSOR INTERFACING LABORATORY TW   25 10   19 P
   07. DIGITAL LABORATORY   PR   50 20   34 P C   17. PROCESSOR INTERFACING LABORATORY OR   50 20   35 P
   08. PROGRAMMING LABORATORY   TW   50 20   40 P C   18. DATA STRUCTURES AND FILES LAB   TW   25 10   20 P
   09. PROGRAMMING LABORATORY   PR   50 20   32 P C   19. DATA STRUCTURES AND FILES LAB   PR   50 20   38 P
   10. COMMUNICATION AND LANGUAGE LAB. TW 50 20   42 P C   20. OBJECT ORIENTED PROGRAMMING LAB TW   50 20   39 P
                                                             21. OBJECT ORIENTED PROGRAMMING LAB PR   50 20   37 P
GRAND TOTAL = 888/1500, RESULT: HIGHER SECOND CLASS
ORDN. 1 MARKS :
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   S80058618   PILAJI EKTA BALAJI   ANUJA   , 71201016F   , S80058618 ,   PICT   , S80058618
   01. DISCRETE STRUCTURES   PP   100 40   65 P C   11. ENG MATHS III   PP   100 40   45 P
   02. COMPUTER ORGANIZATION   PP   100 40   50 P C   12. COMPUTER GRAPHICS   PP   100 40   45 P
   03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 40   59 P C 13. PROCESSOR ARCHITECTURE & INTER. PP   100 40   47 P
   04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40   60 P C   14. DATA STRUCTURES AND FILES   PP   100 40   54 P
   05. HUMANITIES AND SOCIAL SCIENCES PP 100 40   54 P C   15. DATA COMMUNICATION   PP   100 40   47 P
   06. DIGITAL LABORATORY   TW   50 20   40 P C   16. PROCESSOR INTERFACING LABORATORY TW   25 10   18 P
   07. DIGITAL LABORATORY   PR   50 20   36 P C   17. PROCESSOR INTERFACING LABORATORY OR   50 20   34 P
   08. PROGRAMMING LABORATORY   TW   50 20   40 P C   18. DATA STRUCTURES AND FILES LAB   TW   25 10   21 P
   09. PROGRAMMING LABORATORY   PR   50 20   40 P C   19. DATA STRUCTURES AND FILES LAB   PR   50 20   42 P
   10. COMMUNICATION AND LANGUAGE LAB. TW 50 20   43 P C   20. OBJECT ORIENTED PROGRAMMING LAB TW   50 20   39 P
                                                             21. OBJECT ORIENTED PROGRAMMING LAB PR   50 20   41 P
GRAND TOTAL = 920/1500, RESULT: FIRST CLASS
ORDN. 1 MARKS :
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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
       OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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S80058619   POHANKAR RASIKA AVINASH   ANJALI   , 71201017D   , S80058619 ,   PICT   , S80058619
01. DISCRETE STRUCTURES   PP   100 40   75 P C   11. ENG MATHS III   PP   100 40   89 P
02. COMPUTER ORGANIZATION   PP   100 40   79 P C   12. COMPUTER GRAPHICS   PP   100 40   50 P
03. DIGITAL ELECTRONICS & LOGIC DESIGPP   100 40   80 P C   13. PROCESSOR ARCHITECTURE & INTER. PP   100 40   61 P
04. FUNDAMENTAL OF DATA STRUCTURES PP   100 40   73 P C   14. DATA STRUCTURES AND FILES   PP   100 40   65 P
05. HUMANITIES AND SOCIAL SCIENCES PP   100 40   65 P C   15. DATA COMMUNICATION   PP   100 40   62 P
06. DIGITAL LABORATORY   TW   50 20   47 P C   16. PROCESSOR INTERFACING LABORATORY TW   25 10   23 P
07. DIGITAL LABORATORY   PR   50 20   46 P C   17. PROCESSOR INTERFACING LABORATORY OR   50 20   42 P
08. PROGRAMMING LABORATORY   TW   50 20   48 P C   18. DATA STRUCTURES AND FILES LAB   TW   25 10   24 P
09. PROGRAMMING LABORATORY   PR   50 20   46 P C   19. DATA STRUCTURES AND FILES LAB   PR   50 20   48 P
10. COMMUNICATION AND LANGUAGE LAB. TW   50 20   48 P C   20. OBJECT ORIENTED PROGRAMMING LAB TW   50 20   46 P
                                                          21. OBJECT ORIENTED PROGRAMMING LAB PR   50 20   48 P
GRAND TOTAL = 1165/1500, RESULT: FIRST CLASS WITH DISTINCTION
ORDN. 1 MARKS :
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S80058620   POTDUKHE VAISHNAVI NILKANTH   SANGEETA   , 71201018B   , S80058620 ,   PICT   , S80058620
01. DISCRETE STRUCTURES   PP   100 40   52 P C   11. ENG MATHS III   PP   100 40   40 P
02. COMPUTER ORGANIZATION   PP   100 40   54 P C   12. COMPUTER GRAPHICS   PP   100 40   40 P
03. DIGITAL ELECTRONICS & LOGIC DESIGPP   100 40   62 P C   13. PROCESSOR ARCHITECTURE & INTER. PP   100 40   40 P
04. FUNDAMENTAL OF DATA STRUCTURES PP   100 40   51 P C   14. DATA STRUCTURES AND FILES   PP   100 40   48 P
05. HUMANITIES AND SOCIAL SCIENCES PP   100 40   41 P C   15. DATA COMMUNICATION   PP   100 40   40 P
06. DIGITAL LABORATORY   TW   50 20   39 P C   16. PROCESSOR INTERFACING LABORATORY TW   25 10   17 P
07. DIGITAL LABORATORY   PR   50 20   35 P C   17. PROCESSOR INTERFACING LABORATORY OR   50 20   20 P
08. PROGRAMMING LABORATORY   TW   50 20   38 P C   18. DATA STRUCTURES AND FILES LAB   TW   25 10   19 P
09. PROGRAMMING LABORATORY   PR   50 20   41 P C   19. DATA STRUCTURES AND FILES LAB   PR   50 20   40 P
10. COMMUNICATION AND LANGUAGE LAB. TW   50 20   42 P C   20. OBJECT ORIENTED PROGRAMMING LAB TW   50 20   36 P
                                                          21. OBJECT ORIENTED PROGRAMMING LAB PR   50 20   37 P
GRAND TOTAL = 832/1500, RESULT: HIGHER SECOND CLASS
ORDN. 1 MARKS :
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S80058621   POTNIS ISHA RAHUL   SUVARNA   , 71201019L   , S80058621 ,   PICT   , S80058621
01. DISCRETE STRUCTURES   PP   100 40   79 P C   11. ENG MATHS III   PP   100 40   45 P
02. COMPUTER ORGANIZATION   PP   100 40   70 P C   12. COMPUTER GRAPHICS   PP   100 40   52 P
03. DIGITAL ELECTRONICS & LOGIC DESIGPP   100 40   77 P C   13. PROCESSOR ARCHITECTURE & INTER. PP   100 40   49 P
04. FUNDAMENTAL OF DATA STRUCTURES PP   100 40   67 P C   14. DATA STRUCTURES AND FILES   PP   100 40   52 P
05. HUMANITIES AND SOCIAL SCIENCES PP   100 40   54 P C   15. DATA COMMUNICATION   PP   100 40   65 P
06. DIGITAL LABORATORY   TW   50 20   46 P C   16. PROCESSOR INTERFACING LABORATORY TW   25 10   20 P
07. DIGITAL LABORATORY   PR   50 20   40 P C   17. PROCESSOR INTERFACING LABORATORY OR   50 20   36 P
08. PROGRAMMING LABORATORY   TW   50 20   47 P C   18. DATA STRUCTURES AND FILES LAB   TW   25 10   22 P
09. PROGRAMMING LABORATORY   PR   50 20   43 P C   19. DATA STRUCTURES AND FILES LAB   PR   50 20   44 P
10. COMMUNICATION AND LANGUAGE LAB. TW   50 20   47 P C   20. OBJECT ORIENTED PROGRAMMING LAB TW   50 20   45 P
                                                          21. OBJECT ORIENTED PROGRAMMING LAB PR   50 20   37 P
GRAND TOTAL = 1037/1500, RESULT: FIRST CLASS WITH DISTINCTION
ORDN. 1 MARKS :
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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.
        OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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S80058622   PRASHUL SINGH   ARCHANA   , 71201022L   , S80058622 ,   PICT   , S80058622
01. DISCRETE STRUCTURES   PP   100 40   57 P C   11. ENG MATHS III   PP   100 40   73 P
02. COMPUTER ORGANIZATION   PP   100 40   65 P C   12. COMPUTER GRAPHICS   PP   100 40   44 P
03. DIGITAL ELECTRONICS & LOGIC DESIGPP   100 40   61 P C   13. PROCESSOR ARCHITECTURE & INTER. PP   100 40   45 P
04. FUNDAMENTAL OF DATA STRUCTURES PP   100 40   62 P C   14. DATA STRUCTURES AND FILES   PP   100 40   57 P
05. HUMANITIES AND SOCIAL SCIENCES PP   100 40   55 P C   15. DATA COMMUNICATION   PP   100 40   54 P
06. DIGITAL LABORATORY   TW   50 20   39 P C   16. PROCESSOR INTERFACING LABORATORY TW   25 10   21 P
07. DIGITAL LABORATORY   PR   50 20   42 P C   17. PROCESSOR INTERFACING LABORATORY OR   50 20   43 P
08. PROGRAMMING LABORATORY   TW   50 20   38 P C   18. DATA STRUCTURES AND FILES LAB   TW   25 10   20 P
09. PROGRAMMING LABORATORY   PR   50 20   43 P C   19. DATA STRUCTURES AND FILES LAB   PR   50 20   43 P
10. COMMUNICATION AND LANGUAGE LAB. TW   50 20   39 P C   20. OBJECT ORIENTED PROGRAMMING LAB TW   50 20   39 P
                                                           21. OBJECT ORIENTED PROGRAMMING LAB PR   50 20   39 P
GRAND TOTAL = 979/1500, RESULT: FIRST CLASS
ORDN. 1 MARKS :
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S80058623   PRERIT VILAS AUTI   INDU   , 71201023J   , S80058623 ,   PICT   , S80058623
01. DISCRETE STRUCTURES   PP   100 40   71 P C   11. ENG MATHS III   PP   100 40   60 P
02. COMPUTER ORGANIZATION   PP   100 40   80 P C   12. COMPUTER GRAPHICS   PP   100 40   51 P
03. DIGITAL ELECTRONICS & LOGIC DESIGPP   100 40   66 P C   13. PROCESSOR ARCHITECTURE & INTER. PP   100 40   54 P
04. FUNDAMENTAL OF DATA STRUCTURES PP   100 40   66 P C   14. DATA STRUCTURES AND FILES   PP   100 40   56 P
05. HUMANITIES AND SOCIAL SCIENCES PP   100 40   54 P C   15. DATA COMMUNICATION   PP   100 40   60 P
06. DIGITAL LABORATORY   TW   50 20   46 P C   16. PROCESSOR INTERFACING LABORATORY TW   25 10   21 P
07. DIGITAL LABORATORY   PR   50 20   40 P C   17. PROCESSOR INTERFACING LABORATORY OR   50 20   34 P
08. PROGRAMMING LABORATORY   TW   50 20   47 P C   18. DATA STRUCTURES AND FILES LAB   TW   25 10   21 P
09. PROGRAMMING LABORATORY   PR   50 20   44 P C   19. DATA STRUCTURES AND FILES LAB   PR   50 20   45 P
10. COMMUNICATION AND LANGUAGE LAB. TW   50 20   46 P C   20. OBJECT ORIENTED PROGRAMMING LAB TW   50 20   44 P
                                                           21. OBJECT ORIENTED PROGRAMMING LAB PR   50 20   41 P
GRAND TOTAL = 1047/1500, RESULT: FIRST CLASS WITH DISTINCTION
ORDN. 1 MARKS :
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S80058624   RAHUL KUMAR   USHA   , 71201029H   , S80058624 ,   PICT   , S80058624
01. DISCRETE STRUCTURES   PP   100 40   65 P C   11. ENG MATHS III   PP   100 40   45 P
02. COMPUTER ORGANIZATION   PP   100 40   63 P C   12. COMPUTER GRAPHICS   PP   100 40   42 P
03. DIGITAL ELECTRONICS & LOGIC DESIGPP   100 40   64 P C   13. PROCESSOR ARCHITECTURE & INTER. PP   100 40   26 F
04. FUNDAMENTAL OF DATA STRUCTURES PP   100 40   47 P C   14. DATA STRUCTURES AND FILES   PP   100 40   46 P
05. HUMANITIES AND SOCIAL SCIENCES PP   100 40   43 P C   15. DATA COMMUNICATION   PP   100 40   57 P
06. DIGITAL LABORATORY   TW   50 20   35 P C   16. PROCESSOR INTERFACING LABORATORY TW   25 10   15 P
07. DIGITAL LABORATORY   PR   50 20   35 P C   17. PROCESSOR INTERFACING LABORATORY OR   50 20   28 P
08. PROGRAMMING LABORATORY   TW   50 20   32 P C   18. DATA STRUCTURES AND FILES LAB   TW   25 10   19 P
09. PROGRAMMING LABORATORY   PR   50 20   44 P C   19. DATA STRUCTURES AND FILES LAB   PR   50 20   40 P
10. COMMUNICATION AND LANGUAGE LAB. TW   50 20   37 P C   20. OBJECT ORIENTED PROGRAMMING LAB TW   50 20   35 P
                                                           21. OBJECT ORIENTED PROGRAMMING LAB PR   50 20   43 P
GRAND TOTAL = 861/1500, RESULT: FAILS A.T.K.T.
ORDN. 1 MARKS :
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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
      OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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  S80058625   RANSHEVRE ROHIT YASHWANT             USHA              , 71350933D    , S80058625 ,   PICT     , S80058625
  01. DISCRETE STRUCTURES           PP   100  40   52  P C     11. ENG MATHS III                 PP   100  40   16  F
  02. COMPUTER ORGANIZATION         PP   100  40   67  P C     12. COMPUTER GRAPHICS             PP   100  40   44  P
  03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 40   53  P C     13. PROCESSOR ARCHITECTURE & INTER. PP 100  40   40  P
  04. FUNDAMENTAL OF DATA STRUCTURES PP  100  40   52  P C     14. DATA STRUCTURES AND FILES     PP   100  40   40  P
  05. HUMANITIES AND SOCIAL SCIENCES PP  100  40   58  P C     15. DATA COMMUNICATION            PP   100  40   49  P
  06. DIGITAL LABORATORY            TW    50  20   33  P C     16. PROCESSOR INTERFACING LABORATORY TW  25  10   12  P
  07. DIGITAL LABORATORY            PR    50  20   37  P       17. PROCESSOR INTERFACING LABORATORY OR  50  20   08  F
  08. PROGRAMMING LABORATORY        TW    50  20   27  P C     18. DATA STRUCTURES AND FILES LAB  TW    25  10   14  P
  09. PROGRAMMING LABORATORY        PR    50  20   30  P C     19. DATA STRUCTURES AND FILES LAB  PR    50  20   25  P
  10. COMMUNICATION AND LANGUAGE LAB. TW  50  20   33  P C     20. OBJECT ORIENTED PROGRAMMING LAB  TW  50  20   30  P
                                                              21. OBJECT ORIENTED PROGRAMMING LAB  PR  50  20   16  F
GRAND TOTAL =  736/1500, RESULT: FAILS A.T.K.T.
ORDN. 1 MARKS :
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  S80058627   RASKAR SUNITA ASHOK               SUSHILA           , 71350934B    , S80058627 ,   PICT     , S80058627
  01. DISCRETE STRUCTURES           PP   100  40   57  P C     11. ENG MATHS III                 PP   100  40   26  F
  02. COMPUTER ORGANIZATION         PP   100  40   51  P C     12. COMPUTER GRAPHICS             PP   100  40   45  P
  03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 40   52  P C     13. PROCESSOR ARCHITECTURE & INTER. PP 100  40   41  P
  04. FUNDAMENTAL OF DATA STRUCTURES PP  100  40   42  P C     14. DATA STRUCTURES AND FILES     PP   100  40   55  P
  05. HUMANITIES AND SOCIAL SCIENCES PP  100  40   58  P C     15. DATA COMMUNICATION            PP   100  40   60  P
  06. DIGITAL LABORATORY            TW    50  20   38  P C     16. PROCESSOR INTERFACING LABORATORY TW  25  10   20  P
  07. DIGITAL LABORATORY            PR    50  20   32  P C     17. PROCESSOR INTERFACING LABORATORY OR  50  20   25  P
  08. PROGRAMMING LABORATORY        TW    50  20   38  P C     18. DATA STRUCTURES AND FILES LAB  TW    25  10   23  P
  09. PROGRAMMING LABORATORY        PR    50  20   30  P C     19. DATA STRUCTURES AND FILES LAB  PR    50  20   43  P
  10. COMMUNICATION AND LANGUAGE LAB. TW  50  20   36  P C     20. OBJECT ORIENTED PROGRAMMING LAB  TW  50  20   40  P
                                                              21. OBJECT ORIENTED PROGRAMMING LAB  PR  50  20   32  P
GRAND TOTAL =  844/1500, RESULT: FAILS A.T.K.T.
ORDN. 1 MARKS :
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  S80058628   RATHI TUSHAR SANDEEP              SANGITA           , 71201033F    , S80058628 ,   PICT     , S80058628
  01. DISCRETE STRUCTURES           PP   100  40   55  P C     11. ENG MATHS III                 PP   100  40   40  P
  02. COMPUTER ORGANIZATION         PP   100  40   65  P C     12. COMPUTER GRAPHICS             PP   100  40   48  P
  03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 40   63  P C     13. PROCESSOR ARCHITECTURE & INTER. PP 100  40   40  P
  04. FUNDAMENTAL OF DATA STRUCTURES PP  100  40   40  P C     14. DATA STRUCTURES AND FILES     PP   100  40   42  P
  05. HUMANITIES AND SOCIAL SCIENCES PP  100  40   41  P C     15. DATA COMMUNICATION            PP   100  40   50  P
  06. DIGITAL LABORATORY            TW    50  20   35  P C     16. PROCESSOR INTERFACING LABORATORY TW  25  10   14  P
  07. DIGITAL LABORATORY            PR    50  20   24  P C     17. PROCESSOR INTERFACING LABORATORY OR  50  20   30  P
  08. PROGRAMMING LABORATORY        TW    50  20   37  P C     18. DATA STRUCTURES AND FILES LAB  TW    25  10   18  P
  09. PROGRAMMING LABORATORY        PR    50  20   32  P C     19. DATA STRUCTURES AND FILES LAB  PR    50  20   43  P
  10. COMMUNICATION AND LANGUAGE LAB. TW  50  20   38  P C     20. OBJECT ORIENTED PROGRAMMING LAB  TW  50  20   31  P
                                                              21. OBJECT ORIENTED PROGRAMMING LAB  PR  50  20   39  P
GRAND TOTAL =  825/1500, RESULT: HIGHER SECOND CLASS
ORDN. 1 MARKS :
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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.
         OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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S80058629   RATHOD NEHA SANJAY   NALINI   , 71201035B   , S80058629 ,   PICT   , S80058629
01. DISCRETE STRUCTURES   PP   100 40   64 P C   11. ENG MATHS III   PP   100 40   78 P
02. COMPUTER ORGANIZATION   PP   100 40   81 P C   12. COMPUTER GRAPHICS   PP   100 40   61 P
03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 40   73 P C   13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 53 P
04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40   66 P C   14. DATA STRUCTURES AND FILES   PP   100 40   70 P
05. HUMANITIES AND SOCIAL SCIENCES PP 100 40   65 P C   15. DATA COMMUNICATION   PP   100 40   61 P
06. DIGITAL LABORATORY   TW   50 20   44 P C   16. PROCESSOR INTERFACING LABORATORY TW 25 10 23 P
07. DIGITAL LABORATORY   PR   50 20   40 P C   17. PROCESSOR INTERFACING LABORATORY OR 50 20 44 P
08. PROGRAMMING LABORATORY   TW   50 20   39 P C   18. DATA STRUCTURES AND FILES LAB TW   25 10   23 P
09. PROGRAMMING LABORATORY   PR   50 20   42 P C   19. DATA STRUCTURES AND FILES LAB PR   50 20   47 P
10. COMMUNICATION AND LANGUAGE LAB. TW 50 20   46 P C   20. OBJECT ORIENTED PROGRAMMING LAB TW   50 20   44 P
                                                           21. OBJECT ORIENTED PROGRAMMING LAB PR   50 20   40 P

GRAND TOTAL = 1104/1500, RESULT: FIRST CLASS WITH DISTINCTION

ORDN. 1 MARKS :
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S80058630   RITESH RAJIV POREY   AMRUTA   , 71201040J   , S80058630 ,   PICT   , S80058630
01. DISCRETE STRUCTURES   PP   100 40   58 P C   11. ENG MATHS III   PP   100 40   40 P
02. COMPUTER ORGANIZATION   PP   100 40   62 P C   12. COMPUTER GRAPHICS   PP   100 40   52 P
03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 40   70 P C   13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 52 P
04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40   53 P C   14. DATA STRUCTURES AND FILES   PP   100 40   54 P
05. HUMANITIES AND SOCIAL SCIENCES PP 100 40   71 P C   15. DATA COMMUNICATION   PP   100 40   62 P
06. DIGITAL LABORATORY   TW   50 20   42 P C   16. PROCESSOR INTERFACING LABORATORY TW 25 10 18 P
07. DIGITAL LABORATORY   PR   50 20   36 P C   17. PROCESSOR INTERFACING LABORATORY OR 50 20 34 P
08. PROGRAMMING LABORATORY   TW   50 20   37 P C   18. DATA STRUCTURES AND FILES LAB TW   25 10   23 P
09. PROGRAMMING LABORATORY   PR   50 20   41 P C   19. DATA STRUCTURES AND FILES LAB PR   50 20   46 P
10. COMMUNICATION AND LANGUAGE LAB. TW 50 20   44 P C   20. OBJECT ORIENTED PROGRAMMING LAB TW   50 20   43 P
                                                           21. OBJECT ORIENTED PROGRAMMING LAB PR   50 20   40 P

GRAND TOTAL = 978/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :
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S80058631   SAHARSH BHATIA   KIRAN   , 71201046H   , S80058631 ,   PICT   , S80058631
01. DISCRETE STRUCTURES   PP   100 40   50 P C   11. ENG MATHS III   PP   100 40   11 F
02. COMPUTER ORGANIZATION   PP   100 40   53 P C   12. COMPUTER GRAPHICS   PP   100 40   41 P
03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 40   44 P   13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 18 F
04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40   42 P C   14. DATA STRUCTURES AND FILES   PP   100 40   53 P
05. HUMANITIES AND SOCIAL SCIENCES PP 100 40   48 P C   15. DATA COMMUNICATION   PP   100 40   45 P
06. DIGITAL LABORATORY   TW   50 20   28 P C   16. PROCESSOR INTERFACING LABORATORY TW 25 10 10 P
07. DIGITAL LABORATORY   PR   50 20   27 P C   17. PROCESSOR INTERFACING LABORATORY OR 50 20 07 F
08. PROGRAMMING LABORATORY   TW   50 20   29 P C   18. DATA STRUCTURES AND FILES LAB TW   25 10   13 P
09. PROGRAMMING LABORATORY   PR   50 20   38 P C   19. DATA STRUCTURES AND FILES LAB PR   50 20   30 P
10. COMMUNICATION AND LANGUAGE LAB. TW 50 20   32 P C   20. OBJECT ORIENTED PROGRAMMING LAB TW   50 20   23 P
                                                           21. OBJECT ORIENTED PROGRAMMING LAB PR   50 20   39 P

GRAND TOTAL = 681/1500, RESULT: FAILS A.T.K.T.   RESULT RESERVED FOR BKLG

ORDN. 1 MARKS :
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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.
        OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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S80058632   SAKSHI SIRPAL   ANUPAMA   , 71201048D   , S80058632 ,   PICT   , S80058632

01. DISCRETE STRUCTURES	PP	100	40	65	P C		11. ENG MATHS III	PP	100	40	54	P		
02. COMPUTER ORGANIZATION	PP	100	40	61	P C		12. COMPUTER GRAPHICS	PP	100	40	56	P		
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	65	P C		13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	53	P		
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	60	P C		14. DATA STRUCTURES AND FILES	PP	100	40	54	P		
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	67	P C		15. DATA COMMUNICATION	PP	100	40	59	P		
06. DIGITAL LABORATORY	TW	50	20	40	P C		16. PROCESSOR INTERFACING LABORATORY	TW	25	10	21	P		
07. DIGITAL LABORATORY	PR	50	20	39	P C		17. PROCESSOR INTERFACING LABORATORY	OR	50	20	40	P		
08. PROGRAMMING LABORATORY	TW	50	20	40	P C		18. DATA STRUCTURES AND FILES LAB	TW	25	10	21	P		
09. PROGRAMMING LABORATORY	PR	50	20	33	P C		19. DATA STRUCTURES AND FILES LAB	PR	50	20	40	P		
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	43	P C		20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	41	P		
							21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	42	P		

GRAND TOTAL = 994/1500, RESULT: FIRST CLASS WITH DISTINCTION
ORDN. 1 MARKS :
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S80058633   SALUNKE GAURI RANGNATH   ANITA   , 71201049B   , S80058633 ,   PICT   , S80058633

01. DISCRETE STRUCTURES	PP	100	40	76	P C		11. ENG MATHS III	PP	100	40	77	P		
02. COMPUTER ORGANIZATION	PP	100	40	78	P C		12. COMPUTER GRAPHICS	PP	100	40	51	P		
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	80	P C		13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	51	P		
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	49	P C		14. DATA STRUCTURES AND FILES	PP	100	40	61	P		
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	68	P C		15. DATA COMMUNICATION	PP	100	40	67	P		
06. DIGITAL LABORATORY	TW	50	20	47	P C		16. PROCESSOR INTERFACING LABORATORY	TW	25	10	23	P		
07. DIGITAL LABORATORY	PR	50	20	42	P C		17. PROCESSOR INTERFACING LABORATORY	OR	50	20	37	P		
08. PROGRAMMING LABORATORY	TW	50	20	48	P C		18. DATA STRUCTURES AND FILES LAB	TW	25	10	24	P		
09. PROGRAMMING LABORATORY	PR	50	20	44	P C		19. DATA STRUCTURES AND FILES LAB	PR	50	20	45	P		
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	48	P C		20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	45	P		
							21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	48	P		

GRAND TOTAL = 1109/1500, RESULT: FIRST CLASS WITH DISTINCTION
ORDN. 1 MARKS :
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S80058634   SAPKALE SNEHAL SURESH   USHA   , 71201052B   , S80058634 ,   PICT   , S80058634

01. DISCRETE STRUCTURES	PP	100	40	40	P C		11. ENG MATHS III	PP	100	40	11	F		
02. COMPUTER ORGANIZATION	PP	100	40	48	P C		12. COMPUTER GRAPHICS	PP	100	40	28	F		
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	40	P C		13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	29	F		
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	45	P C		14. DATA STRUCTURES AND FILES	PP	100	40	40	P		
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	40	P C		15. DATA COMMUNICATION	PP	100	40	40	P		
06. DIGITAL LABORATORY	TW	50	20	31	P C		16. PROCESSOR INTERFACING LABORATORY	TW	25	10	12	P		
07. DIGITAL LABORATORY	PR	50	20	35	P C		17. PROCESSOR INTERFACING LABORATORY	OR	50	20	25	P		
08. PROGRAMMING LABORATORY	TW	50	20	25	P C		18. DATA STRUCTURES AND FILES LAB	TW	25	10	17	P		
09. PROGRAMMING LABORATORY	PR	50	20	30	P C		19. DATA STRUCTURES AND FILES LAB	PR	50	20	25	P		
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	34	P C		20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	34	P		
							21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	28	P		

GRAND TOTAL = 657/1500, RESULT: FAILS A.T.K.T.
ORDN. 1 MARKS :
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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
         OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S80058635   SARODE ASHWINI RAJENDRA   SANGITA   , 71201053L   , S80058635 ,   PICT   , S80058635
01. DISCRETE STRUCTURES   PP   100 40   69 P C   11. ENG MATHS III   PP   100 40   52 P
02. COMPUTER ORGANIZATION   PP   100 40   73 P C   12. COMPUTER GRAPHICS   PP   100 40   51 P
03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 40   63 P C   13. PROCESSOR ARCHITECTURE & INTER. PP 100 40   55 P
04. FUNDAMENTAL OF DATA STRUCTURES PP   100 40   69 P C   14. DATA STRUCTURES AND FILES   PP   100 40   63 P
05. HUMANITIES AND SOCIAL SCIENCES PP   100 40   67 P C   15. DATA COMMUNICATION   PP   100 40   71 P
06. DIGITAL LABORATORY   TW   50 20   45 P C   16. PROCESSOR INTERFACING LABORATORY TW 25 10   21 P
07. DIGITAL LABORATORY   PR   50 20   30 P C   17. PROCESSOR INTERFACING LABORATORY OR 50 20   36 P
08. PROGRAMMING LABORATORY   TW   50 20   46 P C   18. DATA STRUCTURES AND FILES LAB TW   25 10   22 P
09. PROGRAMMING LABORATORY   PR   50 20   42 P C   19. DATA STRUCTURES AND FILES LAB PR   50 20   41 P
10. COMMUNICATION AND LANGUAGE LAB. TW   50 20   45 P C   20. OBJECT ORIENTED PROGRAMMING LAB TW   50 20   45 P
                                                              21. OBJECT ORIENTED PROGRAMMING LAB PR   50 20   42 P
GRAND TOTAL = 1048/1500, RESULT: FIRST CLASS WITH DISTINCTION
ORDN. 1 MARKS :

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S80058636   SAWANT ARUNDHATI ULHAS   MADHAVI   , 71201058M   , S80058636 ,   PICT   , S80058636
01. DISCRETE STRUCTURES   PP   100 40   64 P C   11. ENG MATHS III   PP   100 40   44 P
02. COMPUTER ORGANIZATION   PP   100 40   60 P C   12. COMPUTER GRAPHICS   PP   100 40   48 P
03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 40   71 P C   13. PROCESSOR ARCHITECTURE & INTER. PP 100 40   40 P
04. FUNDAMENTAL OF DATA STRUCTURES PP   100 40   57 P C   14. DATA STRUCTURES AND FILES   PP   100 40   44 P
05. HUMANITIES AND SOCIAL SCIENCES PP   100 40   64 P C   15. DATA COMMUNICATION   PP   100 40   59 P
06. DIGITAL LABORATORY   TW   50 20   39 P C   16. PROCESSOR INTERFACING LABORATORY TW 25 10   21 P
07. DIGITAL LABORATORY   PR   50 20   37 P C   17. PROCESSOR INTERFACING LABORATORY OR 50 20   35 P
08. PROGRAMMING LABORATORY   TW   50 20   39 P C   18. DATA STRUCTURES AND FILES LAB TW   25 10   22 P
09. PROGRAMMING LABORATORY   PR   50 20   40 P C   19. DATA STRUCTURES AND FILES LAB PR   50 20   45 P
10. COMMUNICATION AND LANGUAGE LAB. TW   50 20   39 P C   20. OBJECT ORIENTED PROGRAMMING LAB TW   50 20   42 P
                                                              21. OBJECT ORIENTED PROGRAMMING LAB PR   50 20   41 P
GRAND TOTAL = 951/1500, RESULT: FIRST CLASS
ORDN. 1 MARKS :

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S80058637   SHAH DISHANT UMESH   SHILPA   , 71201059K   , S80058637 ,   PICT   , S80058637
01. DISCRETE STRUCTURES   PP   100 40   40 P C   11. ENG MATHS III   PP   100 40   40 P
02. COMPUTER ORGANIZATION   PP   100 40   59 P C   12. COMPUTER GRAPHICS   PP   100 40   50 P
03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 40   43 P C   13. PROCESSOR ARCHITECTURE & INTER. PP 100 40   40 P
04. FUNDAMENTAL OF DATA STRUCTURES PP   100 40   59 P C   14. DATA STRUCTURES AND FILES   PP   100 40   40 P
05. HUMANITIES AND SOCIAL SCIENCES PP   100 40   68 P C   15. DATA COMMUNICATION   PP   100 40   44 P
06. DIGITAL LABORATORY   TW   50 20   22 P C   16. PROCESSOR INTERFACING LABORATORY TW 25 10   10 P
07. DIGITAL LABORATORY   PR   50 20   31 P   17. PROCESSOR INTERFACING LABORATORY OR 50 20   06 F
08. PROGRAMMING LABORATORY   TW   50 20   26 P C   18. DATA STRUCTURES AND FILES LAB TW   25 10   10 P
09. PROGRAMMING LABORATORY   PR   50 20   30 P C   19. DATA STRUCTURES AND FILES LAB PR   50 20   AA F
10. COMMUNICATION AND LANGUAGE LAB. TW   50 20   31 P C   20. OBJECT ORIENTED PROGRAMMING LAB TW   50 20   20 P
                                                              21. OBJECT ORIENTED PROGRAMMING LAB PR   50 20   AA F
GRAND TOTAL = 669/1500, RESULT: FAILS A.T.K.T.
ORDN. 1 MARKS :

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
       OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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S80058638   SHAH RUTUJA RAJESH              BEENA            , 71201061M   , S80058638 ,  PICT     , S80058638
01. DISCRETE STRUCTURES           PP  100 40  73 P C    11. ENG MATHS III               PP  100 40  82 P
02. COMPUTER ORGANIZATION         PP  100 40  78 P C    12. COMPUTER GRAPHICS           PP  100 40  57 P
03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 40  77 P C  13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 64 P
04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40  73 P C    14. DATA STRUCTURES AND FILES    PP  100 40  71 P
05. HUMANITIES AND SOCIAL SCIENCES PP 100 40  70 P C    15. DATA COMMUNICATION          PP  100 40  69 P
06. DIGITAL LABORATORY            TW   50 20  45 P C    16. PROCESSOR INTERFACING LABORATORY TW  25 10  23 P
07. DIGITAL LABORATORY            PR   50 20  43 P C    17. PROCESSOR INTERFACING LABORATORY OR  50 20  45 P
08. PROGRAMMING LABORATORY        TW   50 20  45 P C    18. DATA STRUCTURES AND FILES LAB TW   25 10  24 P
09. PROGRAMMING LABORATORY        PR   50 20  43 P C    19. DATA STRUCTURES AND FILES LAB PR   50 20  43 P
10. COMMUNICATION AND LANGUAGE LAB. TW 50 20  45 P C    20. OBJECT ORIENTED PROGRAMMING LAB TW  50 20  45 P
                                                        21. OBJECT ORIENTED PROGRAMMING LAB PR  50 20  48 P
GRAND TOTAL = 1163/1500, RESULT: FIRST CLASS WITH DISTINCTION
ORDN. 1 MARKS :
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S80058639   SHARIKA KHURANA               RASHMI           , 71201064F   , S80058639 ,  PICT     , S80058639
01. DISCRETE STRUCTURES           PP  100 40  62 P C    11. ENG MATHS III               PP  100 40  16 F
02. COMPUTER ORGANIZATION         PP  100 40  56 P C    12. COMPUTER GRAPHICS           PP  100 40  46 P
03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 40  64 P C  13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 23 F
04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40  61 P C    14. DATA STRUCTURES AND FILES    PP  100 40  40 P
05. HUMANITIES AND SOCIAL SCIENCES PP 100 40  62 P C    15. DATA COMMUNICATION          PP  100 40  56 P
06. DIGITAL LABORATORY            TW   50 20  33 P C    16. PROCESSOR INTERFACING LABORATORY TW  25 10  14 P
07. DIGITAL LABORATORY            PR   50 20  32 P C    17. PROCESSOR INTERFACING LABORATORY OR  50 20  08 F
08. PROGRAMMING LABORATORY        TW   50 20  37 P C    18. DATA STRUCTURES AND FILES LAB TW   25 10  14 P
09. PROGRAMMING LABORATORY        PR   50 20  38 P C    19. DATA STRUCTURES AND FILES LAB PR   50 20  12 F
10. COMMUNICATION AND LANGUAGE LAB. TW 50 20  39 P C    20. OBJECT ORIENTED PROGRAMMING LAB TW  50 20  33 P
                                                        21. OBJECT ORIENTED PROGRAMMING LAB PR  50 20  40 P
GRAND TOTAL =  786/1500, RESULT: FAILS A.T.K.T.
ORDN. 1 MARKS :
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S80058640   SHIKHAR SHANKAR KHANDELWAL     KANTA            , 71101005G   , S80058640 ,  PICT     , S80058640
01. DISCRETE STRUCTURES           PP  100 40  AA F      11. ENG MATHS III               PP  100 40  AA F
02. COMPUTER ORGANIZATION         PP  100 40  AA F      12. COMPUTER GRAPHICS           PP  100 40  AA F
03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 40  43 P C  13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 AA F
04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40  25 F      14. DATA STRUCTURES AND FILES    PP  100 40  AA F
05. HUMANITIES AND SOCIAL SCIENCES PP 100 40  AA F      15. DATA COMMUNICATION          PP  100 40  AA F
06. DIGITAL LABORATORY            TW   50 20  20 P C    16. PROCESSOR INTERFACING LABORATORY TW  25 10  10 P
07. DIGITAL LABORATORY            PR   50 20  31 P C    17. PROCESSOR INTERFACING LABORATORY OR  50 20  AA F
08. PROGRAMMING LABORATORY        TW   50 20  22 P C    18. DATA STRUCTURES AND FILES LAB TW   25 10  10 P
09. PROGRAMMING LABORATORY        PR   50 20  28 P C    19. DATA STRUCTURES AND FILES LAB PR   50 20  AA F
10. COMMUNICATION AND LANGUAGE LAB. TW 50 20  22 P C    20. OBJECT ORIENTED PROGRAMMING LAB TW  50 20  20 P
                                                        21. OBJECT ORIENTED PROGRAMMING LAB PR  50 20  AA F
GRAND TOTAL =  231/1500, RESULT: FAILS                 RESULT RESERVED FOR BKLG
ORDN. 1 MARKS :
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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.
      OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S80058641   SHINDE ANIKET NAVNATH              BABYNANDA           , 71350935L    , S80058641 ,   PICT     , S80058641
01. DISCRETE STRUCTURES           PP   100  40   40  P C    11. ENG MATHS III                PP   100  40   17  F
02. COMPUTER ORGANIZATION         PP   100  40   62  P C    12. COMPUTER GRAPHICS            PP   100  40   46  P
03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 40   69  P C    13. PROCESSOR ARCHITECTURE & INTER. PP 100 40  45  P
04. FUNDAMENTAL OF DATA STRUCTURES PP  100  40   66  P C    14. DATA STRUCTURES AND FILES     PP   100  40   56  P
05. HUMANITIES AND SOCIAL SCIENCES PP  100  40   57  P C    15. DATA COMMUNICATION           PP   100  40   62  P
06. DIGITAL LABORATORY            TW    50  20   38  P C    16. PROCESSOR INTERFACING LABORATORY TW 25 10  14  P
07. DIGITAL LABORATORY            PR    50  20   20  P C    17. PROCESSOR INTERFACING LABORATORY OR 50 20  31  P
08. PROGRAMMING LABORATORY        TW    50  20   40  P C    18. DATA STRUCTURES AND FILES LAB  TW    25  10   19  P
09. PROGRAMMING LABORATORY        PR    50  20   42  P C    19. DATA STRUCTURES AND FILES LAB  PR    50  20   47  P
10. COMMUNICATION AND LANGUAGE LAB. TW 50  20   38  P C    20. OBJECT ORIENTED PROGRAMMING LAB TW   50  20   40  P
                                                           21. OBJECT ORIENTED PROGRAMMING LAB PR   50  20   35  P
GRAND TOTAL =  884/1500, RESULT: FAILS A.T.K.T.
ORDN. 1 MARKS :
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S80058642   SHUBHAM BOHRA                      MONA                , 71201073E    , S80058642 ,   PICT     , S80058642
01. DISCRETE STRUCTURES           PP   100  40   57  P C    11. ENG MATHS III                PP   100  40   60  P
02. COMPUTER ORGANIZATION         PP   100  40   42  P C    12. COMPUTER GRAPHICS            PP   100  40   44  P
03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 40   41  P C    13. PROCESSOR ARCHITECTURE & INTER. PP 100 40  AA  F
04. FUNDAMENTAL OF DATA STRUCTURES PP  100  40   46  P C    14. DATA STRUCTURES AND FILES     PP   100  40   42  P
05. HUMANITIES AND SOCIAL SCIENCES PP  100  40   41  P C    15. DATA COMMUNICATION           PP   100  40   44  P
06. DIGITAL LABORATORY            TW    50  20   24  P C    16. PROCESSOR INTERFACING LABORATORY TW 25 10  11  P
07. DIGITAL LABORATORY            PR    50  20   25  P C    17. PROCESSOR INTERFACING LABORATORY OR 50 20  06  F
08. PROGRAMMING LABORATORY        TW    50  20   29  P C    18. DATA STRUCTURES AND FILES LAB  TW    25  10   11  P
09. PROGRAMMING LABORATORY        PR    50  20   32  P C    19. DATA STRUCTURES AND FILES LAB  PR    50  20   25  P
10. COMMUNICATION AND LANGUAGE LAB. TW 50  20   31  P C    20. OBJECT ORIENTED PROGRAMMING LAB TW   50  20   28  P
                                                           21. OBJECT ORIENTED PROGRAMMING LAB PR   50  20   37  P
GRAND TOTAL =  676/1500, RESULT: FAILS A.T.K.T.
ORDN. 1 MARKS :
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S80058643   SIDDHARTH DALAL                    REKHA               , 71201075M    , S80058643 ,   PICT     , S80058643
01. DISCRETE STRUCTURES           PP   100  40   64  P C    11. ENG MATHS III                PP   100  40   79  P
02. COMPUTER ORGANIZATION         PP   100  40   68  P C    12. COMPUTER GRAPHICS            PP   100  40   50  P
03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 40   76  P C    13. PROCESSOR ARCHITECTURE & INTER. PP 100 40  43  P
04. FUNDAMENTAL OF DATA STRUCTURES PP  100  40   70  P C    14. DATA STRUCTURES AND FILES     PP   100  40   59  P
05. HUMANITIES AND SOCIAL SCIENCES PP  100  40   55  P C    15. DATA COMMUNICATION           PP   100  40   58  P
06. DIGITAL LABORATORY            TW    50  20   38  P C    16. PROCESSOR INTERFACING LABORATORY TW 25 10  21  P
07. DIGITAL LABORATORY            PR    50  20   44  P C    17. PROCESSOR INTERFACING LABORATORY OR 50 20  32  P
08. PROGRAMMING LABORATORY        TW    50  20   39  P C    18. DATA STRUCTURES AND FILES LAB  TW    25  10   21  P
09. PROGRAMMING LABORATORY        PR    50  20   43  P C    19. DATA STRUCTURES AND FILES LAB  PR    50  20   41  P
10. COMMUNICATION AND LANGUAGE LAB. TW 50  20   41  P C    20. OBJECT ORIENTED PROGRAMMING LAB TW   50  20   43  P
                                                           21. OBJECT ORIENTED PROGRAMMING LAB PR   50  20   36  P
GRAND TOTAL = 1021/1500, RESULT: FIRST CLASS WITH DISTINCTION
ORDN. 1 MARKS :
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     NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.
           OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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    S80058644   SIDHESH BADRINARAYAN   KAUSALYA   , 71201077H   , S80058644 ,   PICT   , S80058644
    01. DISCRETE STRUCTURES   PP   100 40   60 P C   11. ENG MATHS III   PP   100 40   61 P
    02. COMPUTER ORGANIZATION   PP   100 40   83 P C   12. COMPUTER GRAPHICS   PP   100 40   62 P
    03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 40   80 P C   13. PROCESSOR ARCHITECTURE & INTER. PP 100 40   67 P
    04. FUNDAMENTAL OF DATA STRUCTURES PP   100 40   71 P C   14. DATA STRUCTURES AND FILES   PP   100 40   69 P
    05. HUMANITIES AND SOCIAL SCIENCES PP   100 40   73 P C   15. DATA COMMUNICATION   PP   100 40   62 P
    06. DIGITAL LABORATORY   TW   50 20   43 P C   16. PROCESSOR INTERFACING LABORATORY TW   25 10   20 P
    07. DIGITAL LABORATORY   PR   50 20   38 P C   17. PROCESSOR INTERFACING LABORATORY OR   50 20   42 P
    08. PROGRAMMING LABORATORY   TW   50 20   44 P C   18. DATA STRUCTURES AND FILES LAB   TW   25 10   24 P
    09. PROGRAMMING LABORATORY   PR   50 20   45 P C   19. DATA STRUCTURES AND FILES LAB   PR   50 20   48 P
    10. COMMUNICATION AND LANGUAGE LAB. TW 50 20   42 P C   20. OBJECT ORIENTED PROGRAMMING LAB TW   50 20   42 P
                                                                 21. OBJECT ORIENTED PROGRAMMING LAB PR   50 20   46 P
    GRAND TOTAL = 1122/1500, RESULT: FIRST CLASS WITH DISTINCTION
    ORDN. 1 MARKS :
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    S80058645   SYED JUNAID ALI MASOOD ALI   SYEDA JABEEN FATEMA , 71201088C   , S80058645 ,   PICT   , S80058645
    01. DISCRETE STRUCTURES   PP   100 40   46 P   11. ENG MATHS III   PP   100 40   40 P
    02. COMPUTER ORGANIZATION   PP   100 40   63 P C   12. COMPUTER GRAPHICS   PP   100 40   43 P
    03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 40   40 P C   13. PROCESSOR ARCHITECTURE & INTER. PP 100 40   40 P
    04. FUNDAMENTAL OF DATA STRUCTURES PP   100 40   40 P C   14. DATA STRUCTURES AND FILES   PP   100 40   40 P
    05. HUMANITIES AND SOCIAL SCIENCES PP   100 40   63 P C   15. DATA COMMUNICATION   PP   100 40   52 P
    06. DIGITAL LABORATORY   TW   50 20   39 P C   16. PROCESSOR INTERFACING LABORATORY TW   25 10   17 P
    07. DIGITAL LABORATORY   PR   50 20   24 P C   17. PROCESSOR INTERFACING LABORATORY OR   50 20   21 P
    08. PROGRAMMING LABORATORY   TW   50 20   35 P C   18. DATA STRUCTURES AND FILES LAB   TW   25 10   19 P
    09. PROGRAMMING LABORATORY   PR   50 20   35 P C   19. DATA STRUCTURES AND FILES LAB   PR   50 20   40 P
    10. COMMUNICATION AND LANGUAGE LAB. TW 50 20   39 P C   20. OBJECT ORIENTED PROGRAMMING LAB TW   50 20   35 P
                                                                 21. OBJECT ORIENTED PROGRAMMING LAB PR   50 20   36 P
    GRAND TOTAL = 807/1500, RESULT: SECOND CLASS
    ORDN. 1 MARKS :
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    S80058646   TAKALKAR TANMAYEE SUHAS   JYOTI   , 71201089M   , S80058646 ,   PICT   , S80058646
    01. DISCRETE STRUCTURES   PP   100 40   74 P C   11. ENG MATHS III   PP   100 40   40 P
    02. COMPUTER ORGANIZATION   PP   100 40   66 P C   12. COMPUTER GRAPHICS   PP   100 40   64 P
    03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 40   69 P C   13. PROCESSOR ARCHITECTURE & INTER. PP 100 40   73 P
    04. FUNDAMENTAL OF DATA STRUCTURES PP   100 40   62 P C   14. DATA STRUCTURES AND FILES   PP   100 40   59 P
    05. HUMANITIES AND SOCIAL SCIENCES PP   100 40   64 P C   15. DATA COMMUNICATION   PP   100 40   63 P
    06. DIGITAL LABORATORY   TW   50 20   35 P C   16. PROCESSOR INTERFACING LABORATORY TW   25 10   18 P
    07. DIGITAL LABORATORY   PR   50 20   39 P C   17. PROCESSOR INTERFACING LABORATORY OR   50 20   41 P
    08. PROGRAMMING LABORATORY   TW   50 20   38 P C   18. DATA STRUCTURES AND FILES LAB   TW   25 10   21 P
    09. PROGRAMMING LABORATORY   PR   50 20   43 P C   19. DATA STRUCTURES AND FILES LAB   PR   50 20   43 P
    10. COMMUNICATION AND LANGUAGE LAB. TW 50 20   35 P C   20. OBJECT ORIENTED PROGRAMMING LAB TW   50 20   36 P
                                                                 21. OBJECT ORIENTED PROGRAMMING LAB PR   50 20   35 P
    GRAND TOTAL = 1018/1500, RESULT: FIRST CLASS WITH DISTINCTION
    ORDN. 1 MARKS :
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    NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.
          OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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    S80058647   THORAT SAYALI SURESH   SULABHA   , 71201095F   , S80058647 ,   PICT   , S80058647
    01. DISCRETE STRUCTURES   PP   100 40   44 P C   11. ENG MATHS III   PP   100 40   40 P
    02. COMPUTER ORGANIZATION   PP   100 40   40 P C   12. COMPUTER GRAPHICS   PP   100 40   40 P
    03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 40   40 P C   13. PROCESSOR ARCHITECTURE & INTER. PP   100 40   40 P
    04. FUNDAMENTAL OF DATA STRUCTURES PP   100 40   40 P C   14. DATA STRUCTURES AND FILES   PP   100 40   54 P
    05. HUMANITIES AND SOCIAL SCIENCES PP   100 40   41 P C   15. DATA COMMUNICATION   PP   100 40   42 P
    06. DIGITAL LABORATORY   TW   50 20   35 P C   16. PROCESSOR INTERFACING LABORATORY TW 25 10   16 P
    07. DIGITAL LABORATORY   PR   50 20   33 P C   17. PROCESSOR INTERFACING LABORATORY OR 50 20   22 P
    08. PROGRAMMING LABORATORY   TW   50 20   34 P C   18. DATA STRUCTURES AND FILES LAB   TW   25 10   16 P
    09. PROGRAMMING LABORATORY   PR   50 20   26 P C   19. DATA STRUCTURES AND FILES LAB   PR   50 20   30 P
    10. COMMUNICATION AND LANGUAGE LAB. TW   50 20   39 P C   20. OBJECT ORIENTED PROGRAMMING LAB TW   50 20   32 P
                                                                 21. OBJECT ORIENTED PROGRAMMING LAB PR   50 20   37 P
GRAND TOTAL = 741+09/1500, RESULT: SECOND CLASS [O.2]
ORDN. 1 MARKS :
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    S80058648   TIKONE SNEHAL SHAM   SUNITA   , 71350936J   , S80058648 ,   PICT   , S80058648
    01. DISCRETE STRUCTURES   PP   100 40   45 P C   11. ENG MATHS III   PP   100 40   40 P
    02. COMPUTER ORGANIZATION   PP   100 40   44 P C   12. COMPUTER GRAPHICS   PP   100 40   42 P
    03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 40   41 P C   13. PROCESSOR ARCHITECTURE & INTER. PP   100 40   46 P
    04. FUNDAMENTAL OF DATA STRUCTURES PP   100 40   56 P C   14. DATA STRUCTURES AND FILES   PP   100 40   56 P
    05. HUMANITIES AND SOCIAL SCIENCES PP   100 40   58 P C   15. DATA COMMUNICATION   PP   100 40   50 P
    06. DIGITAL LABORATORY   TW   50 20   37 P C   16. PROCESSOR INTERFACING LABORATORY TW 25 10   22 P
    07. DIGITAL LABORATORY   PR   50 20   25 P C   17. PROCESSOR INTERFACING LABORATORY OR 50 20   20 P
    08. PROGRAMMING LABORATORY   TW   50 20   41 P C   18. DATA STRUCTURES AND FILES LAB   TW   25 10   21 P
    09. PROGRAMMING LABORATORY   PR   50 20   30 P C   19. DATA STRUCTURES AND FILES LAB   PR   50 20   15# P
    10. COMMUNICATION AND LANGUAGE LAB. TW   50 20   42 P C   20. OBJECT ORIENTED PROGRAMMING LAB TW   50 20   44 P
                                                                 21. OBJECT ORIENTED PROGRAMMING LAB PR   50 20   45 P
GRAND TOTAL = 820/1500, RESULT: SECOND CLASS   # [O.4]
ORDN. 1 MARKS :
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    S80058649   WADKE RUCHA VIJAY   ANURADHA   , 71201114F   , S80058649 ,   PICT   , S80058649
    01. DISCRETE STRUCTURES   PP   100 40   25 F   11. ENG MATHS III   PP   100 40   16 F
    02. COMPUTER ORGANIZATION   PP   100 40   55 P   12. COMPUTER GRAPHICS   PP   100 40   40 P
    03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 40   50 P   13. PROCESSOR ARCHITECTURE & INTER. PP   100 40   40 P
    04. FUNDAMENTAL OF DATA STRUCTURES PP   100 40   42 P   14. DATA STRUCTURES AND FILES   PP   100 40   40 P
    05. HUMANITIES AND SOCIAL SCIENCES PP   100 40   67 P   15. DATA COMMUNICATION   PP   100 40   41 P
    06. DIGITAL LABORATORY   TW   50 20   35 P C   16. PROCESSOR INTERFACING LABORATORY TW 25 10   17 P
    07. DIGITAL LABORATORY   PR   50 20   05 F   17. PROCESSOR INTERFACING LABORATORY OR 50 20   09 F
    08. PROGRAMMING LABORATORY   TW   50 20   33 P C   18. DATA STRUCTURES AND FILES LAB   TW   25 10   19 P
    09. PROGRAMMING LABORATORY   PR   50 20   27 P   19. DATA STRUCTURES AND FILES LAB   PR   50 20   10 F
    10. COMMUNICATION AND LANGUAGE LAB. TW   50 20   40 P C   20. OBJECT ORIENTED PROGRAMMING LAB TW   50 20   38 P
                                                                 21. OBJECT ORIENTED PROGRAMMING LAB PR   50 20   40 P
GRAND TOTAL = 689/1500, RESULT: FAILS A.T.K.T.   RESULT RESERVED FOR BKLG
ORDN. 1 MARKS :
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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
       OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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    S80058650   WALZADE ROHAN SUNIL   MANISHA   , 71201118J   , S80058650 ,   PICT   , S80058650
    01. DISCRETE STRUCTURES   PP   100 40   46 P C   11. ENG MATHS III   PP   100 40   50 P
    02. COMPUTER ORGANIZATION   PP   100 40   45 P C   12. COMPUTER GRAPHICS   PP   100 40   40 P
    03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 40   41 P C   13. PROCESSOR ARCHITECTURE & INTER. PP 100 40   47 P
    04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40   47 P C   14. DATA STRUCTURES AND FILES   PP   100 40   46 P
    05. HUMANITIES AND SOCIAL SCIENCES PP 100 40   68 P C   15. DATA COMMUNICATION   PP   100 40   40 P
    06. DIGITAL LABORATORY   TW   50 20   32 P C   16. PROCESSOR INTERFACING LABORATORY TW   25 10   14 P
    07. DIGITAL LABORATORY   PR   50 20   20 P C   17. PROCESSOR INTERFACING LABORATORY OR   50 20   06 F
    08. PROGRAMMING LABORATORY   TW   50 20   30 P C   18. DATA STRUCTURES AND FILES LAB   TW   25 10   16 P
    09. PROGRAMMING LABORATORY   PR   50 20   26 P C   19. DATA STRUCTURES AND FILES LAB   PR   50 20   35 P
    10. COMMUNICATION AND LANGUAGE LAB. TW 50 20   36 P C   20. OBJECT ORIENTED PROGRAMMING LAB TW   50 20   27 P
                                                                21. OBJECT ORIENTED PROGRAMMING LAB PR   50 20   36 P
GRAND TOTAL = 748/1500, RESULT: FAILS A.T.K.T.
ORDN. 1 MARKS :

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    S80058651   YADAV AKASH VINOD   VIJAYA   , 71350937G   , S80058651 ,   PICT   , S80058651
    01. DISCRETE STRUCTURES   PP   100 40   50 P   11. ENG MATHS III   PP   100 40   20 F
    02. COMPUTER ORGANIZATION   PP   100 40   52 P C   12. COMPUTER GRAPHICS   PP   100 40   40 P
    03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 40   63 P C   13. PROCESSOR ARCHITECTURE & INTER. PP 100 40   44 P
    04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40   49 P C   14. DATA STRUCTURES AND FILES   PP   100 40   60 P
    05. HUMANITIES AND SOCIAL SCIENCES PP 100 40   55 P C   15. DATA COMMUNICATION   PP   100 40   49 P
    06. DIGITAL LABORATORY   TW   50 20   33 P C   16. PROCESSOR INTERFACING LABORATORY TW   25 10   13 P
    07. DIGITAL LABORATORY   PR   50 20   20 P C   17. PROCESSOR INTERFACING LABORATORY OR   50 20   23 P
    08. PROGRAMMING LABORATORY   TW   50 20   33 P C   18. DATA STRUCTURES AND FILES LAB   TW   25 10   15 P
    09. PROGRAMMING LABORATORY   PR   50 20   34 P C   19. DATA STRUCTURES AND FILES LAB   PR   50 20   40 P
    10. COMMUNICATION AND LANGUAGE LAB. TW 50 20   34 P C   20. OBJECT ORIENTED PROGRAMMING LAB TW   50 20   35 P
                                                                21. OBJECT ORIENTED PROGRAMMING LAB PR   50 20   33 P
GRAND TOTAL = 795/1500, RESULT: FAILS A.T.K.T.
ORDN. 1 MARKS :

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    S80058652   YADAV GAURAV RAJENDRA   RASHMI   , 71201121J   , S80058652 ,   PICT   , S80058652
    01. DISCRETE STRUCTURES   PP   100 40   43 P C   11. ENG MATHS III   PP   100 40   16 F
    02. COMPUTER ORGANIZATION   PP   100 40   65 P C   12. COMPUTER GRAPHICS   PP   100 40   54 P
    03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 40   62 P C   13. PROCESSOR ARCHITECTURE & INTER. PP 100 40   55 P
    04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40   62 P C   14. DATA STRUCTURES AND FILES   PP   100 40   69 P
    05. HUMANITIES AND SOCIAL SCIENCES PP 100 40   63 P C   15. DATA COMMUNICATION   PP   100 40   58 P
    06. DIGITAL LABORATORY   TW   50 20   41 P C   16. PROCESSOR INTERFACING LABORATORY TW   25 10   17 P
    07. DIGITAL LABORATORY   PR   50 20   42 P C   17. PROCESSOR INTERFACING LABORATORY OR   50 20   30 P
    08. PROGRAMMING LABORATORY   TW   50 20   39 P C   18. DATA STRUCTURES AND FILES LAB   TW   25 10   20 P
    09. PROGRAMMING LABORATORY   PR   50 20   30 P C   19. DATA STRUCTURES AND FILES LAB   PR   50 20   41 P
    10. COMMUNICATION AND LANGUAGE LAB. TW 50 20   42 P C   20. OBJECT ORIENTED PROGRAMMING LAB TW   50 20   38 P
                                                                21. OBJECT ORIENTED PROGRAMMING LAB PR   50 20   35 P
GRAND TOTAL = 922/1500, RESULT: FAILS A.T.K.T.
ORDN. 1 MARKS :

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
      OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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 S80058653   ZANJARE SHRIDATTA GOVIND   PRAMILA   , 71201125M   , S80058653 ,   PICT   , S80058653
 01. DISCRETE STRUCTURES   PP   100 40   48 P   11. ENG MATHS III   PP   100 40   27 F
 02. COMPUTER ORGANIZATION   PP   100 40   43 P C   12. COMPUTER GRAPHICS   PP   100 40   51 P
 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 40   49 P C   13. PROCESSOR ARCHITECTURE & INTER. PP 100 40   40 P
 04. FUNDAMENTAL OF DATA STRUCTURES PP   100 40   46 P C   14. DATA STRUCTURES AND FILES   PP   100 40   40 P
 05. HUMANITIES AND SOCIAL SCIENCES PP   100 40   68 P C   15. DATA COMMUNICATION   PP   100 40   54 P
 06. DIGITAL LABORATORY   TW   50 20   42 P C   16. PROCESSOR INTERFACING LABORATORY TW 25 10   18 P
 07. DIGITAL LABORATORY   PR   50 20   27 P   17. PROCESSOR INTERFACING LABORATORY OR 50 20   08 F
 08. PROGRAMMING LABORATORY   TW   50 20   42 P C   18. DATA STRUCTURES AND FILES LAB TW   25 10   20 P
 09. PROGRAMMING LABORATORY   PR   50 20   25 P C   19. DATA STRUCTURES AND FILES LAB PR   50 20   28 P
 10. COMMUNICATION AND LANGUAGE LAB. TW   50 20   46 P C   20. OBJECT ORIENTED PROGRAMMING LAB TW   50 20   40 P
                                                                21. OBJECT ORIENTED PROGRAMMING LAB PR   50 20   35 P
GRAND TOTAL = 797/1500, RESULT: FAILS A.T.K.T.
ORDN. 1 MARKS :
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 S80058654   ADEP AKASH RAJENDRA   KALPANA   , 71100717K   , S80058654 ,   PICT   , S80058654
 01. DISCRETE STRUCTURES   PP   100 40   40 P C   11. ENG MATHS III   PP   100 40   40 P
 02. COMPUTER ORGANIZATION   PP   100 40   40 P C   12. COMPUTER GRAPHICS   PP   100 40   57 P C
 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 40   40 P C   13. PROCESSOR ARCHITECTURE & INTER. PP 100 40   40 P C
 04. FUNDAMENTAL OF DATA STRUCTURES PP   100 40   40 P C   14. DATA STRUCTURES AND FILES   PP   100 40   40 P C
 05. HUMANITIES AND SOCIAL SCIENCES PP   100 40   40 P C   15. DATA COMMUNICATION   PP   100 40   55 P C
 06. DIGITAL LABORATORY   TW   50 20   31 P C   16. PROCESSOR INTERFACING LABORATORY TW 25 10   10 P C
 07. DIGITAL LABORATORY   PR   50 20   23 P C   17. PROCESSOR INTERFACING LABORATORY OR 50 20   22 P C
 08. PROGRAMMING LABORATORY   TW   50 20   27 P C   18. DATA STRUCTURES AND FILES LAB TW   25 10   11 P C
 09. PROGRAMMING LABORATORY   PR   50 20   20 P C   19. DATA STRUCTURES AND FILES LAB PR   50 20   38 P C
 10. COMMUNICATION AND LANGUAGE LAB. TW   50 20   35 P C   20. OBJECT ORIENTED PROGRAMMING LAB TW   50 20   33 P C
                                                                21. OBJECT ORIENTED PROGRAMMING LAB PR   50 20   32 P C
GRAND TOTAL = 714/1500, RESULT: PASS CLASS
ORDN. 1 MARKS :
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 S80058655   ADITYA SINGH SOLANKI   DIMPLE   , 71100719F   , S80058655 ,   PICT   , S80058655
 01. DISCRETE STRUCTURES   PP   100 40   53 P C   11. ENG MATHS III   PP   100 40   40 P C
 02. COMPUTER ORGANIZATION   PP   100 40   51 P C   12. COMPUTER GRAPHICS   PP   100 40   43 P C
 03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 40   40 P C   13. PROCESSOR ARCHITECTURE & INTER. PP 100 40   40 P C
 04. FUNDAMENTAL OF DATA STRUCTURES PP   100 40   42 P C   14. DATA STRUCTURES AND FILES   PP   100 40   40 P C
 05. HUMANITIES AND SOCIAL SCIENCES PP   100 40   46 P C   15. DATA COMMUNICATION   PP   100 40   41 P C
 06. DIGITAL LABORATORY   TW   50 20   30 P C   16. PROCESSOR INTERFACING LABORATORY TW 25 10   18 P C
 07. DIGITAL LABORATORY   PR   50 20   30 P C   17. PROCESSOR INTERFACING LABORATORY OR 50 20   20 P C
 08. PROGRAMMING LABORATORY   TW   50 20   34 P C   18. DATA STRUCTURES AND FILES LAB TW   25 10   17 P C
 09. PROGRAMMING LABORATORY   PR   50 20   23 P C   19. DATA STRUCTURES AND FILES LAB PR   50 20   21 P
 10. COMMUNICATION AND LANGUAGE LAB. TW   50 20   36 P C   20. OBJECT ORIENTED PROGRAMMING LAB TW   50 20   36 P C
                                                                21. OBJECT ORIENTED PROGRAMMING LAB PR   50 20   25 P
GRAND TOTAL = 726/1500, RESULT: PASS CLASS
ORDN. 1 MARKS :
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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S80058658 ANKIT V BANSAL   URMILA   , 71045371L   , S80058658 , PICT   , S80058658

01. DISCRETE STRUCTURES	PP	100	40	40	P C		11. ENG MATHS III	PP	100	40	15	F
02. COMPUTER ORGANIZATION	PP	100	40	40	P C		12. COMPUTER GRAPHICS	PP	100	40	40	P C
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	40	P C		13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	48	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	40	P C		14. DATA STRUCTURES AND FILES	PP	100	40	40	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	40	P C		15. DATA COMMUNICATION	PP	100	40	40	P C
06. DIGITAL LABORATORY	TW	50	20	25	P C		16. PROCESSOR INTERFACING LABORATORY	TW	25	10	10	P C
07. DIGITAL LABORATORY	PR	50	20	21	P C		17. PROCESSOR INTERFACING LABORATORY	OR	50	20	20	P
08. PROGRAMMING LABORATORY	TW	50	20	39	P C		18. DATA STRUCTURES AND FILES LAB	TW	25	10	10	P C
09. PROGRAMMING LABORATORY	PR	50	20	30	P		19. DATA STRUCTURES AND FILES LAB	PR	50	20	40	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	28	P C		20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	30	P C
							21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	26	P

GRAND TOTAL = 662/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

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S80058661 BACHHAV HARSHAL RAGHUNATH   VIMAL   , 70925349E   , S80058661 , PICT   , S80058661

01. DISCRETE STRUCTURES	PP	100	40	AA	F		11. ENG MATHS III	PP	100	40	AA	F
02. COMPUTER ORGANIZATION	PP	100	40	05	F		12. COMPUTER GRAPHICS	PP	100	40	AA	F
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	AA	F		13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	AA	F
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	AA	F		14. DATA STRUCTURES AND FILES	PP	100	40	AA	F
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	40	P C		15. DATA COMMUNICATION	PP	100	40	AA	F
06. DIGITAL LABORATORY	TW	50	20	25	P C		16. PROCESSOR INTERFACING LABORATORY	TW	25	10	10	P C
07. DIGITAL LABORATORY	PR	50	20	AA	F		17. PROCESSOR INTERFACING LABORATORY	OR	50	20	AA	F
08. PROGRAMMING LABORATORY	TW	50	20	32	P C		18. DATA STRUCTURES AND FILES LAB	TW	25	10	10	P C
09. PROGRAMMING LABORATORY	PR	50	20	AA	F		19. DATA STRUCTURES AND FILES LAB	PR	50	20	AA	F
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	25	P C		20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	20	P C
							21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	AA	F

GRAND TOTAL = 167/1500, RESULT: FAILS

ORDN. 1 MARKS :

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S80058663 BHAND PRITI PARAJI   RUKHMINI   , 71100748K   , S80058663 , PICT   , S80058663

01. DISCRETE STRUCTURES	PP	100	40	57	P C		11. ENG MATHS III	PP	100	40	46	P C
02. COMPUTER ORGANIZATION	PP	100	40	47	P C		12. COMPUTER GRAPHICS	PP	100	40	44	P C
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	40	P C		13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	40	P C
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	45	P C		14. DATA STRUCTURES AND FILES	PP	100	40	61	P C
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	42	P C		15. DATA COMMUNICATION	PP	100	40	42	P C
06. DIGITAL LABORATORY	TW	50	20	30	P C		16. PROCESSOR INTERFACING LABORATORY	TW	25	10	17	P C
07. DIGITAL LABORATORY	PR	50	20	32	P C		17. PROCESSOR INTERFACING LABORATORY	OR	50	20	20	P C
08. PROGRAMMING LABORATORY	TW	50	20	37	P C		18. DATA STRUCTURES AND FILES LAB	TW	25	10	16	P C
09. PROGRAMMING LABORATORY	PR	50	20	20$	P C		19. DATA STRUCTURES AND FILES LAB	PR	50	20	25	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	38	P C		20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	33	P C
							21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	30	P C

GRAND TOTAL = 762/1500, RESULT: SECOND CLASS   [$ O.1]

ORDN. 1 MARKS : (09)2,

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S80058664   CHAUDHARI PRANITA RAVISHANKAR   MEENA   , 71100764M   , S80058664 ,   PICT   , S80058664

01. DISCRETE STRUCTURES	PP	100	40	45	P C		11. ENG MATHS III	PP	100	40	40	P C
02. COMPUTER ORGANIZATION	PP	100	40	40	P C		12. COMPUTER GRAPHICS	PP	100	40	45	P C
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	49	P C		13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	40	P C
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	44	P C		14. DATA STRUCTURES AND FILES	PP	100	40	52	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	40	P C		15. DATA COMMUNICATION	PP	100	40	40	P
06. DIGITAL LABORATORY	TW	50	20	36	P C		16. PROCESSOR INTERFACING LABORATORY	TW	25	10	14	P C
07. DIGITAL LABORATORY	PR	50	20	35	P C		17. PROCESSOR INTERFACING LABORATORY	OR	50	20	28	P C
08. PROGRAMMING LABORATORY	TW	50	20	37	P C		18. DATA STRUCTURES AND FILES LAB	TW	25	10	10	P C
09. PROGRAMMING LABORATORY	PR	50	20	26	P C		19. DATA STRUCTURES AND FILES LAB	PR	50	20	20	P C
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	37	P C		20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	36	P C
							21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	35	P C

GRAND TOTAL = 749+01/1500, RESULT: SECOND CLASS [O.2]

ORDN. 1 MARKS :

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S80058665   CHOKHAR PRITHVIRAJ BALASAHEB   VIMAL   , 71100770F   , S80058665 ,   PICT   , S80058665

01. DISCRETE STRUCTURES	PP	100	40	40	P C		11. ENG MATHS III	PP	100	40	11	F
02. COMPUTER ORGANIZATION	PP	100	40	40	P C		12. COMPUTER GRAPHICS	PP	100	40	20	F
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	42	P		13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	19	F
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	40	P C		14. DATA STRUCTURES AND FILES	PP	100	40	40	P C
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	40	P C		15. DATA COMMUNICATION	PP	100	40	40	P
06. DIGITAL LABORATORY	TW	50	20	23	P C		16. PROCESSOR INTERFACING LABORATORY	TW	25	10	15	P C
07. DIGITAL LABORATORY	PR	50	20	22	P C		17. PROCESSOR INTERFACING LABORATORY	OR	50	20	27	P C
08. PROGRAMMING LABORATORY	TW	50	20	29	P C		18. DATA STRUCTURES AND FILES LAB	TW	25	10	15	P C
09. PROGRAMMING LABORATORY	PR	50	20	30	P C		19. DATA STRUCTURES AND FILES LAB	PR	50	20	AA	F
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	31	P C		20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	29	P C
							21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	21	P

GRAND TOTAL = 574/1500, RESULT: FAILS A.T.K.T.   RESULT RESERVED FOR BKLG

ORDN. 1 MARKS :

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S80058666   DAHAWAD SUNIL NAMDEO   VANITA   , 71241757F   , S80058666 ,   PICT   , S80058666

01. DISCRETE STRUCTURES	PP	100	40	40	P		11. ENG MATHS III	PP	100	40	24	F
02. COMPUTER ORGANIZATION	PP	100	40	57	P C		12. COMPUTER GRAPHICS	PP	100	40	40	P C
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	40	P C		13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	54	P C
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	54	P C		14. DATA STRUCTURES AND FILES	PP	100	40	40	P C
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	42	P C		15. DATA COMMUNICATION	PP	100	40	43	P C
06. DIGITAL LABORATORY	TW	50	20	35	P C		16. PROCESSOR INTERFACING LABORATORY	TW	25	10	13	P C
07. DIGITAL LABORATORY	PR	50	20	22	P C		17. PROCESSOR INTERFACING LABORATORY	OR	50	20	29	P C
08. PROGRAMMING LABORATORY	TW	50	20	38	P C		18. DATA STRUCTURES AND FILES LAB	TW	25	10	15	P C
09. PROGRAMMING LABORATORY	PR	50	20	32	P C		19. DATA STRUCTURES AND FILES LAB	PR	50	20	37	P C
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	39	P C		20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	35	P C
							21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	33	P

GRAND TOTAL = 762/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.
        OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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S80058669   GAWANDE ANAGHA ARVIND   MAYA   , 71100800M   , S80058669 ,   PICT   , S80058669
01. DISCRETE STRUCTURES   PP   100 40   47 P C   11. ENG MATHS III   PP   100 40   24 F
02. COMPUTER ORGANIZATION   PP   100 40   42 P C   12. COMPUTER GRAPHICS   PP   100 40   53 P C
03. DIGITAL ELECTRONICS & LOGIC DESIGPP   100 40   58 P C   13. PROCESSOR ARCHITECTURE & INTER. PP   100 40   52 P C
04. FUNDAMENTAL OF DATA STRUCTURES PP   100 40   43 P C   14. DATA STRUCTURES AND FILES   PP   100 40   53 P C
05. HUMANITIES AND SOCIAL SCIENCES PP   100 40   47 P C   15. DATA COMMUNICATION   PP   100 40   51 P C
06. DIGITAL LABORATORY   TW   50 20   33 P C   16. PROCESSOR INTERFACING LABORATORY TW   25 10   18 P C
07. DIGITAL LABORATORY   PR   50 20   32 P C   17. PROCESSOR INTERFACING LABORATORY OR   50 20   28 P C
08. PROGRAMMING LABORATORY   TW   50 20   38 P C   18. DATA STRUCTURES AND FILES LAB   TW   25 10   16 P C
09. PROGRAMMING LABORATORY   PR   50 20   23 P C   19. DATA STRUCTURES AND FILES LAB   PR   50 20   38 P C
10. COMMUNICATION AND LANGUAGE LAB. TW   50 20   39 P C   20. OBJECT ORIENTED PROGRAMMING LAB TW   50 20   36 P C
                                                           21. OBJECT ORIENTED PROGRAMMING LAB PR   50 20   37 P C
GRAND TOTAL = 808/1500, RESULT: FAILS A.T.K.T.
ORDN. 1 MARKS :
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S80058670   GHODAKE PRAJAKTA DHANANJAY   SWATI   , 71100802H   , S80058670 ,   PICT   , S80058670
01. DISCRETE STRUCTURES   PP   100 40   49 P C   11. ENG MATHS III   PP   100 40   40 P
02. COMPUTER ORGANIZATION   PP   100 40   47 P C   12. COMPUTER GRAPHICS   PP   100 40   45 P C
03. DIGITAL ELECTRONICS & LOGIC DESIGPP   100 40   43 P C   13. PROCESSOR ARCHITECTURE & INTER. PP   100 40   40 P C
04. FUNDAMENTAL OF DATA STRUCTURES PP   100 40   40 P C   14. DATA STRUCTURES AND FILES   PP   100 40   43 P C
05. HUMANITIES AND SOCIAL SCIENCES PP   100 40   45 P C   15. DATA COMMUNICATION   PP   100 40   47 P C
06. DIGITAL LABORATORY   TW   50 20   30 P C   16. PROCESSOR INTERFACING LABORATORY TW   25 10   10 P C
07. DIGITAL LABORATORY   PR   50 20   25 P C   17. PROCESSOR INTERFACING LABORATORY OR   50 20   20 P C
08. PROGRAMMING LABORATORY   TW   50 20   24 P C   18. DATA STRUCTURES AND FILES LAB   TW   25 10   13 P C
09. PROGRAMMING LABORATORY   PR   50 20   30 P C   19. DATA STRUCTURES AND FILES LAB   PR   50 20   20 P C
10. COMMUNICATION AND LANGUAGE LAB. TW   50 20   31 P C   20. OBJECT ORIENTED PROGRAMMING LAB TW   50 20   30 P C
                                                           21. OBJECT ORIENTED PROGRAMMING LAB PR   50 20   33 P
GRAND TOTAL = 705/1500, RESULT: PASS CLASS
ORDN. 1 MARKS :
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S80058671   GHODE AMAR PRADEEPKUMAR   SINDHU   , 71045436J   , S80058671 ,   PICT   , S80058671
01. DISCRETE STRUCTURES   PP   100 40   52 P C   11. ENG MATHS III   PP   100 40   20 F
02. COMPUTER ORGANIZATION   PP   100 40   42 P C   12. COMPUTER GRAPHICS   PP   100 40   42 P C
03. DIGITAL ELECTRONICS & LOGIC DESIGPP   100 40   44 P C   13. PROCESSOR ARCHITECTURE & INTER. PP   100 40   40 P C
04. FUNDAMENTAL OF DATA STRUCTURES PP   100 40   40 P C   14. DATA STRUCTURES AND FILES   PP   100 40   49 P C
05. HUMANITIES AND SOCIAL SCIENCES PP   100 40   41 P C   15. DATA COMMUNICATION   PP   100 40   45 P C
06. DIGITAL LABORATORY   TW   50 20   20 P C   16. PROCESSOR INTERFACING LABORATORY TW   25 10   11 P C
07. DIGITAL LABORATORY   PR   50 20   33 P C   17. PROCESSOR INTERFACING LABORATORY OR   50 20   22 P C
08. PROGRAMMING LABORATORY   TW   50 20   30 P C   18. DATA STRUCTURES AND FILES LAB   TW   25 10   12 P C
09. PROGRAMMING LABORATORY   PR   50 20   33 P C   19. DATA STRUCTURES AND FILES LAB   PR   50 20   30 P C
10. COMMUNICATION AND LANGUAGE LAB. TW   50 20   25 P C   20. OBJECT ORIENTED PROGRAMMING LAB TW   50 20   26 P C
                                                           21. OBJECT ORIENTED PROGRAMMING LAB PR   50 20   20 P C
GRAND TOTAL = 677/1500, RESULT: FAILS A.T.K.T.
ORDN. 1 MARKS :
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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.
        OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S80058673   GORADE TUSHAR BHAUSAHEB   ALKA   , 71100810J   , S80058673 ,   PICT   , S80058673
01. DISCRETE STRUCTURES   PP   100 40   40 P C   11. ENG MATHS III   PP   100 40   23 F
02. COMPUTER ORGANIZATION   PP   100 40   40 P C   12. COMPUTER GRAPHICS   PP   100 40   40 P C
03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 40   42 P C   13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 40 P C
04. FUNDAMENTAL OF DATA STRUCTURES PP   100 40   46 P C   14. DATA STRUCTURES AND FILES   PP   100 40   40 P C
05. HUMANITIES AND SOCIAL SCIENCES PP   100 40   40 P C   15. DATA COMMUNICATION   PP   100 40   40 P
06. DIGITAL LABORATORY   TW   50 20   32 P C   16. PROCESSOR INTERFACING LABORATORY TW 25 10 10 P C
07. DIGITAL LABORATORY   PR   50 20   24 P C   17. PROCESSOR INTERFACING LABORATORY OR 50 20 20 P C
08. PROGRAMMING LABORATORY   TW   50 20   22 P C   18. DATA STRUCTURES AND FILES LAB TW   25 10 13 P C
09. PROGRAMMING LABORATORY   PR   50 20   28 P C   19. DATA STRUCTURES AND FILES LAB PR   50 20 34 P C
10. COMMUNICATION AND LANGUAGE LAB. TW   50 20   29 P C   20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 35 P C
                                                            21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 35 P C
GRAND TOTAL = 673/1500, RESULT: FAILS A.T.K.T.
ORDN. 1 MARKS :
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S80058675   HARKIRPAL SINGH   PARAMJEET KAUR   , 71045444K   , S80058675 ,   PICT   , S80058675
01. DISCRETE STRUCTURES   PP   100 40   16 F   11. ENG MATHS III   PP   100 40   00 F
02. COMPUTER ORGANIZATION   PP   100 40   52 P   12. COMPUTER GRAPHICS   PP   100 40   40 P
03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 40   40 P   13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 27 F
04. FUNDAMENTAL OF DATA STRUCTURES PP   100 40   26 F   14. DATA STRUCTURES AND FILES   PP   100 40   43 P
05. HUMANITIES AND SOCIAL SCIENCES PP   100 40   58 P   15. DATA COMMUNICATION   PP   100 40   52 P
06. DIGITAL LABORATORY   TW   50 20   21 P C   16. PROCESSOR INTERFACING LABORATORY TW 25 10 10 P C
07. DIGITAL LABORATORY   PR   50 20   21 P   17. PROCESSOR INTERFACING LABORATORY OR 50 20 21 P C
08. PROGRAMMING LABORATORY   TW   50 20   20 P C   18. DATA STRUCTURES AND FILES LAB TW   25 10 10 P C
09. PROGRAMMING LABORATORY   PR   50 20   22 P C   19. DATA STRUCTURES AND FILES LAB PR   50 20 AA F
10. COMMUNICATION AND LANGUAGE LAB. TW   50 20   32 P C   20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 22 P C
                                                            21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 34 P
GRAND TOTAL = 567/1500, RESULT: FAILS
ORDN. 1 MARKS :
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S80058676   JAIPURIA ROHIT SHARADKUMAR   AMITA   , 71100828M   , S80058676 ,   PICT   , S80058676
01. DISCRETE STRUCTURES   PP   100 40   47 P C   11. ENG MATHS III   PP   100 40   40 P C
02. COMPUTER ORGANIZATION   PP   100 40   40 P C   12. COMPUTER GRAPHICS   PP   100 40   40 P C
03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 40   40 P C   13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 40 P C
04. FUNDAMENTAL OF DATA STRUCTURES PP   100 40   42 P C   14. DATA STRUCTURES AND FILES   PP   100 40   43 P C
05. HUMANITIES AND SOCIAL SCIENCES PP   100 40   43 P C   15. DATA COMMUNICATION   PP   100 40   40 P C
06. DIGITAL LABORATORY   TW   50 20   33 P C   16. PROCESSOR INTERFACING LABORATORY TW 25 10 19 P C
07. DIGITAL LABORATORY   PR   50 20   35 P C   17. PROCESSOR INTERFACING LABORATORY OR 50 20 29 P C
08. PROGRAMMING LABORATORY   TW   50 20   25 P C   18. DATA STRUCTURES AND FILES LAB TW   25 10 15 P C
09. PROGRAMMING LABORATORY   PR   50 20   22 P C   19. DATA STRUCTURES AND FILES LAB PR   50 20 40 P
10. COMMUNICATION AND LANGUAGE LAB. TW   50 20   33 P C   20. OBJECT ORIENTED PROGRAMMING LAB TW 50 20 39 P C
                                                            21. OBJECT ORIENTED PROGRAMMING LAB PR 50 20 28 P C
GRAND TOTAL = 733/1500, RESULT: PASS CLASS
ORDN. 1 MARKS :
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      NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.
             OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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      S80058677   KHADSE MAYUR DILIP   PRAMILA   , 71100857E   , S80058677 ,   PICT   , S80058677
      01. DISCRETE STRUCTURES   PP   100 40   40 P C   11. ENG MATHS III   PP   100 40   22 F
      02. COMPUTER ORGANIZATION   PP   100 40   40 P C   12. COMPUTER GRAPHICS   PP   100 40   41 P C
      03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 40   45 P C   13. PROCESSOR ARCHITECTURE & INTER. PP 100 40   41 P C
      04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40   51 P C   14. DATA STRUCTURES AND FILES   PP   100 40   42 P C
      05. HUMANITIES AND SOCIAL SCIENCES PP 100 40   51 P C   15. DATA COMMUNICATION   PP   100 40   40 P C
      06. DIGITAL LABORATORY   TW   50 20   23 P C   16. PROCESSOR INTERFACING LABORATORY TW   25 10   12 P C
      07. DIGITAL LABORATORY   PR   50 20   38 P C   17. PROCESSOR INTERFACING LABORATORY OR   50 20   26 P C
      08. PROGRAMMING LABORATORY   TW   50 20   28 P C   18. DATA STRUCTURES AND FILES LAB   TW   25 10   11 P C
      09. PROGRAMMING LABORATORY   PR   50 20   29 P C   19. DATA STRUCTURES AND FILES LAB   PR   50 20   27 P C
      10. COMMUNICATION AND LANGUAGE LAB. TW   50 20   32 P C   20. OBJECT ORIENTED PROGRAMMING LAB TW   50 20   29 P C
                                                                      21. OBJECT ORIENTED PROGRAMMING LAB PR   50 20   25 P C
      GRAND TOTAL = 693/1500, RESULT: FAILS A.T.K.T.
      ORDN. 1 MARKS :
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      S80058678   KOSHTI ANUJA RAMESH   MANGAL   , 71045485G   , S80058678 ,   PICT   , S80058678
      01. DISCRETE STRUCTURES   PP   100 40   58 P C   11. ENG MATHS III   PP   100 40   26 F
      02. COMPUTER ORGANIZATION   PP   100 40   41 P C   12. COMPUTER GRAPHICS   PP   100 40   53 P C
      03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 40   53 P C   13. PROCESSOR ARCHITECTURE & INTER. PP 100 40   44 P C
      04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40   50 P C   14. DATA STRUCTURES AND FILES   PP   100 40   49 P C
      05. HUMANITIES AND SOCIAL SCIENCES PP 100 40   54 P C   15. DATA COMMUNICATION   PP   100 40   40 P C
      06. DIGITAL LABORATORY   TW   50 20   34 P C   16. PROCESSOR INTERFACING LABORATORY TW   25 10   13 P C
      07. DIGITAL LABORATORY   PR   50 20   22 P C   17. PROCESSOR INTERFACING LABORATORY OR   50 20   23 P C
      08. PROGRAMMING LABORATORY   TW   50 20   37 P C   18. DATA STRUCTURES AND FILES LAB   TW   25 10   17 P C
      09. PROGRAMMING LABORATORY   PR   50 20   35 P C   19. DATA STRUCTURES AND FILES LAB   PR   50 20   28 P
      10. COMMUNICATION AND LANGUAGE LAB. TW   50 20   37 P C   20. OBJECT ORIENTED PROGRAMMING LAB TW   50 20   36 P C
                                                                      21. OBJECT ORIENTED PROGRAMMING LAB PR   50 20   20 P C
      GRAND TOTAL = 770/1500, RESULT: FAILS A.T.K.T.
      ORDN. 1 MARKS :
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      S80058679   KULKARNI ALOK DIPAK   GEETA   , 71045491M   , S80058679 ,   PICT   , S80058679
      01. DISCRETE STRUCTURES   PP   100 40   AA F   11. ENG MATHS III   PP   100 40   AA F
      02. COMPUTER ORGANIZATION   PP   100 40   AA F   12. COMPUTER GRAPHICS   PP   100 40   AA F
      03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 40   AA F   13. PROCESSOR ARCHITECTURE & INTER. PP 100 40   AA F
      04. FUNDAMENTAL OF DATA STRUCTURES PP 100 40   45 P C   14. DATA STRUCTURES AND FILES   PP   100 40   AA F
      05. HUMANITIES AND SOCIAL SCIENCES PP 100 40   40 P C   15. DATA COMMUNICATION   PP   100 40   AA F
      06. DIGITAL LABORATORY   TW   50 20   22 P C   16. PROCESSOR INTERFACING LABORATORY TW   25 10   15 P C
      07. DIGITAL LABORATORY   PR   50 20   33 P C   17. PROCESSOR INTERFACING LABORATORY OR   50 20   AA F
      08. PROGRAMMING LABORATORY   TW   50 20   35 P C   18. DATA STRUCTURES AND FILES LAB   TW   25 10   15 P C
      09. PROGRAMMING LABORATORY   PR   50 20   35 P C   19. DATA STRUCTURES AND FILES LAB   PR   50 20   29 P C
      10. COMMUNICATION AND LANGUAGE LAB. TW   50 20   37 P C   20. OBJECT ORIENTED PROGRAMMING LAB TW   50 20   35 P C
                                                                      21. OBJECT ORIENTED PROGRAMMING LAB PR   50 20   40 P C
      GRAND TOTAL = 381/1500, RESULT: FAILS   RESULT RESERVED FOR BKLG
      ORDN. 1 MARKS :
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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.

          OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S80058680   MANSI MATHUR   PREETI   , 71100890G   , S80058680 ,   PICT   , S80058680

01. DISCRETE STRUCTURES	PP	100	40	40	P C		11. ENG MATHS III	PP	100	40	23	F	
02. COMPUTER ORGANIZATION	PP	100	40	48	P C		12. COMPUTER GRAPHICS	PP	100	40	46	P C
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	54	P C		13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	42	P C
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	46	P C		14. DATA STRUCTURES AND FILES	PP	100	40	51	P C
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	53	P C		15. DATA COMMUNICATION	PP	100	40	46	P C
06. DIGITAL LABORATORY	TW	50	20	31	P C		16. PROCESSOR INTERFACING LABORATORY	TW	25	10	14	P C
07. DIGITAL LABORATORY	PR	50	20	30	P C		17. PROCESSOR INTERFACING LABORATORY	OR	50	20	28	P C
08. PROGRAMMING LABORATORY	TW	50	20	30	P C		18. DATA STRUCTURES AND FILES LAB	TW	25	10	13	P C
09. PROGRAMMING LABORATORY	PR	50	20	20	P C		19. DATA STRUCTURES AND FILES LAB	PR	50	20	42	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	31	P C		20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	35	P C
							21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	25	P C

GRAND TOTAL = 748/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

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S80058681   MULAY AMIT YESHWANT   SNEHAL   , 71045530F   , S80058681 ,   PICT   , S80058681

01. DISCRETE STRUCTURES	PP	100	40	61	P C		11. ENG MATHS III	PP	100	40	40	P	
02. COMPUTER ORGANIZATION	PP	100	40	44	P C		12. COMPUTER GRAPHICS	PP	100	40	56	P C
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	44	P C		13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	40	P C
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	41	P C		14. DATA STRUCTURES AND FILES	PP	100	40	51	P C
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	53	P C		15. DATA COMMUNICATION	PP	100	40	40	P C
06. DIGITAL LABORATORY	TW	50	20	20	P C		16. PROCESSOR INTERFACING LABORATORY	TW	25	10	10	P C
07. DIGITAL LABORATORY	PR	50	20	38	P C		17. PROCESSOR INTERFACING LABORATORY	OR	50	20	20	P C
08. PROGRAMMING LABORATORY	TW	50	20	25	P C		18. DATA STRUCTURES AND FILES LAB	TW	25	10	12	P C
09. PROGRAMMING LABORATORY	PR	50	20	29	P C		19. DATA STRUCTURES AND FILES LAB	PR	50	20	21	P C
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	25	P C		20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	23	P C
							21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	25	P C

GRAND TOTAL = 718/1500, RESULT: PASS CLASS

ORDN. 1 MARKS :

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S80058682   MULLA ASMA MUSA   RABIYA   , 71134970D   , S80058682 ,   PICT   , S80058682

01. DISCRETE STRUCTURES	PP	100	40	49	P C		11. ENG MATHS III	PP	100	40	40	P	
02. COMPUTER ORGANIZATION	PP	100	40	40	P C		12. COMPUTER GRAPHICS	PP	100	40	40	P C
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	40	P C		13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	40	P C
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	48	P C		14. DATA STRUCTURES AND FILES	PP	100	40	54	P C
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	44	P C		15. DATA COMMUNICATION	PP	100	40	43	P C
06. DIGITAL LABORATORY	TW	50	20	38	P C		16. PROCESSOR INTERFACING LABORATORY	TW	25	10	21	P C
07. DIGITAL LABORATORY	PR	50	20	22	P C		17. PROCESSOR INTERFACING LABORATORY	OR	50	20	29	P C
08. PROGRAMMING LABORATORY	TW	50	20	42	P C		18. DATA STRUCTURES AND FILES LAB	TW	25	10	22	P C
09. PROGRAMMING LABORATORY	PR	50	20	33	P C		19. DATA STRUCTURES AND FILES LAB	PR	50	20	22	P C
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	37	P C		20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	43	P C
							21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	20	P C

GRAND TOTAL = 767/1500, RESULT: SECOND CLASS

ORDN. 1 MARKS :

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.
       OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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 S80058685   MUTHA VINITA VINOD                 SUREKHA              , 71100904L    , S80058685 ,  PICT     , S80058685
 01. DISCRETE STRUCTURES            PP  100 40  58 P C   11. ENG MATHS III                   PP  100 40  40 P
 02. COMPUTER ORGANIZATION          PP  100 40  40 P C   12. COMPUTER GRAPHICS               PP  100 40  51 P C
 03. DIGITAL ELECTRONICS & LOGIC DESIGPP  100 40  42 P C   13. PROCESSOR ARCHITECTURE & INTER.  PP  100 40  48 P C
 04. FUNDAMENTAL OF DATA STRUCTURES PP  100 40  49 P C   14. DATA STRUCTURES AND FILES        PP  100 40  50 P C
 05. HUMANITIES AND SOCIAL SCIENCES PP  100 40  40 P C   15. DATA COMMUNICATION               PP  100 40  51 P C
 06. DIGITAL LABORATORY             TW   50 20  32 P C   16. PROCESSOR INTERFACING LABORATORY TW   25 10  13 P C
 07. DIGITAL LABORATORY             PR   50 20  32 P C   17. PROCESSOR INTERFACING LABORATORY OR   50 20  35 P C
 08. PROGRAMMING LABORATORY         TW   50 20  35 P C   18. DATA STRUCTURES AND FILES LAB    TW   25 10  13 P C
 09. PROGRAMMING LABORATORY         PR   50 20  38 P C   19. DATA STRUCTURES AND FILES LAB    PR   50 20  31 P C
 10. COMMUNICATION AND LANGUAGE LAB. TW   50 20  34 P C   20. OBJECT ORIENTED PROGRAMMING LAB  TW   50 20  35 P C
                                                        21. OBJECT ORIENTED PROGRAMMING LAB  PR   50 20  44 P C
GRAND TOTAL =  811/1500, RESULT: SECOND CLASS
ORDN. 1 MARKS :
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 S80058686   NAIK RASHMI BHARAT                 MEENA                , 71100907E    , S80058686 ,  PICT     , S80058686
 01. DISCRETE STRUCTURES            PP  100 40  46 P C   11. ENG MATHS III                   PP  100 40  41 P
 02. COMPUTER ORGANIZATION          PP  100 40  59 P C   12. COMPUTER GRAPHICS               PP  100 40  43 P C
 03. DIGITAL ELECTRONICS & LOGIC DESIGPP  100 40  40 P C   13. PROCESSOR ARCHITECTURE & INTER.  PP  100 40  40 P C
 04. FUNDAMENTAL OF DATA STRUCTURES PP  100 40  49 P C   14. DATA STRUCTURES AND FILES        PP  100 40  46 P C
 05. HUMANITIES AND SOCIAL SCIENCES PP  100 40  48 P C   15. DATA COMMUNICATION               PP  100 40  48 P C
 06. DIGITAL LABORATORY             TW   50 20  38 P C   16. PROCESSOR INTERFACING LABORATORY TW   25 10  18 P C
 07. DIGITAL LABORATORY             PR   50 20  22 P C   17. PROCESSOR INTERFACING LABORATORY OR   50 20  31 P C
 08. PROGRAMMING LABORATORY         TW   50 20  34 P C   18. DATA STRUCTURES AND FILES LAB    TW   25 10  18 P C
 09. PROGRAMMING LABORATORY         PR   50 20  20 P C   19. DATA STRUCTURES AND FILES LAB    PR   50 20  38 P C
 10. COMMUNICATION AND LANGUAGE LAB. TW   50 20  41 P C   20. OBJECT ORIENTED PROGRAMMING LAB  TW   50 20  40 P C
                                                        21. OBJECT ORIENTED PROGRAMMING LAB  PR   50 20  32 P C
GRAND TOTAL =  792/1500, RESULT: SECOND CLASS
ORDN. 1 MARKS :
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 S80058688   NILESH D PHADTARE                  KUNDA                , 70925526J    , S80058688 ,  PICT     , S80058688
 01. DISCRETE STRUCTURES            PP  100 40  43 P C   11. ENG MATHS III                   PP  100 40  40 P C
 02. COMPUTER ORGANIZATION          PP  100 40  50 P C   12. COMPUTER GRAPHICS               PP  100 40  43 P C
 03. DIGITAL ELECTRONICS & LOGIC DESIGPP  100 40  50 P C   13. PROCESSOR ARCHITECTURE & INTER.  PP  100 40  25 F
 04. FUNDAMENTAL OF DATA STRUCTURES PP  100 40  40 P C   14. DATA STRUCTURES AND FILES        PP  100 40  72 P C
 05. HUMANITIES AND SOCIAL SCIENCES PP  100 40  40 P C   15. DATA COMMUNICATION               PP  100 40  43 P C
 06. DIGITAL LABORATORY             TW   50 20  35 P C   16. PROCESSOR INTERFACING LABORATORY TW   25 10  20 P C
 07. DIGITAL LABORATORY             PR   50 20  31 P C   17. PROCESSOR INTERFACING LABORATORY OR   50 20  20 P C
 08. PROGRAMMING LABORATORY         TW   50 20  43 P C   18. DATA STRUCTURES AND FILES LAB    TW   25 10  23 P C
 09. PROGRAMMING LABORATORY         PR   50 20  32 P C   19. DATA STRUCTURES AND FILES LAB    PR   50 20  25 P C
 10. COMMUNICATION AND LANGUAGE LAB. TW   50 20  36 P C   20. OBJECT ORIENTED PROGRAMMING LAB  TW   50 20  44 P C
                                                        21. OBJECT ORIENTED PROGRAMMING LAB  PR   50 20  41 P C
GRAND TOTAL =  796/1500, RESULT: FAILS A.T.K.T.
ORDN. 1 MARKS :
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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.
          OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S80058689   PATEL NAZNIN JAVED   MAHERUNISSA   , 71134973J   , S80058689 ,   PICT   , S80058689

01. DISCRETE STRUCTURES	PP	100	40	40	P C	11. ENG MATHS III	PP	100	40	40	P
02. COMPUTER ORGANIZATION	PP	100	40	40	P C	12. COMPUTER GRAPHICS	PP	100	40	48	P C
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	45	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	40	P C
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	44	P C	14. DATA STRUCTURES AND FILES	PP	100	40	64	P C
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	46	P C	15. DATA COMMUNICATION	PP	100	40	53	P C
06. DIGITAL LABORATORY	TW	50	20	35	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	14	P C
07. DIGITAL LABORATORY	PR	50	20	40	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	22	P C
08. PROGRAMMING LABORATORY	TW	50	20	42	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	19	P C
09. PROGRAMMING LABORATORY	PR	50	20	22	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	35	P C
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	43	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	41	P C
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	35	P C

GRAND TOTAL = 808/1500, RESULT: SECOND CLASS

ORDN. 1 MARKS :

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S80058692   PAWAR NEHA ANIL   JAYA   , 71100949L   , S80058692 ,   PICT   , S80058692

01. DISCRETE STRUCTURES	PP	100	40	40	P C	11. ENG MATHS III	PP	100	40	13	F
02. COMPUTER ORGANIZATION	PP	100	40	45	P C	12. COMPUTER GRAPHICS	PP	100	40	55	P C
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	40	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	40	P C
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	50	P C	14. DATA STRUCTURES AND FILES	PP	100	40	44	P C
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	51	P C	15. DATA COMMUNICATION	PP	100	40	42	P C
06. DIGITAL LABORATORY	TW	50	20	35	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	12	P C
07. DIGITAL LABORATORY	PR	50	20	34	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	24	P C
08. PROGRAMMING LABORATORY	TW	50	20	34	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	15	P C
09. PROGRAMMING LABORATORY	PR	50	20	25	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	37	P C
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	38	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	33	P C
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	AA	F

GRAND TOTAL = 707/1500, RESULT: FAILS A.T.K.T.   RESULT RESERVED FOR BKLG

ORDN. 1 MARKS :

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S80058693   PAWAR PRASAD GORAKSH   MEENAKSHI   , 71100951B   , S80058693 ,   PICT   , S80058693

01. DISCRETE STRUCTURES	PP	100	40	43	P C	11. ENG MATHS III	PP	100	40	52	P
02. COMPUTER ORGANIZATION	PP	100	40	41	P C	12. COMPUTER GRAPHICS	PP	100	40	41	P C
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	41	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	40	P C
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	43	P C	14. DATA STRUCTURES AND FILES	PP	100	40	45	P C
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	53	P C	15. DATA COMMUNICATION	PP	100	40	40	P C
06. DIGITAL LABORATORY	TW	50	20	24	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	10	P C
07. DIGITAL LABORATORY	PR	50	20	30	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	28	P C
08. PROGRAMMING LABORATORY	TW	50	20	28	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	10	P C
09. PROGRAMMING LABORATORY	PR	50	20	22	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	42	P C
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	34	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	24	P C
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	37	P C

GRAND TOTAL = 728/1500, RESULT: PASS CLASS

ORDN. 1 MARKS :

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S80058694   RAHUL SHARMA   PROMILA   , 71100963F   , S80058694 ,   PICT   , S80058694

01. DISCRETE STRUCTURES	PP	100	40	50	P C	11. ENG MATHS III	PP	100	40	44	P C
02. COMPUTER ORGANIZATION	PP	100	40	44	P C	12. COMPUTER GRAPHICS	PP	100	40	40	P C
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	40	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	40	P C
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	52	P C	14. DATA STRUCTURES AND FILES	PP	100	40	40	P C
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	49	P C	15. DATA COMMUNICATION	PP	100	40	66	P C
06. DIGITAL LABORATORY	TW	50	20	21	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	10	P C
07. DIGITAL LABORATORY	PR	50	20	23	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	20	P C
08. PROGRAMMING LABORATORY	TW	50	20	20	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	10	P C
09. PROGRAMMING LABORATORY	PR	50	20	23	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	44	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	31	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	22	P C
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	31	P C

GRAND TOTAL = 720/1500, RESULT: PASS CLASS

ORDN. 1 MARKS :

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S80058695   RATTAN NAKUL VISHWAS   BIMLA   , 71100971G   , S80058695 ,   PICT   , S80058695

01. DISCRETE STRUCTURES	PP	100	40	47	P C	11. ENG MATHS III	PP	100	40	56	P
02. COMPUTER ORGANIZATION	PP	100	40	41	P C	12. COMPUTER GRAPHICS	PP	100	40	44	P C
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	53	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	48	P C
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	52	P C	14. DATA STRUCTURES AND FILES	PP	100	40	40	P C
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	40	P C	15. DATA COMMUNICATION	PP	100	40	49	P C
06. DIGITAL LABORATORY	TW	50	20	33	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	16	P C
07. DIGITAL LABORATORY	PR	50	20	36	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	38	P C
08. PROGRAMMING LABORATORY	TW	50	20	37	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	17	P C
09. PROGRAMMING LABORATORY	PR	50	20	38	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	39	P C
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	38	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	40	P C
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	39	P C

GRAND TOTAL = 841/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

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S80058696   SAHARE NUPUR ARVIND   MAMTA   , 71100982B   , S80058696 ,   PICT   , S80058696

01. DISCRETE STRUCTURES	PP	100	40	40	P C	11. ENG MATHS III	PP	100	40	58	P C
02. COMPUTER ORGANIZATION	PP	100	40	54	P C	12. COMPUTER GRAPHICS	PP	100	40	53	P C
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	46	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	58	P C
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	52	P C	14. DATA STRUCTURES AND FILES	PP	100	40	46	P C
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	43	P C	15. DATA COMMUNICATION	PP	100	40	56	P C
06. DIGITAL LABORATORY	TW	50	20	34	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	17	P C
07. DIGITAL LABORATORY	PR	50	20	22	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	30	P C
08. PROGRAMMING LABORATORY	TW	50	20	38	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	15	P C
09. PROGRAMMING LABORATORY	PR	50	20	30	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	41	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	39	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	38	P C
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	30	P

GRAND TOTAL = 840/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.
          OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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S80058697   SARTHAK MAJITHIA   POOJA   , 71045598E   , S80058697 ,   PICT   , S80058697
01. DISCRETE STRUCTURES   PP   100 40   63 P C   11. ENG MATHS III   PP   100 40   24 F
02. COMPUTER ORGANIZATION   PP   100 40   52 P C   12. COMPUTER GRAPHICS   PP   100 40   57 P C
03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 40   62 P C   13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 41 P C
04. FUNDAMENTAL OF DATA STRUCTURES PP   100 40   63 P C   14. DATA STRUCTURES AND FILES   PP   100 40   42 P C
05. HUMANITIES AND SOCIAL SCIENCES PP   100 40   48 P C   15. DATA COMMUNICATION   PP   100 40   48 P C
06. DIGITAL LABORATORY   TW   50 20   20 P C   16. PROCESSOR INTERFACING LABORATORY TW 25 10   10 P C
07. DIGITAL LABORATORY   PR   50 20   30 P C   17. PROCESSOR INTERFACING LABORATORY OR 50 20   20 P C
08. PROGRAMMING LABORATORY   TW   50 20   20 P C   18. DATA STRUCTURES AND FILES LAB   TW   25 10   15 P C
09. PROGRAMMING LABORATORY   PR   50 20   48 P C   19. DATA STRUCTURES AND FILES LAB   PR   50 20   43 P C
10. COMMUNICATION AND LANGUAGE LAB. TW   50 20   30 P C   20. OBJECT ORIENTED PROGRAMMING LAB TW   50 20   20 P C
                                                            21. OBJECT ORIENTED PROGRAMMING LAB PR   50 20   39 P C
GRAND TOTAL = 795/1500, RESULT: FAILS A.T.K.T.
ORDN. 1 MARKS :
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S80058699   SHIRBHATE ABHILASH DNYANESHWAR   RAJSHRI   , 71101010C   , S80058699 ,   PICT   , S80058699
01. DISCRETE STRUCTURES   PP   100 40   46 P C   11. ENG MATHS III   PP   100 40   40 P
02. COMPUTER ORGANIZATION   PP   100 40   40 P C   12. COMPUTER GRAPHICS   PP   100 40   43 P C
03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 40   63 P C   13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 41 P C
04. FUNDAMENTAL OF DATA STRUCTURES PP   100 40   48 P C   14. DATA STRUCTURES AND FILES   PP   100 40   40 P C
05. HUMANITIES AND SOCIAL SCIENCES PP   100 40   40 P C   15. DATA COMMUNICATION   PP   100 40   43 P C
06. DIGITAL LABORATORY   TW   50 20   33 P C   16. PROCESSOR INTERFACING LABORATORY TW 25 10   10 P C
07. DIGITAL LABORATORY   PR   50 20   22 P C   17. PROCESSOR INTERFACING LABORATORY OR 50 20   36 P C
08. PROGRAMMING LABORATORY   TW   50 20   36 P C   18. DATA STRUCTURES AND FILES LAB   TW   25 10   13 P C
09. PROGRAMMING LABORATORY   PR   50 20   27 P C   19. DATA STRUCTURES AND FILES LAB   PR   50 20   32 P C
10. COMMUNICATION AND LANGUAGE LAB. TW   50 20   35 P C   20. OBJECT ORIENTED PROGRAMMING LAB TW   50 20   29 P C
                                                            21. OBJECT ORIENTED PROGRAMMING LAB PR   50 20   35 P C
GRAND TOTAL = 752/1500, RESULT: SECOND CLASS
ORDN. 1 MARKS :
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S80058700   SOLANKE BHUSHAN NARENDRA   REKHA   , 71101017L   , S80058700 ,   PICT   , S80058700
01. DISCRETE STRUCTURES   PP   100 40   44 P C   11. ENG MATHS III   PP   100 40   19 F
02. COMPUTER ORGANIZATION   PP   100 40   43 P C   12. COMPUTER GRAPHICS   PP   100 40   43 P C
03. DIGITAL ELECTRONICS & LOGIC DESIGPP 100 40   40 P C   13. PROCESSOR ARCHITECTURE & INTER. PP 100 40 46 P C
04. FUNDAMENTAL OF DATA STRUCTURES PP   100 40   48 P C   14. DATA STRUCTURES AND FILES   PP   100 40   57 P C
05. HUMANITIES AND SOCIAL SCIENCES PP   100 40   40 P C   15. DATA COMMUNICATION   PP   100 40   46 P C
06. DIGITAL LABORATORY   TW   50 20   33 P C   16. PROCESSOR INTERFACING LABORATORY TW 25 10   10 P C
07. DIGITAL LABORATORY   PR   50 20   30 P C   17. PROCESSOR INTERFACING LABORATORY OR 50 20   30 P C
08. PROGRAMMING LABORATORY   TW   50 20   31 P C   18. DATA STRUCTURES AND FILES LAB   TW   25 10   12 P C
09. PROGRAMMING LABORATORY   PR   50 20   24 P C   19. DATA STRUCTURES AND FILES LAB   PR   50 20   35 P C
10. COMMUNICATION AND LANGUAGE LAB. TW   50 20   35 P C   20. OBJECT ORIENTED PROGRAMMING LAB TW   50 20   33 P C
                                                            21. OBJECT ORIENTED PROGRAMMING LAB PR   50 20   34 P C
GRAND TOTAL = 733/1500, RESULT: FAILS A.T.K.T.
ORDN. 1 MARKS :
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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.

          OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S80058703   TEJASH KUMAR   RITA   , 71045642F   , S80058703 ,   PICT   , S80058703

01. DISCRETE STRUCTURES	PP	100	40	40	P		11. ENG MATHS III	PP	100	40	AA	F
02. COMPUTER ORGANIZATION	PP	100	40	44	P C		12. COMPUTER GRAPHICS	PP	100	40	40	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	40	P C		13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	40	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	40	P C		14. DATA STRUCTURES AND FILES	PP	100	40	40	P
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	40	P C		15. DATA COMMUNICATION	PP	100	40	44	P
06. DIGITAL LABORATORY	TW	50	20	24	P C		16. PROCESSOR INTERFACING LABORATORY	TW	25	10	10	P C
07. DIGITAL LABORATORY	PR	50	20	23	P C		17. PROCESSOR INTERFACING LABORATORY	OR	50	20	20	P
08. PROGRAMMING LABORATORY	TW	50	20	20	P C		18. DATA STRUCTURES AND FILES LAB	TW	25	10	10	P C
09. PROGRAMMING LABORATORY	PR	50	20	20	P C		19. DATA STRUCTURES AND FILES LAB	PR	50	20	30	P
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	33	P C		20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	27	P C
							21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	28	P

GRAND TOTAL = 613/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

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S80058705   VICHARE GAURAV GIRISH   VINA   , 71101047B   , S80058705 ,   PICT   , S80058705

01. DISCRETE STRUCTURES	PP	100	40	64	P C		11. ENG MATHS III	PP	100	40	59	P C
02. COMPUTER ORGANIZATION	PP	100	40	42	P C		12. COMPUTER GRAPHICS	PP	100	40	45	P C
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	46	P C		13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	40	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	51	P C		14. DATA STRUCTURES AND FILES	PP	100	40	46	P C
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	40	P C		15. DATA COMMUNICATION	PP	100	40	47	P C
06. DIGITAL LABORATORY	TW	50	20	30	P C		16. PROCESSOR INTERFACING LABORATORY	TW	25	10	20	P C
07. DIGITAL LABORATORY	PR	50	20	30	P C		17. PROCESSOR INTERFACING LABORATORY	OR	50	20	36	P C
08. PROGRAMMING LABORATORY	TW	50	20	28	P C		18. DATA STRUCTURES AND FILES LAB	TW	25	10	15	P C
09. PROGRAMMING LABORATORY	PR	50	20	25	P C		19. DATA STRUCTURES AND FILES LAB	PR	50	20	30	P C
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	38	P C		20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	41	P C
							21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	24	P C

GRAND TOTAL = 797/1500, RESULT: SECOND CLASS

ORDN. 1 MARKS :

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S80058706   VIRWANI SUNNY JAMANLAL   KIRTI   , 71101051L   , S80058706 ,   PICT   , S80058706

01. DISCRETE STRUCTURES	PP	100	40	40	P C		11. ENG MATHS III	PP	100	40	21	F
02. COMPUTER ORGANIZATION	PP	100	40	47	P C		12. COMPUTER GRAPHICS	PP	100	40	40	P C
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	43	P		13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	45	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	45	P C		14. DATA STRUCTURES AND FILES	PP	100	40	41	P C
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	42	P C		15. DATA COMMUNICATION	PP	100	40	56	P C
06. DIGITAL LABORATORY	TW	50	20	28	P C		16. PROCESSOR INTERFACING LABORATORY	TW	25	10	10	P C
07. DIGITAL LABORATORY	PR	50	20	34	P C		17. PROCESSOR INTERFACING LABORATORY	OR	50	20	30	P C
08. PROGRAMMING LABORATORY	TW	50	20	33	P C		18. DATA STRUCTURES AND FILES LAB	TW	25	10	11	P C
09. PROGRAMMING LABORATORY	PR	50	20	27	P C		19. DATA STRUCTURES AND FILES LAB	PR	50	20	21	P C
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	34	P C		20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	30	P C
							21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	33	P C

GRAND TOTAL = 711/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.
       OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S80058707   WADILE NITIN ADHAR   SUMAN   , 71101052J   , S80058707 ,   PICT   , S80058707

01. DISCRETE STRUCTURES	PP	100	40	51	P C		11. ENG MATHS III	PP	100	40	40	P C
02. COMPUTER ORGANIZATION	PP	100	40	46	P C		12. COMPUTER GRAPHICS	PP	100	40	41	P C
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	40	P C		13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	29	F
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	48	P C		14. DATA STRUCTURES AND FILES	PP	100	40	51	P C
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	40	P C		15. DATA COMMUNICATION	PP	100	40	40	P C
06. DIGITAL LABORATORY	TW	50	20	35	P C		16. PROCESSOR INTERFACING LABORATORY	TW	25	10	10	P C
07. DIGITAL LABORATORY	PR	50	20	20	P C		17. PROCESSOR INTERFACING LABORATORY	OR	50	20	34	P C
08. PROGRAMMING LABORATORY	TW	50	20	37	P C		18. DATA STRUCTURES AND FILES LAB	TW	25	10	12	P C
09. PROGRAMMING LABORATORY	PR	50	20	20	P C		19. DATA STRUCTURES AND FILES LAB	PR	50	20	38	P C
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	37	P C		20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	32	P C
							21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	21	P C

GRAND TOTAL = 722/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

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S80058709   ANUSHKA GHOGALE   NIVEDITA   , 70925337M   , S80058709 ,   PICT   , S80058709

01. DISCRETE STRUCTURES	PP	100	40	40	P		11. ENG MATHS III	PP	100	40	03	F
02. COMPUTER ORGANIZATION	PP	100	40	40	P C		12. COMPUTER GRAPHICS	PP	100	40	42	P
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	46	P		13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	40	P
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	40	P C		14. DATA STRUCTURES AND FILES	PP	100	40	40	P C
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	40	P C		15. DATA COMMUNICATION	PP	100	40	42	P C
06. DIGITAL LABORATORY	TW	50	20	23	P C		16. PROCESSOR INTERFACING LABORATORY	TW	25	10	16	P C
07. DIGITAL LABORATORY	PR	50	20	32	P C		17. PROCESSOR INTERFACING LABORATORY	OR	50	20	25	P C
08. PROGRAMMING LABORATORY	TW	50	20	27	P C		18. DATA STRUCTURES AND FILES LAB	TW	25	10	12	P C
09. PROGRAMMING LABORATORY	PR	50	20	20	P C		19. DATA STRUCTURES AND FILES LAB	PR	50	20	20	P C
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	31	P C		20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	34	P C
							21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	30	P

GRAND TOTAL = 643/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

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S80058710   PATHAK ANUJA PRAVIN   UJJWALA   , 71100934B   , S80058710 ,   PICT   , S80058710

01. DISCRETE STRUCTURES	PP	100	40	40	P C		11. ENG MATHS III	PP	100	40	12	F
02. COMPUTER ORGANIZATION	PP	100	40	40	P C		12. COMPUTER GRAPHICS	PP	100	40	40	P C
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	43	P C		13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	40	P C
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	48	P C		14. DATA STRUCTURES AND FILES	PP	100	40	47	P C
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	55	P C		15. DATA COMMUNICATION	PP	100	40	48	P C
06. DIGITAL LABORATORY	TW	50	20	34	P C		16. PROCESSOR INTERFACING LABORATORY	TW	25	10	16	P C
07. DIGITAL LABORATORY	PR	50	20	30	P C		17. PROCESSOR INTERFACING LABORATORY	OR	50	20	38	P C
08. PROGRAMMING LABORATORY	TW	50	20	29	P C		18. DATA STRUCTURES AND FILES LAB	TW	25	10	16	P C
09. PROGRAMMING LABORATORY	PR	50	20	26	P C		19. DATA STRUCTURES AND FILES LAB	PR	50	20	20	P C
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	36	P C		20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	36	P C
							21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	27	P C

GRAND TOTAL = 721/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
       OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER
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S80058711   SHUKLA MANISH NITYANAND   MADHURI   , 71045622M   , S80058711 ,   PICT   , S80058711

01. DISCRETE STRUCTURES	PP	100	40	57	P C	11. ENG MATHS III	PP	100	40	05	F
02. COMPUTER ORGANIZATION	PP	100	40	44	P C	12. COMPUTER GRAPHICS	PP	100	40	40	P C
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	57	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	51	P C
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	49	P C	14. DATA STRUCTURES AND FILES	PP	100	40	40	P C
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	45	P C	15. DATA COMMUNICATION	PP	100	40	45	P C
06. DIGITAL LABORATORY	TW	50	20	28	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	12	P C
07. DIGITAL LABORATORY	PR	50	20	30	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	21	P C
08. PROGRAMMING LABORATORY	TW	50	20	28	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	11	P C
09. PROGRAMMING LABORATORY	PR	50	20	32	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	28	P C
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	31	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	27	P C
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	37	P C

GRAND TOTAL = 718/1500, RESULT: FAILS A.T.K.T.
ORDN. 1 MARKS :
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S80058712   TEKE AMRUTA PRAKASH   SHALINEE   , 71101028F   , S80058712 ,   PICT   , S80058712

01. DISCRETE STRUCTURES	PP	100	40	47	P C	11. ENG MATHS III	PP	100	40	43	P
02. COMPUTER ORGANIZATION	PP	100	40	47	P C	12. COMPUTER GRAPHICS	PP	100	40	54	P C
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	42	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	44	P C
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	41	P C	14. DATA STRUCTURES AND FILES	PP	100	40	53	P C
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	40	P C	15. DATA COMMUNICATION	PP	100	40	54	P C
06. DIGITAL LABORATORY	TW	50	20	34	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	17	P C
07. DIGITAL LABORATORY	PR	50	20	22	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	28	P C
08. PROGRAMMING LABORATORY	TW	50	20	31	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	14	P C
09. PROGRAMMING LABORATORY	PR	50	20	20	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	26	P C
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	40	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	39	P C
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	30	P C

GRAND TOTAL = 766/1500, RESULT: SECOND CLASS
ORDN. 1 MARKS :
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S80058717   MOHAMMED AAQUIB ANSARI MOHAMMED YUSUF   SUFIYA   , 71045526H   ,   ,   PICT   , S80058717

01. DISCRETE STRUCTURES	PP	100	40	43	P C	11. ENG MATHS III	PP	100	40	40	P C
02. COMPUTER ORGANIZATION	PP	100	40	41	P C	12. COMPUTER GRAPHICS	PP	100	40	50	P C
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	45	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	40	P C
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	40	P C	14. DATA STRUCTURES AND FILES	PP	100	40	73	P C
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	40	P C	15. DATA COMMUNICATION	PP	100	40	45	P C
06. DIGITAL LABORATORY	TW	50	20	20	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	10	P C
07. DIGITAL LABORATORY	PR	50	20	AA	F	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	AA	F
08. PROGRAMMING LABORATORY	TW	50	20	20	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	13	P C
09. PROGRAMMING LABORATORY	PR	50	20	35	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	39	P C
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	20	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	20	P C
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	20	P C

GRAND TOTAL = 654/1500, RESULT: FAILS A.T.K.T.
ORDN. 1 MARKS :
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NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE,   SEAT NO.
       OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

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S80058718   MUNOT PAYAL KAMLESHKUMAR   MEENAKSHI   , 70925517K   ,   ,   PICT   , S80058718

No.	Subject		Max	Min	Obt	P/F	C	No.	Subject		Max	Min	Obt	P/F	C
01.	DISCRETE STRUCTURES	PP	100	40	51	P	C	11.	ENG MATHS III	PP	100	40	AA	F	
02.	COMPUTER ORGANIZATION	PP	100	40	40	P	C	12.	COMPUTER GRAPHICS	PP	100	40	AA	F	
03.	DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	50	P	C	13.	PROCESSOR ARCHITECTURE & INTER.	PP	100	40	AA	F	
04.	FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	45	P	C	14.	DATA STRUCTURES AND FILES	PP	100	40	40	P	C
05.	HUMANITIES AND SOCIAL SCIENCES	PP	100	40	40	P	C	15.	DATA COMMUNICATION	PP	100	40	AA	F	
06.	DIGITAL LABORATORY	TW	50	20	35	P	C	16.	PROCESSOR INTERFACING LABORATORY	TW	25	10	11	P	C
07.	DIGITAL LABORATORY	PR	50	20	20	P	C	17.	PROCESSOR INTERFACING LABORATORY	OR	50	20	AA	F	
08.	PROGRAMMING LABORATORY	TW	50	20	33	P	C	18.	DATA STRUCTURES AND FILES LAB	TW	25	10	15	P	C
09.	PROGRAMMING LABORATORY	PR	50	20	20	P	C	19.	DATA STRUCTURES AND FILES LAB	PR	50	20	27	P	C
10.	COMMUNICATION AND LANGUAGE LAB.	TW	50	20	40	P	C	20.	OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	41	P	C
								21.	OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	AA	F	

GRAND TOTAL = 508/1500, RESULT: FAILS

ORDN. 1 MARKS :

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