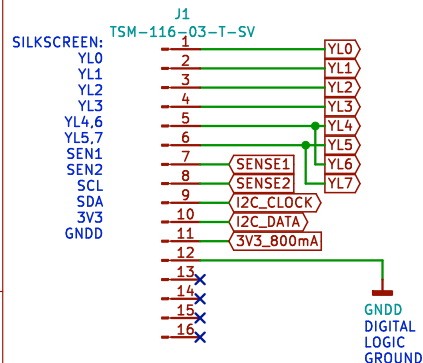


CORE MATRIX



SILKSCREEN: Pixel 0-7 ->

SILKSCREEN: Pixel 56-63 ->

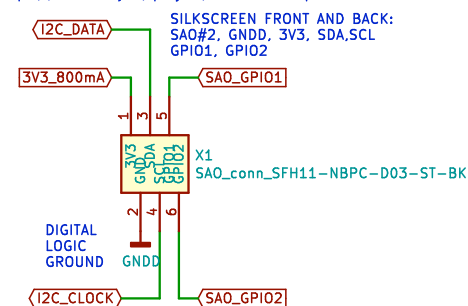
SILKSCREEN: <-bit 63-56

SILKSCREEN: <-bit 7-0

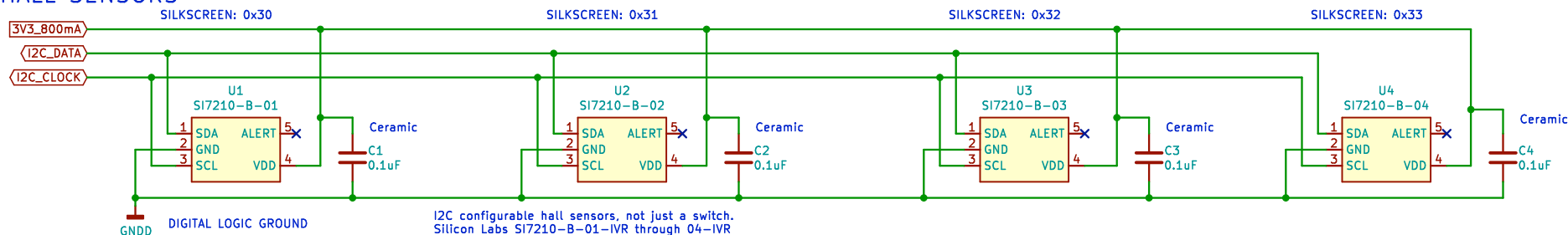
Core_Memory_Bx8_Array_Front_Facing

[OPTIONAL] SAO #2 EXPANSION

SIMPLE ADD ONS see:
<https://hackaday.io/project/175182-simple-add-ons-sao>

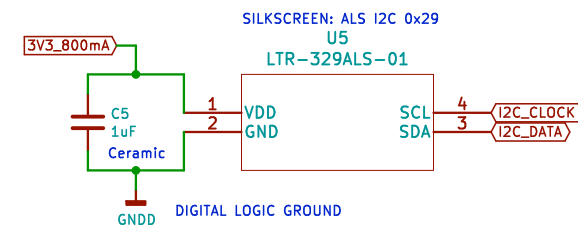


HALL SENSORS



I2C configurable hall sensors, not just a switch.
Silicon Labs SI7210-B-01-IVR through 04-IVR
Low (push-pull) up to 20 mT, SOT23-5
<https://www.digikey.com/product-detail/en/silicon-labs/SI7210-B-04-IVR/336-4129-1-ND/7648844>

AUTOMATIC LIGHT SENSOR I2C 0x29



SILKSCREEN MISC.

CORE BOARD V0.4-NP [REL. DATE]. ANDY GEPPERT
Core64.Machineldeas.com
Interactive Core Memory

SILKSCREEN GRAPHICS

L1 Core_64_Logo_9mm_tall
L2 Core_64_M-+S_Buttons_4mm

NEON
PIXELS
EDITION

All non-polarized capacitors are X7R or X5R ceramic unless otherwise noted.

Andy Geppert
As prototyped
Core64.Machineldeas.com

Sheet: /
File: Core64 CB v0.4-NP.sch

Title: Core64 CB (Core Board)

Size: A Date: 2020-11-25

KiCad E.D.A. kicad (5.1.2-1)-1

Rev: 0.4-NP

Id: 1/1