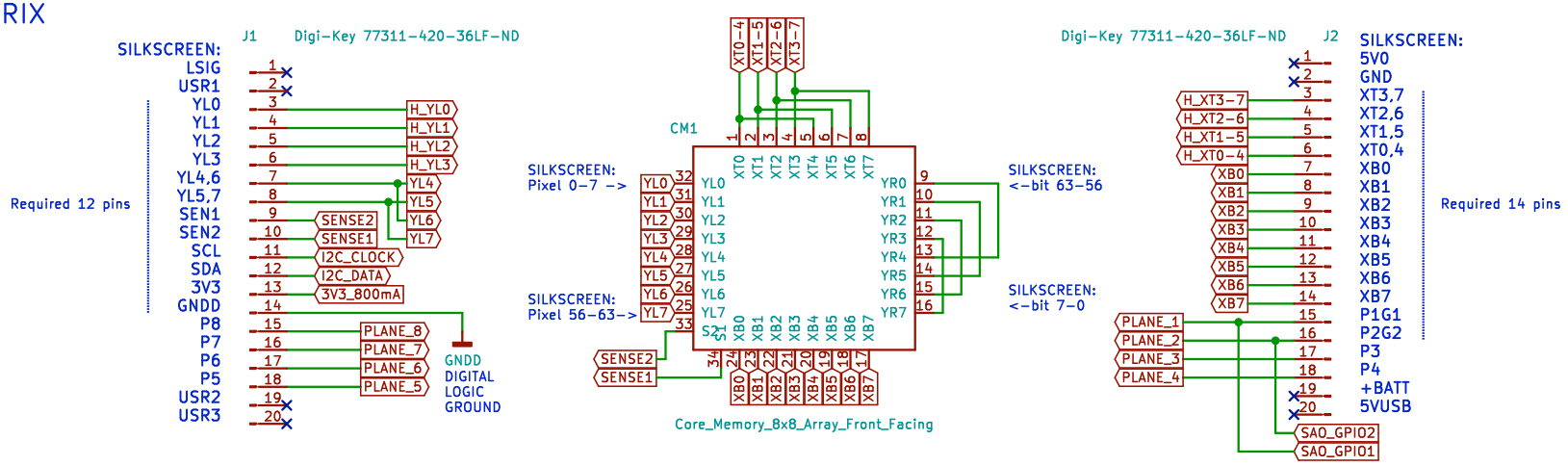
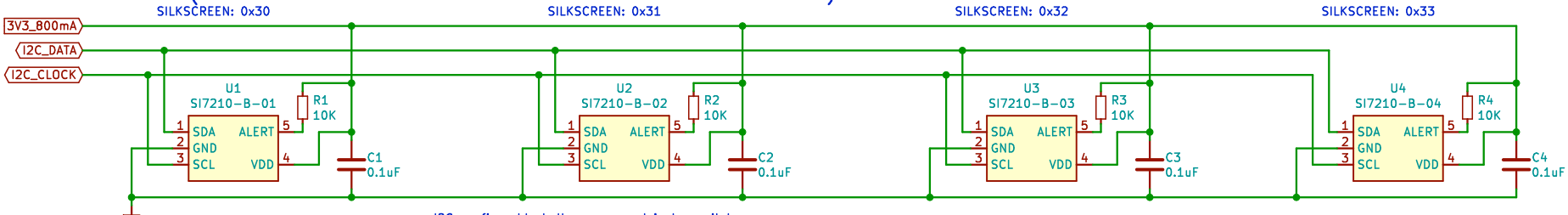
CORE MATRIX



HALL SENSORS (SEE SECOND SHEET FOR HALL SWITCH ALTERNATES)



I2C configurable hall sensors, not just a switch.
Silicon Labs SI7210-B-01-IVR through 04-IVR
Low (push-pull) up to 20 mT, SOT23-5
https://www.digikey.com/product-detail/en/silicon-labs/SI7210-B-04-IVR/336-4129-1-ND/7648844
Recommended sensitivity ± (N and S) 3-7 mT (30-70 Gauss)

REQUIRED: CONFIGURE FOR SINGLE CORE PLANE

STEP 1: Solder eight SJs JP9 through JP16.

STEP 2: Do NOT solder any JPs JP1 through JP8.

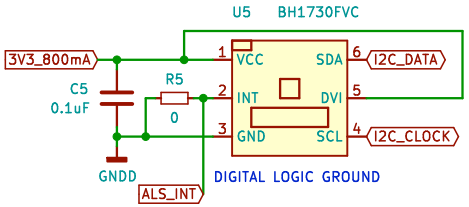
STEP 3: No additional components needed.

STEP 4 (LOGIC BOARD): See instructions on Logic Board, if any.

Sheet: Core64 CB V0.5 Optional

File: Core64 CB V0.5 Optional.sch

AMBIENT LIGHT SENSOR I2C 0x29 (SEE SECOND SHEET FOR ALTERNATIVES)



SILKSCREEN MISC.

CORE BOARD V0.5 [REL. DATE]. ANDY GEPPERT
Core64.MachineIdeas.com
Interactive Core Memory

SILKSCREEN GRAPHICS

L1
Core_64_Logo_9mm_tall

L2
Core_64_M-+S_Buttons_4mm

All capacitors ceramic X7R unless otherwise noted.

*** Work in progress ***
Visit www.Core64.io for information on assembly and optional features.
Concept and design by Andy Geppert @ www.MachineIdeas.com

Sheet: /
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Title: Core64 CB (Core Board)

Size: A	Date: 2021-02-15	**Rev: 0.5**
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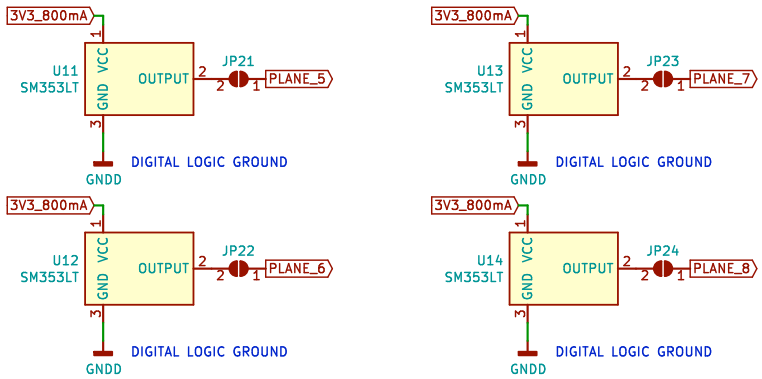
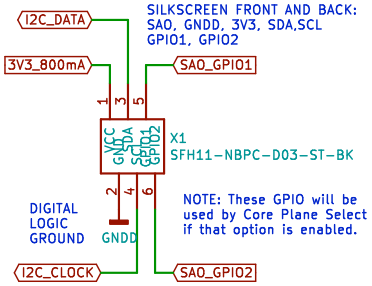
HALL SWITCH ALTERNATES



[OPTIONAL] SAO #2 EXPANSION

SIMPLE ADD ONS see:
https://hackaday.io/project/175182-simple-add-ons-sao



[OPTIONAL] CORE PLANE SELECT

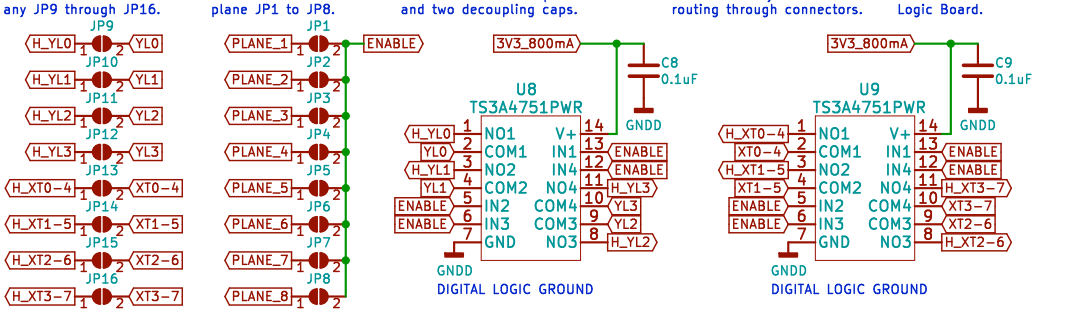
STEP 1: Do NOT solder any JP9 through JP16.

STEP 2: Solder ONE plane JP1 to JP8.

STEP 3: Install two quad switches and two decoupling caps.

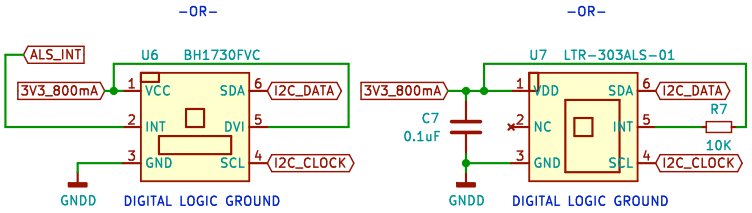
STEP 4: Modify sense wire routing through connectors.

STEP 5 (LOGIC BOARD): See instructions on Logic Board.



AMBIENT LIGHT SENSOR I2C 0x29

SILKSCREEN: ALS I2C 0x29



Sheet: /Core64 CB V0.5 Optional/
File: Core64 CB V0.5 Optional.sch

Title:

Size: A4	Date: 2021-02-15	**Rev:**
KiCad E.D.A. kicad (5.1.2-1)-1		Id: 2/2