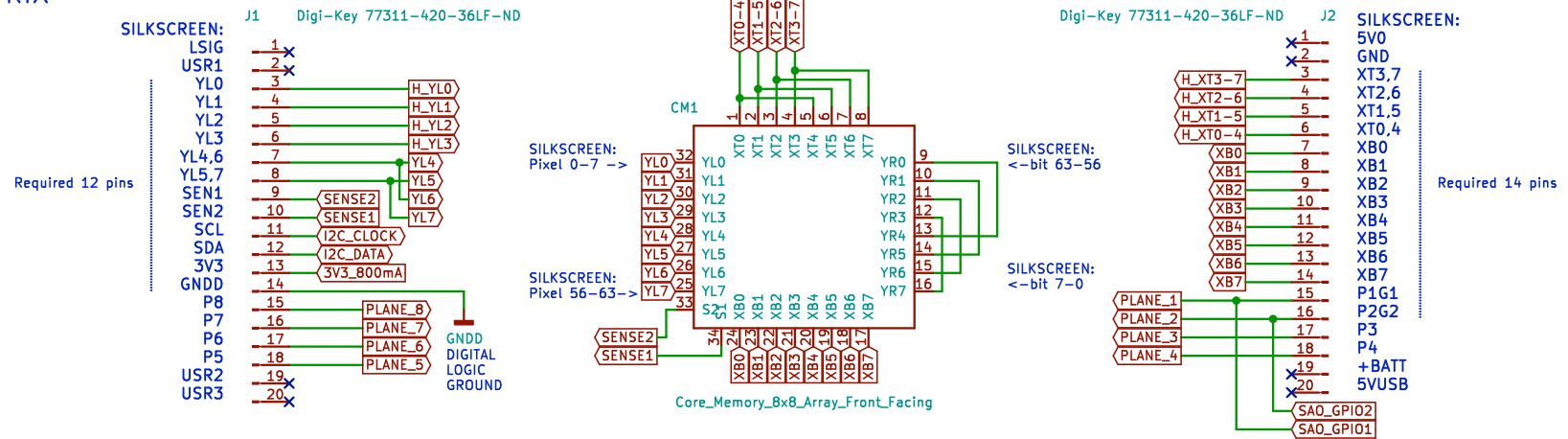
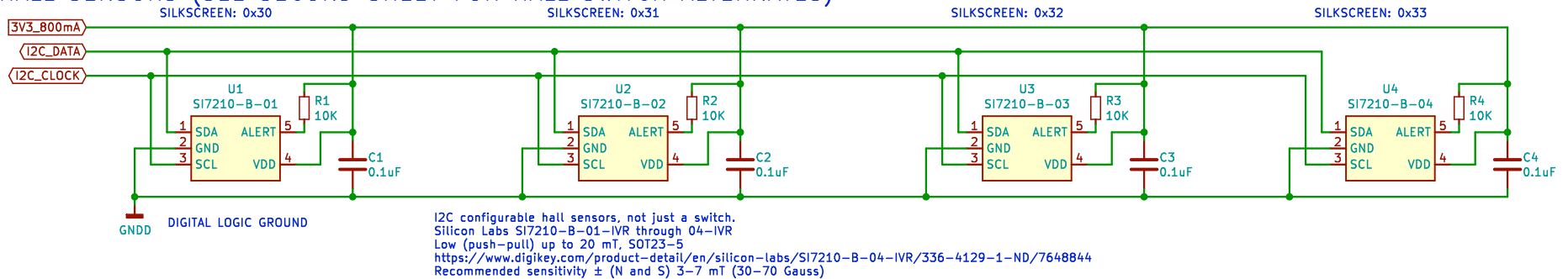


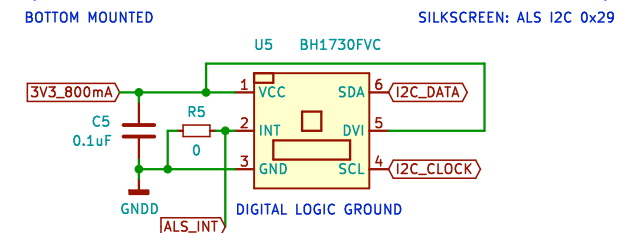
## CORE MATRIX



## HALL SENSORS (SEE SECOND SHEET FOR HALL SWITCH ALTERNATES)



## AMBIENT LIGHT SENSOR I2C 0x29 (SEE SECOND SHEET FOR ALTERNATIVES)



## REQUIRED: CONFIGURE FOR SINGLE CORE PLANE

- STEP 1: Solder eight SJs JP9 through JP16.
- STEP 2: Do NOT solder any JPs JP1 through JP8.
- STEP 3: No additional components needed.
- STEP 4 (LOGIC BOARD): See instructions on Logic Board, if any.

Sheet: Core64 CB V0.5 Optional



File: Core64 CB V0.5 Optional.sch

## SILKSCREEN MISC.

CORE BOARD V0.5 [REL. DATE]. ANDY GEPPERT  
Core64.MachineIdeas.com  
Interactive Core Memory

## SILKSCREEN GRAPHICS

L1 Core\_64\_Logo\_9mm\_tall  
L2 Core\_64\_M-+S.Buttons\_4mm

All capacitors ceramic X7R unless otherwise noted.

\*\*\* As prototyped \*\*\*

Visit [www.Core64.io](http://www.Core64.io) for information on assembly and optional features.

Concept and design by Andy Geppert © [www.MachineIdeas.com](http://www.MachineIdeas.com)

Sheet: /  
File: Core64 CB v0.5.sch

Title: Core64 CB (Core Board)

Size: A Date: 2021-02-28

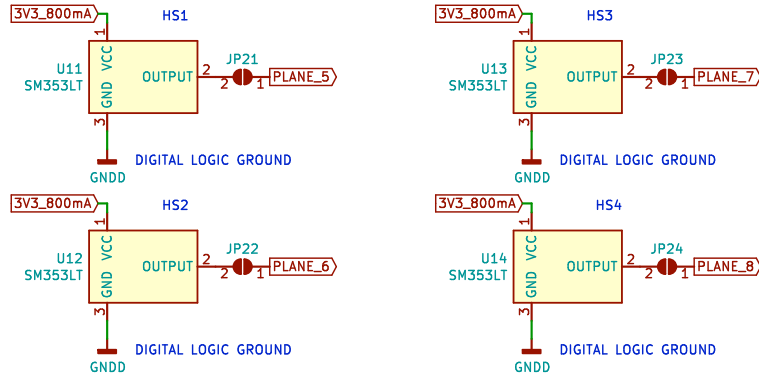
KiCad E.D.A. eeschema (5.1.2-1)-1

Rev: 0.5

Id: 1/2

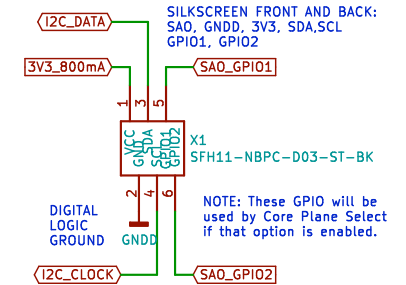
## [ALTERNATE] HALL SWITCH ALTERNATES

To use Hall Switches, solder four SJs JP21–24.



## [OPTIONAL] SAO #2 EXPANSION

SIMPLE ADD ONS see:  
<https://hackaday.io/project/175182-simple-add-ons-sao>



## [OPTIONAL] MULTI CORE PLANE SELECT

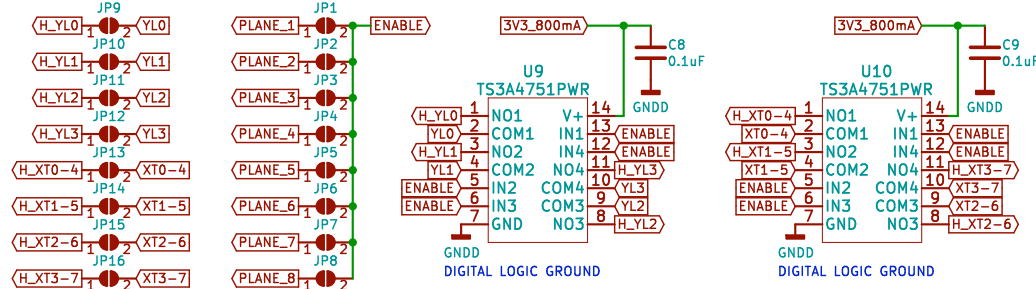
STEP 1: Do NOT solder any JP9 through JP16.

STEP 2: Solder ONE plane JP1 to JP8.

STEP 3: Install two quad switches and two decoupling caps.

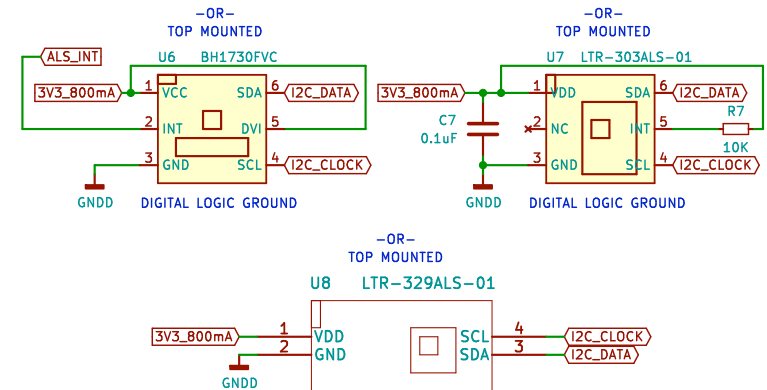
STEP 4: Modify sense wire routing through connectors.

STEP 5 (LOGIC BOARD): See instructions on Logic Board.



## [ALTERNATE] AMBIENT LIGHT SENSORS I2C 0x29

SILKSCREEN: ALS I2C 0x29



All capacitors ceramic X7R unless otherwise noted.

\*\*\* As prototyped \*\*\*

Visit [www.Core64.io](http://www.Core64.io) for information on assembly and optional features.

Concept and design by Andy Geppert • [www.Machineldeas.com](http://www.Machineldeas.com)

Sheet: /Core64 CB V0.5 Optional/

File: Core64 CB V0.5 Optional.sch

Title: Core64 CB (Core Board)

Size: A4

Date: 2021-02-28

Rev: 0.5

KiCad E.D.A. eeschema (5.1.2-1)-1

Id: 2/2