

Instruction	Opcode	RegWrite	MemWrite	Load	Byte	ALUSrcE	MemoReg
lw	0000011	1	0	1	0	1	1
lb	0000011	1	0	1	1	1	1
sw	0100011	0	1	0	0	1	x
sb	0100011	0	1	0	1	1	x
add	0110011	1	0	x	0	0	0