Instruction	MUX4_sel	Mux8_sel	MUX7_sel	ALU_control	MUX10_Sel	Mux9_sel	mem_write	mem_read	Reg_write	Mux12_sle	Mux11_sel
ADD	1	_000	_000	1	0	0	0	0	1	_00	10
ADZ	1	_000	_000	1	0	0	0	0	1	_00	10
ADC	1	_000	_000	1	0	0	0	0	1	_00	10
NDU	1	_000	_000	0	0	0	0	0	1	_00	10
NDZ	1	_000	_000	0	0	0	0	0	1	_00	10
NDC	1	_000	_000	0	0	0	0	0	1	_00	10
ADI	1	_001	_000	1	0	0	0	0	1	_00	_01
LHI	0	_000	_000	0	0	0	0	0	1	11	_00
LW	1	_000	_001	1	0	1	0	1	1	_01	_00
SW	1	_000	_001	1	0	0	1	0	0	_00	_00
LM	0	_010	_000	1	1	0	0	1	L2_1(7-0)!=0	_01	11
SM	0	_010	_000	1	1	0	L2_1(7-0)!=0	0	0	_00	_00
LM'	0	_011	_010	1	1	0	0	1	L2_1(7-0)!=0	_01	11
SM'	0	_011	_010	1	1	0	L2_1(7-0)!=0	0	0	_00	_00
BEQ	1	_000	_000	0	0		0	0	1	_00	_00
JAL	0	_000	_000	0	0	0	0	0	1	10	_00
JLR	1	_000	_000	0	0	0	0	0	1	10	_00