FLORIDA STATE UNIVERSITY FAMU-FSU COLLEGE OF ENGINEERING

AN ADAPTABLE SIGNAL INTERFACE TO ENABLE A FAULT MANAGEMENT DEVELOPMENT PROCESS FOR A MEDIUM VOLTAGE DC MICROGRID

By

CARLOS A. WONG

A Thesis submitted to the Department of Electrical and Computer Engineering in partial fulfillment of the requirements for the degree of Master of Science

2020

Carlos A. Wong defended this thesis on November 17, 2020. The members of the supervisory committee were:	
	Michael Steurer
	Professor Directing Thesis
	Sastry Pamidi
	Committee Member
	Omar Faruque
	Committee Member
	Karl Schoder
	Committee Member
	and approved the above-named committee members, and certifies in accordance with university requirements.