

MLP-Aware Reuse Distance Prediction Based LLC Replacement for Chip Multiprocessors

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- MLP-awareness
 - improvement on the LRU replacement policy used in RD prediction based LLC replacement paper
- PC based dynamic MLP based cost predictor *
 - improvement on the delta based static, offline method for determining the predictability of MLP based costs used in the MLP-aware replacement paper
 - in the original paper (page 4): The average delta value is also fairly slow, which means that the next-time mlp-cost for a cache block remains fairly close to the current mlp-cost. Thus, the current mlp-cost can be used as a predictor of the next mlp-cost of the same block in MLP-aware cache replacement.