

**apbSubSystem**

**Pin Description**

|  |  |  |  |
| --- | --- | --- | --- |
| **Name** | **Type** | **Direction** | **Description** |
| clk |  | in |  |
| rst\_an |  | in |  |
| Interrupt\_IRL[3:0] |  | out |  |
| Interrupt\_IRQVEC[3:0] |  | in |  |
| Interrupt\_INTack |  | in |  |
| ex\_ambaAHB\_hsel |  | in |  |
| ex\_ambaAHB\_hready\_resp |  | out |  |
| ex\_ambaAHB\_hrdata[31:0] |  | out |  |
| ex\_ambaAHB\_hprot[3:0] |  | in |  |
| ex\_ambaAHB\_htrans[1:0] |  | in |  |
| ex\_ambaAHB\_hsize[2:0] |  | in |  |
| ex\_ambaAHB\_hready |  | in |  |
| ex\_ambaAHB\_hwrite |  | in |  |
| ex\_ambaAHB\_hburst[2:0] |  | in |  |
| ex\_ambaAHB\_haddr[31:0] |  | in |  |
| ex\_ambaAHB\_hresp[1:0] |  | out |  |
| ex\_ambaAHB\_hwdata[31:0] |  | in |  |
| i\_apbbus\_slv4\_paddr[31:0] |  | out |  |
| i\_apbbus\_slv4\_psel |  | out |  |
| i\_apbbus\_slv4\_pwrite |  | out |  |
| i\_apbbus\_slv4\_pwdata[31:0] |  | out |  |
| i\_apbbus\_slv4\_penable |  | out |  |
| i\_apbbus\_slv4\_prdata[31:0] |  | in |  |
| i\_apbbus\_slv5\_paddr[31:0] |  | out |  |
| i\_apbbus\_slv5\_psel |  | out |  |
| i\_apbbus\_slv5\_pwrite |  | out |  |
| i\_apbbus\_slv5\_pwdata[31:0] |  | out |  |
| i\_apbbus\_slv5\_penable |  | out |  |
| i\_apbbus\_slv5\_prdata[31:0] |  | in |  |
| i\_apbbus\_slv6\_paddr[31:0] |  | out |  |
| i\_apbbus\_slv6\_psel |  | out |  |
| i\_apbbus\_slv6\_pwrite |  | out |  |
| i\_apbbus\_slv6\_pwdata[31:0] |  | out |  |
| i\_apbbus\_slv6\_penable |  | out |  |
| i\_apbbus\_slv6\_prdata[31:0] |  | in |  |
| i\_apbbus\_slv7\_paddr[31:0] |  | out |  |
| i\_apbbus\_slv7\_psel |  | out |  |
| i\_apbbus\_slv7\_pwrite |  | out |  |
| i\_apbbus\_slv7\_pwdata[31:0] |  | out |  |
| i\_apbbus\_slv7\_penable |  | out |  |
| i\_apbbus\_slv7\_prdata[31:0] |  | in |  |