

| # | Status | Title | Resources | Given Work | Planned Start | Planned End | % completed | Predecessors |
|----|--------|---|---------------------|------------|---------------------|---------------------|----------------|--------------|
| 25 | ▲ | Mechanical | J.B.; N.... | | Oct 9, 2015 | Jan 7, 2016 | | |
| 26 | ■ | 3D Design Review | J.B. | 1 day | Oct 9, 2015 | Oct 9, 2015 | | |
| 27 | ■ | Incorporate Changes | J.B. | 2 days | Oct 9, 2015 | Oct 12, 2015 | | 26SS |
| 28 | ■ | BOM Preparation and Quotation of Silicone, FDM and Tool Mould | J.B. | 1 day | Oct 13, 2015 | Oct 13, 2015 | | 27 |
| 29 | ■ | Name Plate Design and Preparation | J.B. | 2 days | Oct 13, 2015 | Oct 14, 2015 | | 28SS |
| 30 | ■ | Prepare Test Plan | J.B. | 1 day | Oct 15, 2015 | Oct 15, 2015 | | 29 |
| 31 | ■ | Wire Harnness Preparation | J.B. | 2 days | Oct 15, 2015 | Oct 16, 2015 | | 30SS |
| 32 | ● | Finalize Silicone Vendor | N.C. | | Oct 17, 2015 | Oct 17, 2015 | | 28 |
| 33 | ● | Finalize FDM Vendor | N.C. | | Oct 17, 2015 | Oct 17, 2015 | | 28 |
| 34 | ● | Finalize Tool Vendor | N.C. | | Oct 17, 2015 | Oct 17, 2015 | | 28 |
| 35 | ○ | Launch Silicone Mould | S.M.V. | 15 days | Oct 19, 2015 | Nov 6, 2015 | | 31; 32 |
| 36 | ○ | Launch FDM Mould | F.M.V. | 7 days | Oct 19, 2015 | Oct 27, 2015 | | 31; 33 |
| 37 | ○ | Packing Design | J.B. | 3 days | Oct 19, 2015 | Oct 21, 2015 | | 31 |
| 38 | ○ | Meter Assembly for Fitment Verification (FDM) | J.B.; V.B. | 6 days | Oct 28, 2015 | Oct 30, 2015 | | 36 |
| 39 | ○ | Design feedback incorporate | J.B. | 2 days | Nov 2, 2015 | Nov 3, 2015 | | 38 |
| 40 | ○ | Packing Development | P.V. | 40 days | Oct 22, 2015 | Dec 16, 2015 | | 37 |
| 41 | ○ | Launch Tool Mould | T.M.V. | 40 days | Nov 4, 2015 | Dec 29, 2015 | | 39 |
| 42 | ○ | T1 Sample Verification | J.B. | 5 days | Dec 30, 2015 | Jan 5, 2016 | | 41 |
| 43 | ○ | Mechanical Pilot Production (15 Nos) | T.M.V. | 2 days | Jan 6, 2016 | Jan 7, 2016 | | 42 |
| 44 | ○ | Mechanical Ready for Type Test | | | Jan 7, 2016 | Jan 7, 2016 | | 43 |
| 45 | ● | Firmware | S.S.; P.V... | | Oct 12, 2015 | Jan 29, 2016 | 5.03% | |
| 46 | ✓ | Requirement Analysis | S.S. | | Oct 12, 2015 | Oct 16, 2015 | 100.00% | |
| 47 | ✓ | Study of PRD | S.S. | 2 days | Oct 12, 2015 | Oct 13, 2015 | 100.00% | |
| 48 | ✓ | Preparation of product Specification | S.S. | 2 days | Oct 14, 2015 | Oct 15, 2015 | 100.00% | 47 |
| 49 | ✓ | Preparation of Design Document | S.S. | 1 day | Oct 16, 2015 | Oct 16, 2015 | 100.00% | 48 |
| 50 | ● | Design Feasibility | P.V.; S.S. | | Oct 13, 2015 | Oct 21, 2015 | 44.44% | |
| 51 | ✓ | Clock Initialization | P.V. | 2 days | Oct 13, 2015 | Oct 14, 2015 | 100.00% | |
| 52 | ✓ | i2C Drivers | P.V. | 1 day | Oct 15, 2015 | Oct 15, 2015 | 100.00% | 51 |

