

#	Status	Title	Resources	Given Work	Planned Start	Planned End	% completed	Predecessors
89	●	Validation	D.S.		Oct 5, 2015	Jan 18, 2016	14.29%	
90	✓	Phython learning and SP Bug Verification	D.S.	5 days	Oct 5, 2015	Oct 9, 2015	100.00%	
91	✓	Script Preparation	D.S.	5 days	Oct 12, 2015	Oct 16, 2015	100.00%	90
92	○	Script Verification and Data Retention testing	D.S.	5 days	Oct 19, 2015	Oct 23, 2015		91
93	○	SP FW Verification	D.S.	5 days	Oct 26, 2015	Oct 30, 2015		92
94	○	SP Calibration with BCS	D.S.	5 days	Nov 2, 2015	Nov 6, 2015		93
95	○	Test Plan Preparation for SSN	D.S.	5 days	Nov 9, 2015	Nov 13, 2015		94
96	○	New HW Testing (Duplicate Task) + DLMS	D.S.	5 days	Nov 20, 2015	Nov 26, 2015		7; 54; 69; 95
97	○	New HW Testing Continued + Weekly DLMS Release	D.S.	5 days	Nov 27, 2015	Dec 3, 2015		57; 96
98	○	New HW Testing Continued + Weekly DLMS Release	D.S.	5 days	Dec 4, 2015	Dec 10, 2015		97
99	○	New HW Testing Continued + Weekly DLMS Release	D.S.	5 days	Dec 11, 2015	Dec 17, 2015		98
100	○	New HW Testing Continued + Weekly DLMS Release	D.S.	5 days	Dec 18, 2015	Dec 24, 2015		99
101	○	New HW Testing Continued + Weekly DLMS Release	D.S.	5 days	Jan 4, 2016	Jan 8, 2016		87; 100
102	○	Testing with Meter to be released for Type Test (5 Nos)	D.S.	10 days	Jan 5, 2016	Jan 18, 2016		12
103	○	BCS with Modbus++ Support		1 day ?	Oct 19, 2015	Oct 19, 2015		
104	○	BCS with DLMS Support		1 day ?	Oct 19, 2015	Oct 19, 2015		
105	○	Type Test	C.; Y.		Jan 11, 2016	Feb 19, 2016		
106	○	Release Meter for Type Test 13779/16444	C.	30 days	Jan 11, 2016	Feb 19, 2016		15; 35; 101
107	○	Release Meter for Type Test 15959	Y.	30 days	Jan 11, 2016	Feb 19, 2016		15; 35; 101
108	○	BIS			Oct 19, 2015	Oct 19, 2015		
109	○	Hire BIS Engg		1 day ?	Oct 19, 2015	Oct 19, 2015		
110	○	Manufacturing		1 day ?	Oct 19, 2015	Oct 19, 2015		