#	Status	Title	Resources	Given Work	Planned Start	Planned End	% completed	Predecessors
161	■	**Validation**	**C.S.**		**Oct 1, 2015**	**Jan 1, 2016**		
162	■	Verified 3 pphase Metrology	C.S.	5 days	Oct 1, 2015	Oct 7, 2015		
163	■	Studied IS 16444	C.S.	2 days	Oct 8, 2015	Oct 9, 2015		162
164	■	Script Verification and Data Retention testing	C.S.	3 days	Oct 12, 2015	Oct 14, 2015		163
165	○	PP FW Verification	C.S.	5 days	Oct 19, 2015	Oct 23, 2015		164
166	○	PP FW Verification	C.S.	10 days	Oct 26, 2015	Nov 6, 2015		165
167	○	Test Plan Preparation for SSN	C.S.	5 days	Nov 9, 2015	Nov 13, 2015		166
168	○	New HW Testing (Duplicate Task)	C.S.	5 days	Nov 23, 2015	Nov 27, 2015		7; 141; 167
169	○	New HW Testing Continued + Weekly DLMS Release	C.S.	5 days	Nov 30, 2015	Dec 4, 2015		168
170	○	New HW Testing Continued + Weekly DLMS Release	C.S.	5 days	Dec 7, 2015	Dec 11, 2015		169
171	○	New HW Testing Continued + Weekly DLMS Release	C.S.	5 days	Dec 14, 2015	Dec 18, 2015		170
172	○	New HW Testing Continued + Weekly DLMS Release	C.S.	5 days	Dec 21, 2015	Dec 25, 2015		171
173	○	New HW Testing Continued + Weekly DLMS Release	C.S.	5 days	Dec 28, 2015	Jan 1, 2016		172
174	○	BCS with Modbus++ Support		1 day ?	Oct 19, 2015	Oct 19, 2015		
175	○	BCS with DLMS Support		1 day ?	Oct 19, 2015	Oct 19, 2015		
176	○	**Type Test**	**C.V.; Y.V.**		**Feb 8, 2016**	**Mar 21, 2016**		
177	○	Release Meter for Type Test 13779/16444	C.V.	30 days	Feb 8, 2016	Mar 21, 2016		24; 44; 45; 161
178	○	Release Meter for Type Test 15959	Y.V.	30 days	Feb 8, 2016	Mar 21, 2016		24; 44; 45; 161
179	○	**BIS**			**Oct 19, 2015**	**Oct 19, 2015**		
180	○	Hire BIS Engg		1 day ?	Oct 19, 2015	Oct 19, 2015		
181	○	Manufacturing		1 day ?	Oct 19, 2015	Oct 19, 2015		