## SEEKING FULL-TIME EMPLOYMENT STARTING SUMMER 2020

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Education

**Carnegie Mellon University** 

M.S. IN ELECTRICAL AND COMPUTER ENGINEERING (OVERALL GPA 3.65/4.00)

Pittsburgh, Pennsylvania Expected May 2020

Pittsburgh, Pennsylvania

**Carnegie Mellon University** 

B.S. IN ELECTRICAL AND COMPUTER ENGINEERING WITH UNIVERSITY HONORS (OVERALL GPA 3.49/4.00)

· CIT Dean's List Fall 2015, Spring 2017, and Spring 2018, Senior Leadership Recognition, University Honors

May 2019

Selected Coursework

Logic Design and Verification

Intro to Computer Architecture Operating Systems Design & Impl. Embedded Systems Software Engineering Advanced Storage Systems

Structure & Design of Digital Systems

Capstone Design Secure Coding

Experience .

**NVIDIA Corporation** 

Santa Clara, California

GPU RTL DESIGN INTERN

• Future contributions to GPU submodules on the GPU RTL Design team.

**NVIDIA Corporation** 

**CUDA Systems Software Intern** 

Santa Clara, California

July 2019 - September 2019

September 2019 - December 2019

• Future work on the Unified Virtual Memory subsystem of the CUDA computing platform. Interface CPU and GPU memories.

MITRE Corporation

EMBEDDED SOFTWARE COOP/INTERN

Bedford, Massachusetts

May 2018 - August 2018

- Develop and test a multiple power & clock fault testing suite as Python interfaces for Arbitrary Waveform/Function Generators.
- Profile Dwenguino AVR instructions and develop suite for profiling other hardware implementations.
- · Engineer methods to extract cryptographic information at a distance by means of electromagnetic emissions.
- · Alter programmable logic for secure video game console eCTF to store symmetric keys. Win 2nd place in eCTF and Iron Flag Award.

Robotany

Pittsburgh, Pennsylvania

**ELECTRICAL ENGINEERING POWER SYSTEMS INTERN** 

- Design 3-phase delta 240V electrical power system for robotic vertical farm, fixture wiring, and control racks.
- · Specify and implement power system including conduits, transformers, and load centers. Manage and direct assembly teams.

## Auditory Lab (Profs. Heller and Grover)

Pittsburgh, Pennsylvania

ELECTRICAL AND COMPUTER ENGINEERING RESEARCH & DEVELOPMENT ENGINEER

May 2016 - May 2017

May 2017 - May 2018

- · Engineer mobile application to assist in teaching echolocation skills to visually impaired persons on an interdisciplinary team.
- Provide back-end design for and administrate data collection server.

Skills & Hobbies

SystemVerilog FPGAs ARM Python Computer Architecture Realtime Embedded Power Systems Climbing C / C++ Git Reverse Engineering Lab Equipment Rapid Prototyping Hiking/Backpacking

## Projects & Extracurriculars

- RTL Design/Verification Projects Logic Design & Verification 18-341, Fall 2018, Computer Architecture 18-447, Spring 2019 Design and implement optimized pipelined matrix multiply-accumulate, concurrent "NoC" packet router, and more in SystemVerilog. Implement a series of increasingly complex RISC-V processors ranging from a single-cycle design to an out-of-order architecture.
- UNIX-like Kernel from Scratch Operating Systems Design & Implementation 15-410/605, Spring 2018 Design & implement a small (14k loc) UNIX-like kernel with a partner over 8 weeks. Some features include kernel-level threads, user-level threads, condition variables, mutexes, readers-writers locks, and virtual memory management.
- Build 18 Hardware Hackathon Builder (January 2017)

During a week-long Hardware Hackathon, design and implement a protocol for driving a 96 x 64 RGB LED Matrix via an FPGA in SystemVerilog. Use this display to show programmatically derived 3D animations.

Activities Board Technical Committee Core Member & Executive Board (2015 - Present)

Direct and implement production services for campus events, including 3-phase power systems and professional audio and lighting rigs.

CMU Explorers Club (CMUX) MEMBER (2015), HIKING CHAIR & TREASURER (2016-PRESENT)

CMUX is an club dedicated to making outdoor activities more accessible. Organize hikes as Hiking Chair. Manage finances as Treasurer.