Falsification-Driven Verification

Alex Groce, Iftekhar Ahmed, Carlos Jensen
School of Electrical Engineering and Computer Science
Oregon State University, Corvallis, Oregon
Email: agroce@gmail.com, ahmedi@onid.oregonstate.edu, cjensen@eecs.orst.edu

Paul E. McKenney
IBM Linux Technology Center
Email: paulmck@linux.vnet.ibm.com

Abstract—**Formal verification has finally advanced to a state where non-experts, including systems software developers, may want to verify the correctness of small but critical modules. Unfortunately, despite considerable efforts in the area, determining if a "verification" actually verifies what the author intends it to is still difficult, even for model checking experts. Previous approaches from the model checking community are valuable, but difficult to understand and limited in applicability. Developers using a tool like a bounded model checker need verification coverage in terms of the software they are verifying, rather than in model checking terms. In this paper we propose a tool framework and methodology to allow both developers and expert users to determine, more precisely, just what it is that they have verified for software systems. Our basic approach is based on a novel variation of mutation analysis and a conceptual model of verification based on Popper's notion of falsification. We use the popular C/C++ bounded model checker CBMC, modified to allow a user to determine the "strength" of a mutant, and show that our approach is applicable not only to simple (but complete, within bounds) verification of data structures and sorting routines, and verification of code taken from Mozilla's JavaScript engine, but to understanding an ongoing effort to verify the Linux kernel Read-Copy-Update mechanism.**

I. INTRODUCTION

Software model checking [1] has recently, thanks to improvements in model checking tools as well as SAT and SMT solvers, and the large amount of memory available even on commodity workstations, become a potentially valuable tool for developers of critical software modules who want to, at minimum, perform a very aggressive search for bugs and, at best, prove correctness of their code. Tools such as CBMC [2] (the C Bounded Model Checker) allow a software engineer to model check code by writing what is essentially a generalized test harness[1] in the language of the Software Under Test (SUT). Figure 1 shows a CBMC harness for sorting routines. This is a simple program, but typical of the structure of a small verification problem. Only a few aspects of this code differ from normal testing. First, `nondet_int` in CBMC can return any value, nondeterministically. It is not equivalent to a "random" choice but true nondeterminism: CBMC will explore all possible values. The `__CPROVER_assume` statement is used to restrict the program executions considered: it has the usual `assume` semantics [3], [4], so CBMC ignores all executions that violate assumptions.

CBMC compiles a harness and the SUT (here a quick sort implementation) into a goto-program, instruments this

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[1]By a harness we mean a program that defines the environment in which the software is verified, defines the form of valid tests, and provides correctness properties; in CBMC such a harness looks very similar to a harness for more traditional software testing.

```c
#include <stdio.h>
#include "sort.h"
int a[SIZE];
int ref[SIZE];
int nondet_int ();
int main () {
  int i, v, prev;
  int s = nondet_int();
  __CPROVER_assume((s > 0) && (s <=SIZE));
  for (i = 0; i < s; i++) {
    v = nondet_int();
    printf ("LOG: ref[%d] = %d\n", i, v);
    ref[i] = v;
    a[i] = v;
  }
  sort(a, s);
  prev = a[0];
  for (i = 0; i < s; i++) {
    printf ("LOG: a[%d] = %d\n", i, a[i]);
    assert (a[i] >= prev);
    prev = a[i];
  }
}
```

Fig. 1. CBMC harness to check a sorting routine.

program with property checks for assertions, array bounds violations, etc., and then unrolls loops based on a user-provided *unwinding bound* to produce a SAT problem or SMT constraint such that satisfying assignments are representations of a trace demonstrating a property violation, known as a *counterexample* [5]. For CBMC, this means that if *any possible execution allowed by the harness* violates any properties checked, a counterexample will be produced. This includes user-specified assertions and automatically generated properties such as array bounds and pointer validity checks. One such generated property is that no loop in the program executes more than the *unwinding bound* times. For example, if we run CBMC on the harness shown and set the unwinding bound to 3 and add `-DSIZE=2`, we will check the correctness of the SUT over *all possible arrays* of size 2 or less, including checking that sorting never requires passing through any loop more than 3 times (counting the iteration where the bound is exceeded).

When a model checker produces a counterexample, a developer's task is straightforward, if sometimes difficult: either the SUT has a fault, or the harness itself is flawed. In both cases, the status of the verification effort is the counterxample trace, is full of evidence as to the reason for the failure to verify the SUT. Moreover, any solution (fix to SUT or harness) is easily checked: if it is correct, the model checker stops reporting the previous counterexample. This is essentially a normal debugging problem, but with the advantage that solutions are easily checked.

Unfortunately, model checkers do not invariably report counterexamples: eventually the SUT is likely to satisfy the

properties encoded in the harness! It is in this case that problems arise: what, precisely, has been verified? Does the harness in fact specify all aspects of correctness required? Is the SUT correct? Formal verification is not only subject to the many issues that make "no faults detected" results dubious in testing [6], [7], but also to more subtle problems. For example, an incorrect *assume* statement may constrain a system so that not only are there no counterexamples, there are no (interesting) executions of the system at all.

This problem has concerned the model checking community for some time [8], [9], and resulted in efforts to define *coverage metrics* for model checking. While such metrics are interesting and useful, however, they have typically been aimed at the hardware verification community, and often useful primarily to experts in formal verification. In this paper, we adapt more traditional mutation testing [10], [11] to the problem of software verification. A mutant of a program is a version of the program that introduces a small syntactic change. The idea behind mutation testing is that a good test suite will be able to detect when (as is usually the case) such a change introduces a bug in the SUT. In the case of bounded model checking, since we aim at *verification* rather than merely good testing, it seems clear that surviving mutants are likely to indicate a weakness of the verification.

The use of mutation testing most often seen in the software engineering literature will not suffice in this case: simply noting a mutation kill rate is not enough. The typical small scope of the code to be verified, and the presumed importance of code targeted for verification suggests an approach in which *individual mutants* are examined by the developer. Without additional assistance, such an approach cannot scale. We propose that the capabilities of the model checking tool, the nature of formal verification, and the adoption of certain best practices can make this seemingly too-demanding approach in fact practical for real verification tasks.

Our primary contribution in this paper is a *falsification-driven* approach to verification: an extension of traditional mutation testing that aids the user of a model checker in understanding successful (and "successful") verification results, determining when a harness is not actually strong enough to ensure correctness, and correcting the verification harness. To support this approach, we show how to use mutation testing to choose a problem size in bounded model checking, how to mutate a harness to determine if any similar harnesses have an equal (or better) mutation kill rate, and most importantly, how to modify CBMC, a harness, and mutants to automatically produce *successful high-coverage executions covering mutated code* in order to understand mutant behavior and find subtle harness flaws. This approach, unlike a simpler method of searching for cases where the mutated code and original code behave differently for the same inputs, applies even to verification of reactive and concurrent systems, where there is no easily obtained notion of "for the same inputs." We also propose the use of mutation analysis to gain limited confidence in program correctness even past model checker scalability limits. At a more general level, we discuss the fundamental nature of "verification" in a real-world context where specifications are never known to be complete. We propose that falsification, as in Popper's philosophy of science [12], is a more useful conceptual framework for most software

```
#include "sort.h"
void quickSort( int a[], int l, int r)
{
  printf ("LOG: called with l=%d, r=%d\n", l, r);
  int j;
  if( l < r )
    {
      // divide and conquer
      j = partition( a, l, r);
      quickSort( a, l, j-1);
      quickSort( a, j+1, r);
    }
}
int partition( int a[], int l, int r) {
  int pivot, i, j, t;
  pivot = a[l];
  i = l; j = r+1;
  while( 1)
    {
      do ++i; while( i <= r && a[i] <= pivot );
      do --j; while( a[j] > pivot );
      if( i >= j ) break;
      t = a[i]; a[i] = a[j]; a[j] = t;
    }
  t = a[l]; a[l] = a[j]; a[j] = t;
  return j;
}
void sort(int a[], unsigned int size) {
  quickSort(a, 0, size-1);
}
```

Fig. 2. Quick sort code.

verification efforts: rather than focusing on what can be proven about a program, it may be best to focus on how a verification effort distinguishes the "real" program from similar alternative programs that can be shown to *not* match the theory of program behavior. This approach aims at verification, but continually evaluates and refines verification effort by its ability to *falsify* rather than to verify.

II. A SIMPLE EXAMPLE VERIFICATION

As an example of the proposed verification methodology, consider again the harness shown in Figure 1. If we take the first hit on Google for "quick sort in C" [13], shown in Figure 2[2], we can model check it using the harness, defining `SIZE=2` and setting the unwinding bound to 3 (we need one more unwinding than the largest possible number of items in the array). CBMC reports `VERIFICATION SUCCESSFUL` in less than a second. Does this mean we have verified what we want to verify? How do we understand this "successful" verification result better?

A. Finding a Good Problem Size

The first question we face is whether 2 is really a good maximum array size to examine. The problem of determining a *completeness threshold* (an execution-length bound sufficient to prove correctness in all cases for a given property) for bounded model checking is fundamentally difficult [14] and is, for real-world C programs, more an art than a science at present[3]. Are there bugs for which 2 is too small an array size? In order to find out, we generate a set of mutants for

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[2]In fact, that actual code is incorrect, with an access `a[i]` that does not properly use short circuiting logical operators to protect array bounds; CBMC detected this, and we fixed it for this paper.

[3]In our own practice, the most common way of setting it is to guess a bound and see if the resulting problem is too large for the available computational resources.

`quicksort.c`. Using the mutation tool for C code developed by Jamie Andrews [15], we can produce 81 mutants of this code in less than a second. We then run the harness with unwinding bound 2 (and `SIZE=1`) on each of the 81 mutants. The process takes less than a minute and a half (on a Macbook Pro with dual-core 3.1GHz Intel Core i7, using only one core). CBMC reports that 6 mutants do not compile (these remove variable declarations, for the most part), 4 are detected by the harness (a counterexample is produced: we say the mutant is *killed*), and 71 mutants pass without detection (the verification is successful, in which case we say the mutant *survives*). Clearly length 1 arrays are not sufficient to detect even the most glaring bugs in a sort algorithm (no surprise; all size 1 arrays are sorted). What about our choice of size 2? Re-running the mutants (dropping those already killed by the smaller bound) takes slightly over 13 minutes (due to one mutant requiring over 8 minutes to model check) and reduces the number of surviving mutants to 26. We could inspect these mutants by hand, but it seems highly unlikely that a *complete verification* over all possible arrays with a good specification for sorting would produce such a poor mutation kill rate. If we up the size limit to 3 (the verification taking just over 33 minutes), only 8 mutants survive. Note that each problem, due to the harness' assignment of s to any size smaller than the current size, include all smaller problem sizes. This makes the behavior of the verification problem size setting match that of CBMC where an unwinding bound is a maximum, rather than a fixed size. We assume in all cases below[4] that harnesses are constructed so that increasing problem size includes all smaller problems as a best practice.

At this point, we can increase `SIZE` to 4 (which will kill one additional mutant), but the time required to check the remaining mutants is growing rapidly. In fact, completing the check for size 4, even though only the original program and 8 mutants have to be checked, requires *nearly 9 hours*. When the model checking difficulty grows more slowly with problem size, we propose increasing size until the number of mutants killed does not increase with a step up in size (we call such a size *mutant-stable*). However, in many cases, such as this one, the time required to check mutants starts growing unacceptably. We propose a more efficient algorithm for finding a mutant-stable size below (Figure 7), and mutations can be checked in parallel, but the fundamental problem for size 4 (and above) is that some individual mutants require hours to produce a `VERIFICATION SUCCESSFUL` result. What is a developer to do?

B. Examining Surviving Mutants

The developer should turn to the surviving mutants. The 8 surviving mutants for size 3 are shown in Figure 3.

The comment indicates the type of mutant, and the line # in the quick sort file is also given for reference. The relevant lines are marked in Figure 2. Some of these mutants are easily seen to be equivalent to the original code. For example, the two `rep_const` mutations simply change a `while(1)` into an equivalent infinite loop with a different constant non-zero value. These two mutants could in fact have been automatically removed from the set, like uncompilable mutants, by checking

```
 9 :  /*(rep_op)*/ if (l <= r)
26 :  /*(rep_const)*/ while(-1)
26 :  /*(rep_const)*/ while( ((1)+1))
28 :  /*(rep_op)*/ do ++i; while(i<r && a[i]<=pivot);
28 :  /*(rep_op)*/ do ++i; while(i!=r && a[i]<=pivot);
28 :  /*(rep_op)*/ do ++i; while(i<=r && a[i]<pivot);
30 :  /*(rep_op)*/ if( i > j ) break;
31 :  /*(del_stmt)*/ t=a[i]; /*  a[i]=a[j]; */  a[j]=t;
```

Fig. 3. Surviving mutants at `SIZE=3`.

```
LOG: ref[0] = 2147414872
LOG: ref[1] = 2147480408
LOG: ref[2] = -1073743560
LOG: called with l=0, r=2
LOG: called with l=0, r=-1
LOG: called with l=1, r=2
LOG: called with l=1, r=1
LOG: called with l=3, r=2
LOG: a[0] = 2147414872
LOG: a[1] = 2147480408
LOG: a[2] = 2147480408
```

Fig. 4. Witness to the harness' inability to kill the `del_stmt` mutant.

their compiled code for equivalence with the original program [16]. We suggest always pruning mutants via Trivial Compiler Equivalence (TCE) before working with mutants. The remaining 6 mutants produce different binaries when compiled with an optimizing compiler, so require manual analysis. The 5 `rep_op` mutations all alter comparison operators by changing their truth value on one corner case (when two values are equal), and we may suspect that quick sort is robust to, for example, changing `i <= r` to `i != r` since `i` is initially set to `l`, which we know to be less than `r`.

The `del_stmt` mutant, however, is clearly problematic. How can quick sort be correct if the inner loop's swapping of `a[i]` and `a[j]` is changed to instead copy `a[i]` to `a[j]`? The consequences of this mutant are clearly drastic, but why are they not detected by our harness? We find out by asking CBMC to produce an execution such that 1) the mutated code is covered 2) other coverage is maximized (to avoid degenerate executions, e.g., over size 1 arrays) and 3) the execution is *not a counterexample*. We have modified CBMC, and written instrumentation tools that produce a modified mutant and harness, allowing us to pose such queries. Running CBMC in this new mode, with the target of maximum branch coverage and statement coverage of the `del_stmt` mutant (or, rather, the statement after it in the CFG, since it no longer exists), we produce the "counterexample" in Figure 4 in less than a minute[5]. Our harness checks that the array `a` is *sorted* after the call to `sort`, but it does not check that it is a permutation of the original array!

We might have discovered this problem by a different method: if we remove the call to `sort` in the harness, and replace it by a loop assigning `nondet_int` to every element in array `a` (a kind of most-general any-order type-correct "mutant" of `sort`), we can run the modified CBMC and see examples of executions our harness allows, which should include *any sorted* array. The problem with this method is that, while it sometimes works, CBMC is also free to set all elements in all arrays to 0, and to generally provide

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[4]There is one noted exception in Section IV-D.

[5]We show the output of the print statements, not the full CBMC trace, in the interest of space and ease of understanding; examining this output alone, initially, is the most likely course of action for a developer as well.

```
...
   int i, v, count, qcount, prev;
...
   sort(a, s);
   // Pick a value to check
   v = nondet_int();
   count = 0;
   qcount = 0;
...
   for (i = 0; i < s; i++) {
...
      if (ref[i] == v)
        count++;
      if (a[i] == v)
        qcount++;
...
   assert (count == qcount);
}
```

Fig. 5.   Modifying the harness to ensure `a` is a permutation of `ref`.

an uninformative example of a successful execution. The requirement to cover a mutant (and as much other code as possible) helps guide CBMC to a successful execution that is *likely to be incorrect*, because a non-equivalent mutant *changes the original program's behavior*. Moreover, while the problem with the harness in this case is simple, understanding arbitrary "passing" but wrong executions can be very difficult without the ability to think about *a specific bug* the model checker is missing. Moreover, basing the production of witnesses on mutants allows us to compare harnesses even over killed mutants: if one harness reduces the set of passing executions for a mutant, it is arguably a better verification of correctness than one allowing more executions of the mutant, even if both produce a counterexample killing the mutant. Unlike traditional mutation analysis, we can take the question "how killed is this mutant" seriously because we aim at exhaustive testing. A harness is most effective with respect to a mutant if it allows *no* executions covering the mutant to pass, or, to be precise, only those such that the relevant behavior is still identical to the correct program.

The witness tells us that the sorting harness needs to be modified. It is too *weak*. We say that harness is weak if it fails to detect incorrect executions. One harness is stronger than another if it detects more failures; we can indirectly estimate strength by determining how many mutants a harness can kill at a given problem size, and how executions covering killed mutants can still satisfy the harness. Figure 5 shows how to modify the sorting harness to check this additional critical property[6]. Because CBMC is exhaustive, instead of performing a complete check for permutation, we can detect violation of the property by letting `v` be any value, and ensuring that both `a` and `ref` contain the same *number* of elements equal to `v`. If `a` is *not* a permutation of `ref`, ∃v such that this is not true, and we can rely on CBMC to report it as part of a counterexample. This is a useful reminder that while a CBMC harness resembles a C program to test the SUT, it can make use of approaches to testing that are not viable in a non-exhaustive context.

If we modify the harness as above, we can re-check our mutants. In practice, we would remove the two we determined are equivalent based on compiler output, but for the sake of

comparison we retain all mutants. With the revised harness, checking mutants at `SIZE=1` takes slightly longer (8 more seconds) and kills the same number of mutants, since the problem is the size, not the harness. At `SIZE=2` mutant kill results are again unchanged, but the run now only requires slightly over 5 minutes. Finally, at `SIZE=3`, we kill the `del_stmt` mutant that previously survived, and the process requires only 14 minutes, only slightly more than at `SIZE` 2 with the weaker harness. The `SIZE` 3 verification is stable. Checking this by running all surviving mutants at `SIZE` 4 now only requires slightly more than an hour, nearly an order of magnitude faster than with the weaker harness. A stronger harness not only finds more bugs in the SUT, it often *speeds up verification* by making the set of satisfying instances larger for the SAT solver. Unsatisfiability is sometimes easier to prove when the possible executions to consider are reduced.

As briefly mentioned in the introduction, it is also possible to understand a mutant by modifying the harness to call both the mutated code and the original code on the same inputs, and search for witnesses where 1) the execution is passing but 2) the return value(s) for the mutated code differ(s) from the original code. However, this increases the complexity of the model checking problem (checking equivalence of two functions is often much harder than a harness specification of valid runs) and does not easily apply to any verifications other than simple function calls on given data. For example, forcing the same interleavings in threaded programs, or detecting all differences in state-modification for more reactive code is often infeasible or requires significant human intervention. While we do apply differential checks in some cases below, and describe an algorithm for the approach, we do not propose this as a core technique suitable for general-purpose falsification-driven verification.

C. Mutating the Harness

Previous efforts to understand model checking results have also considered mutants to the property, usually given as a temporal logic formula [17]. Once a developer is satisfied with a harness, has a mutant-stable bound for verification, and is convinced all surviving mutants are semantically equivalent to the original program (or, if not equivalent, also satisfy the same correct specification), we propose the developer *mutate the test harness itself*. The idea is to check that 1) most mutants of the harness reject the SUT and 2) the remaining mutants have a mutant kill rate no greater than that of the harness. For our fixed harness, there are 61 mutants, of which 2 do not compile. Of these, 40 produce an incorrect counterexample for the original, correct, quick sort. An additional 10 have mutant kill rates worse than the original harness (from as low as 5% of mutants killed to only a few percent worse than the fixed harness). The remaining 9 mutants have the same ability to kill mutants as the original harness. Most of these involve modifying a relational operator in a loop or an assumption in a way that does not change the verification problem. The only interesting surviving harness mutant is one that removes the assignment of a fresh non-deterministic value to `v` after the call to `sort`. This means the check for permutation difference will always be performed on the last element of `ref`. On reflection, it seems plausible that this is sufficient to produce a counterexample for all the quick sort mutants, but it is

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[6]In fact, if we choose a `val` to check before we assign to `ref` and count that value's appearances, we can completely dispense with storing `ref` at all. In practice, this is more difficult for non-CBMC experts to understand, and makes model checking only slightly faster.

clearly not an improvement to the harness, in terms of either verification strength or clarity.

In addition to showing the current harness is at least a "local minima" with respect to mutants, mutation analysis of the harness also provides some evidence of our technique's ability to detect subtle specification and environment flaws. In particular, it shows the value of *inspecting all surviving mutants*. One mutant modifies the assumption on s to be < SIZE rather than <= SIZE, which is the same as lowering the SIZE by one; this is a fairly easy mistake to make in a harness, and likely not much more uncommon than off-by-one bugs in actual code. This reduces the effectiveness of the verification by 19 mutants, so is likely not to escape notice, and would also (in our framework) simply result in a higher size being chosen as mutant-stable.

Another harness mutant, however, only kills 4 fewer mutants than the original harness: deleting the assignment prev = a[i]. Traditional coverage and some variants of model checking coverage cannot detect this problem: because of the assignment to prev outside the loop, the variable is used in the specification, and in fact used to detect many faults (it eliminates any mutants that can cause a[0] to not be the least element in the array). The specification "covers" the behavior of quick sort in general, since the permutation requirement remains in place. However, it cannot detect versions of quick sort that 1) preserve permutation and 2) make the first element always the minimum, but 3) don't always sort the array correctly. In particular, the call to quickSort with j + 1 can be removed or modified to j + 2 without detection. Some loop bound mutants the "true" harness kills also survive, but examining the deleted/removed recursive calls would inform the user of the problem in this case. Our modified CBMC can, as for our first harness bug, produce a witness showing a permutated array with correct a[0] ordering but with out-of-order later elements.

III. Algorithms and Techniques

Falsification-driven verificaiton is a semi-automated approach that relies heavily on algorithmic and tool support. While the typically smaller scope of code targeted for verification (vs. testing) makes the work easier, it is not likely to be feasible without automation of many subtasks. Existing tools make producing a set of mutants and checking them using a harness relatively easy, but other tasks require new algorithms and tools. Figure 6 shows the basic flow, which is directed not by a fixed algorithm but by the intelligence and experience [18] of the developer. Novel tools or techniques are on the left side of the diagram (mutation analysis itself is not novel, but our tool for integrating this with the model checker and recording results for use by other parts of the toolchain is non-standard), other than the model checker itself. We show only top-level functionality that a developer uses, not supporting algorithms for instrumentation of SUT and harness (mutants).

One important need is a version of CBMC capable of applying built-in assertions checks (e.g, bounds checks, pointer dereference, divsion by zero) as *assumptions* rather than assertions. Converting assertions to assumptions is the means by which we produce *passing* executions showing how a harness can fail to detect a mutant. For developer-provided
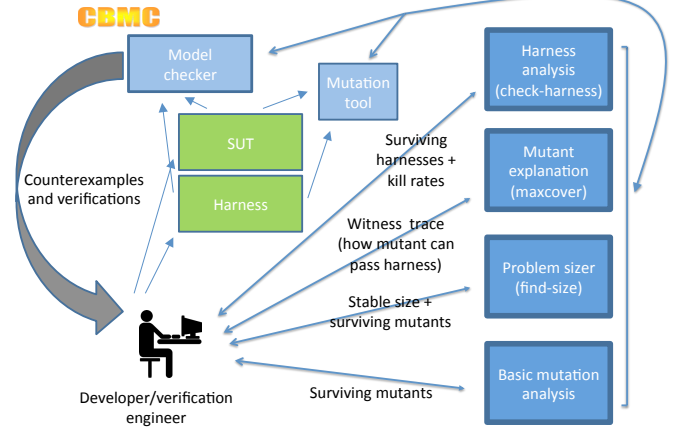

Fig. 6.   Basic flow of falsification-driven verification.

```
(int, survivors) find-size(H, M, S_0: int,
                          O: int → options,
                          U: int → int)

S = S_0-1
r' = {}
changed = False
while changed:
   TOP:
   S = S + 1
   changed = False
   r = r'
   r' = {}
   for m ∈ M:
      if m ∉ r:
         r[m] = check(H,m,U(S),O(S))
         if r[m] == VERIFICATION FAILED:
            //once killed, assume always killed
            M = M \ m
      if r[m] == VERIFICATION SUCCESSFUL:
         r'[m] = check(H,m,U(S+1),O(S+1))
         if r'[m] == VERIFICATION FAILED:
            M = M \ m
            changed = True
            goto TOP
// No result changed, so S is mutant-stable
return (S-1, M)
```

Fig. 7.   Algorithm 1: Finding size/unwinding bound and surviving mutants.

harnesses, this can be performed by automatic source-to-source transformation of the harness, but CBMC's internal constraints have to be handled inside the model checker. We implemented a new CBMC command-line option, --find-success that transforms all CBMC-generated assertions into assumptions. In all algorithms in this paper, check means applying standard model checking, with any needed automatic property checks, while scheck indicates applying our modified CBMC with find-success enabled. In Figure 6 we assume the use of a modified version of CBMC.

Figure 7-11 show core algorithms (implemented as prototype tools in our framework). Two of these are support algorithms for instrumentation, used in the production of witnesses for understanding surviving mutants (Figures 8 and 9), while the other three (Figures 7, 10, and 11) face developers. The uses of these algorithms are described at a high level in the introductory example (in Sections II-A, II-B, and II-C, respectively), and in the case studies below.

```
harness covering(H, TARGET)

H' = H
for stmt ∈ H':
    if stmt == assert(P):
        stmt = assume(P);
cover = [
  assume(total_coverage >= TARGET);
  assert(!mutant_covered);
]
insert cover at end of H'.main()
return H'
```

Fig. 8. Algorithm 2: Convert harness into maximal coverage search.

```
mutant coverinst(m)

n = 0
m' = m
for if_stmt c in m':
    if c has no else:
        add [else {}] to c
for basic_block b in m':
    b = [if !covered[n] {
            covered[n] = 1;
            total_covered += 1;
         }
         b]
    n = n + 1
for stmt s in m':
    if MUTANT(s):
        s = [{mutant_covered = 1;
              s}]
m' = [int total_covered = 0;
      int mutant_covered = 0;
      int covered[n];
      m']
return m'
```

Fig. 9. Algorithm 3: Instrument mutant for coverage.

IV. EXPERIMENTAL RESULTS AND CASE STUDIES

A. Algorithm Implementations

1) Binary Search:

2) Doubly-linked List Insertion Sort:

3) AVL Tree:

4) Dijkstra's Algorithm:

5) Merge With Duplicate Removal:

```
trace maxcover(m, H, S)

m' = coverinst(m)
T = 0
trace = ∅
failed = False
while (not failed)
    H' = covering(H, T)
    r = scheck(H, m', U(S), O(S))
    if r == VERIFICATION SUCCESSFUL:
        // Can't achieve this level of coverage
        failed = True
    else if r == VERIFICATION FAILED:
        trace = r.trace
        // Instead of incrementing by 1, see
        // what actual coverage was and
        // increase by one from that baseline.
        T = trace.read(total_covered) + 1
return trace
```

Fig. 10. Algorithm 4: Find a maximally covering execution trace that covers a mutant.

```
report check-harness(SUT, M, H, M(H), S)

K_H = killed(M, H, S)
Hkills = ∅
Hequal = ∅
Hbetter = ∅
for H_i in M(H):
    original = check(H_i, SUT, U(S), O(S))
    // ``kills'' the SUT -- an invalid harness
    if original == VERIFICATION FAILED:
        Hkills += H_i
    else: // check if this kills fewer mutants
        K_{H_i} = killed(M, H, S)
        if |K_{H_i}| > |K_H|:
            Hbetter += H_i
        if |K_{H_i}| == |K_H|:
            Hequal += H_i
        else:
            Hkills += H_i
return (Hkills, Hequal, Hbetter)
```

Fig. 11. Algorithm 5: Analyze a harness.

```
jsint
js_BoyerMooreHorspool(const jschar *text, jsint textlen,
                      const jschar *pat, jsint patlen,
                      jsint start)
{
    jsint i, j, k, m;
    uint8 skip[BMH_CHARSET_SIZE];
    jschar c;
    JS_ASSERT(0 < patlen && patlen <= BMH_PATLEN_MAX);
    for (i = 0; i < BMH_CHARSET_SIZE; i++)
        skip[i] = (uint8)patlen;
    m = patlen - 1;
    for (i = 0; i < m; i++) {
        c = pat[i];
        if (c >= BMH_CHARSET_SIZE)
            return BMH_BAD_PATTERN;
        skip[c] = (uint8)(m - i);
    }
    for (k = start + m;
         k < textlen;
         k += ((c = text[k]) >= BMH_CHARSET_SIZE) ?
             patlen : skip[c]) {
        for (i = k, j = m; ; i--, j--) {
            if (j < 0)
                return i + 1;
            if (text[i] != pat[j])
                break;
        }
    }
    return -1;
}
```

Fig. 12. SpiderMonkey 1.6 Boyer-Moore-Horspool code.

B. SpiderMonkey Boyer-Moore-Horspool Implementation

Figures 12 and 13 show, respectively, the source code and an initial harness for verification of the Boyer-Moore-Horspool substring finding algorithm [19], [20] in version 1.6 of Mozilla's SpiderMonkey JavaScript engine.

Verifying this code presents one immediate issue that is not unusual in verification: how to handle an assert in the code being verified. An assert at the end of a function or in the main body is typically an additional part of the specification, and is often best left unchanged. An assert at the beginning of a function's body, however, is typically a preconditon for the code, indicating that when it is violated the code will not behave correctly. It is natural to consider changing such an assertion into an assume and ignoring any problems produced by calling the code with non-conforming inputs.

While this can be a useful technique, for instance when it is hard to write a harness that only produces valid inputs, but

```c
#include "bmh.h"
int nondet_int();
unsigned int nondet_uint();
int main() {
  int i;
  unsigned int v;
  char itext[TSIZE];
  char ipat[PSIZE];
  unsigned int itext_s = nondet_uint();
  __CPROVER_assume(itext_s < TSIZE);
  unsigned int ipat_s = nondet_uint();
  __CPROVER_assume(ipat_s < PSIZE);
  printf ("LOG: size text=%u, pat=%u\n", itext_s, ipat_s);
  for (i = 0; i < itext_s; i++) {
    v = nondet_unit();
    __CPROVER_assume((long)v < (long)BMH_CHARSET_SIZE);
    itext[i] = v;
    printf ("LOG: text[%d] = %u\n", i, itext[i]);
    __CPROVER_assume(itext[i] < BMH_CHARSET_SIZE);
  }
  for (i = 0; i < ipat_s; i++) {
    v = nondet_uint();
    __CPROVER_assume((long)v < (long)BMH_CHARSET_SIZE);
    ipat[i] = v;
    printf ("LOG: pat[%d] = %u\n", i, ipat[i]);
    __CPROVER_assume(ipat[i] < BMH_CHARSET_SIZE);
  }
  jsint r = js_BoyerMooreHorspool(itext, itext_s,
               ipat, ipat_s, 0);
  printf ("LOG: return = %d\n", r);
  int pos, ppos, found;
  v = nondet_uint();
  printf ("LOG: looking at %u\n", v);
  __CPROVER_assume(v >= 0);
  if (r == -1) {
    __CPROVER_assume(v < itext_s);
    pos = v; ppos = 0; found = 1;
    while (ppos < ipat_s) {
      printf ("LOG: itext[%d] = %u, ipat[%d] = %u\n",
              pos, itext[pos], ppos, ipat[ppos]);
      if ((pos>=itext_s)||(itext[pos]!=ipat[ppos])) {
        found = 0;
        break;
      }
      pos++; ppos++;
    }
    assert (!found);
  } else {
    pos = r; ppos = 0;
    while (ppos < ipat_s) {
      assert (itext[pos] == ipat[ppos]);
      pos++; ppos++;
    }
    v = nondet_uint();
    printf ("LOG: looking at %u\n", v);
    __CPROVER_assume(v < r);
    pos = v; ppos = 0; found = 1;
    while (ppos < ipat_s) {
      printf ("LOG: itext[%d] = %u, ipat[%d] = %u\n",
              pos, itext[pos], ppos, ipat[ppos]);
      if ((pos>=itext_s)||(itext[pos]!=ipat[ppos])) {
        found = 0;
        break;
      }
      pos++; ppos++;
    }
    assert (!found);
  }
}
```

Fig. 13. Boyer-Moore-Horspool harness.

easy to filter out the invalid inputs and only verify behavior for those, it is also a dangerous technique. Mutation analysis of the harness shows that 4 is a mutant-stable size (where the same size is used for text length, pattern length, and character set size), with a kill rate of 72.3%. On initial examination, the 20 surviving mutants do not seem problematic. A large number involve the `JS_ASSERT` converted to a `__CPROVER_assume`, showing the harness cannot tell if the assumption is incorrect, which is not surprising (the harness only generates good inputs, and some of the mutants simply discard too many inputs).

At this point, we were satisfied with our harness, and ran a check on mutants of the harness itself. To our surprise, three mutants of the harness had a *better* kill rate than the "correct" harness, killing 73.5% of mutants. Investigating these "better" harnessess showed mutants that broke processing of some return values in such a way that, while these harnessess failed to detect certain major bugs in the code, they were able to detect some assumption mutants. Guided by this, we produced a revised harness that raised the kill rate to 79.52%. However, on examining the surviving mutants, we realized that our verification was unsatisfactory as a good regression for the Boyer-Moore-Horspool code: in particular, if the assertion were ever modified to allow bad inputs to pass through, or otherwise incorrectly changed, we would miss some mutants. We then changed the `JS_ASSERT` into code that returned a special value to signal assertion failure, and modified the harness once more, allowing some incorrect values to pass through and checking that assertion failure happened if, and only if, the inputs were invalid. This harness killed 89.2% of mutants, and the six surviving mutants were easily understood to be equivalent to the BMH code under all valid inputs (in one case we weren't certain about, we had CBMC verify that for all non-assertion violating inputs, this was true up to a large bound). The new harness, informed by the harness mutations, in fact had a better mutant kill rate for size 3 (80.7%) than our first harness had at the mutant-stable point. It was this example that convinced us that harness mutation was an important technique, not only for evaluating our methodology, but in its own right.

C. Linux Kernel RCU Verification Challenges

1) Introduction to RCU: RCU is a synchronization mechanism that is sometimes used as a replacement for reader-writer locking for linked structures, but with extremely lightweight readers [21]. In the limiting case, which is achieved in server-class builds of the Linux kernel, the overhead of entering and exiting an *RCU read-side critical section* (using rcu_read_lock () and rcu_read_unlock (), respectively) is exactly zero [22]. Thus, RCU can achieve excellent performance, scalability, real-time response, and energy efficiency for read-mostly workloads [21], [23], [24].

However, lightweight readers imply that updaters cannot exclude readers, which means that updaters must take care to avoid disrupting readers. Updaters typically maintain multiple versions of the portion of the data structure being updated, removing old versions only when readers are no longer accessing them. To this end, RCU provides synchronize_rcu (), which waits for a *grace period*, that is, for all pre-existing RCU readers to complete. RCU updaters typically removes a data element (thus rendering it inaccessible

```
1  static int rcu_read_nesting_global;
2
3  static void rcu_read_lock(void)
4  {
5    (void)__sync_fetch_and_add(&rcu_read_nesting_global, 2);
6  }
7
8  static void rcu_read_unlock(void)
9  {
10    (void)__sync_fetch_and_add(&rcu_read_nesting_global, -2);
11 }
12
13 static void synchronize_rcu(void)
14 {
15   for (;;) {
16     if (__sync_fetch_and_xor(&rcu_read_nesting_global, 1) < 2)
17       return;
18     SET_NOASSERT();
19     return;
20   }
21 }
```

Fig. 14.   Approximate Model of RCU

Fig. 15.   Average times for killed mutants (SAT) vs. unkilled mutants (UNSAT) for all experiments.

to new readers), invokes synchronize_rcu (), and then reclaims the newly removed element. Production-quality implementations of synchronize_rcu () use batching techniques to achieve extremely scalability, so that a single underlying grace period can satisfy more than 1,000 concurrent updates in the Linux kernel [25].

The Linux kernel contains more than 10,000 uses of the RCU API [26], and a userspace RCU library [27], [28] is seeing significant use in userspace applications. That said, it is clear that validation and verification is a first-class concern for RCU implementations.

Because both RCU and the Linux kernel are moving targets, any validation and verification must be both automated and repeatable, that is, must be something that could be included in a regression-test suite. The rcutorture stress-test suite qualifies, but it would be good to evaluate its effectiveness on the one hand (via permutation analysis) and to include formal verification on the other. However, regression-test use of formal verification required that the following properties: (1) Direct use of source code, (2) Automatic discarding of irrelevant source statements, (3) Reasonable memory and CPU overhead, (4) Automatic mapping of error reports to source lines, and (5) Modest additional input beyond the source code. Failing to provide any of these requirements provides unacceptable vulnerability to human error. This raise the question of whether readily available formal-verification tools meet these requirements.

We use a pair of RCU-related benchmarks to provide the beginnings of an answer to this question. The first benchmark applies formal verification to the simplest of the Linux kernel's RCU implementations, Tiny RCU [29], which targets single-CPU systems. This model includes Tiny RCU's handling of idle CPUs as well as the its (trivial) grace-period detection scheme. The second benchmark creates the trivial model approximating an RCU implementation for multiprocessor systems shown in Figure 14. In this model, the number of RCU read-side critical sections currently in flight is tracked by the global variable rcu_read_nesting_global , which is atomically incremented by rcu_read_lock () and atomically decremented by rcu_read_unlock (). This allows synchronize_rcu () to atomically XOR rcu_read_nesting_global 's bottom bit to
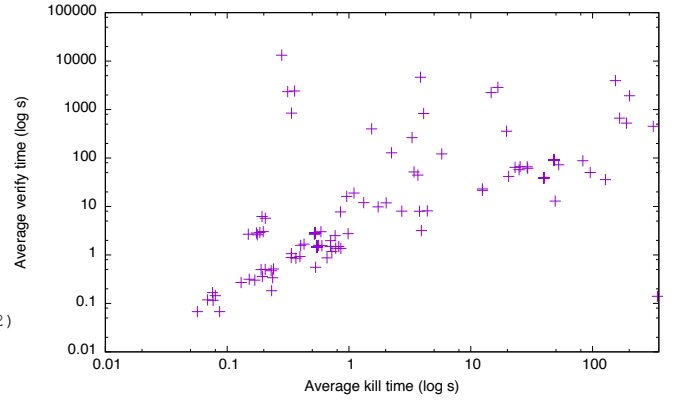
detect whether the current execution has waited for all pre-existing readers (over-approximated by checking the absence of all readers), with SET_NOASSERT() being invoked to suppress all future assertions. Although this model has a number of shortcomings, perhaps most prominently excessive read-side ordering, it is capable of detecting common RCU-usage bugs, including failure to wait for an RCU grace period, failure to enclose read-side references in an RCU read-side critical section.

Nevertheless, it is important to note that these two benchmarks are not examples of useful verifications, but rather the simplest examples of a class that might some day expand to include useful verifications.

(And of course, CBMC proved capable of handling simple variable-pair litmus tests, as well as single-threaded examples involving linked lists, but proved incapable of handling multi-threaded tests involving linked lists. The maintainers of CBMC are working to improve its handling of pointers.)

D. Plausible Verification by Failure to Falsify

A key problem in model checking is the state explosion problem, or, more simply (and more accurately, in that number of states is not always the determining factor in symbolic methods) the problem of scalability. As discussed above, even proving binary search correct over the full domain of integer inputs is not possible within a reasonable time frame. Even when verification is impossible at the desired problem size falsification can provide limited confidence in the correctness of a program. In particular, we observe from all of our experiments that the average time, for any program and harness pair, to verify the original code and all surviving mutants is *much higher* than the average time to produce a counterexample for killed mutants. Showing that a constraint is satisfiable is, usually, easier than proving it is unsatisfiable. This is not limited to SAT solvers; we used SAT rather than SMT in our experiments because we generally found Z3 to be slower than CBMC's built in version of MiniSAT[30] in almost all cases, but Z3 also aims to be fast at producing satisfying assignments, not proving UNSAT [31], and our few runs with Z3 showed the same pattern.

Figure 15 shows (with log scales on both axes) the average running times for all experiments (including faulty versions

of the harness, incorrect runtime parameters, etc.) performed in the course of this work. The general trend is clear: time to verify is usually worse than time to kill, and the worst average time to kill (about 350 seconds) is much better than many average verification times. One use of this relationship is that, in cases where all (non-equivalent) mutants of the SUT are killed, but the SUT verification fails to complete, the SUT might be considered *provisionally* verified. In particular, the larger the ratio between the timeout for failed verification and the longest kill time for any mutant, the "more likely" to be correct we can consider the SUT (the same holds with respect to memory use limits). This belief can be further justified by modifying the harness to force mutant kills to use large problem sizes, violating the usual inclusiveness rule (that way, if the new size allows a counterexample not previously existing, the mutant killing problem for mutants killable at smaller sizes better approximates the counterexample construction problem for the actual fault).

V. Discussion

A. Falsification vs. Verification

The core idea of this paper is that, while successful verification is the *result* that a developer seeks when verifying a program, it is most meaningful in a context provided by many failed verifications. The useful model checking harness (e.g., specification) essentially, is one that *prohibits* certain execution sequences. This is not controversial; a good property is defined by its rejection of bad behavior. However, in most verification efforts, there is a focus on arriving at a successful verification, which sheds very little light on what, exactly is verified. By focusing on mutants throughout the verification process, our approach shifts the emphasis to one of "verifying" the verification itself by repeatedly *falsifying* claims that various incorrect programs satisfy the property. This is, at a conceptual level, akin to Karl Popper's philosophy of science [12].

For Popper, all scientific knowledge is provisional, and the key to the scientific approach is a critical effort, based on *prohibitive* theories. In brief, Popper proposes that proper science must be strongly grounded in a search for counterexamples. Using mutants as a basis for verification is akin to this approach, with the harness taken to be the "theory" of the empirical behavior of the world. Mutants, in this view, are counterfactual worlds that are likely to violate any correct theory of the actual world. A "scientific theory" (that is, a harness) is proven effective by its ability to be *shown to be false* in these counterfactual worlds. If we can prove a theory is incorrect for an "incorrect" world and cannot prove it is incorrect for the real world, that gives us greater confidence (always provisional, since our understanding of the world, e.g., any complex software system, is almost always limited and prone to error) that the theory is indeed true of the real world/program. Of course, generating alternative worlds and showing that, for example, special relativity is easily falsified in a world where special relativity does not in fact hold, is not practical in scientific discovery. It is, however, quite easy in the artificial "scientific discovery" sense of verifying properties of computer programs.

B. The Power of Exhaustive Nondeterminism

The ability to improve a harness based on surviving mutants (or on passing runs of killed mutants) essentially relies on the nature of exhaustive bounded model checking based on constraint solving. In non-exhaustive automated testing, the answer to why a mutant is not killed is, often, neither "the oracle is not good enough" or even "the test process is inadequate and needs to be modified" but "you didn't get lucky." That is, killing all mutants is, in many cases, not something we can expect of non-exhaustive test suites. Random testing [32], [33] can perform well in general as a bug-finding method, but its failure to kill any individual mutant is likely to be a matter of probability, rather than a flaw *per se* in the testing itself. In verification, however, there are no accidents: if a harness verifies an incorrect program, either the assumptions, the specification, or the problem size are *necessarily* in need of correction. However, the approach we propose is most suited to the analysis space in which CBMC is situated: on the one hand, within a known bound, its results are exhaustive; on the other hand, the method behaves much like a dynamic analysis, in that there are no false positives, and counterexamples and success witnesses are concrete, never-spurious, test cases.

VI. Related Work

The idea that a "successful verification" in model checking (or even theorem proving) often simply indicates an inadequate property is long-standing [9], [8]. Use of mutants [17], [34] to provide a coverage measure dates back both to these early explorations and relatively recent work [35]. However, in these efforts the mutation was usually applied to hardware models, and (critically) the surviving mutants were used to identify "uncovered" portions of a model, rather than presented to a developer for examination and understanding directly. To our knowledge, no previous work presented passing executions of a source code mutant as a guide to understanding specification weakness. Our modification of the harness is a source-code analogue to attempts to modify logical formulas, e.g., the effort to (in a narrow, vacuity-based sense) produce the *strongest passing LTL formula* of Chockler et al. [36]. We are not the first to note that model checking, at present, due to the "many obstacles" in proving a system correct, is primarily used for falsification [37]. Previous work on the topic [37] focused on abstractions based on under-approximation, to ensure counterexamples were not spurious. We instead preserve the goal of verification[7], but *drive* the verification process, from the human point of view, by repeated falsification of incorrect systems.

More distantly related is the general effort to determine the quality not only of test suites (which is often focused on missing tests within the "range" of testing, not a problem for CBMC) but of test oracles and entire testing infrastructures. The problem of "testing the tester" [6] is fundamental to all efforts to improve software quality. Recent efforts of most interest have focused on measuring *checked coverage* [38], [39], [40], where a metric tries to make sure the code under test potentially changes the value of an assert, using dynamic slicing [41], [42]. This is weaker than requiring the oracle kill

---

[7]Note that we use a model checking approach that already guarantees non-spurious counterexamples, and provides bounded rather than full verification.

a mutant, our goal, but more manageable for testing, where complete behavioral coverage is less feasible than in model checking (and where source code sizes combined with test inadequacy may make hand mutation analysis infeasible).

Our idea of examining successful executions to better understand surviving (and even killed) mutants is a peculiar variation of the fault localization and error explanation problem in model checking [43], with the twist being that we are "explaining" an artificial fault that 1) typically does not cause a test failure (for surviving mutants) and 2) has an obviously known location.

VII. Conclusions and Future Work

Our CBMC patch for `find-success` mode, the various verification harnesses, and our experimental results are all available at `https://github.com/agroce/cbmcmutate`.

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