

Office Use Only				

Semester Two 2018 Examination Period SAMPLE PAPER Excults of Information Technology

	Faculty of Ir	nformation		
EXAM CODES:	FIT1008			
TITLE OF PAPER:	Introduction to	o computer scie	ence PAPER 1	
EXAM DURATION:	3 hours writing	g time		
READING TIME:	10 minutes			
☐ Monash Extension ☐ Other (specify) During an exam, you muyour exam. This includes calculator, pencil case, obelow. Items/materials be in your possession. No examination material or noting down content following your exam.	Clayton Off Campus Learning st not have in your poses books, notes, paper, ear writing on any part or on your desk, chair, in als are to be removed to of exam material for po	□ Parkville g ✓ Malaysia ssession any ite electronic devic f your body. A your clothing c from the room ersonal use or or attempting t	Peninsula Sth Africa m/material that te/s, mobile phor ny authorised ite or otherwise on y This includes re to share with any	
AUTHORISED MATERIAL	<u>.s</u>			
OPEN BOOK		☐ YES	✓ NO	
CALCULATORS		□ YES	√ NO	
SPECIFICALLY PERMITTE if yes, items permitted a		□ YES	√ NO	
Candidates n	nust complete this sect	tion if required	to write answer	s within this paper
STUDENT ID:		DE	ESK NUMBER:	

Important Information

When writing python code, ensure you follow conventions for Python 3.x and avoid using any in built python functions with greater than O(1) complexity.

Any MIPS code you write for this exam must satisfy the following requirements:

- use only instructions and commands listed in the MIPS reference sheet (provided at the end of this exam)
- be a faithful translation of equivalent python code (unless told otherwise)

Write down any assumptions you make.

Do not write anything in this table. It is for office use only.

Page	Points	Score
5	6	
7	6	
9	6	
11	4	
13	4	
15	4	
17	6	
19	6	
23	6	
25	6	
27	6	
Total:	60	

Question 1: [3 marks]	
What must be true of a list to perform binary search?	Why?

Question 2: [1 marks]

Why is it not valid to treat n=0 as a best case scenario for some an algorithm's time complexity?

Question 3: [2 marks]

What would happen if a recursive function didn't have a base case? Why?

Question 4: [2 marks]

What are the best and worst case time complexities for:

i Heap sort ii Quick sort

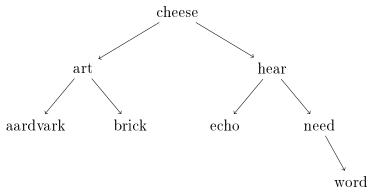
Question 5: [4 marks]

Implement the following faithfully in MIPS:

```
num = 100
while num > 0:
    num = num - 1
print(num)
```

Question 6: [2 marks]

Show what would be printed if we output a **post-order** traversal of the binary search tree below:



In the tree above, 'cheese' is the root with left child 'art' and right child 'hear'. 'art' has a left child of 'aardvark' and a right of 'brick' 'hear' has 'echo' and 'need' as its left and right children.

'need' has a right child of 'word'

Question 7: [2 marks]

Why should function arguments be stored in a stack-frame rather than held in registers (i.e. in MIPS)?

Question 8: [2 marks]

Students of FIT1008 should answer the following:

Why is it that when performing a jal command, \$ra gets set to PC+4?

Question 9: [2 marks]	
Students of FIT1008 should answer the following:	
Provide a useful test case (input state, expected output and reason) for the _method of an arrayList class.	_len

Students of FIT1008 should answer the following:

Question 10: [2 marks]

Consider the idea of a class to represent a line on the x-y plane. Give an example of a useful attribute and method for this class.

Question 11: [4 marks]

Consider the *Mystery* method (of the *linkedList* class) as defined below.

```
def Mystery(self, k):
    self._mystery_aux(self.head)

def _mystery_aux(self, current):
    S = None
    while not current is None:
        S = current.next
        if not S is None and not S.next is None:
            S = S.next
            current.next = S
            current = current.next
```

Given the linked list below:

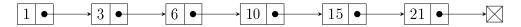


Figure 1: a linked list containing the elements 1,3,6,10,15,21,28,36

Show or describe the effect of *Mystery* on this list. What does *Mystery* do in general?

Question 12: [10 marks]

Consider the sequence represented by the following recurrence relation:

$$A(n) = \begin{cases} A(n-1) + 2 \times A(n-2); & n > 1 \\ 1; & n = 0, 1 \end{cases}$$

(a) (4 marks) Create an iterator (A_iter) capable of generating the first k elements of this sequence

For instance, if we ran the following

We would expect the output of 1,1,3,5,11,21

(b) (6 marks) Define a recursive **dynamic programming** solution to this problem (dp_A) which returns an array A_table where $A_table[i] = A(i)$. What benefits does this have over a **naive** recursive implementation?

If you aren't sure how to apply dynamic programming in code, you should explain the approach with a written description (in enough detail that it can be followed) or as pseudocode

Question 13: [6 marks]

Abby Sträctip is developing an online taxi manager for the hotel chain RoomE. The purpose is to allow hotel guests to request a taxi from their hotel room (with time and destination). In the system Abby wants a structure to hold each guest organised so that they can be easily retrieved in order of departure time (with no particular order for taxis departing at the same time). Guests are then added to the structure when they make a booking and removed from the structure at the time of their taxi's departure.

Abby is planning for at most 3400 guests to be in the system at once (the maximum capacity of the hotel) but it's likely there will be significantly fewer than this in actually (perhaps a few hundred at once).

(a) (3 marks) What kind of ADT is appropriate in this situation? Justify your answer.

(b) (3 marks) Should this ADT be implemented using an array, nodes, or would either be equally appropriate? Justify you answer.

Question 14: [6 marks]

Consider the task of computing the 'width' of nodes in a binary search tree. In this case, the 'width' refers to the difference between the minimum and maximum values in a given subtree. For instance, given a tree as below

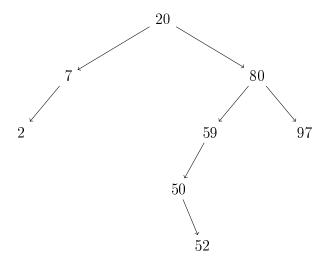


Figure 2: binary search tree containing 20, 7, 80, 2, 59, 97, 50, 52

The root node (20) would have a 'width' of 95 as 2 is the minimum element that is a descendent of 20 and 97 is the maximum element which is a descendent of 20. In the same way, 7 has a width of 5 as the minimum element is 2 but 7 has no larger children so we use the 7 itself. Any nodes with no children have a 'width' of 0 as the minimum and maximum element in their subtree is the node itself.

(a) (4 marks) Prepare a recursive method *findWidth* of the *Binary Search Tree* class which determines the 'width' of each node of the tree and assigns this as a property to that node when computed.

(b) (2 marks) What is the **worst case** complexity of the recursive function you prepared in 14(a)? Justify your answer. If you make any assumptions about the shape of the tree, you should mention these in your answer. **No marks without explanation**

Question 15: [6 marks]

In managing hash tables, we often refer to both

- 1. primary clustering and
- 2. secondary clustering

What are these? Is it possible to reduce the impact of them? Justify your answer.

Students of FIT1008 should answer the following:

Question 16: [6 marks]

Explain in words and/or with the aid of a diagram the best and worst case time complexities of merge sort.

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MIPS reference sheet for FIT1008

Table 1: System calls

Call code	Service	Arguments	Returns	Notes
(\$v0)				
1	Print integer	\$a0 = value to print	-	value is signed
4	Print string	\$a0 = address of string to print	-	string must be termi-
				nated with '\0'
5	Input integer	-	v0 = entered integer	value is signed
8	Input string	\$a0 = address at which the	_	returns if \$a1-1 char-
		string will be stored		acters or Enter typed,
		\$a1 = maximum number of		the string is termi-
		characters in the string		nated with '\0'
9	Allocate memory	\$a0 = number of bytes	v0 = address of first byte	-
10	Exit	-	-	ends simulation

Table 2: General-purpose registers

Table 2. General purpose registers			
Number	Name	Purpose	
R00	\$zero	provides constant zero	
R01	\$at	reserved for assembler	
R02, R03	\$v0, \$v1	system call code, return value	
R04-R07	\$a0\$a3	system call and function arguments	
R08-R15	\$t0\$t7	temporary storage (caller-saved)	
R16-R23	\$s0\$s7	temporary storage (callee-saved)	
R24, R25	\$t8, \$t9	temporary storage (caller-saved)	
R28	\$gp	pointer to global area	
R29	\$sp	stack pointer	
R30	\$fp	frame pointer	
R31	\$ra	return address	

Table 3: Assembler directives

.data	assemble into data segment
.text	assemble into text (code) segment
.word w1[, w2,]	allocate word(s) with initial value(s)
.space n	allocate n bytes uninitialized, unaligned space
.ascii "string"	allocate ASCII string, do not terminate
.asciiz "string"	allocate ASCII string, terminate with ' $\0$ '

Table 4: Function calling convention

On function call:

Caller:	Callee:	
saves temporary registers on stack	saves value of \$ra on stack	
passes arguments on stack	saves value of \$fp on stack	
calls function using jal fn_label	copies \$sp to \$fp	
	allocates local variables on stack	

On function return:

Callee:	Caller:
sets \$v0 to return value	clears arguments off stack
clears local variables off stack	restores temporary registers off stack
restores saved \$fp off stack	uses return value in \$v0
restores saved \$ra off stack	
returns to caller with jr \$ra	

A partial instruction set is provided below. The following conventions apply.

Instruction Format

Rsrc, Rsrc1, Rsrc2: source operand(s), - must be a register value(s) Src2; source operand - may be an immediate value or a register value

Rdest: destination, must be a register

Imm: Immediate value, may be 32 or 16 bits (Imm16: only 16-bit value)

Addr: Address in the form: offset(Rsrc) ie. absolute address = Rsrc + offset

label: label of an instruction

★: pseudoinstruction

Immediate Form -: no immediate form, or this is the immediate form (*: immediate form is a pseduoinstruction)

Unsigned form (append 'u' to instruction name):(-: no unsigned form, or this is the unsigned form)

Table 5: MIPS instruction set

Instruction format	Meaning	Operation	Immediate	Unsigned
add Rdest, Rsrc1, Src2	Add	Rdest = Rsrc1 + Src2	addi	addu (no overflow trap)
sub Rdest, Rsrc1, Src2	Subtract	Rdest = Rsrc1 + Src2 Rdest = Rsrc1 - Src2	_ addi	subu (no overflow trap)
mult Rsrc1, Src2	Multiply	Hi:Lo = Rsrc1 * Src2	_	mulu
div Rsrc1, Src2	Divide	Lo = Rsrc1/Src2;	_	divu
div itsici, bicz	Divide	Hi = Rsrc1 % Src2	_	divu
and Rdest, Rsrc1, Src2	Bitwise AND	Rdest = Rsrc1 & Src2	andi	-
or Rdest, Rsrc1, Src2	Bitwise OR	Rdest = Rsrc1 Src2 $Rdest = Rsrc1 Src2$	ori	
xor Rdest, Rsrc1, Src2	Bitwise XOR	$Rdest = Rsrc1 \land Src2$ $Rdest = Rsrc1 \land Src2$	xori	-
nor Rdest, Rsrc1, Src2	Bitwise NOR	$ Rdest = Rsrc1 \wedge Src2 $ $Rdest = \sim (Rsrc1 \mid Src2)$	XOII	_
not Rsrc1, Src2 *	Bitwise NOT	` ' /	_	_
		$Rdest = \sim (Rsrc1)$	11	-
sllv Rdest, Rsrc1, Src2	Shift Left Logical	Rdest = Rsrc1 << Src2	sll	-
srlv Rdest, Rsrc1, Src2	Shift Right Logical	Rdest = Rsrc1 >> Src2	srl	-
and Data Danel Cost	Chift Diabt Asithasatia	(MSB=0)		
srav Rdest, Rsrc1, Src2	Shift Right Arithmetic	Rdest = Rsrc1 >> Src2	sra	-
D 1 C 2	N	(MSB preserved)		
move Rsrc1, Src2 *	Move	Rdest = Rsrc	-	-
mfhi Rdest	Move from Hi	Rdest = Hi	-	-
mflo Rdest	Move from Lo	Rdest = Lo	-	-
li Rdest, Imm ⋆	Load Immediate	Rdest = Imm	-	-
la Rdest, Addr (or label) \star	Load Address	Rdest = Addr (or label)	-	-
lw Rdest, Addr	Load word	Rdest = mem32[Addr]	-	-
sw Rsrc, Addr	Store word	mem32[Addr] = Rsrc	-	-
beq Rsrc1, Rsrc2, label	Branch if equal	if (Rsrc1 == Rsrc2)	*	-
		PC = label		
bne Rsrc1, Rsrc2, label	Branch if not equal	if $(Rsrc1 != Rsrc2)$	*	-
		PC = label		
blt Rsrc1, Rsrc2, label \star	Branch if less than	if $(Rsrc1 < Rsrc2)$	*	unsigned operands
		PC = label		
ble Rsrc1, Rsrc2, label \star	Branch if less or equal to	if $(Rsrc1 \le Rsrc2)$	*	unsigned operands
		PC = label		
bgt Rsrc1, Rsrc2, label ⋆	Branch if greater than	if $(Rsrc1 > Rsrc2)$	*	unsigned operands
		PC = label		
bge Rsrc1, Rsrc2, label ⋆	Branch if greater or equal to	if $(Rsrc1 \ge Rsrc2)$	*	unsigned operands
		PC = label		
slt Rdest, Rsrc1, Src2	Set if less than	if $(Rsrc1 < Src2)$	slti	sltu
, , ,		Rdest = 1		
		else $Rdest = 0$		
j label	Jump	PC = label	_	_
jal label	Jump and link	\$ra = PC + 4;	_	_
J 2000 01		PC = label		
jr Rsrc	Jump register	PC = Rsrc	_	_
jalr Rsrc	Jump and link register	\$ra = PC + 4;	_	_
Jan 16010	damp and mix register	PC = Rsrc	_	
syscall	system call	depends on call code in		-
Бубсан	System Can	\$v0	_	
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