

Prasiddha Siwakoti

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Education

- 2017–2020 **MSc in Information and Communication Engineering**, *Institute of Engineering, Pulchowk Campus, Tribhuvan University, Lalitpur, Nepal*, (Score: 83.13%)
○ Thesis Title "Classifiable Handwritten Characters Image Super-Resolution Using a Generative Adversarial Network"
- 2012–2016 **BE in Electronics and Communication Engineering**, *Himalaya College of Engineering, Tribhuvan University, Lalitpur, Nepal*, (Score: 74.51%)
○ Thesis Title "Integrated Biometric Access Control System"

Research Experience

- October 2015– April 2016 **Research Intern**, *LogicTronix*, Lalitpur, Nepal
○ Studied FPGA IP development and hardware design.
○ Hands-on experience using Xilinx development environments, such as Vivado and ISE.
○ Worked on projects involving custom IP core development and hardware design

Publications

P. Siwakoti and S. K. Ghimire. Devnagari Handwritten Characters Image Super-Resolution based on Enhanced SRGAN. *Journal of the Institute of Engineering*, 16(1):103–109, 2021.

Teaching Experience

- October 2017–April 2018 **Part-time Faculty**, *Faculty of Management, Nepal Commerce Campus, Tribhuvan University*, Kathmandu, Nepal
○ Delivered computer graphics lectures to undergraduate students on topics such as raster and vector graphics, 2D and 3D transformations, shading and rendering.
○ Conducted labs and provided guidance to students to reinforce concepts learned in lectures.
○ Prepared and graded exams, assignments, and lab reports
- April 2016 -October 2016 **Assistant Instructor**, *LogicTronix Nepal*, Lalitpur, Nepal
○ Assisted in teaching tools: Xilinx Vivado and ISE, Languages: VHDL and Verilog to undergraduate students in a digital design course

Work Experience

2018–Present **Telecom Engineer** , *Nepal Telecom* , Kathmadu, Nepal

- Integrated Maintenance: Maintained and optimized multi-service networks, including GSM, LTE, Fiber and Microwave links, identified and resolved network issues to minimize downtime
- Transport Network: Managed and optimized Multivendor Routers and Switches in both L2 and L3 layer both logically and physically
- Automation: Implemented network and system automation tools in Python using Netmiko, Napalm, and Nornir, to automate network configuration, deployment, and monitoring tasks

October 2016–April 2017 **RF Engineer** , *Subisu Cablenet Ltd.*, Kathmandu, Nepal

- Worked on Docsis technology

Projects and Presentations

- Presentation Presented project at 7th National Conference on Science and Technology, By NAST
- Image Processing Integrated Biometric Access Control System
- ML Generating Digits Using GAN

Skills

- Programming Python, JS, C/C++
- Machine Learning Classification, Regression, Clustering, Deep Learning, GAN
- ML Tools TensorFlow, PyTorch, Scikit-learn, Pandas, NumPy
- Networking Switching: vlans, Routing: ospf, isis, vpls(martini), ibgp, vrf
- Automation Netmiko, Napalm, Nornir
- Languages English (fluent), Hindi (fluent), Nepali(Proficient)

Awards and Scholarships

- 1st Prize Won the First National FPGA Competition
- Scholarship Partial Scholarship offered by Himalaya College of Engineering for B.E
- Scholarship Full Scholarship for studying High School (10+2) By Higher Secondary Education Board

References

Dinesh Chandra Panthiy*Deputy Manager**Nepal Telecom*

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Krishna Gahire*CEO, LogicTronix Nepal**Digitronix Nepal*

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and Computer Engineering**Computer Science & Engineering**Institute of Engineering, Pulchowk Campus*

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