

LAYER-STACK

01-16 01-20

Sym	No	Mils	MM	Qty	Plated
+	1	12	0.30	389	YES
×	2	24	0.60	10	YES
	3	35	0.90	2	NOT
<b>♦</b>	4	40	1.02	2	YES
X	5	52	1.32	36	YES

2015-08-16 21:09:39 f=2.10 E:\git\clay\Circuit\P1R5\P1R5.brd

- PCB: HASL finish Green LPI solder mask
- White silkscreen
  Panelization is at discretion of manufacturer.
  Artwork files for this PCB

- Artwork files for this
  --- Top layer
  --- Second layer
  --- Third layer
  --- Bottom layer
  --- Top soldermask
  --- Bottom soldermask
  --- Top silkscreen

- --- Drill file --- Top solder paste mask
- Board outline is square. Outlines drawn inside the square (top right, bottom left) should be ignored.

- Assembly:
   CBA is to be labeled on bottom side as shown
  --- Line 1: CLAY P1R5
  --- Line 2: Name of manufacturer
  --- Line 3: Serial number of fomat YYWWXXXX
  ----- WW: Week of manufacture
  ----- YY: Year of manufacture
  ----- XXXX: Unique serial number for the week of manufacture

Clay P1R5 2015-08-14 GBH