

GND

CHG_IN

USBPWR

USB_SEL

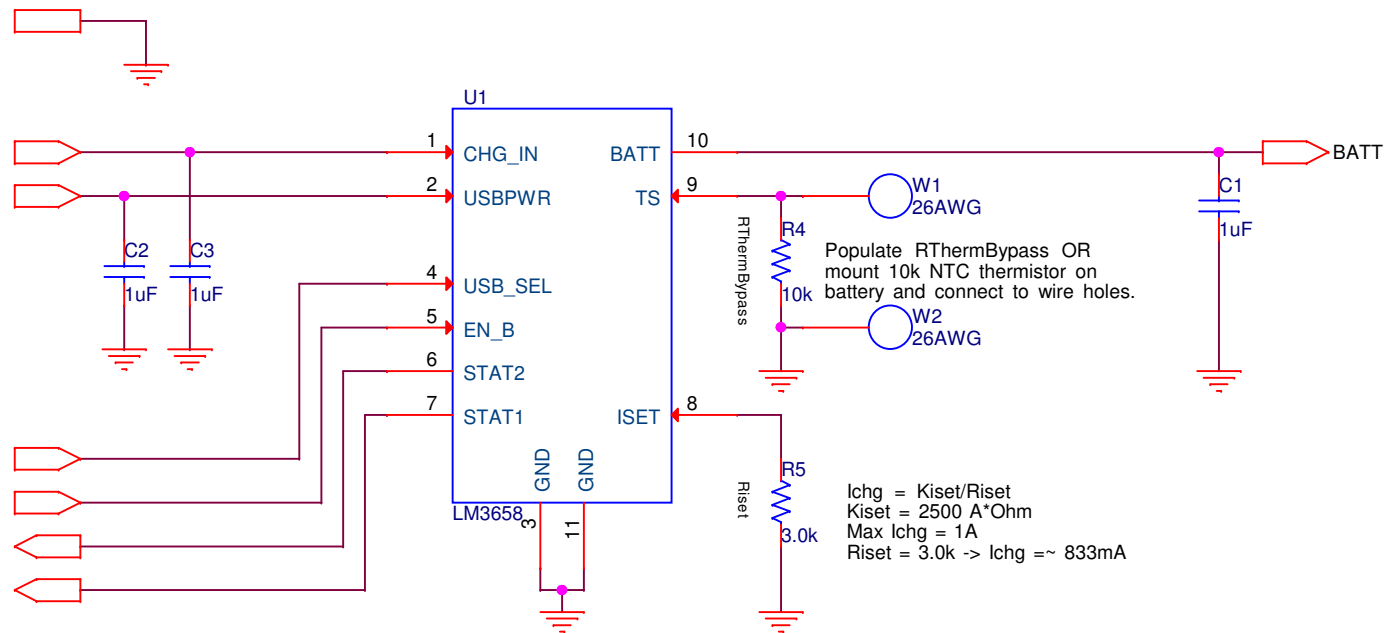
EN_B

STAT2

STAT1

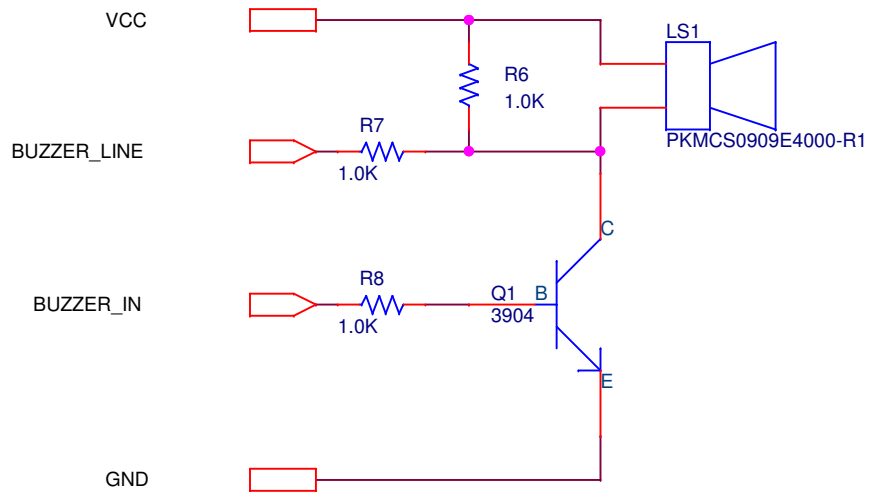
STAT pins 2/1

00: Power down, charging suspended or interrupted
01: Pre-qualification. CC/CV charging, top-off mode
10: Charge complete
11: Bad battery or LDO mode.
See page 9 of LM3658 datasheet

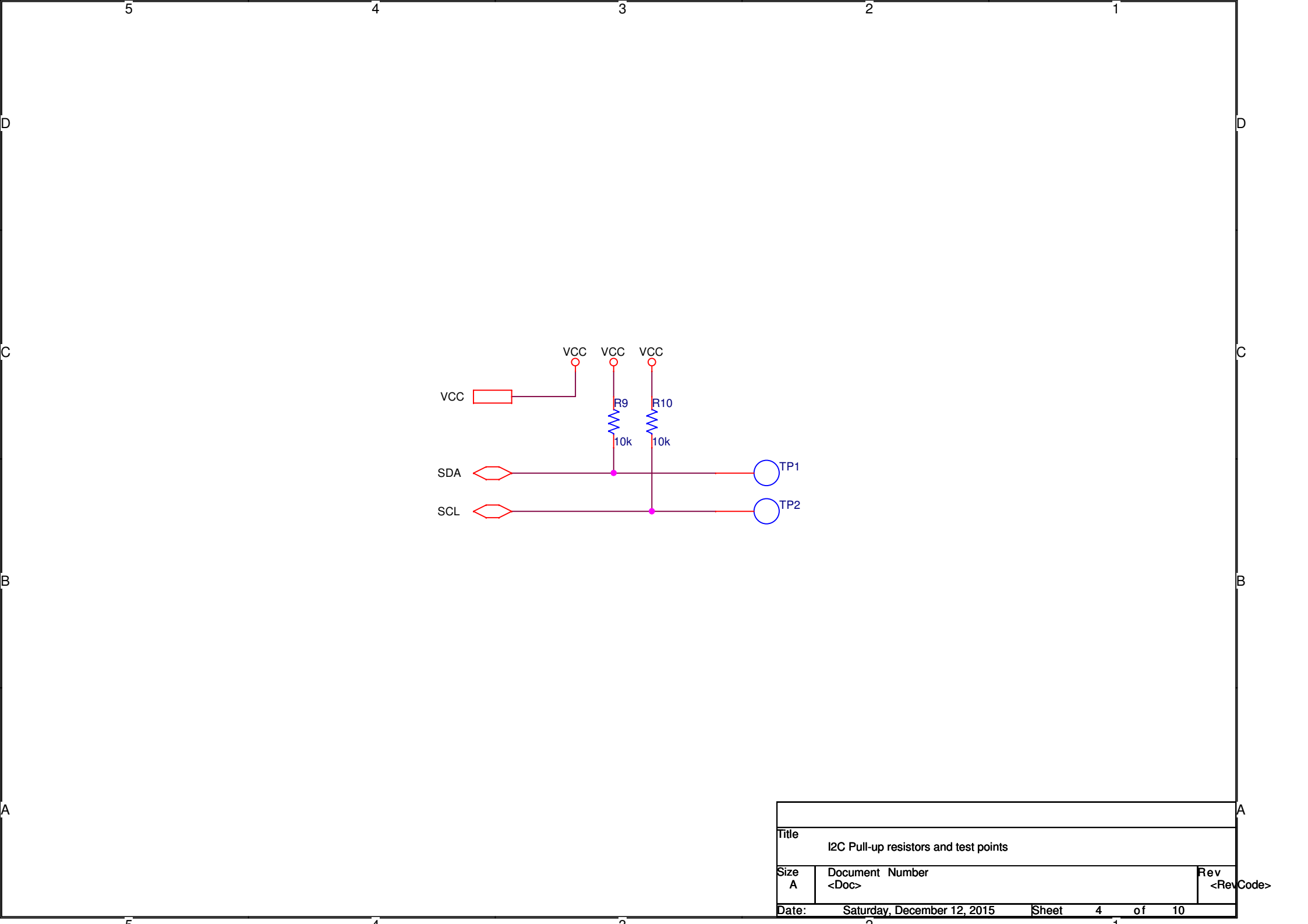


| | | |
|------------------------------|---------------------------|---------------|
| Title | | |
| Li Battery Charger TI LM3658 | | |
| Size A | Document Number <Doc> | Rev <RevCode> |
| Date: | Sunday, December 13, 2015 | Sheet 2 of 10 |

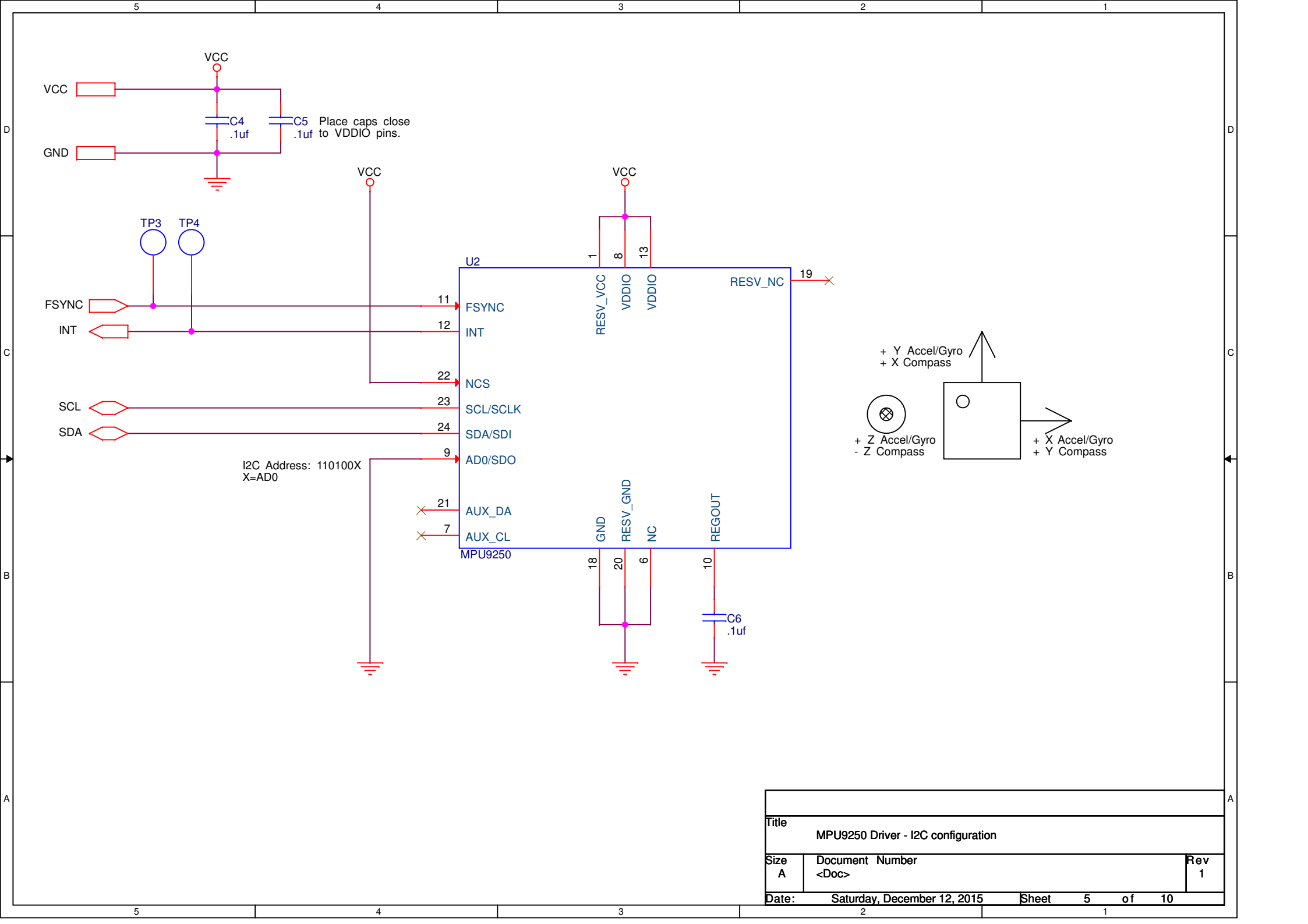
BUZZER_LINE could be connected directly to an ADC to potentially pick up vibrations

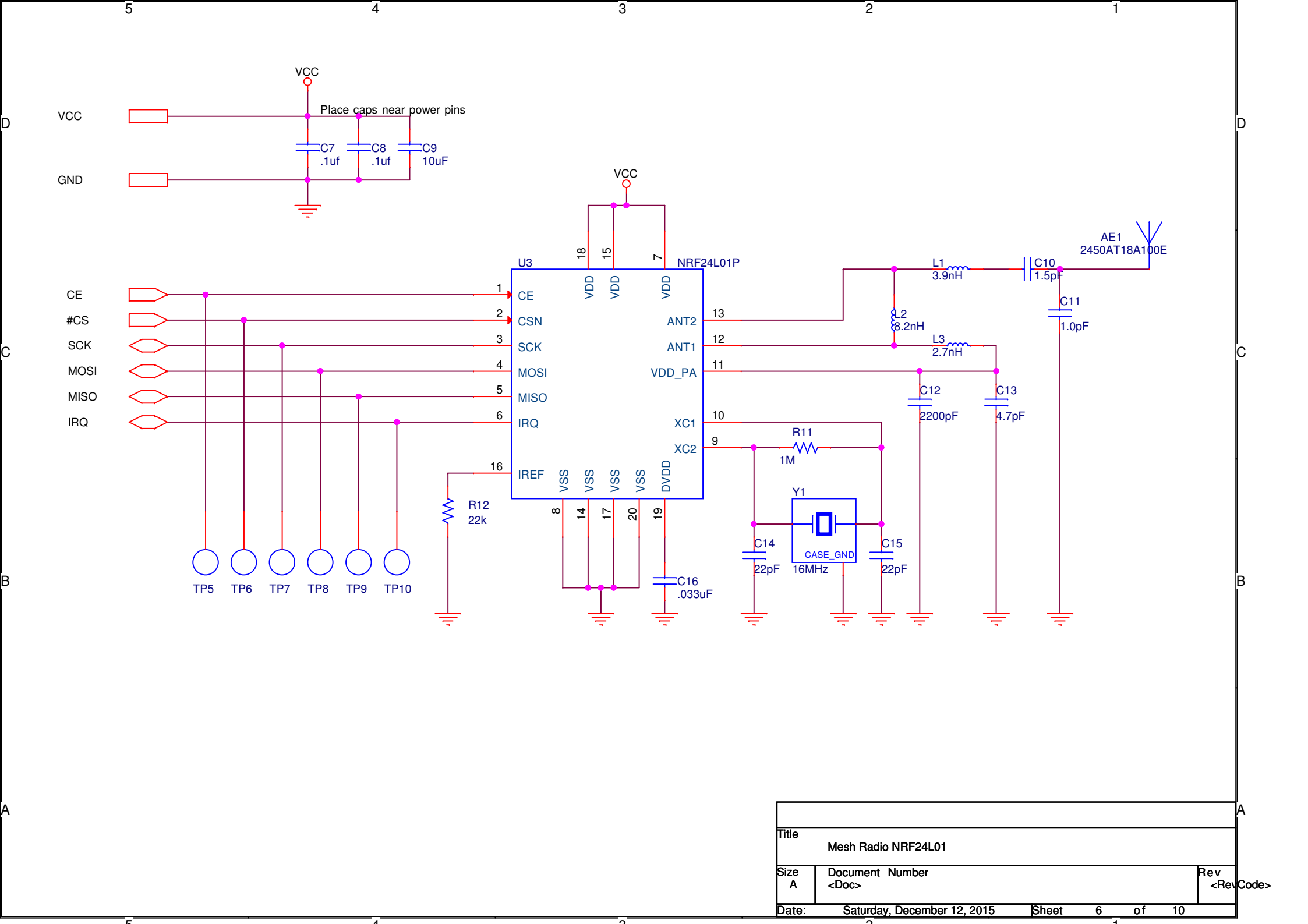


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|-------------------------------|---------------------------|----------------|
| Title | | |
| Piezo Buzzer With Analog Line | | |
| Size A | Document Number <Doc> | Rev <Rev Code> |
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| | | |
|---------------------------------------|-----------------------------|----------------|
| Title | | |
| I2C Pull-up resistors and test points | | |
| Size A | Document Number <Doc> | Rev <Rev Code> |
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D

C

B

A

D

C

B

A

PT6IAD01_SE16SP1_PCS1UART1_TXSDH02_D1TRACE_CLKOUT10D1_SDARTO_CLKOUT
PT6ILLWU_P1AD01_SE16SP1_SOUTUART1_RXSDH02_D1TRACE_D3D01_SCLSP1_SIN
PT6ILLWU_P1AD01_DP1AD01_SE16SP1_SCKUART1_CTS_BSDH02_DCLKTRACE_D2
PT6IAD01_DM1AD01_SE16SP1_SINUART1_CTS_BSDH02_CMDTRACE_D1SP1_SOUT
PT6ILLWU_P2SP1_PCS3UART2_TXSDH02_D3TRACE_D0
PT6ISPH1_PCS2UART2_RXSDH02_D2FTM0_CH0
PT6ISPH1_PCS3UART2_CTS_B1Q25_MCLKFTM0_CH1USB_SOF_OUT

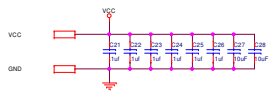
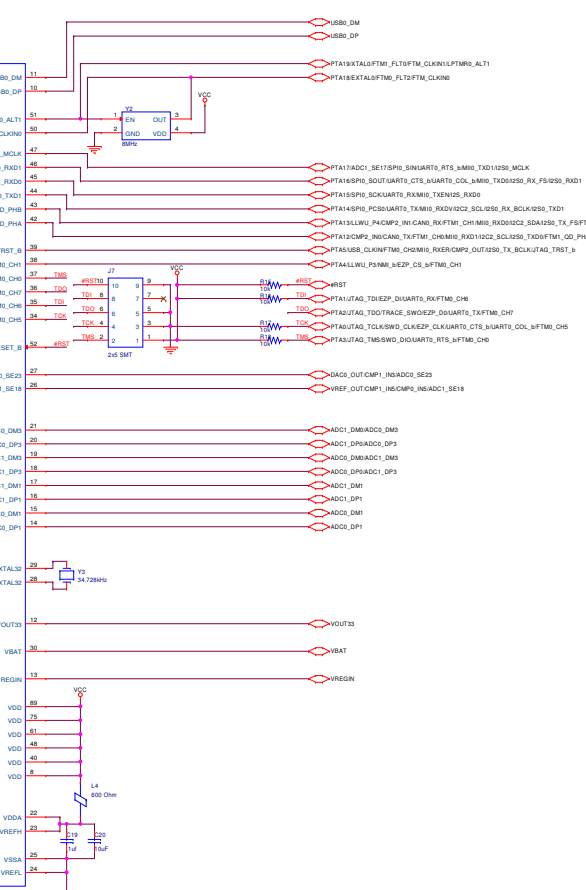
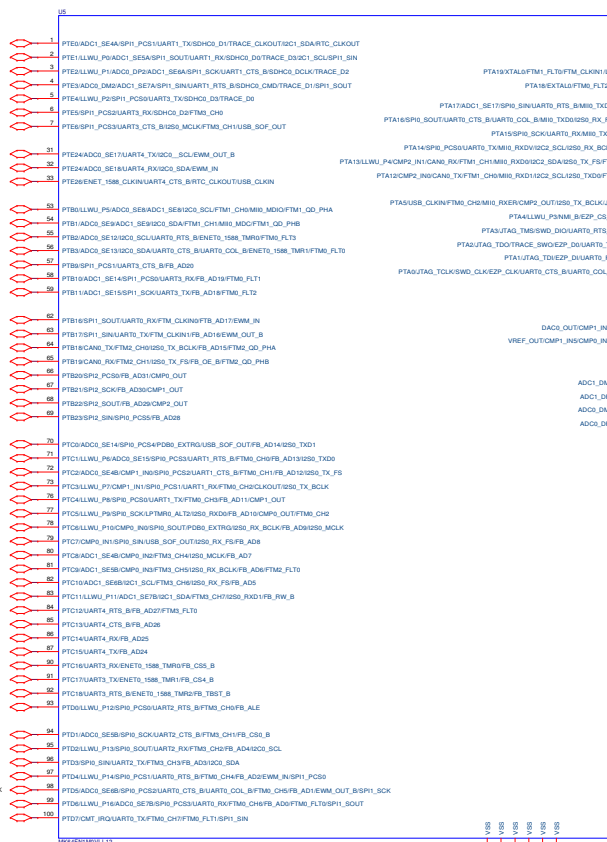
PT6IAD01_SE11UART1_TX1Q01_SCL1EWM_OUT_0
PT6IAD01_SE16UART1_RX1Q01_SDAEWM_IN
PT6ISENET_158_CLKINUART1_CTS_B1RTC_CLKOUTUSB_CLKIN

PT6ILLWU_P5AD01_SE1AD01_SE1Q01_SCLFTM0_CH0M01_MDIOFTM0_QD_PHA
PT6IAD01_SE1AD01_SE1Q01_SDAFTM0_CH1M01_MDIOFTM0_QD_PHB
PT6IAD01_SE1Q01_SCLUART1_RTS_B1ENET0_158_TMR0FTM0_FLT3
PT6IAD01_SE1Q01_SCLUART1_CTS_B1ENET0_158_TMR1FTM0_FLT0
PT6ISPH1_PCS3UART2_CTS_B1FB_A205
PT6IAD01_SE14SP1_PCS3UART2_RXFB_AD18FTM0_FLT1
PT6IAD01_SE15SP1_SCKUART2_TXFB_AD18FTM0_FLT2

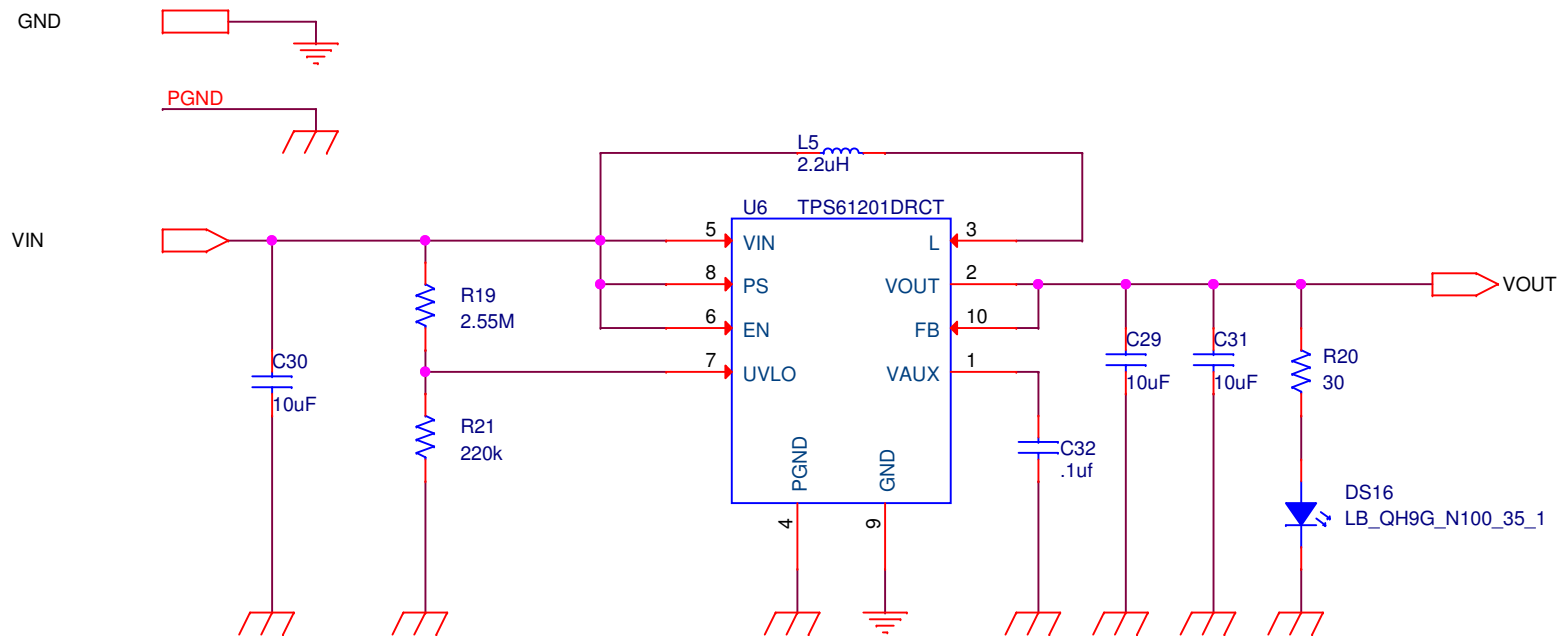
PT6I6SP1_SOUTUART1_RXFTM0_CLKINFTB_AD11EWM_IN
PT6I7SP1_SINUART1_TXFTM0_CLKINFTB_AD16EWM_OUT_0
PT6I8CAN0_TXFTM0_CH01Q25_TX_BCLKFB_AD15FTM2_QD_PHA
PT6I8CAN0_RXFTM0_CH01Q25_TX_F5FB_DE_BFTM2_QD_PHB
PT6ISPH2_PCS5FB_AD11CMP1_OUT
PT6I5SP2_SCKFB_AD08CMP1_OUT
PT6I2SP2_SOUTFB_AD08CMP2_OUT
PT6I2SP2_SINSP2_PCS5FB_AD08

PT6IAD01_SE14SP2_PCS4PDB0_EXTG0USB_SOF_OUTFB_AD14Q25_TXD1
PT6ILLWU_P1AD01_SE15SP2_PCS3UART1_RTS_BFTM0_CH0FB_AD13Q25_TXD0
PT6IAD01_SE1CMP1_INSP2_PCS3UART1_CTS_BFTM0_CH0FB_AD13Q25_TX_FS
PT6ILLWU_P1CMP1_INSP2_PCS3UART1_RXFTM0_CH0CLKOUTQ25_TX_BCLK
PT6ILLWU_P6SP2_PCS3UART1_TXFTM0_CH3FB_AD11CMP1_OUT
PT6ILLWU_P6SP2_SCKLFTM0_AL1Q25_RXD0FB_AD10CMP1_OUTFTM0_CH2
PT6ILLWU_P10CMP1_INSP2_SOUTPDB0_EXTG0Q25_RX_BCLKFB_AD01Q25_MCLK
PT6ICMP1_INSP2_SINUSB_SOF_OUTQ25_RX_F5FB_A08
PT6IAD01_SE16CMP1_INFTM0_CH0Q25_MCLKFB_AD7
PT6IAD01_SE16CMP1_INFTM0_CH0Q25_RX_BCLKFB_AD0FTM2_FLT0
PT6IAD01_SE16AD01_SCLFTM0_CH0Q25_RX_F5FB_A05
PT6ILLWU_P11AD01_SE17Q01_SDAFTM0_CH1Q25_RXD1FB_RX_B
PT6I2UART4_RTS_B1FB_AD27FTM0_FLT0
PT6I3UART4_CTS_B1FB_A026
PT6I4UART4_RXFB_A025
PT6I5UART4_TXFB_A024
PT6I6UART4_RXENET0_158_TMR0FB_CSS_0
PT6I7UART4_RXENET0_158_TMR1FB_CSS_0
PT6I8UART4_RTS_B1ENET0_158_TMR0FB_TEST_0
PT6ILLWU_P12SP2_PCS3UART2_RTS_BFTM0_CH0FB_ALE

PT6IAD01_SE16SP2_SCKUART2_CTS_BFTM0_CH1FB_CSS_0
PT6ILLWU_P13SP2_SOUTUART2_RXFTM0_CH0FB_AD01Q01_SCL
PT6ISPH1_SINUART2_TXFTM0_CH0FB_AD01Q01_SDA
PT6ILLWU_P14SP2_PCS3UART2_RTS_BFTM0_CH0FB_AD0EWM_INSP1_PCS0
PT6IAD01_SE16SP2_PCS3UART2_CTS_BUART0_COL_BFTM0_CH0FB_AD1EWM_OUT_BSP1_SCK
PT6ILLWU_P16AD01_SE17SP2_PCS3UART2_RXFTM0_CH0FB_AD0FTM0_FLT5SP1_SOUT
PT6ICMT_IRQUART2_TXFTM0_CH0FTM0_FLT1SP1_SIN



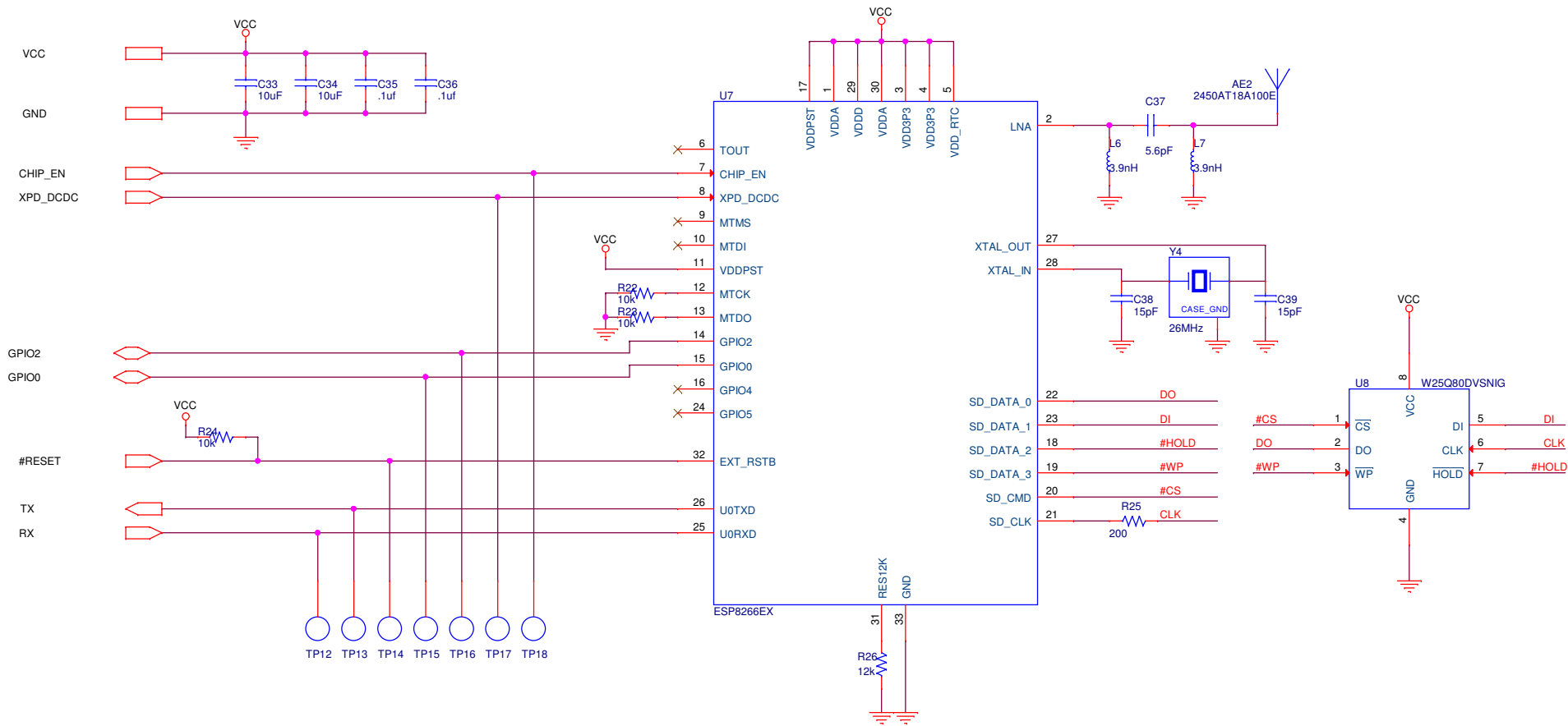
| N94 uC block | | | | Rev | |
|--------------|----------|--------|-----|-----|--|
| ID | Document | Number | Rev | Rev | |
| 0 | 0 | 0 | 0 | 0 | |
| 1 | 1 | 1 | 1 | 1 | |
| 2 | 2 | 2 | 2 | 2 | |
| 3 | 3 | 3 | 3 | 3 | |
| 4 | 4 | 4 | 4 | 4 | |
| 5 | 5 | 5 | 5 | 5 | |
| 6 | 6 | 6 | 6 | 6 | |
| 7 | 7 | 7 | 7 | 7 | |
| 8 | 8 | 8 | 8 | 8 | |
| 9 | 9 | 9 | 9 | 9 | |
| 10 | 10 | 10 | 10 | 10 | |



Input dropout voltage of ~3.15v
 $R_{uv3} = R_{uv4} * (V_{in_min} / V_{uvlo} - 1)$
 $2.55M = 220k * (3.15V / .25v - 1)$

Connect PGND and GND
 near package GND pin
 with NET_SHORT shape
 or vias.

| | | |
|----------------------|-----------------------------|---------------|
| Title | | |
| 3.3V Fixed Regulator | | |
| Size | Document Number | Rev |
| A | <Doc> | <RevCode> |
| Date: | Saturday, December 12, 2015 | Sheet 9 of 10 |



| | | |
|----------------|-----------------------------|----------------|
| Title | | |
| WiFi ESP8266EX | | |
| Size | Document Number | Rev |
| B | <Doc> | <RevCode> |
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