

G A AHEESHA SHABARAYA

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PROFESSIONAL SUMMARY

With a strong foundation in Electronics and Communication Engineering, I am pursuing a dynamic role where I can apply both hard work and smart work to drive innovation and mutual success. My projects, such as Tarkakalpa CPU simulation and a solar greenhouse, showcase my skills in digital logic design and sustainable practices.

EDUCATION

Vivekananda College of Engineering and Technology *Present*
Bachelor of Engineering in Electronics and Communication Engineering, CGPA - 8.85.

Vivekananda Pre-University College, Puttur *2021*
PUC in Electronics, Percentage - 88%.

Shri Rama High School, pattoor *2019*
PUC in Electronics, Percentage - 85.4%.

TECHNICAL SKILLS

Programming Languages	Verilog HDL, C, Java, Python
Hardware Skills	Circuit Analysis, Digital Logic Design

PROJECTS

TARKAKALPA

An 8-bit CPU simulation

- Developed "Tarkakalpa," an 8-bit CPU simulation in Logisim, utilizing logic gates for operations like addition, subtraction, and branching.
- Led a multidisciplinary team to design the CPU architecture in Logisim and implement custom assembly instructions for data processing and control.
- Delivered a fully functional CPU simulation that demonstrated core operations and enhanced the team's understanding of digital logic design and collaboration.

SOLAR GREENHOUSE

Sustainable solar greenhouse project

- Implemented a robust solar greenhouse initiative integrating smart sensors for temperature and humidity control.
- Installed solar panels to supply eco-friendly energy, enabling precise temperature control and automated irrigation systems for optimized farming practices.
- Achieved 2nd prize at the Viveka krishi Anveshana Project Exhibition in Puttur, February 2023, highlighting the project's impact on sustainable agriculture.

CERTIFIED COURSES

VLSI Digital Design, Infosys Springboard	<i>Jul 2024</i>
Programming Using Java, Infosys Springboard	<i>Feb 2024</i>
Basics of Python, Infosys Springboard	<i>Nov 2022</i>