



OS

Diff b/w process & threads

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Process is program
in execution

Threads are the
segment of program.

More exec time

Less exec time

P don't share res

Shares resource

P don't share
data

Threads shares
data

P don't share
memory

Threads shares
Memory

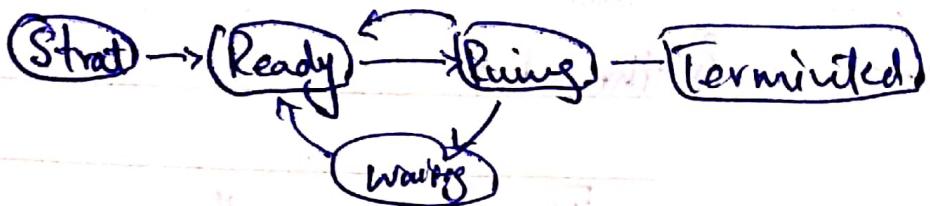
Slower than
threads

Faster than
process

Memory efficient

load balancing, balancing

Process



PCB

Data Str in OS that stores info about P

Sockets

are software obj that establishes a bidirectional network communication b/w client and server.

Sockets are strategies for communication. They are mostly used in client-server network. They acts as the end-point on either side for comm

Multicore programming:

is the development of concurrent systems for deployment on multicore processors. A multicore processor is a single processor containing multiple execution chips in it.

Multiprocess system

have multiple processor connected with mother board



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Q Making connection b/w user & kernel thread
System call

is the programmatic way to request a system service from a Kernel that is running in the OS
(fork(), write(), openFile())

Q Primary Thread Libraries

are the threads that are provided to the programmer for creating and managing threads by using API application program interface
Example : Win32, Java, Pthreads.

Q CPU scheduling

is the process of allocating CPU to a process in ready state and switching b/w process on basis of diff. sched algo.

The purpose is whenever it's idle, OS switch the last process and load the pr ready for exec in CPU.

Critical Section

is the segment of code of a process that is accessing a shared common variable and performing operation on it.

When one program is in its critical section, no other program is allowed to execute its C.R.

Ans

Critical Section Problem :

A procedure or protocol that is used to overcome C.S.P. Each P. that wants to enter in its C.R. checks if another process is in C.R. Then entry section, exit section remainder

Solution

Mutual Ex:

Progress,

Bound



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Q Internal & External

Q Preemptive & Non Preemptive Kernel

It allows one process
to be replaced by
other process

It doesn't allow
context switch

E It is suitable for
real-time programming

It is not suitable
for real time
programming

The process with high
priority are given
to CPU

Each process that is
given to CPU continues
its exec until its
exec is terminated

It allows preemption
of process

It doesn't allow
preemption of
processes



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Load Balancing:

is the core network solution that provides the distribution of client requests to different servers in a server farm.

This allows the availability of application and responsiveness of the client request and prevents overloading.

Cong

Critical Section

Def critical section

Def critical section - problem

↳ developing rules and protocols that are followed by a process in which it makes a request for every parallel program in execution to access critical section

Race condition This request is entertained by entry section which is the entry point of critical section

Entry Section consist of implementation of such code that if critical section is already acquired by some other process, the request for access is denied by entry section.

After entry section, there is code for accessing shared variables or resource.

Exit Section is followed by entry section. This section allows the exit of process from critical section, makes free the available resources and informs all other processes that the critical section is free.

Remainder Section

After critical section is remainder section.

Solution to the critical section problem must satisfy

Mutual Exclusion

Progress

Non-ded Waiting



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Some Solution to the critical Section Problem

Most of the solution utilized lock implementation.

→ Mutex Lock:

provides acquire and release function that is automatically executed

→ Semaphore:

Semaphores utilize 2 basic function wait() and signal() that is automatically executed on Semaphore S, an integer variable.

→ Conditional Variables: utilizes a sequence of processes that are waiting to enter in critical section.

General Architecture

do {

Entry Section

C.S

Exit S

R.S

?while(true);

	T_0	T_1	T_2	T_3
<u>Example</u>	$R_1 = C$	$R_1 = L + T$	$C = R_2$	
T_0	5	6	4	6
T_1	6	5	5	5



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Q Internal & External Fragmentation

Internal Fragmentation

Def

occurs when P is allocated

to greater memory block

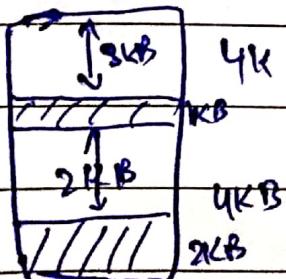
occurs in fixed²

partitioning of M

can be handled

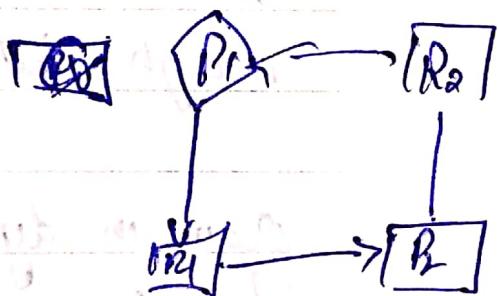
by variable/dynamic partition

Memory Block is greater
the P size



~~Q: Deadlock~~

is the situation in which a number of processes are blocked because each process has been acquiring/holding a resource, and waiting for other resource which was acquired by other process

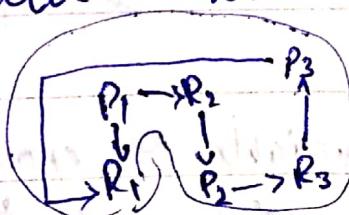


Mutual Exclusiveness \Rightarrow Non sharable Resource

Non-Preemption \Rightarrow

Hold & wait \Rightarrow

Circular wait \Rightarrow





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Deadlock

Def

wait is infinite

Starvation

Def

wait is long but not

infinite

Every deadlock is slow

Every starvations is not
deadlock

Resource are blocked

Can continuously assigned
to higher p

Methods to handle Deadlock

- (i) Deadlock Avoidance (osloige algo)
- (ii) Deadlock Prevention (donot let 4 states^{con} to occur)
- (iii) Deadlock Avoidance (Banks's Algo)
- (iv) Deadlock Detection & Recovery

Deadlock avoidance

=> For every resource allocation, we check
for safe & unsafe sequence.

=> By using Banks's Algo (Djikstra's Algo)

Resource Allocation Graph

Graphically rep of allocation
of resou

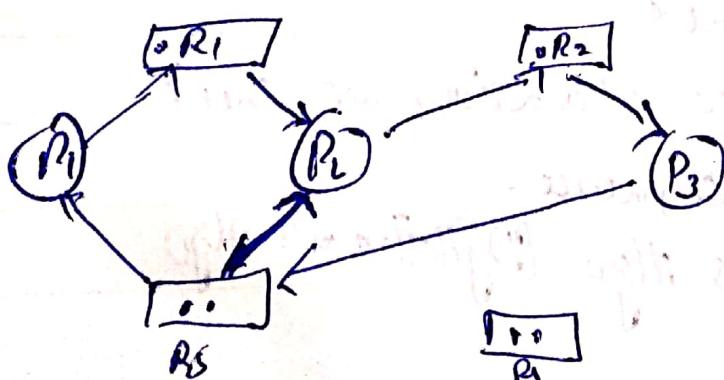
4 components

- ① ○ → processes ✓
- ② □ → Resources ✓
- → Non-shareable
- → Shareable, .. shows no of instances
- ③ R → P → R allocated to P ✓
- ④ P → R → R requested by P ✓

Can find if Deadlock occurs or not

Ex

- No cycle = deadlock never occurs
- Cycle = may or may not deadlock occur





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Banker's Algo

Banker's Algo checks for the safe or unsafe state before allocating resource to a process.

why it is named

if person wants for loan

Bank checks for total money, if greater than reqst then give it to person.

Q) Memory Management

⇒ is the type of resource management

⇒ handles the swapping of pages from main memory to secondary storage

⇒ increase efficiency

Base Reg ⇒ smallest unit of physical address

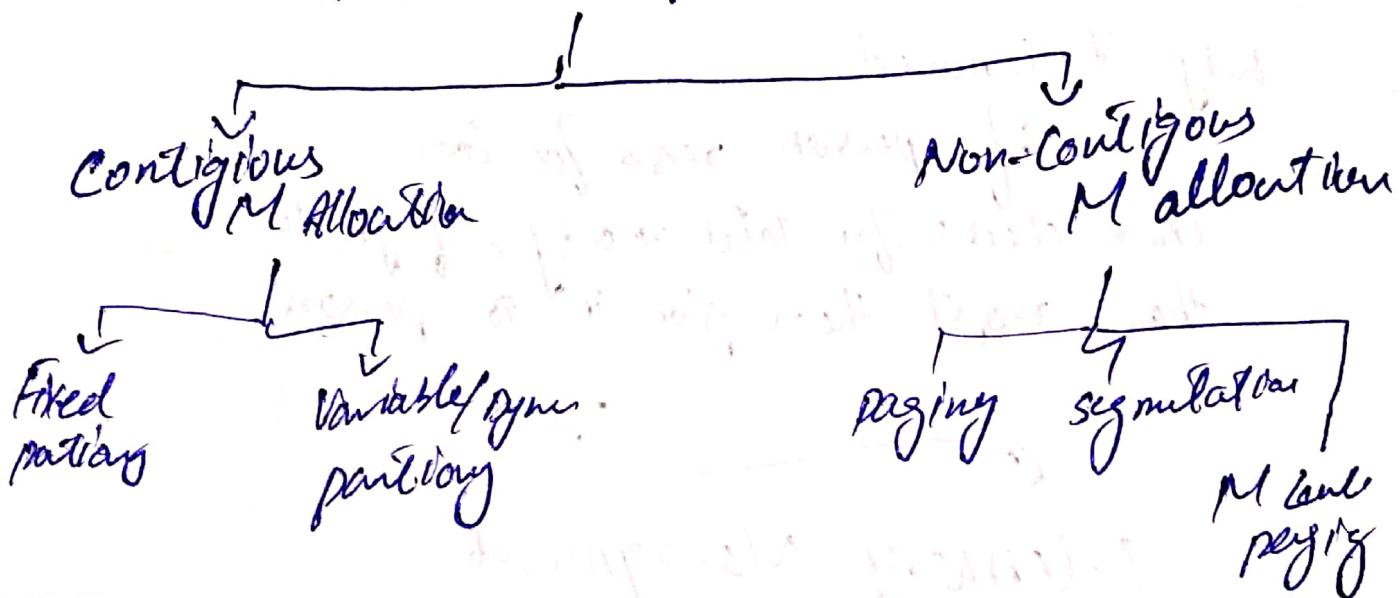
Limit Reg ⇒ size of address or range

CPU direct access to Main Memory

Process are bring to Main Memory from secondary storage

CPU access registers, caches and MM

MM Techniques



Static Loading

all the libraries and modules are loaded into memory for the exe to start.

Dynamic loading

all the libraries, modules and data are loaded into memory when req by the program.



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Static Linking

the linker combines all the data, libraries and program objects into a single executable program to avoid runtime exception.

The linker combines the object modules with object program and convert them into absolute executable program.

Dynamic Linking

In dynamic linking, instead of linking actual modules and libraries, a reference is linked with the dynamic module at the time of compilation.

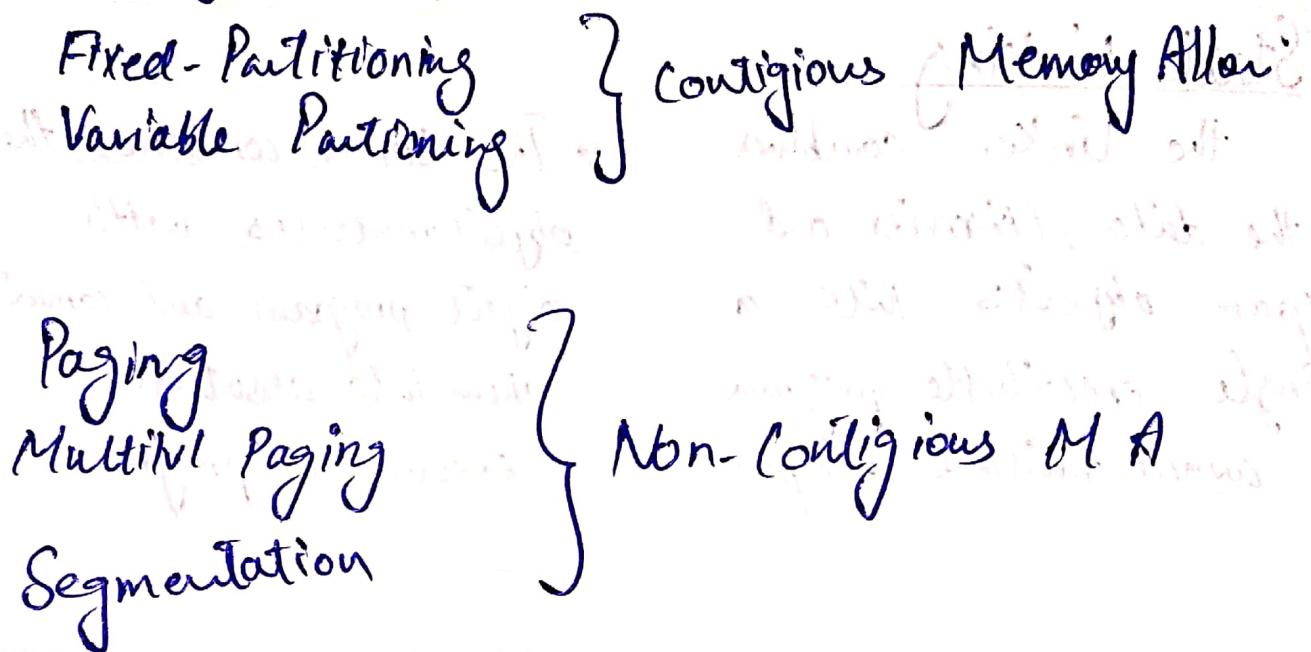
Swapping

is the process of moving a process out of Main and storing it to Swap & vice versa.

Swapping is also called Technique for memory compartment.

To run large programs by swapping pages.

6-Memory Management Techniques



Fixed Partitioning

Best fit algorithm, round robin, first fit, worst fit, etc. It is a simple technique where memory is divided into fixed-size partitions. The size of each partition is determined by the size of the largest process or application that needs to run. This ensures that all processes have enough contiguous memory to run without interruption.

Paging

It is a memory management technique that divides memory into fixed-size pages. Each page is a fixed-size block of memory that is mapped to a specific location in physical memory. This allows for efficient memory management and provides protection for individual pages. It also makes it easier to handle memory protection and access control.



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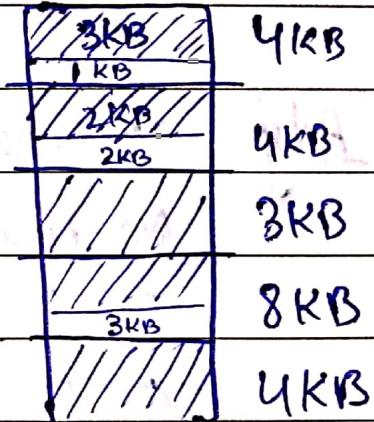
Contiguous Memory Allocation

allocates you to store the process in a contiguous fashion

⇒ Fixed Partitioning

in fixed partitioning

- No of partitions are fixed
- Size of partitions are may or may not be fixed
- Spanning is not allowed in contiguous MA



⇒ Spanning is the process of dividing process in parts

Limitations

⇒ Internal Fragmentation

⇒ Limit of process size

⇒ Limit in degree of Multiprogramming due to fixed no of partitions.

⇒ External Fragmentation

Variable Partitioning

partitions are made when
need by process instead of during
system configuration.

=> Divide ram into parts variable
on run time

- Adv :
- 1. No chance of internal fragmentation
 - 2. No limit for no of partitions
 - a. w.r.t size of memory
 - b. w.r.t no of processes.

Dis Adv

External Fragmentation

(unutilized memory)

multiple free blocks

no contiguous free blocks

no free blocks of sufficient size



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Non-Contiguous Memory Allocation

⇒ processes are allocated in different portions of main memory in non-contiguous form

⇒ reduces the waste of memory but increases the time for context switching and translation of logical address.

⇒ Page Table is used to maintain the pages of programs allocated to frames of MM

Paging

is the process of dividing process into parts.

⇒ Program is first divided into parts before bringing them to MM.

⇒ Main Memory is also divided into frames.

⇒ size of page = size of frame.

⇒ Removes External Fragment.

⇒ Logical address is generated by CPU for program

⇒ Physical Address are actual addresses of memory

⇒ MMU is responsible for conversion of logical address into physical address

⇒ MMU uses page table for this purpose

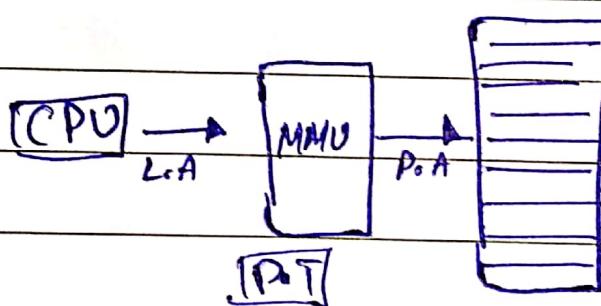


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=> Page Table contains the page no and page offset

Page No	Page Offset
P ₂	0
P ₄	1
P ₃	2
P ₅	3
P ₄	4

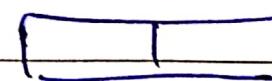


L.A. M.M.

bits
Page No : represents the no of pages of program

Page offset : bits represent the word or page

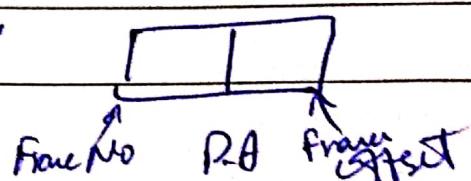
size



P.A

Frame No : Bits rep no of frame

Frame offset : Bits rep page offset

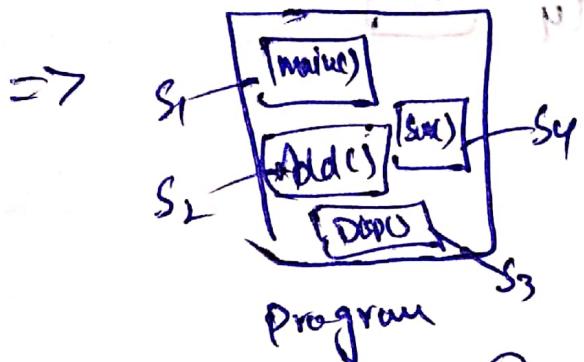


Page Rep Algos

⇒ Segmentation

is the process of dividing program in part and allocating them in MM

It is similar to paging, but it divides parts of program; Paging divides the code, It divides the functionality



⇒ Segments are of diff size

⇒ pages are of same size

⇒ Logical $\xrightarrow{\text{MMU}}$ Physical

⇒ MMU uses segment tab



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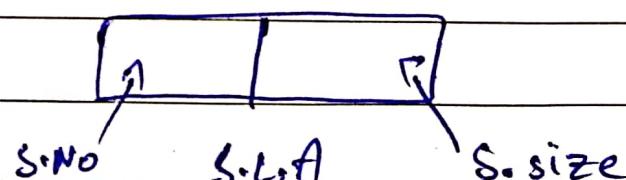
Segment Table

No of segment	B.A	Size
0	3300	200
1	1800	400
2	2700	800
3	2200	700

∴ Base Address = starting address of S

∴ Size = size of segment

CPU → L.A





Components of a Computer System

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Hardware

O.S

Application Software

End users

Services

Computing Enviro

User Interface

Personal Computer

Program Execution

Time Sharing

I/O operation

Client-Server

Resource Management

Distributed

Allocation of R

Cloud based

Communication

Privacy

Security

System Calls

Process Control

CreateProcess(), ExitProcess()

File Management

CreateFile(), ReadFile(), WriteFile()

Device Management

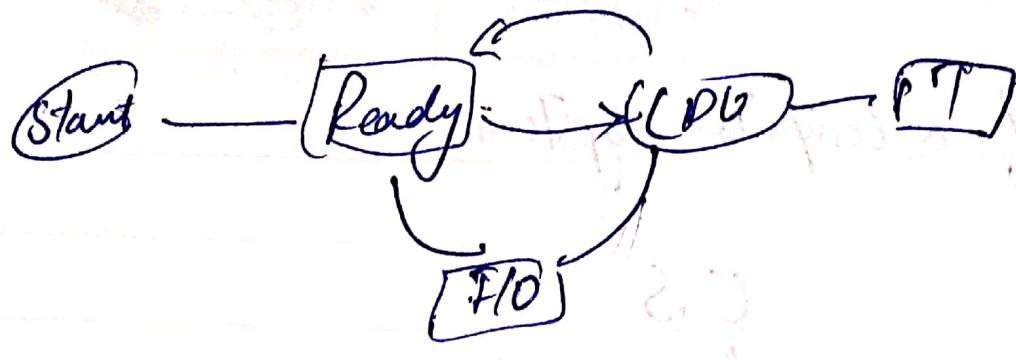
ReadConsole(), WriteConsole()

Info &

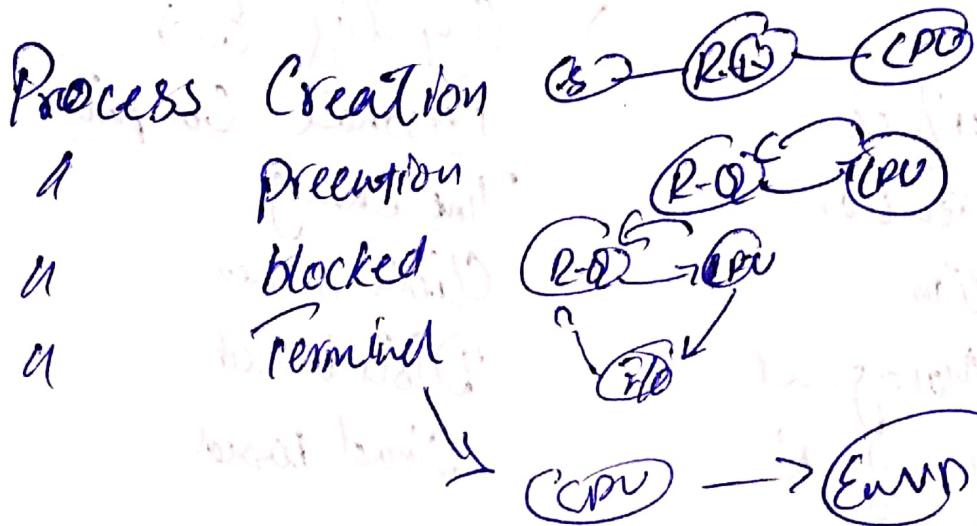
GetProcessID(), SetTimer()

Communication

CreatePipe()



Q Explain operation on process



Thread

- Many to One
- Many to Many
- One to One

Multiple I/Os mapped to single kernel thread.

Thread mapping is done by threads which are blocked by making a blocking system call.

