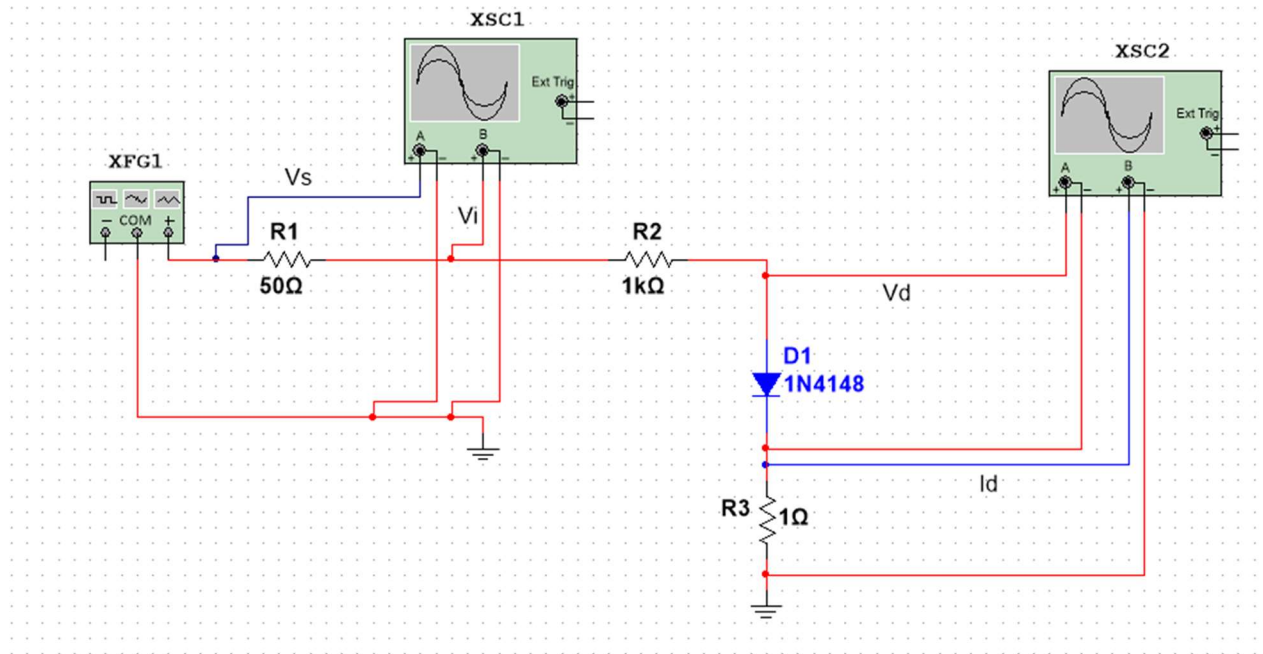
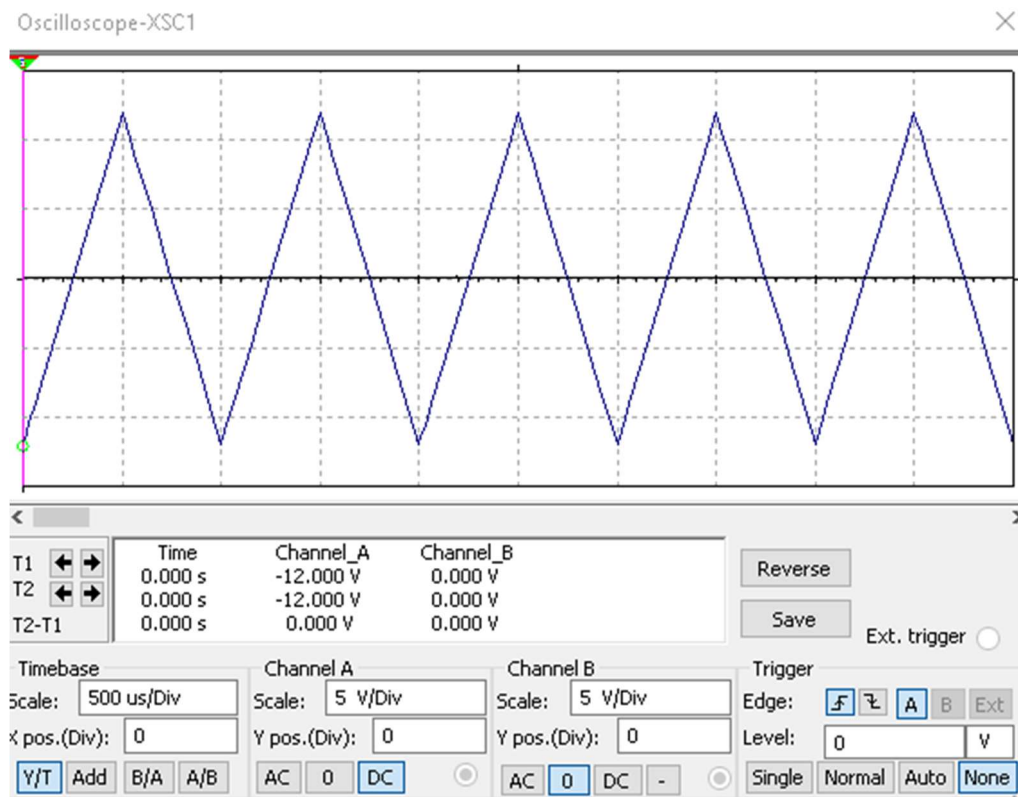


Circuit

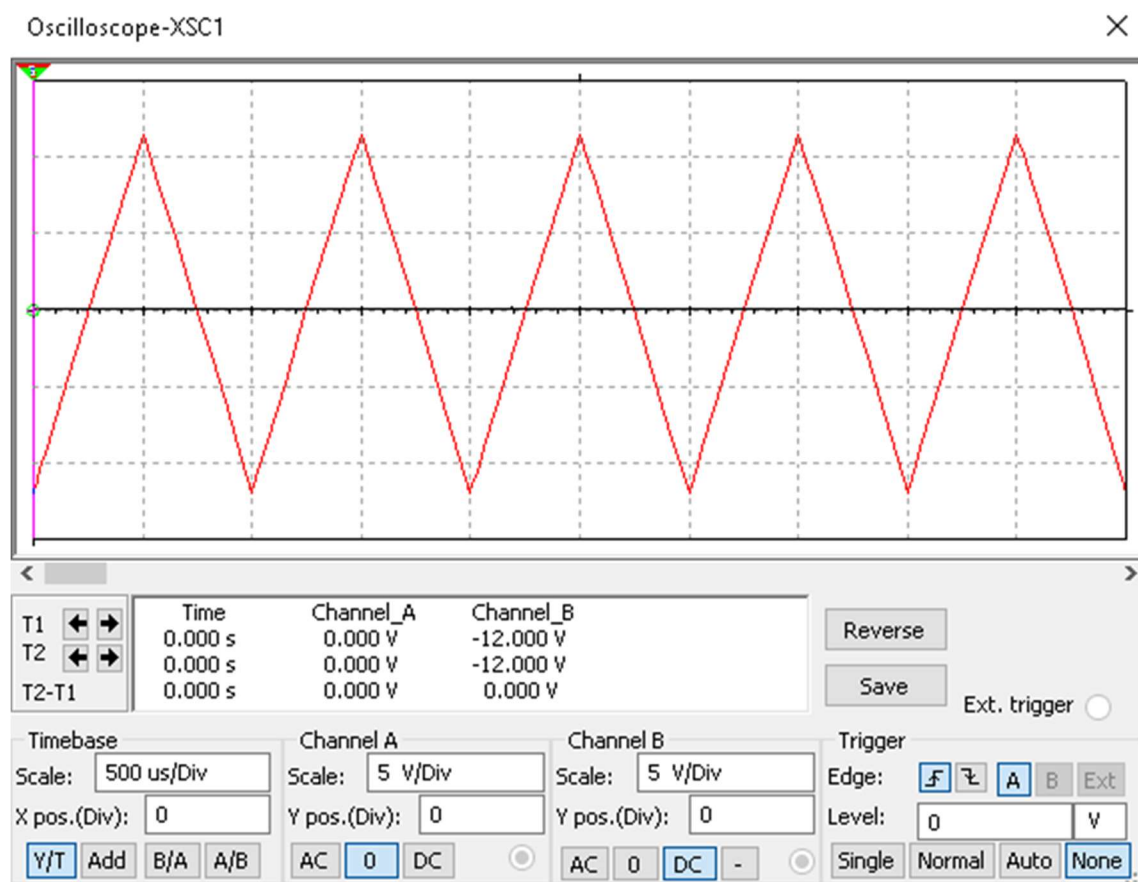


Graph P1(a)

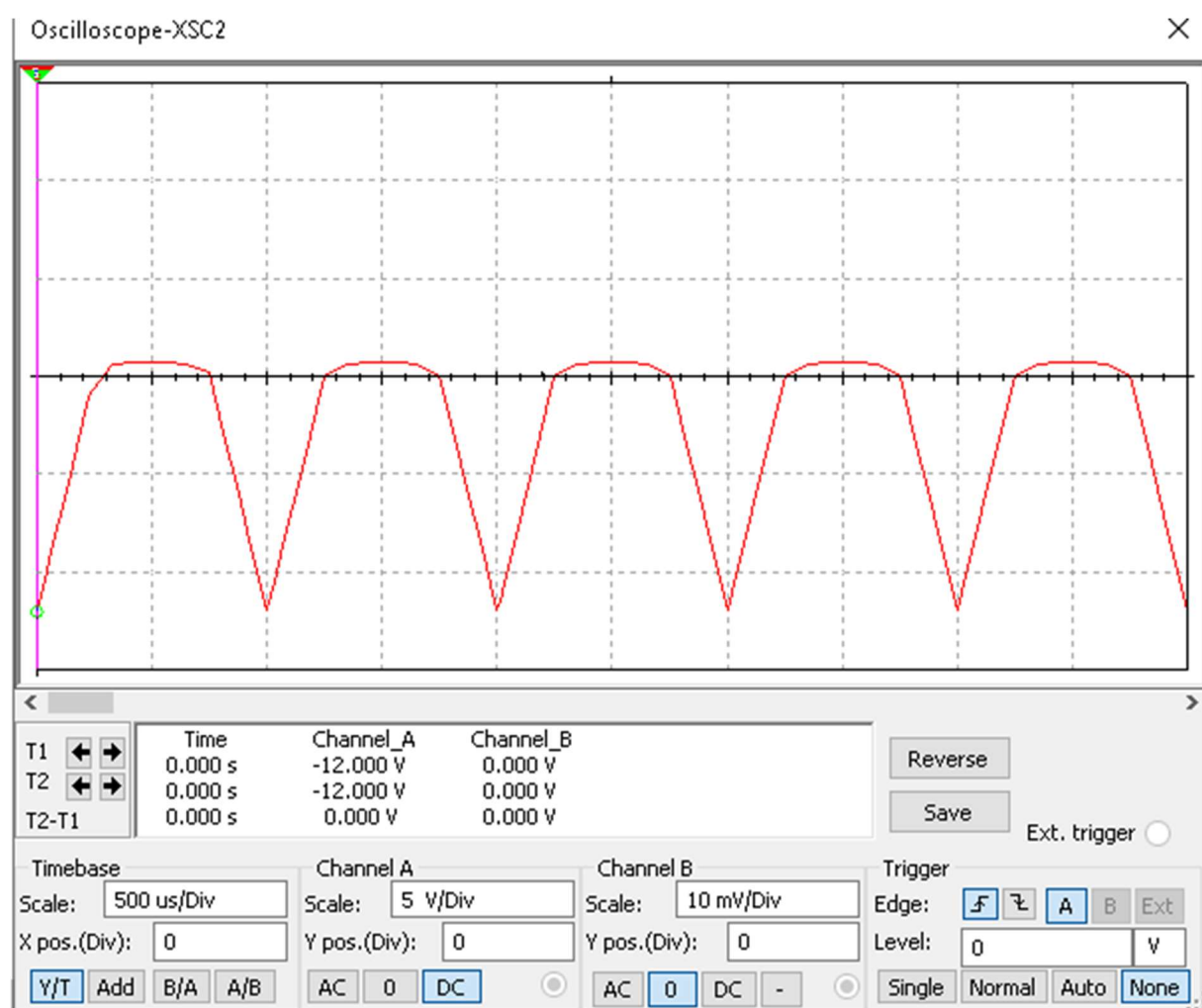
V_s :



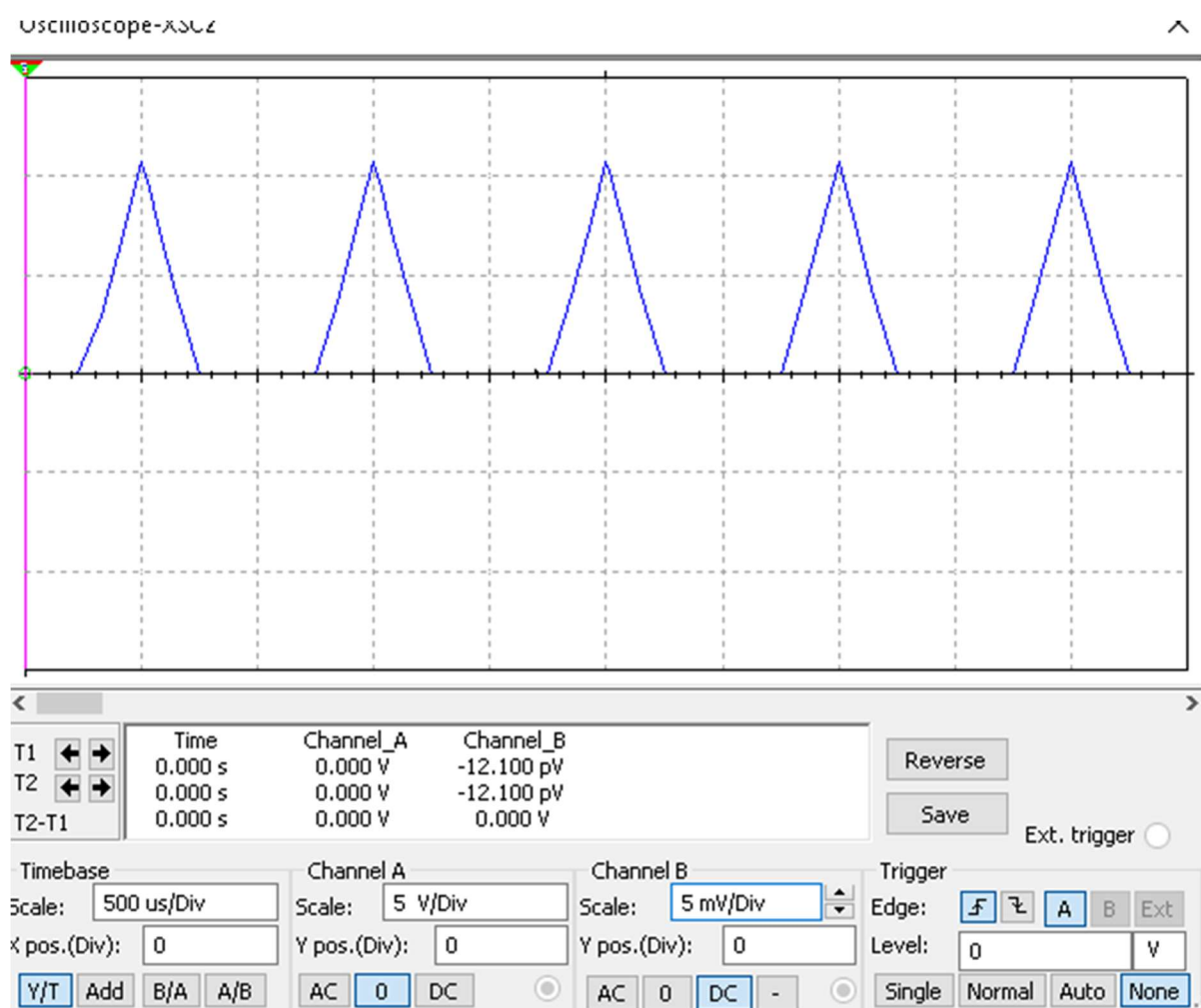
Vi:



Vd:



Id:



Graph P1(b):

