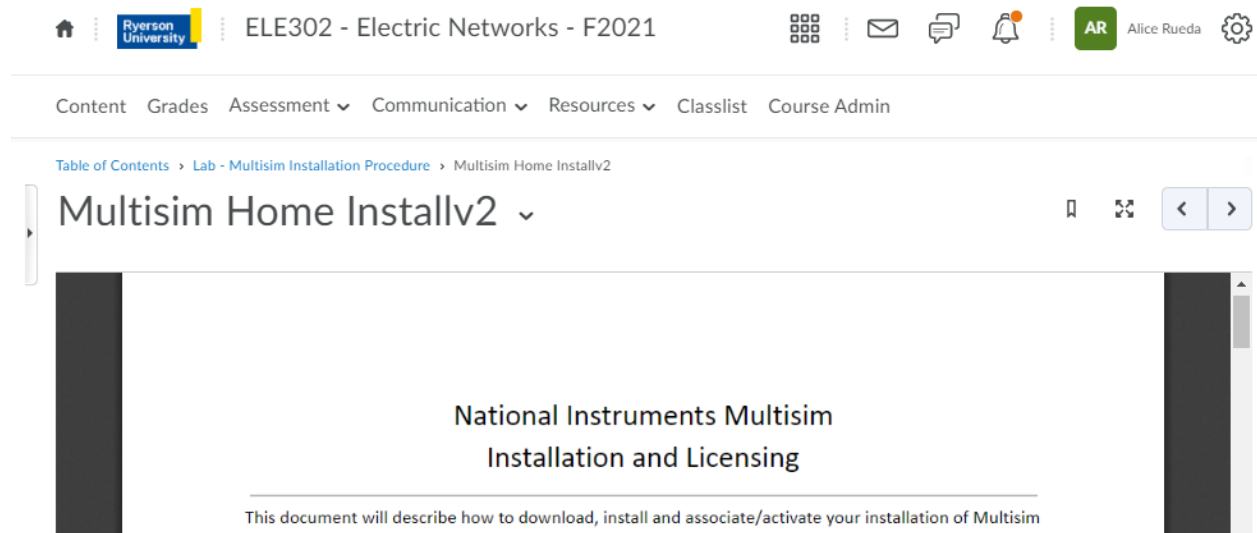


## Simulation Tool

Due to Covid, ELE 302 lab will be online using Multisim. If you have not already done so, please follow the instructions on D2L to install multisim.



The screenshot shows a web browser interface. At the top, the address bar displays 'ELE302 - Electric Networks - F2021'. Below the address bar, there is a navigation menu with links: Content, Grades, Assessment, Communication, Resources, Classlist, and Course Admin. The main content area is titled 'Multisim Home Installv2'. It features a large heading 'National Instruments Multisim Installation and Licensing' and a subtitle 'This document will describe how to download, install and associate/activate your installation of Multisim'.

If you have encountered any problems besides IPv6, please contact Mr. Jason Naughton directly (jnaughto@ryerson.ca).

## Regarding to the Labs

### Mark Allocation

There are three parts for each lab. They are Pre-lab, Lab, and Post-lab (Q&A at the back). Each part is allocated with:

- Pre-lab: 3/10
- Lab: 5/10 ([Individual demonstrations required](#))
- Post-lab (Q&A): 2/10

### Lab Guidelines

- Sign the attendance sheet
- [Experiments conducted in your assigned section will be graded](#)
- Fall 2021: Students will work individually
- Hand-in your work by the end of the lab **INDIVIDUALLY**
- Post-Lab (Q&A) is to be handed in the following week to your TA **INDIVIDUALLY**

## Experiment #1

In this lab, you will study the differences between open-loop voltage gain of the non-ideal Op-Amp and closed-loop voltage gain of inverting and non-inverting amplifier circuits. This lab content is covered in Chapter 5 Operational Amplifiers.

### IMPORTANT:

- Please make sure that your simulation environment has **the Maximum time step (TMAX)" to 10e-5 for all labs**. So, we all use the same sampling.
- Always use nodal analysis for Op-Amp circuits.

## Prelab Tips

Step 2 (a). Finding the  $A_v = V_o/V_i$ ,  $R_i$  and  $R_o$  of the amplifier circuit

Hints: Use  $1V_P$  as your voltage source  $v_i(t)$ .

- Use nodal analysis to find the current going into the circuit,  $i(t)$
- $R_i = v_i(t)/i(t)$
- The nodal analysis will also give you  $V_o$ . Use  $V_o$  to calculate your  $A_v$
- To find  $R_o$ , you need to apply Thevenin's Theorem from Chapter 4 to find  $R_o$ 
  - o Add 1V voltage source to the output
  - o Zero out independent source
  - o Calculate the current from the 1V voltage source ( $I_{TH}$ ),  $R_o = R_{TH} = 1V/I_{TH} \sim 0.09 \Omega$  (something very small.)

## Lab Session 1

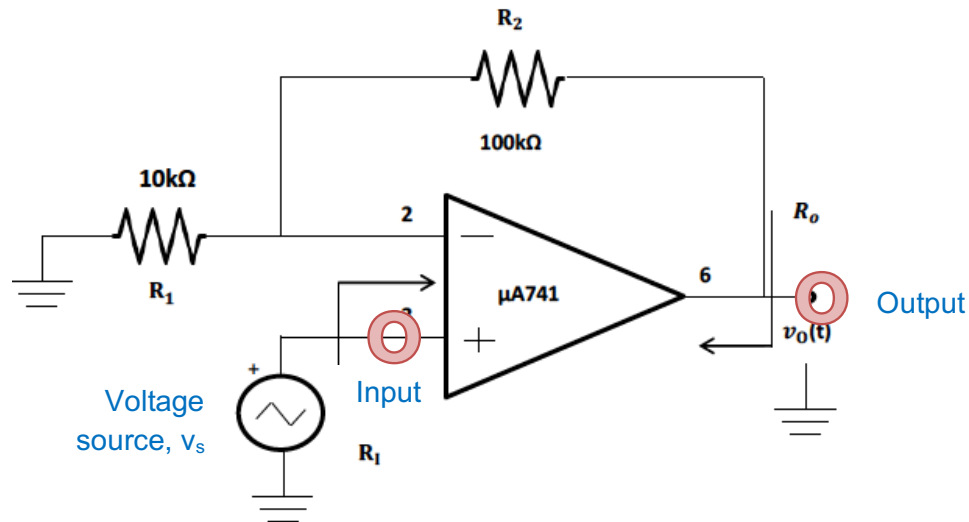
The first part of the lab covers open-loop voltage gain and the non-inverting amplifier.

### Open-loop voltage gain

Given open-loop voltage  $A=0.5 \times 10^6$ ,  $v_+$  is the voltage at the positive pin, and  $v_-$  is the voltage at the negative pin

$$v_o = A v_d (v_+ - v_-)$$

## Non-inverting amplifier



Using nodal analysis at node 2:

$$v_o = \left(1 + \frac{R_2}{R_1}\right) v_i$$

To measure  $R_o$ , you need to measure  $v_{O\_NL}$  and  $v_{O\_L}$ .

- $v_{O\_NL}$ : As it is, open circuit at the output pin
- $v_{O\_L}$ : Add a load resistor at the output pin (6), then measure the voltage

Then, use equation given in step 6 to calculate  $R_o$ .

To measure  $R_i$ , you need to add a resistor ( $2M\Omega$ ) between the voltage source ( $v_s$ ) and the input ( $v_i$ ). Use equation given in step 7 to calculate  $R_i$ .