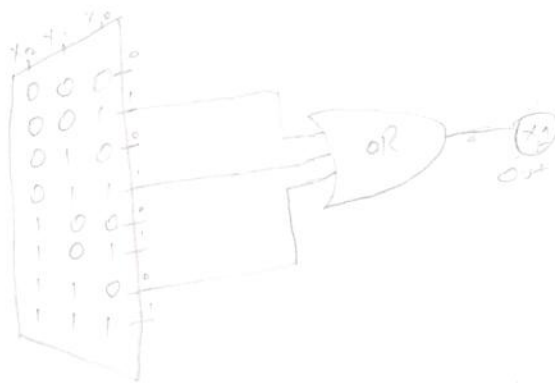


2. (3 points) Given only an 3-8 one hot decoder and an OR gate implement a circuit that behaves like the following function: $m_1 + m_3 + m_6$. There are 3 input variables for this problem

x_2, x_1, x_0

x_2	x_1	x_0	y
0	0	0	0
1	0	0	1
0	1	0	0
1	1	0	1
0	0	1	0
1	0	1	0
0	1	1	1
1	1	1	0



3-8 decoder and OR gate