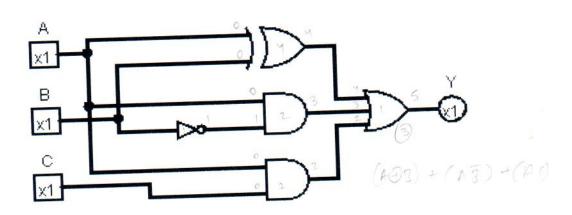
6. (8) Give the following circuit and delays complete the timing diagram.



Element	Delay
NOT	1
AND	2
XOR	4
OR	1

Contamination delay: