

CSC225: Assignment 5

Cache Memory

a. Answer the following:

- 1) Mention the full hierarchy list of memory devices and the technology used to make each one of them.
- 2) Explain the Cache operation (Cache hit and Cache miss).
- 3) In the typical Cache organization, how does the Cache connect to the processor?
- 4) Mention and explain the techniques used in mapping function.
- 5) What is meant by LRU, FIFO and LFU replacement algorithms.