

# Computer Architecture 14-1-2021

Enter Your Name \*

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MS CS 1st Mid Term

All Operating Systems get their total memory initialized from??

- ☐ CPU
- ☒ BIOS
- ☐ ROM
- ☐ RAM

How much video RAM is needed to display 1024x768 at 24-bit color??

- ☐ 1MB
- ☒ 4MB
- ☐ 8MB
- ☐ 512KB



Which I/O port belongs to LPT2??

- ☐ 378H
- ☐ 3f8h
- ☐ 2e8h
- ☒ 278H

Teletypewriter terminal is an example of ?

- ☐ Input device
- ☐ Output device
- ☒ Input/output device
- ☐ Storage device

The CPU Consist of ?

- ☒ arithmetic and logical unit
- ☐ Control unit
- ☐ main storage
- ☐ All of above



Devices are used to receive data from CPU are classified as ?

- ☐ Input Device
- ☒ Output device
- ☐ Storage device
- ☐ All of above

Breaking the instructions Execution into clock cycles are divided into ?

- ☐ two steps
- ☒ three steps
- ☐ four steps
- ☐ none of these

When control unit perform subtraction through ALUop the signal has a binary value of ?

- ☐ 10
- ☐ 11
- ☐ 0
- ☒ 1



The Simple data path for the MIPS architecture has instruction set of ?

- ☐ 8bits
- ☐ 16bits
- ☒ 32bits
- ☐ 64bits

The CPI (Clock per instruction) is given by the following formula:

- ☒  $\text{CPI} = \frac{\text{CPU clock cycles}}{\text{Instruction count}}$
- ☐  $\text{CPI} = \text{Instruction count}$
- ☐  $\text{CPI} = \text{CPU clock cycles}$
- ☐  $\text{CPI} = \text{CPU clock cycles} * \text{Instruction count}$

In the multi-cycle data-path with exception handled, the clock cycle has?

- ☐ 7 control signals
- ☐ 8 control signals
- ☒ 9 control signals
- ☐ 10 control signals



For implementing the IA-32 instruction set, the framework is provided by?

- ☐ Multicycle data path
- ☐ Microprogrammed controller
- ☐ Hardwired control
- ☒ both a and b

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