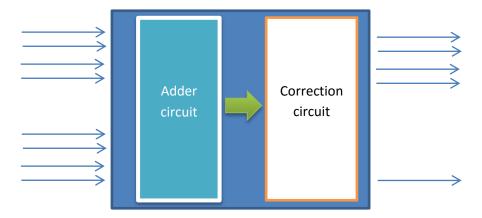
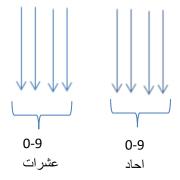
1- BCD 4-Bit Adder:



دايرة ال Adder هنستخدم 4-bit full Adder فاضل بقى ال Adder دايرة ال

BCD Correction circuit .. -1

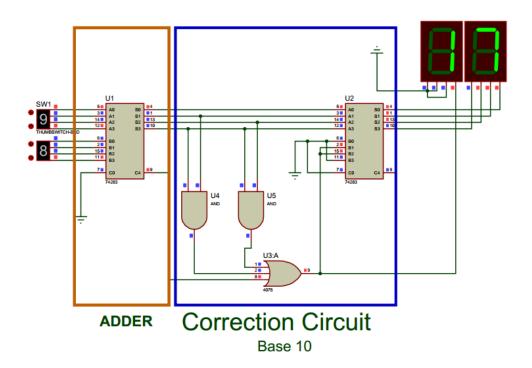
عاوز اعمل دا يره تاخد خرج binary من ال adder حوله ل DCD كل 1 بت من 0 ل 9 كل 4 بت من 0 ل 9



دلوقتی انا طلما بجمع BCD -4-Bit BCD فکبر رقم ممکن یخرج 18 >> 10010 عندی من 0 ل 9 مغیش مشکله بس لما یبقی اکبر هحوله

S4	s3	S2	S1	S0		C0	Z3	Z2	Z1	Z0
0	1	0	1	0	10	1	0	0	0	0
0	1	0	1	1	11	1	0	0	0	1
0	1	1	0	0	12	1	0	0	1	0
0	1	1	0	1	13	1	0	0	1	1
0	1	1	1	0	14	1	0	1	0	0
	1	1	1	1	15	1	0	1	0	1
1	0	0	0	0	16	1	0	1	1	0
1	0	0	0	1	17	1	0	1	1	1
1	0	0	1	0	18	1	1	0	0	0
1	0	0	1	1	19	1	1	0	0	1

If(S3 and S1 =1) or (S3and S2 = 1) or (S4 = 1) then $Add \ 6 \ and \ let \ C0 = 1$



3x4 Multiplier:

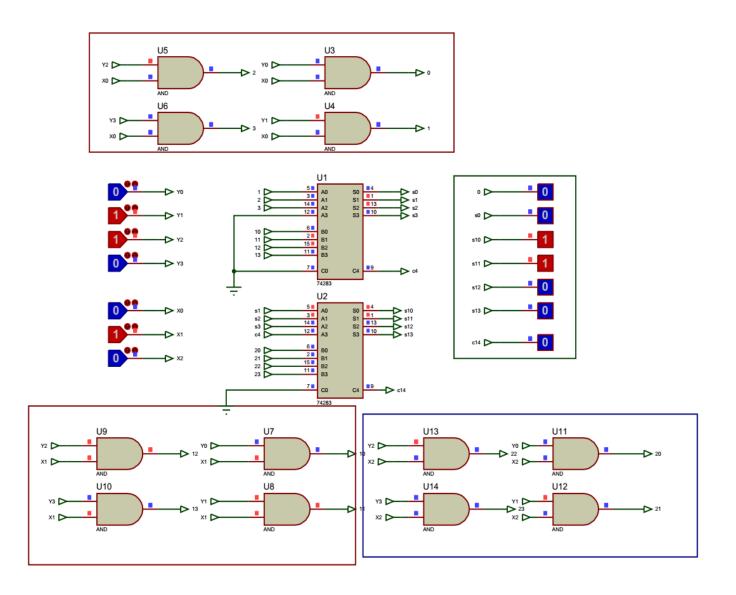
Y3 Y2 Y1 Y0

X2 X1 X0

Y3X0 Y2X0 Y1X0 Y0X0

Y3X1 Y2X1 Y1X1 Y0X1

Y3X2 Y2X2 Y1X2 Y0X2



O->
BCD Adder 3
multiplier--

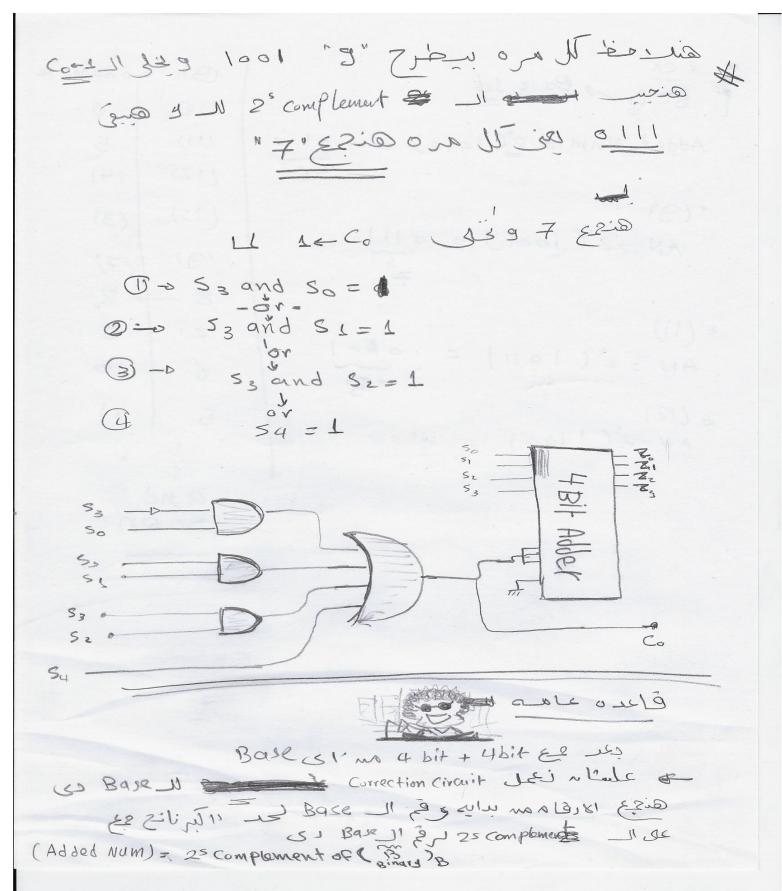
(16) Il vie où l'Base I Correction circuit Let = +

الحل: عن العَمَانَ عَلَيْهُ وَالْمَانَهُ هَا فَي - 8 -

30 -11 n 3-1,1 n 7-1 n 8 -0 8 -0 8 -0 x x x x x x x x x x

16 -8+8 00 72 56910 (X)g= 5-1 ide 80-70 A) 4

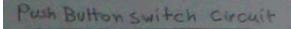
54			1	[-	1/1	1	1	1		
	53	2 4	51	50	//	Co	79	22	21	E0.
4	1	0	0	11	9		C			
0	m	0	90	0	10			0	0	8
G	1	0	10				0	0	0	
0		5	- 0	1	11	1	0	0	,	
	1	11	0	0	12	1	٥			Co
6	1	11	0	1	13			0	\	1
0		, 1	1	0	14		0	1	0	0
		, /	1			1	0		0 1	1
0	1	1	1	1-	15		0	1		
M	0	0	0	0	16			1		0
		-			<u> </u>	\	0	1	1	(
11/1	0	0	0	1	17			1		
11	0	0	4	0	18	No.				
		1	(
					/					
		1			1					
					1					



$$\begin{cases} & 8 \text{ CD} \\ &$$

(3)	C> Added *
(10)	6
(11)	5
(12)	(4)
(13)	(3)
(9)	(7)
8 -	8
7	9
6	10
5	11
1	,
,	
an	9

Soon



Ver Louison Resistor

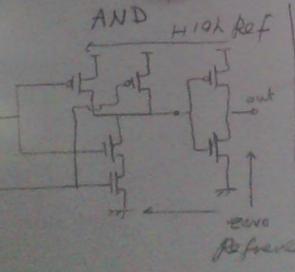
switch on wents high switch & ft out a Low

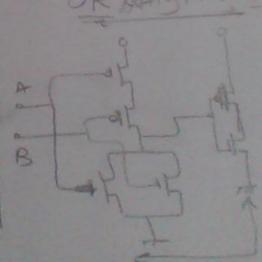
Switch on - pout = low

And arait Low J' of High II gree on! Jim al

And I I I I I I I I

AND I I I OR Keelah &





Temperature.

