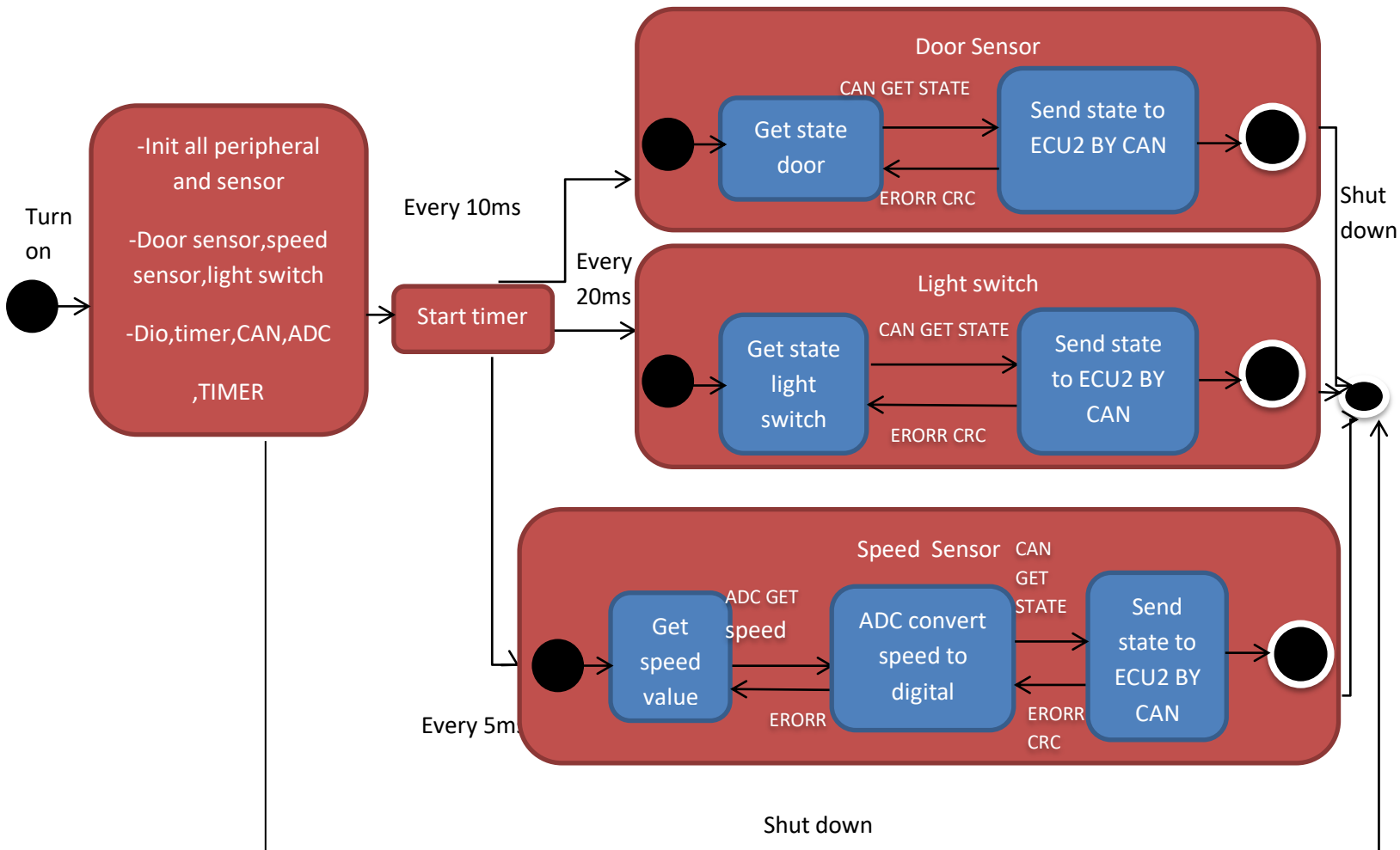
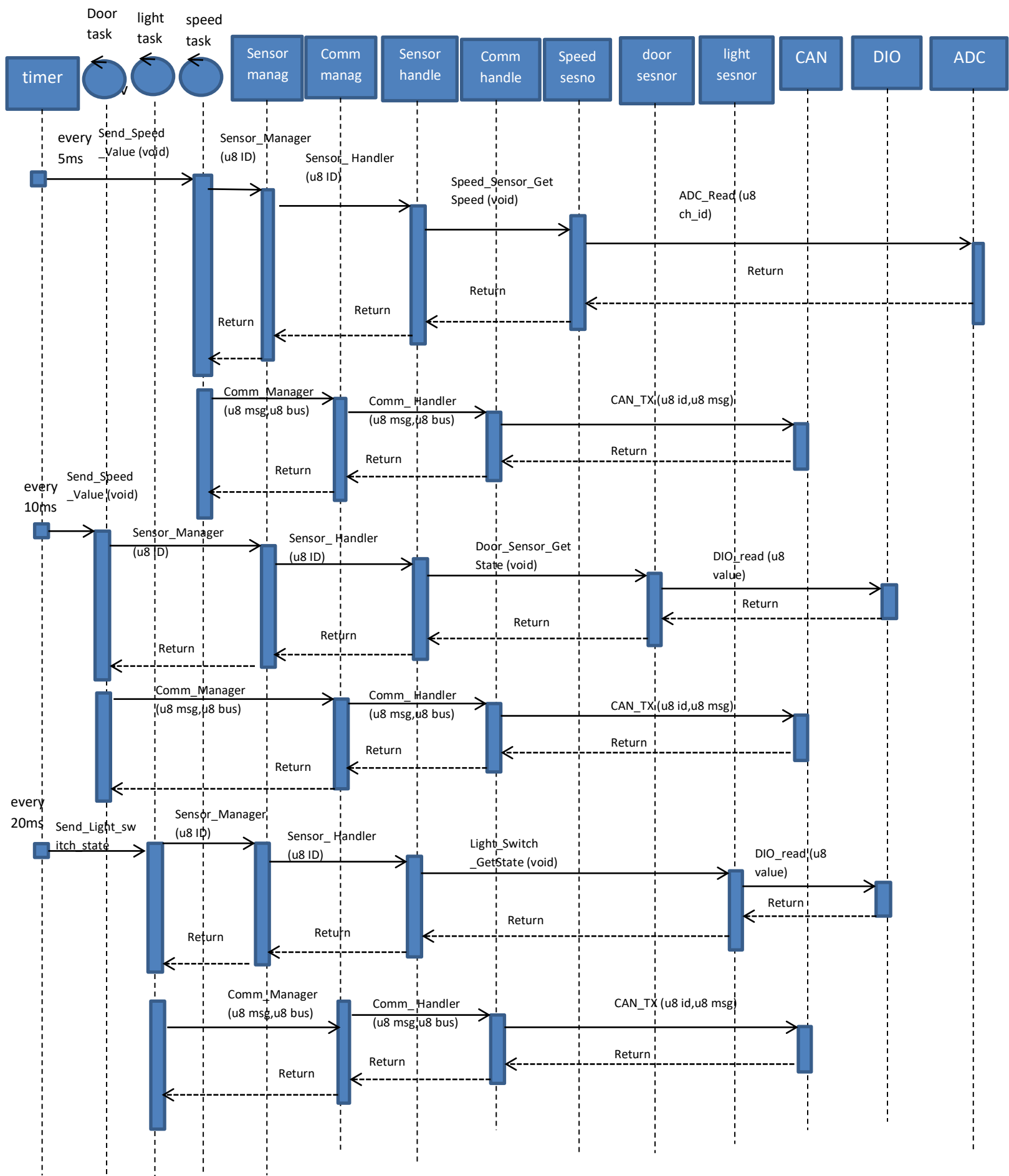


1-ECU1



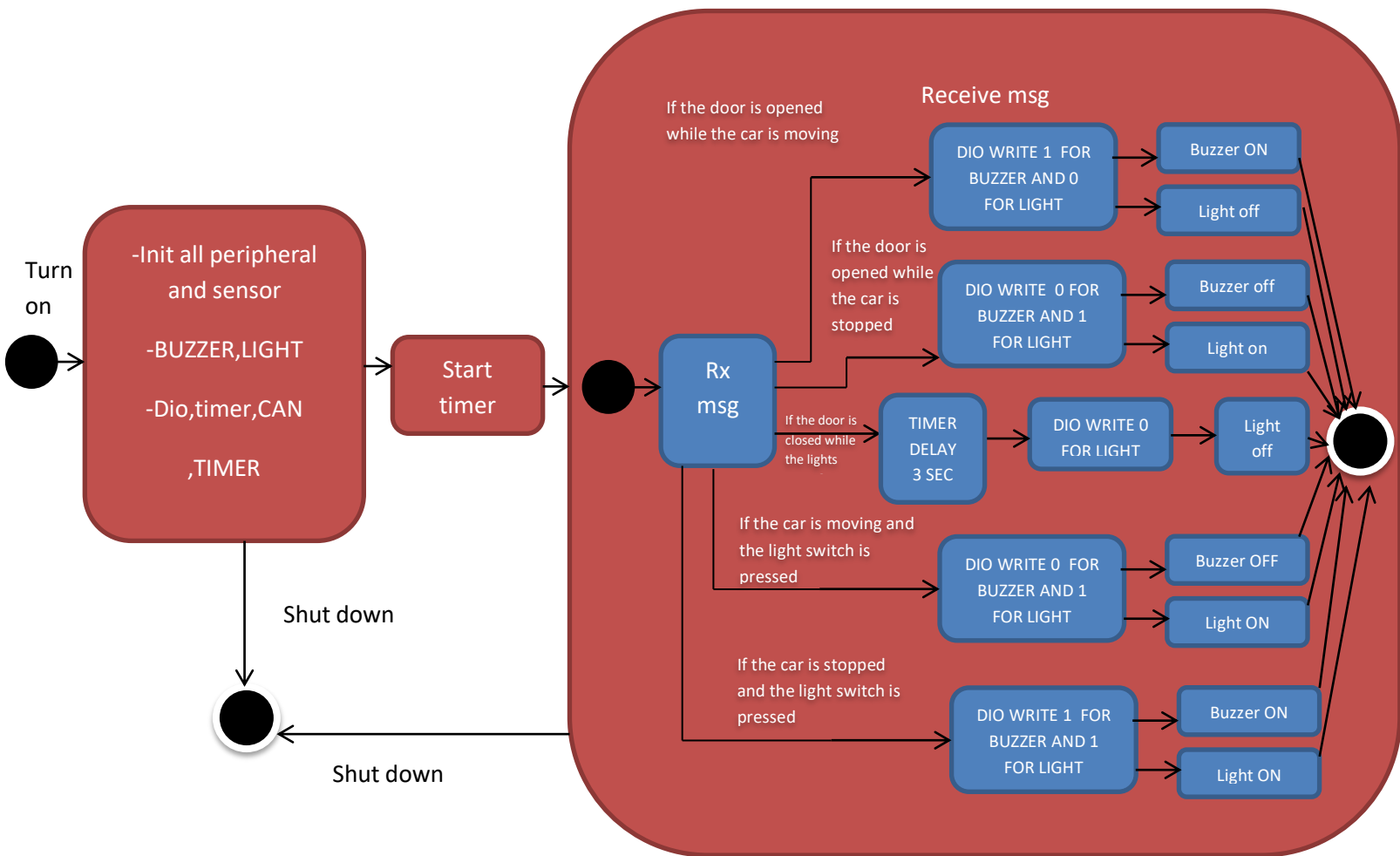


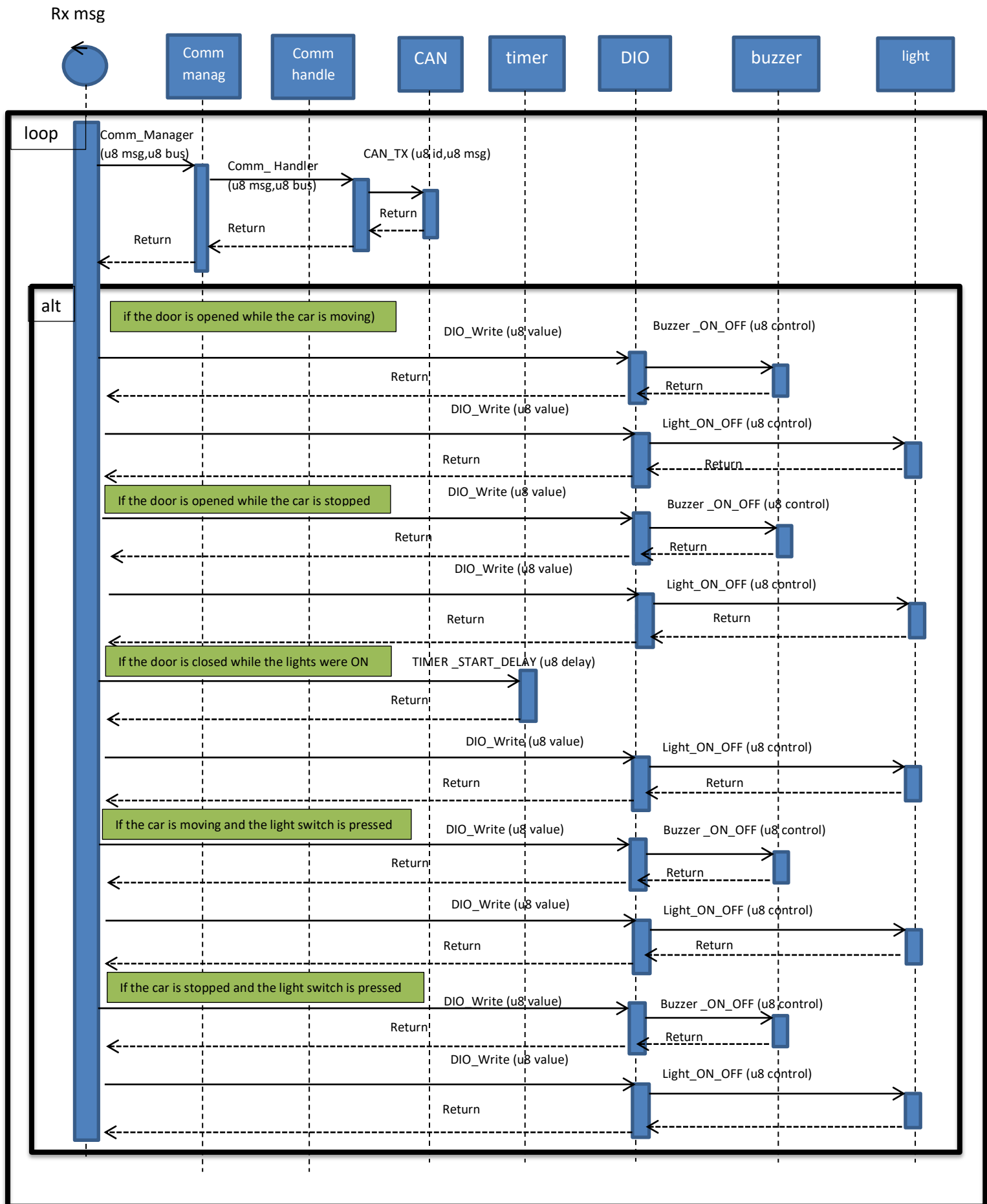
3-CPU LOAD

-System has 3 task .i will assume execution time with door is 1ms with periodic 10m and light switch is 0.5ms with periodic 20ms and speed is 1.5ms with periodic 5ms (Hyper period = 20)

$$U = (E1 + E2 + E3) / H = ((0.5*1) + (1*2) + (1.5*4) / 20) * 100 \% = 42.5\%$$

2-ECU2





3-CPU LOAD

-System has 1 task .i will assume execution time with t1 is 2ms with periodic 4ms and t2 is 1ms with periodic 5ms (Hyper period = 5ms)

$$U = (E1+E2) / H = ((1*2)+(1*1) / 5) * 100 \% = 60\%$$

-Bus Load

-CAN frame consist of below field it is 125 bits and we are using 500 kBit/s bit rate: :

- 1 bit start bit
- 11 bit identifier
- 1 bit RTR
- 6 bit control field
- 0 to 64 bit data field
- 15 bit CRC
- Bit stuffing is possible in the above, for every sequence of 5 consecutive bits of same level. Somewhere around 18 bits in the worst case.
- 3 bit delimiter, ack etc.
- 7 bit end of frame
- 3 bit intermission field after frame

$$\text{bit time} = 1 / \text{bit rate} = 1 / (500 * 1000) \text{ s} = 2 * 10^{-6} \text{ s} = 2 \mu\text{s}$$

$$\text{approximate time to transfer 1 frame is } (2 \mu\text{s/bit} * 125 \text{ bit}) = 250 \mu\text{s}.$$

$$1 \text{ frame every } 5 \text{ ms} = 200 \text{ frame every } 1000 \text{ ms}$$

$$\text{- } 1 \text{ frame every } 10 \text{ ms} = 100 \text{ frame every } 1000 \text{ ms}$$

$$\text{- } 1 \text{ frame every } 20 \text{ ms} = 50 \text{ frame every } 1000 \text{ ms}$$

$$\text{-Total frames in } 1 \text{ s} = 350$$

$$\text{Total time on bus is } 350 * 250 \mu\text{s} = 87500 \mu\text{s}$$

$$\text{Bus load is } ((350 * 250) / (1000 * 1000)) * 100 \% = 8.775 \%$$