Credit Hours System

Architecture

Cairo University

Faculty of Engineering

Architecture Project report
Processor

Names

- Ahmed Hassan Koshek
- Mamdouh Mohamed Rozeka
- Ahmed Mohamed Hamouda
- Youssef Tarek Shaker

How the design solves the pipeline hazards

- There is a forward unit that forwards the data of a register before writing it from ALU to ALU and from Memory to ALU
- There is a hazard detection unit that stalls the pipe if there is a load use

Notes

- If there is an interrupt it will stops the decode instruction and push its PC in the data memory and sets the PC by the value in M[1] in WB